## Scalable Learning of Probabilistic Circuits

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# Acknowledgements

#### **Abstract**

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The rising popularity of generative models together with the growing need for flexible and exact inferences has motivated the machine learning community to look for expressive yet tractable probabilistic models. Probabilistic circuits (PCs) are a family of tractable probabilistic models capable of answering a wide range of queries exactly and in polynomial time. Their operational syntax in the form of a computational graph and their principled probabilistic semantics allow their parameters to be estimated by the highly scalable and efficient optimization techniques used in deep learning. Importantly, tractability is tightly linked to constraints on their underlying graph: by enforcing certain structural assumptions, queries like marginals, maximum a posteriori or entropy become linear time computable while still retaining great expressivity. While inference is usually straightforward, learning PCs that both obey the needed structural restrictions and exploit their expressive power has proven a challenge. Current state-of-the-art structure learning algorithms for PCs can be roughly divided into three main categories. Most learning algorithms seek to generate a usually tree-shaped circuit from recursive decompositions on data, often through clustering and costly statistical (in)dependence tests, which can become prohibitive in higher dimensional data. Alternatively, other approaches involve constructing an intricate network by growing an initial circuit through structural preserving iterative methods. Besides depending on a sufficiently expressive initial structure, these can possibly take several minutes per iteration and many iterations until visible improvement. Lastly, other approaches involve randomly generating a probabilistic circuit by some criterion. Although usually less performant compared to other methods, random PCs are orders of magnitude more time efficient. With this in mind, this dissertation aims to propose fast and scalable random structure learning algorithms for PCs from two different standpoints: from a logical point of view, we efficiently construct a highly structured binary PC that takes certain knowledge in the form of logical constraints and scalably translate them into a probabilistic circuit; from the viewpoint of data guided structure search, we propose hierarchically building PCs from random hyperplanes. We empirically show that either approach is competitive against state-of-the-art methods of the same class, and that their performance can be further boosted by simple ensemble strategies.

Keywords: Probabilistic circuits. Machine learning. Probabilistic models.

## Resumo

Renato Lui Geh. **Aprendizado Escalável de Circuitos Probabilísticos**. Dissertação (Mestrado). Instituto de Matemática e Estatística, Universidade de São Paulo, São Paulo, 2021.

Palavras-chave: Circuitos probabilísticos. Aprendizado de máquina. Modelos probabilísticos.

## Nomenclature

## **List of Symbols**

 $\sigma$  Sigmoid function

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#### **Notation**

We use the following notation throughout the work. Random variables are written in upper case (e.g. X, Y) and their values in lower case (e.g. x, y). We write  $\mathcal{X}$  as the sample space and for independence we use  $\mathbb{L}$  to indicate that two variables X and Y are statistically independent, i.e.  $X \perp Y$ . We identify propositional variables with 0/1-valued random variables, and use them interchangeably. Sets of variables and their joint values are written in boldface (e.g. X, x). Given a Boolean formula f, we write f to denote its semantics, i.e. the Boolean function represented by f. For Boolean formulas f and g, we write f if they are logically equivalent, that is, if f is f in a Boolean function f and write f is f to indicate that f is f for a Boolean function f. We use the notation f in f in

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## **Chapter 1**

## Introduction

When reasoning about the world, rarely can we find a realistic model that perfectly subsumes all of the needed relationships for flawless prediction. As such, the presence of a reliable uncertainty quantifier in intelligent systems is essential in developing performant yet diagnostible agents. This is made explicitly clear in the case of high-risk settings, such as autonomous vehicles or automated power plant systems, where a wrong prediction could cause disastrous consequences. A well known example in the case of the former is obstacle avoidance: while the agent should be capable of accurately identifying obstructions in its way during normal conditions, so should it be able to identify its own lack of confidence in high uncertainty situations like ones brought by environmental factors, such as severe blizzard or heavy rain. In these situations where predictions are highly unreliable, the safest option might be for the agent to first identify its uncertainty, and second to reach out for human help. Other interesting applications of uncertainty quantification include out-of-distribution detection, which in our previous example could be visualized as the agent identifying a human's driving as abnormally irregular (due to inebriation, infirmity, etc.) and appropriately taking control of the vehicle before a potential accident takes place.

One popular approach to quantifying uncertainty is through probability theory. By abstracting the world as a probability distribution with a finite number of observable random variables that encode a possibly incomplete knowledge of the environment, we are, in theory, able to answer a diverse set of complex queries as long as we have access to the (approximate) true data distribution. Practice is far different from theory, however, as most machine learning models either lie within a very limited range in the tractability spectrum in terms of inference (Vergari, Choi, Peharz, et al., 2020) or are too simplistic for complex real-world problems. Besides, although the majority of recent advances in deep learning claim some probabilistic meaning from the model's output, they are often uncalibrated distributions, a result of focusing on maximizing predictive accuracy at the expense of predictive uncertainty (Guo et al., 2017; Ovadia et al., 2019; Chernikova et al., 2019), ultimately producing overconfident and peculiar results (Szegedy et al., 2013; Wei et al., 2018; Su et al., 2019; Chernikova et al., 2019). Crucially, mainstream deep models (i.e. standard neural networks) usually optimize a conditional distribution over the to-be-predicted random variables – and are thus often called discriminative models – and

do not model the actual joint distribution of the data, limiting inference capabilities and uncertainty estimation.

In contrast, *generative* models seek to extract information from the joint (in varying capacities), and have lately seen a sharp rise in interest within deep learning. Despite this, most popular models do not admit either exact or tractable querying of key inference scenarios. For instance, although Generative Adversarial Networks (GANs) allow for efficient sampling (Goodfellow *et al.*, 2014), basic queries such as likelihood or marginals are outside of their capabilities. Similarly, Normalizing Flows (NF) also permit access to efficient sampling, with the added feature of computing likelihoods, but are severely limited by the base distribution when it comes to discrete data (Rezende and Mohamed, 2015; Papamakarios *et al.*, 2021) albeit recent works on discretizing NFs have shown empirically good results (Lippe and Gavves, 2021; Ziegler and Rush, 2019). Variational Auto-Encoders (VAEs) are (under certain conditions) a generalization of NFs (Yu, 2020; Gritsenko *et al.*, 2019) with known extensions for categorical data (Rolfe, 2017; Vahdat, Macready, *et al.*, 2018; Vahdat, Andriyash, *et al.*, 2018), but only permit access to sampling and an upperbound on the likelihood, with the latter available only after solving a complex optimization task (Kingma and Welling, 2014).

Despite the impressive achievements of the aforementioned generative models on realistically producing samples consistent with evidence, in none of the previous models are complex queries like structured prediction under partial observations, *maximum a posteriori* (MAP), conditional or marginal probabilities tractable. An obvious alternative would be Probabilistic Graphical Models (PGMs), although they too suffer from intractability when dealing with high treewidth networks (R. Dechter, 1998; Koller and Friedman, 2009), severely limiting expressivity. Instead, we draw our attention to an expressive class of models that subsumes several families of probabilistic models with tractable inference capabilities.

Probabilistic Circuits (PCs) define a superclass of probabilistic models distincly specified by recursive compositions of distributions through graphical formalisms. Vaguely speaking, PCs are computational graphs akin to neural networks, but whose network structure and computational units abide by special constraints. Within these specific conditions span a wide range of subclasses, each establishing a distinct set of restrictions on their structure in order to enable different segments within the tractability spectrum. As an example, Sum-Product Networks (SPNs, Poon and P. Domingos, 2011) are usually loosely defined over a couple of constraints: namely smoothness and decomposability, which in turn enables likelihood, marginal and conditional computations. Arithmetic Circuits (ACs, DARWICHE, 2003) add determinism to the mix, allowing for tractable computation of MAP probabilities. Similarly, Cutset Networks (CNets, RAHMAN et al., 2014) employ the same constraints as ACs, but accept more expressive distributions as part of their computational units. Probabilistic Sentential Decision Diagrams (PSDDs, KISA et al., 2014), Probabilistic Decision Graphs (PDGs, JAEGER, 2004) and And/Or-Graphs (AOGs, Rina DECHTER and MATEESCU, 2007) all require *smoothness* and *determinism*, but also call for a stronger version of decomposability, permitting all queries previously mentioned as well as computation of the Kullback-Leibler divergence and expectation between two circuits (Choi et al., 2020). Usually, PCs represent the joint distribution of the data, although they are sufficiently expressive for generative and discriminative modeling (Khosravi et al., 2019; Rashwan

*et al.*, 2018; Rooshenas and Lowd, 2016; Gens and P. Domingos, 2012; Shao *et al.*, 2020). In this dissertation though, we shall focus on the generative side of PCs.

While inference is usually straightforward, as we shall see in ??, learning the structure of PCs so that they obey the needed structural restrictions requires either careful handcrafted architectures (Poon and P. Domingos, 2011; Cheng et al., 2014; NATH and P. M. Domingos, 2016) or usually involves running costly (in)dependence tests over most (if not all) variables (Gens and P. Domingos, 2013; Jaini, Ghose, et al., 2018; Vergari, Mauro, et al., 2015; Di MAURO et al., 2017), which can become prohibitive in higher dimension data. Alternatively, some learning algorithms resort to structure preserving iterative methods to grow a PC that already initially satisfies desired constraints, adding complexity to the underlying distribution at each iteration (LIANG, BEKKER, et al., 2017; DANG et al., 2020). However, these can take several iterations until visible improvement and often take several minutes for each iteration when the circuit is big. Common techniques used in deep learning for generating scalable architectures for neural network also pose a problem, as the nature of the needed structural constraints make for sparse computational graphs. To circumvent these issues, work on scaling PCs to higher dimensions has focused mainly on random architectures, with competitive results (Peharz, Vergari, et al., 2020; Mauro et al., 2021; Geн and Denis Deratani Mauá, 2021; Ренак, Lang, et al., 2020). Apart from the scalability side of random structure generation, usual structure learning algorithms often require gridsearch for hyperparameter tuning to achieve top quality performance, which is usually not the case for random algorithms. For the usual data scientist or machine learning practicioner, hyperparameter tuning can become exhaustive, especially if the goal is to analyze and infer from large data, and not to achieve top tier performance on benchmark datasets.

In this dissertation, we propose two scalable structural learning algorithms for probabilistic circuits that are especially suited for large data and fast deployment. They both take advantage of random network generation to quickly construct PCs with little to no need for hyperparameters. The first is effective for constructing PCs from binary data with a highly constrained structure, and thus appropriate when complex querying is needed. The second builds less constrained random PCs, but supports both discrete and continuous data.

#### 1.1 Contributions and Dissertation Outline

We organize this dissertation as follows. We begin Chapter 2 by formally defining probabilistic circuits, conducting a review of some of the structural constraints that we might impose on PCs, as well as what we may gain from them in terms of tractability. We then list existing formalisms that may be viewed as instances of PCs, and what their structure entail in terms of inference power. In Chapter 3, we address existing PC structure learning algorithms, and which guarantees in terms of tractability each give. We cover the two new structure learners in Chapter 4, providing empirical results on their performance. The final chapter is dedicated to summarizing our research contributions and pointing to potential future work in learning PCs.

Our contributions in this dissertation address the following research topics.

#### Scalably learning PCs directly from background knowledge

In Geh and Denis Deratani Mauá (2021), we provide a learning algorithm for PSDDs that learns a PC directly from background knowledge in the form of logical constraints. The algorithm samples a structure from a distribution of possible PSDDs that are weakly consistent with the logical formula. How weak consistency is depends on a parameter that trades permission of false statements as non zero probability events with circuit complexity. We provide the algorithm and empirical results in Section 4.??.

#### Using ensembles to strengthen consistency

The PC sampler given by GEH and Denis Deratani MAUÁ (2021) produces competitive probabilistic models (in terms of likelihood), albeit weak logical models in the sense that it possibly assigns non-zero probability to false variable assignments – as we discuss in Section 4.??, it never assigns zero probability to true statements. By producing many weak models, we not only gain in terms of data fitness, but also consistency: if any one component in the ensemble returns an assignment to be impossible, the whole model should return false.

#### Random projections to efficiently learn PCs

Usual methods often employ clustering algorithms for constructing convex combinations of computational units. These can take many iterations to converge or require space quadratic in the number of data points. Instead, in Section 4.?? we present linear alternatives based on random projections (Freund *et al.*, 2008; Dasgupta and Freund, 2008).

## **Chapter 2**

## **Probabilistic Circuits**

As we briefly mentioned in the last chapter, Probabilistic Circuits (PCs) are conceptualized as computational graphs under special conditions. In this chapter, Section 2.1 to be more precise, we formally define PCs and give an intuition on their syntax, viewing other probabilistic models through the lenses of the PC framework. In Section 2.2, we describe the special structural constraints that give PCs their inference power over other generative models and state which queries (as far as we know) are enabled from each constraint.

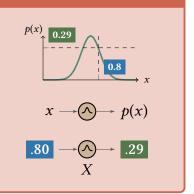
#### 2.1 Distributions as Computational Graphs

Probabilistic circuits are directed acyclic graphs usually recursively defined in terms of their computational units. In its simplest form a PC is a single unit with no outgoing edges whose value corresponds to the result of a function. These are often called *input* nodes, and can take any form as long as its value is tractably computable. More concretely, input nodes typically represent probability density (or mass) functions, although they also support inputs as joint probability density functions of complex non-parametric models as well. To simplify notation, from here on out we shall use the term distribution and probability density (resp. mass) function interchangeably and argue that the input node represents a probability distribution.

#### Example 2.1.1: Gaussians as probabilistic circuits

Let  $L_p$  an input node and  $p(X) = \mathcal{N}(X; \mu, \sigma^2)$  a univariate Gaussian distribution. Computing any query on  $L_p$  is straightforward: any query on  $L_p$  directly translates to p. As an example, suppose  $\mu = 0$  and  $\sigma^2 = 1$  and we wish to compute  $L_p(x = 0.8)$ . The probability of this input shall then be

$$L_p(x = 0.8) = \mathcal{N}(x = 0.8; \mu = 0, \sigma^2 = 1) = 0.29.$$



Let  $L_p$  a PC input node and denote p as its inherent probability distribution. By definition, any query  $f: \mathcal{X} \to \mathcal{Y}$  which is tractable on p is tractable on  $L_p$ . We shall denote  $L_p(X) = p(X)$ , and often omit p when its explicit form is not needed. Evidently, a single input node lacks the expressivity for modeling complex models, otherwise we would have just used the input distribution as a standalone model. The expressiveness of PCs comes from recursively combining distributions into complex functions. This can be done through computational units that either compute convex combinations or products of their children. Let us first look at convex combinations, known in the literature as sum nodes.

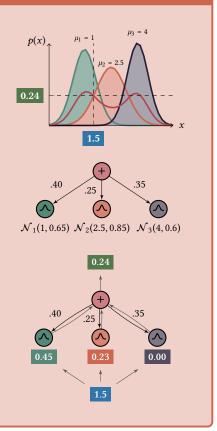
Let S be a PC sum node, and denote by Ch(S) the children nodes of S. For every edge  $\overrightarrow{SC}$  coming out of S and going to C, we attribute a weight  $w_{S,C} > 0$ , such that  $\sum_{C \in Ch(S)} w_{S,C} = 1$ . A sum node semantically defines a mixture model over its children, essentially acting as a latent variable over the component distributions (Poon and P. Domingos, 2011; Peharz, Gens, Pernkopf, *et al.*, 2016). Its value is the weighted sum of its children  $p_S(X = x) = \sum_{C \in Ch(S)} w_{S,C} \cdot p_C(X = x)$ . To simplify notation, we shall use N(X = x) as an alias for  $p_N(X = x)$ , that is, the probability function given by N's induced distribution.

#### Example 2.1.2: Gaussian mixture models as probabilistic circuits

A Gaussian Mixture Model (GMM) defines a mixture over Gaussian components. Say we wish to compute the probability of X = x for a GMM  $\mathcal{G}$  with three components  $\mathcal{N}_1(\mu_1 = 1, \sigma_1^2 = 0.65)$ ,  $\mathcal{N}_2(\mu_2 = 2.5, \sigma_1^2 = 0.85)$  and  $\mathcal{N}_3(\mu_3 = 4, \sigma_3^2 = 0.6)$ , and suppose we have weights set to  $\phi = (0.4, 0.25, 0.35)$ . Computing the probability of G amounts to the weighted summation

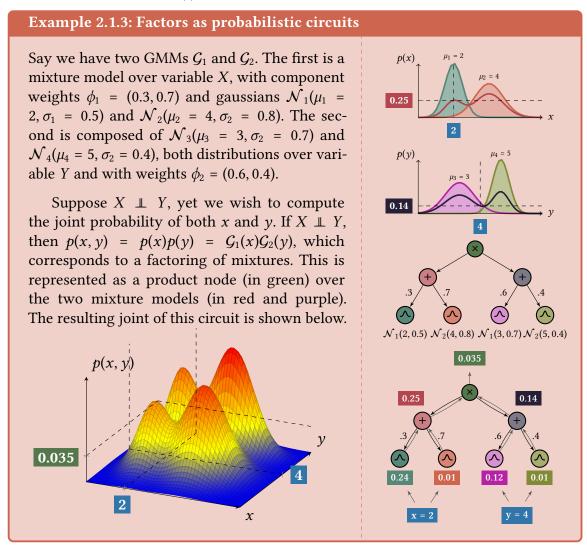
$$\mathcal{G}(X = x) = 0.4 \cdot \mathcal{N}_1(x; \mu_1, \sigma_2^2) + 0.25 \cdot \mathcal{N}_2(x; \mu_2, \sigma_2^2) + 0.35 \cdot \mathcal{N}_3(x; \mu_3, \sigma_3^2),$$

which is equivalent to a computational graph (i.e. a PC) with a sum node whose weights are set to  $\phi$  and children are the components of the mixture. The figure on the right shows  $\mathcal{G}$  (top) and its corresponding PC (middle). Given x=1.5 (in blue), input nodes are computed following the inference flow (bottom, gray edges) up to the root sum node (in red), where a weighted summation is computed to output the probability (in green).



So far, the only nonlinearities present in PCs come from the internal computations of input nodes. In fact, a PC that only contains sums inputs can always be reduced to a sum node rooted PC with a single layer, i.e. a mixture model (see Theorem A.1.1). Adding *product nodes* as another form of nonlinearity increases expressivity sufficiently for PCs

to be capable of representing any (discrete) probability distribution (Darwiche, 2003; Martens and Medabalimi, 2014; Peharz, Tschiatschek, *et al.*, 2015). More importantly, products semantically act as factorizations of their children, indicating an independence relationship between variables from different children. In practice, product nodes are simply products of their childrens' distribution: if P is a PC product node, then its value is given by  $P(X = x) = \prod_{C \in Ch(P)} C(X = x)$ .



Now that we have introduced the three most important computational units in PCs, we are finally ready to formally define probabilistic circuits.

**Definition 2.1.1** (Probabilistic circuit). A probabilistic circuits C is a rooted DAG whose nodes compute any tractable operation of their children, usually either convex combinations, known as sum nodes, or products. Nodes with no outgoing edges, i.e. input nodes, are tractable nonnegative functions whose integrals exist and equal to one. Computing a value from C amounts to a bottom-up feedforward pass from input nodes to root.

While we assume that *tractable* operation or function is acceptable, we are usually interested in  $\mathcal{O}(1)$  time computable operations, and often assume the same of input functions to simplify analysis. Further, in this dissertation we are only interested in convex combinations and products, and as such only these operations are considered. When a

probabilistic circuit  $\mathcal{C}$  contains no consecutive sums or products (i.e. for every sum all of its children are either inputs or products and respectively for products) then it is said to be a *standard* form circuit. Any PC can be transformed into a *standardized* circuit, a process we call *standardization* (see Theorem A.1.2).

#### Remark 2.1.1: On operators and tractability

Throughout this work we consider only products and convex combinations (apart from the implicit operations contained within input nodes) as potential computational units. The question of whether any other operator could be used to gain expressivity without loss of tractability is without a doubt an interesting research question, and one that is actively being pursued. However, this is certainly out of the scope of this dissertation, and so we restrict discussion on this topic and only give a brief comment on operator tractability here, pointing to existing literature in this area of research.

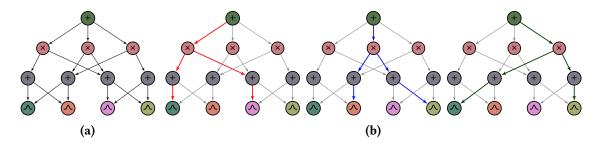
A. Friesen and P. Domingos (2016) formalize the notion of replacing sums and products in PCs with any pair of operators in a commutative semiring, giving results on the conditions for marginalization to be tractable. They provide examples of common semirings and to which known formalisms they correspond to. One such example are PCs under the Boolean semiring ( $\{0,1\}, \lor, \land, 0, 1$ ) for logical inference, which are equivalent to Negation Normal Form (NNF, Barwise, 1982) and constitute an instance of Logic Circuits (LCs), of which Sentential Decision Diagrams (SDDs, Darwiche, 2011) and Binary Decision Diagrams (BDDs, Akers, 1978) are a part of. Another less common semiring in PCs is the real min-sum semiring ( $\mathbb{R}_{\infty}$ , min, +,  $\infty$ , 0) for nonconvex optimization (A. L. Friesen and P. Domingos, 2015).

Recently, Vergari, Choi, Liu, *et al.* (2021) extensively covered tractability conditions and complexity bounds for convex combinations, products, exp (and more generally powers in both naturals and reals), quotients and logarithms, even giving results for complex information-theoretic queries, such as entropies and divergences. Notably, they analyze whether structural constraints (and thus, in a sense, tractability) under these conditions are preserved.

Up to now, we have only considered summations as nonnegative weighted sums. Indeed, in most literature the sum node is defined as a convex combination. However, negative weights have appeared in Logistic Circuits (Liang and Van Den Broeck, 2019) for discriminative modeling; and in Probabilistic Generating Circuits (Zhang *et al.*, 2021), a class of tractable probabilistic models that subsume PCs. Denis D. Mauá *et al.* (2017) extend (nonnegative) weights in sum nodes with probability intervals, effectively inducing a credal set (Cozman, 2000) for measuring imprecision.

Other works include PCs with quotients (Sharir and Shashua, 2018), transformations (Pevný *et al.*, 2020), max (Melibari *et al.*, 2016), and einsum (Peharz, Lang, *et al.*, 2020) operations.

Before we address the key components that make PCs interesting tractable probabilistic models, we must first discuss some important concepts that often come up in PC literature.



**Figure 2.1:** A probabilistic circuit (a) and 3 of the 12 possible induced subcircuits (b).

Mainly, we are interested in defining two notions here: the scope of a unit and induced subcircuits.

In simple terms, the scope of a computational unit N of a PC is merely the set of all variables that appear in the descendants of N. More formally, denote by Sc(N) the set of all variables that appear in N. We inductively compute the scope of circuit by a bottom-up approach: the scope of an input node  $L_p$  is the set of variables that appear in p's distribution<sup>1</sup>, and the scope of any other node is the union of all of its childrens' scopes. The notion of scope is essential to the structural constraints seen in Section 2.2.

As an example, take the circuit from Example 2.1.3. The scope of input nodes  $\triangle$  and  $\triangle$  are  $Sc(\triangle) = Sc(\triangle) = \{X\}$ , while  $Sc(\triangle) = Sc(\triangle) = \{Y\}$ . Consequentially, their parent sum nodes will have the same scope as their children  $Sc(\textcircled{+}) = \{X\}$  and  $Sc(\textcircled{+}) = \{Y\}$ , yet the root node's scope is  $Sc(\textcircled{-}) = \{X, Y\}$ , since its childrens' scopes are distinct.

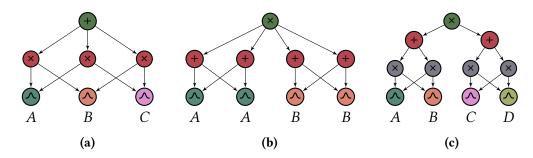
Let C a probabilistic circuit and node  $N \in C$ . We say that  $S_N$  is a subcircuit of C rooted at N if  $S_N$ 's root is N, all nodes and edges in  $S_N$  are also in C and S is also a probabilistic circuit. We now introduce the concept of induced subcircuits (Chan and Darwiche, 2006; Dennis and Ventura, 2015; Peharz, Gens, and P. Domingos, 2014).

**Definition 2.1.2** (Induced subcircuit). Let C a probabilistic circuit. An induced subcircuit S of C is a subcircuit of C such that all edges coming out of product nodes in C are also in S, and of all edges coming out of sum nodes in C, only one is in S.

Examples of induced subcircuits are visualized in Figure 2.1. When the induced subcircuit is a tree, as is the case in Figure 2.1, they are referred to as induced tree (Zhao, Melibari, *et al.*, 2015; Zhao, Adel, *et al.*, 2016).

So far, by Definition 2.1.1 a PC does not yet necessarily represent a probability distribution. To do so, the structure must obey constraints that we have only previously mentioned in passing. We formally define them next.

<sup>&</sup>lt;sup>1</sup> Although we previously defined input nodes as mere functions, here we are explicitly associating a random variable to a *probability* function. Indeed, if we are being rigorous, we should define input nodes as a pair of random variable and function. To save space we instead assume, as previously stated, that the function is seen as both the probability (density) function as well as the distribution itself, and thus its scope is the scope of its distribution, i.e. the random variables that come into play in a probability distribution.



**Figure 2.2:** Decomposable but unsmooth (a), smooth but undecomposable (b), and smooth and decomposable (c) circuits.

#### 2.2 Deciding What to Constraint

In this section we are interested in studying how the structural constraints in PCs enable different inference tasks. We shall first cover the more basic queries, namely *probability of evidence* (EVI), *marginal probability* (MAR), *conditional probability* (CON) and *maximum a posteriori probability* (MAP). After that we address more complex queries such as *mutual information* (MI) and *expectation* (EXP).

The most basic inference task we are interested in computing is the probability of evidence. To unlock this, we must introduce two structural constraints known as *smoothness* and *decomposability*.

**Definition 2.2.1** (Smoothness). A probabilistic circuit C is said to be smooth if for every sum node S in C,  $Sc(C_1) = Sc(C_2)$  for  $C_1, C_2 \in Ch(S)$ .

**Definition 2.2.2** (Decomposability). A probabilistic circuit C is said to be decomposable if for every product node P in C,  $Sc(C_1) \cap Sc(C_2) = \emptyset$  for  $C_1, C_2 \in Ch(P)$ .

For any *smooth* and *decomposable* PC, computing EVI is done in linear time in the number of edges. In fact this is true for MAR and CON as well. To compute marginals, it is sufficient to compute the corresponding marginals with respect to each input node and proceed to propagate values bottom-up. For conditionals, we simply compute two passes: one where we marginalize the conditional variables and the other any other variables that are not present in our query. These procedures are formalized in the theorem below.

**Theorem 2.2.1.** Let C a smooth and decomposable PC. Any one of EVI, MAR or CON can be computed in linear time (in the number of edges of C).

Because EVI, MAR and CON are the most basic forms of querying in a PC, we shall refer to these as the *base queries*. Although smoothness and decomposability are sufficient conditions for tractable base queries, they are not necessary conditions. As a matter of fact, decomposability can be replaced with a third weaker constraint known as *consistency*. Denote by Desc(N) the set of all descendants of N.

**Definition 2.2.3** (Consistency). A probabilistic circuit C is said to be consistent if for any product node P in C, it holds that every two children  $C_1, C_2 \in Ch(P)$  there does not exist two input nodes  $L_p^1 \in Desc(C_1)$  and  $L_q^2 \in Desc(C_2)$  whose scope is the same and  $p(\mathbf{x}) \neq q(\mathbf{x})$  for any  $\mathbf{x} \in \mathcal{X}$ .

In Peharz, Tschiatschek, et al. (2015), Peharz, Tschiatschek, et al. show that although smooth and consistent PCs are more succinct (Darwiche and Marquis, 2002) compared to smooth and decomposable circuits, the gain is only mild, further proving that any smooth and consistent PC can be polynomially translated to a decomposable equivalent. In practice, because constructing decomposable circuits (and verifying decomposability) is much easier compared to the same with consistency, we instead focus on smooth and decomposable PCs.

Suppose we wish to compute the most probable assignment of a variable, say for classification or image reconstruction. To do so, we must compute the conditional query

$$\max_{\mathbf{y}} p(\mathbf{y}|\mathbf{x}) = \max_{\mathbf{y}} \frac{p(\mathbf{y}, \mathbf{x})}{p(\mathbf{x})} = \frac{\max_{\mathbf{y}} p(\mathbf{y}, \mathbf{x})}{p(\mathbf{x})},$$

often called the *maximum a posteriori* (MAP) probability. Although it seems at first like MAP is no harder than computing a CON, it turns out that for smooth and decomposable PCs, MAP is unfortunately intractable (Conaty *et al.*, 2017; Mei *et al.*, 2018). To unlock access to MAP we must make the circuit *deterministic*.

**Definition 2.2.4** (Determinism). A probabilistic circuit C is said to be deterministic if for every sum node  $S \in C$  only one child of S has nonnegative value at a time for any assignment.

At this point, we must introduce a graphical notation for *indicator* nodes. An indicator node is an input node whose function is the characteristic function  $f(x) = \mathbb{1}_{\{x=k\}}$ , i.e. a degenerate function with all on k and zero anywhere else. A special case is when X is binary and k = 1, in which case we say the input node is a literal, denoting by the usual propositional notation X for when k = 1 and  $\neg X$  for k = 0. Graphically, we shall use  $\bigcirc$  for indicators and the propositional notation for literals.

With determinism, we now have access to MAP.

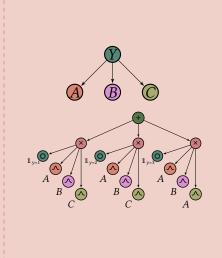
**Theorem 2.2.2.** Let C a smooth, decomposable and deterministic PC. MAP is computable in linear time (in the number of edges of C).

#### Example 2.2.1: Naïve Bayes as probabilistic circuits

Suppose we have census measurements on the age A, body mass B and average amount of cheese eaten in a day C from three different cities Y. Assuming A, B and C are independent, given a sample x = (a, b, c) we can use a Gaussian naïve Bayes to predict x's class

$$p(y|a, b, c) = p(y)p(a|y)p(b|y)p(c|y).$$
 (2.1)

In PC terms, p(y) are the weights for each class and p(z|y) are Gaussian input nodes. To make sure that these are the distributions of feature z given y, we introduce indicator variables "selecting" Y's state. Since the PC is deterministic, we can compute the MAP in linear time by simply replacing the root node with a max, which is equivalent to finding the highest likelihood of x for each city y.



## Appendix A

## **Appendix**

#### A.1 Proofs

**Theorem A.1.1.** Let C a probabilistic circuit whose first l layers are composed solely of sum nodes. Call N the set of all nodes in layer l+1. C is equivalent to a PC C' whose root is a sum node with N as children.

*Proof.* We adapt a similar proof due to JAINI, POUPART, *et al.* (2018). Every sum node is of the form

$$S(\mathbf{x}) = \sum_{C \in Ch(S)} w_{S,C} \cdot C(\mathbf{x}).$$

Particularly, every child C in a sum node in layer  $1 \le i \le l - 1$ , is a sum node, and so for the first layer we have that

$$\begin{split} S(\mathbf{x}) &= \sum_{C_1 \in \text{Ch}(S)} w_{S,C_1} \sum_{C_2 \in \text{Ch}(C_1)} w_{C_1,C_2} \, C_2(\mathbf{x}) \\ &= \sum_{C_1 \in \text{Ch}(S)} \sum_{C_2 \in \text{Ch}(C_1)} w_{S,C_1} \, w_{C_1,C_2} \, C_2(\mathbf{x}). \end{split}$$

Define a one-to-one mapping that takes a tuple  $(C_1, C_2)$  where  $C_1 \in Ch(S)$  and  $C_2 \in Ch(C_1)$  and returns a (unique) path from S to every grandchild  $C_2$  of S. Call **K** the set of all paths, and  $w_{S,C_1}$  and  $w_{C_1,C_2}$  the weights for one such path. We can merge these two weights into a single weight  $w'_{S,C_2} = w_{S,C_1} \cdot w_{C_1,C_2}$ , yielding

$$S(\mathbf{x}) = \sum_{(w_{S,C_1}, w_{C_1,C_2}) \in K} w_{S,C_2}' C_2(\mathbf{x}).$$

This ensures that two consecutive sum layers can be collapsed into a single layer. Particularly, for the first (root) and second layers, the above transformation generates a circuit with one fewer layer and whose root has  $\mathcal{O}(nm)$  edges, where n and m are the number of edges coming from the original root and its children respectively. We can apply this

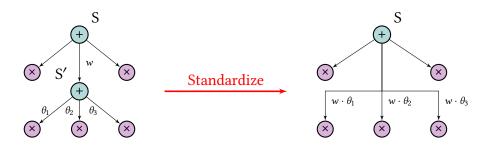
procedure until there are no more consecutive sum nodes. This results in a PC of the form

$$S(\mathbf{x}) = \sum_{C \in Ch(S)} w_{S,C} N(\mathbf{x}),$$

where  $N \in \mathbb{N}$ . The number of children of the resulting root sum node will be exponential on the number of edges of its children.

**Theorem A.1.2** (Standardization). Any probabilistic circuit C can be reduced to a circuit where every sum node contains only products or inputs and every product node contains only sums or inputs.

*Proof.* If C is already standard we are done. Otherwise, there exists either (i) a sum node S with a sum S' as child; or (ii) a product node P with a product P' as child. We first address (i): let W be the weight of edge S and O0 the weights from all edges coming out from S'1.



Connect S with every child of S', assigning as weight  $w \cdot \theta_i$  for each child i. Delete S' and all edges coming out from it. The resulting circuit is computationally equivalent but now without a consecutive pair of sums. This transformation is visualized by the figure above. We do a similar procedure in (ii), but now instead remove P' and connect all children of P' to P, as we show below.



**Theorem 2.2.1.** Let C a smooth and decomposable PC. Any one of EVI, MAR or CON can be computed in linear time (in the number of edges of C).

*Proof.* For a sum node S, we have the following marginalization query

$$\begin{split} \int \mathrm{S}(\mathbf{x},\mathbf{y}) \, \mathrm{d}\mathbf{y} &= \int \sum_{\mathrm{C} \in \mathrm{Ch}(\mathrm{S})} w_{\mathrm{S},\mathrm{C}} \, \mathrm{C}(\mathbf{x},\mathbf{y}) \, \mathrm{d}\mathbf{y} \\ &= \sum_{\mathrm{C} \in \mathrm{Ch}(\mathrm{S})} w_{\Sigma,\mathrm{C}} \int \mathrm{C}(\mathbf{x},\mathbf{y}) \, \mathrm{d}\mathbf{y}. \end{split}$$

Analogously, for a product node

$$\int P(\mathbf{x}, \mathbf{y}) \, d\mathbf{y} = \int \prod_{C \in Ch(P)} C(\mathbf{x}, \mathbf{y}) \, d\mathbf{y}$$
$$= \prod_{C \in Ch(P)} \int C(\mathbf{x}, \mathbf{y}) \, d\mathbf{y}.$$

This ensures that marginals are pushed down to children. This can be done recursively until C is an input node  $L_p$ , in which case we marginalize y according to p, which by definition should be tractable and here we assume can be done in  $\mathcal{O}(1)$ . We have proved the case for MAR. For EVI, we simply assign  $y = \emptyset$  with input nodes acting as probability density functions. Conditionals can easily be computed by an EVI followed by a second pass marginalizing the conditional variables  $p(x|y) = \frac{p(x,y)}{p(y)}$  which are both done in linear time as we have seen here.

**Theorem 2.2.2.** Let C a smooth, decomposable and deterministic PC. MAP is computable in linear time (in the number of edges of C).

*Proof.* For a sum node S, we want to compute the following query

$$\max_{\mathbf{y}} S(\mathbf{y}|\mathbf{x}) = \frac{1}{S(\mathbf{x})} \max_{\mathbf{y}} S(\mathbf{y}, \mathbf{x}) = \frac{1}{S(\mathbf{x})} \max_{\mathbf{y}} \sum_{C \in Ch(S)} w_{S,C} C(\mathbf{y}, \mathbf{x}),$$

yet notice that for any assignment of  $\mathbf{x}$  and  $\mathbf{y}$  only one  $C \in Ch(S)$  must have a nonnegative value by the definition of determinism, so we may replace the summation with a maximization over the children, giving

$$\max_{\mathbf{y}} S(\mathbf{y}|\mathbf{x}) = \frac{1}{S(\mathbf{x})} \max_{\mathbf{y}} \max_{C \in Ch(S)} w_{S,C} C(\mathbf{y}, \mathbf{x}) = \frac{1}{S(\mathbf{x})} \max_{C \in Ch(S)} \max_{\mathbf{y}} w_{S,C} C(\mathbf{y}, \mathbf{x}).$$

For a product node P, we compute

$$\max_{\mathbf{y}} P(\mathbf{y}|\mathbf{x}) = \frac{1}{P(\mathbf{x})} \max_{\mathbf{y}} P(\mathbf{y}, \mathbf{x}) = \frac{1}{P(\mathbf{x})} \max_{\mathbf{y}} \prod_{C \in Ch(P)} C(\mathbf{y}, \mathbf{x}) = \frac{1}{P(\mathbf{x})} \prod_{C \in Ch(P)} \max_{\mathbf{y}} C(\mathbf{y}, \mathbf{x}).$$

This is equivalent to an inductive top-down pass where we maximize instead of sum until we reach all input nodes, in which case we simply maximize the supposedly tractable functions. Once these are computed, we unroll the induction, maximizing over all values.

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