



- Precision Positive Linear Series Pass Voltage Regulation
- 0.45 V Dropout at 3 A
- 50 mV Dropout at 10 mA
- Quiescent Current Under 650 μ A Irrespective of Load
- Adjustable (5-Lead) Output Voltage Version
- Fixed (3-Lead) Versions for 3.3-V and 5-V Outputs
- Logic Shutdown Capability
- Short-Circuit Power Limit of $(3\% \times V_{IN} \times I_{SHORT})$
- Low V_{OUT} to V_{IN} Reverse Leakage
- Thermal Shutdown

description

The UCC283-3/-5/-ADJ family of positive linear series pass voltage regulators are tailored for low-drop-out applications where low quiescent power is important. Fabricated with a BiCMOS technology ideally suited for low input-to-output differential applications, the UCC283-5 passes 3 A while requiring only 0.45 V of typical input voltage headroom (ensured 0.6-V dropout).

These regulators include reverse voltage sensing that prevents current in the reverse direction. Quiescent current is always less than 650 μ A. These devices have been internally compensated in such a way that the need for a minimum output capacitor has been eliminated.

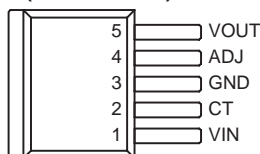
UCC283-3 and UCC283-5 versions are in 3-lead packages and have preset outputs at 3.3 V and 5.0 V respectively. The output voltage is regulated to 1.5% at room temperature. The UCC283-ADJ version, in a 5-lead package, regulates the output voltage programmed by an external resistor ratio.

Short-circuit current is internally limited. The device responds to a sustained overcurrent condition by turning off after a t_{ON} time delay. The device then stays off for a period, t_{OFF} , that is 32 times the t_{ON} delay. The device then begins pulsing on and off at the $t_{ON}/(t_{ON}+t_{OFF})$ duty cycle of 3%. This drastically reduces the power dissipation during short-circuit and means heat sinks need only accommodate normal operation. On the 3-leaded versions of the device t_{ON} is fixed at 750 μ s, on the adjustable 5-leaded versions an external capacitor sets the on time. The off time is always $32 \times t_{ON}$. The external timing control pin, CT, on the 5-leaded versions also serves as a shutdown input when pulled low.

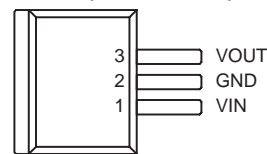
Internal power dissipation is further controlled with thermal overload protection circuitry. Thermal shutdown occurs if the junction temperature exceeds 165°C. The chip remains off until the temperature has dropped 20°C.

The UCC283 series is specified for operation over the industrial range of -40°C to 85°C, and the UCC383 series is specified from 0°C to 70°C. These devices are available in 3- and 5-pin TO-220 and TO-263 power packages.

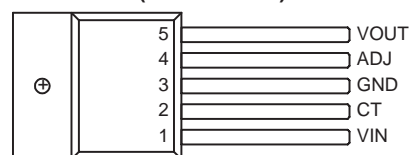
TO-263-5 TD PACKAGE
(FRONT VIEW)



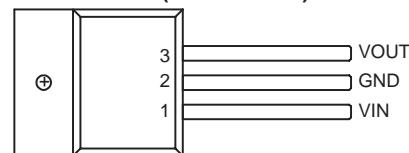
TO-263-3 TD PACKAGE
(FRONT VIEW)



TO-220-5 T PACKAGE
(FRONT VIEW)



TO-220-3 T PACKAGE
(FRONT VIEW)



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

All trademarks are the property of their respective owners.

PRODUCTION DATA information is current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.



UCC283-3, UCC283-5, UCC283-ADJ, UCC383-3, UCC383-5, UCC383-ADJ

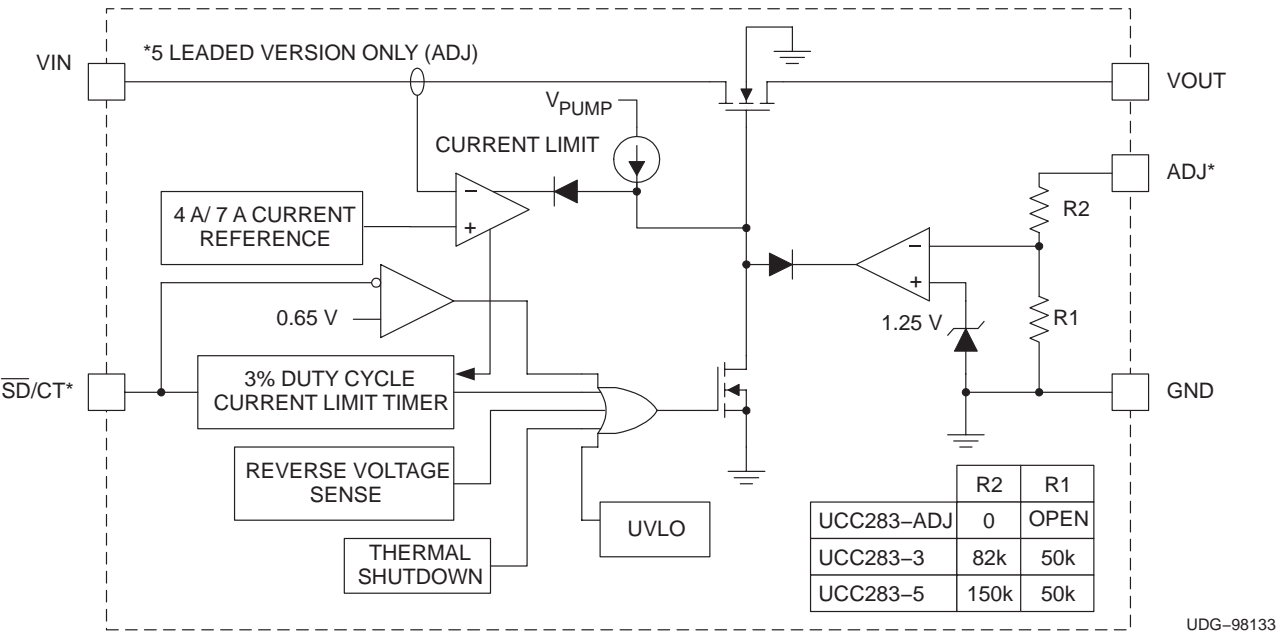
LOW-DROPOUT 3-A LINEAR REGULATOR FAMILY

SLUS215B – OCTOBER 1998 – REVISED FEBRUARY 2004

AVAILABLE OPTIONS ⁽¹⁾							
T _A	OUTPUT VOLTAGE (V)			PACKAGE DEVICES			
				TO-263-3 ⁽²⁾	TO-263-5 ⁽²⁾	TO-220-3	TO-220-5
	MIN	TYP	MAX	TD		T	
-40°C to 85°C	3.22	3.3	3.58	UCC283TD-3	—	UCC283T-3	—
	4.875	5.00	5.125	UCC283TD-5	—	UCC283T-5	—
	ADJ			—	UCC283TD-ADJ	—	UCC283T-ADJ
0°C to 70°C	3.22	3.3	3.58	UCC383TD-3	—	UCC383T-3	—
	4.875	5.00	5.125	UCC383TD-5	—	UCC383T-5	—
	ADJ			—	UCC383TD-ADJ	—	UCC383T-ADJ

- For more package and ordering information, see the Package Option Addendum located at the end of this data sheet.
- For 50 piece reel, add KTTT (e.g., UCC283TDKTTT-3); for 500 piece reel, add TR (e.g., UCC283TDTR-3).

functional block diagram



UCC283-3, UCC283-5, UCC283-ADJ, UCC383-3, UCC383-5, UCC383-ADJ LOW-DROPOUT 3-A LINEAR REGULATOR FAMILY

SLUS215B – OCTOBER 1998 – REVISED FEBRUARY 2004

electrical characteristics $T_A = 0^\circ\text{C}$ to 70°C for the UCC383-x series, $T_A = -40^\circ\text{C}$ to 85°C for the UCC283-x, $V_{VIN} = V_{VOUT} + 1.5\text{ V}$, $I_{OUT} = 10\text{ mA}$, $C_{IN} = 10\text{ }\mu\text{F}$, $C_{OUT} = 22\text{ }\mu\text{F}$. For the UCC283-ADJ, $V_{VIN} = 6.5\text{ V}$, $V_{OUT} = 5.0\text{ V}$, $C_T = 750\text{ pF}$, $T_J = T_A$ unless otherwise stated

PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNITS
UCC283-5 Fixed 5 V, 3 A Family					
Output voltage	$T_J = 25^\circ\text{C}$	4.925	5	5.075	V
	Over temperature	4.875		5.125	V
Line regulation	$V_{VIN} = 5.15\text{ V}$ to 9 V		2	10	mV
Load regulation	$I_{OUT} = 10\text{ mA}$ to 3 A		10	20	mV
Dropout voltage, $V_{DROPOUT} = V_{VIN} - V_{VOUT}$	$I_{OUT} = 3\text{ A}$, $V_{OUT} = 4.85\text{ V}$		0.4	0.6	V
	$I_{OUT} = 1.5\text{ A}$, $V_{OUT} = 4.85\text{ V}$		0.2	0.45	V
	$I_{OUT} = 10\text{ mA}$, $V_{OUT} = 4.85\text{ V}$		50	150	mV
Peak current limit	$V_{VOUT} = 0\text{ V}$	4	7	10	A
Overcurrent threshold		3	4	5.5	A
Current limit duty cycle	$V_{VOUT} = 0\text{ V}$		3%	5%	
Overcurrent time out, t_{ON}	$V_{VOUT} = 0\text{ V}$	400	750	1400	μs
Quiescent current	No load		400	650	μA
Reverse leakage current	$1\text{ V} < V_{VIN} < V_{VOUT}$, $V_{VOUT} \leq 5.1\text{ V}$, at V_{VOUT}		30	75	μA
Undervoltage lockout	V_{IN} where V_{OUT} passes current	2.5	2.8	3	V
UCC283-3 Fixed 3.3 V, 3 A Family					
Output voltage	$T_J = 25^\circ\text{C}$	3.25	3.3	3.35	V
	Over temperature	3.22		3.38	V
Line regulation voltage	$V_{VIN} = 3.45\text{ V}$ to 9 V		2	7	mV
Load regulation voltage	$I_{OUT} = 10\text{ mA}$ to 3 A		7	15	mV
Dropout voltage, $V_{DROPOUT} = V_{VIN} - V_{VOUT}$	$I_{OUT} = 3\text{ A}$, $V_{OUT} = 3.15\text{ V}$		0.5	1	V
	$I_{OUT} = 1.5\text{ A}$, $V_{OUT} = 3.15\text{ V}$		0.25	0.6	V
	$I_{OUT} = 10\text{ mA}$, $V_{OUT} = 3.15\text{ V}$		50	150	mV
Peak current limit	$V_{VOUT} = 0\text{ V}$	4	7	10	A
Overcurrent threshold		3	4	5.5	A
Current limit duty cycle	$V_{VOUT} = 0\text{ V}$		3%	5%	
Overcurrent time out, t_{ON}	$V_{VOUT} = 0\text{ V}$	400	750	1400	μs
Quiescent current	No load		400	650	μA
Reverse leakage current	$1\text{ V} < V_{VIN} < V_{VOUT}$, $V_{VOUT} \leq 3.35\text{ V}$ at V_{VOUT}		30	75	μA
Undervoltage lockout	V_{IN} where V_{OUT} passes current	2.5	2.8	3	V



UCC283-3, UCC283-5, UCC283-ADJ, UCC383-3, UCC383-5, UCC383-ADJ LOW-DROPOUT 3-A LINEAR REGULATOR FAMILY

SLUS215B – OCTOBER 1998 – REVISED FEBRUARY 2004

electrical characteristics $T_A = 0^\circ\text{C}$ to 70°C for the UCC383-x series, $T_A = -40^\circ\text{C}$ to 85°C for the UCC283-x, $V_{IN} = V_{OUT} + 1.5\text{ V}$, $I_{OUT} = 10\text{ mA}$, $C_{IN} = 10\text{ }\mu\text{F}$, $C_{OUT} = 22\text{ }\mu\text{F}$. For the UCC283-ADJ, $V_{IN} = 6.5\text{ V}$, $V_{OUT} = 5.0\text{ V}$, $C_T = 750\text{ pF}$, $T_J = T_A$ unless otherwise stated

PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNITS
UCC283-ADJ Adjustable Output, 3 A Family					
Regulating voltage at ADJ pin	$T_J = 25^\circ\text{C}$	1.23	1.25	1.27	V
	Over temperature	1.22		1.28	V
Line regulation voltage, at ADJ input	$V_{IN} = V_{OUT} + 150\text{ mV}$ to 9 V		1	3	mV
Load regulation voltage, at ADJ input	$I_{OUT} = 10\text{ mA}$ to 3 A		2	5	mV
Dropout voltage, $V_{DROPOUT} = V_{IN} - V_{OUT}$	$V_{OUT} = 4.85\text{ V}$, $I_{OUT} = 3\text{ A}$		0.4	0.6	V
	$V_{OUT} = 4.85\text{ V}$, $I_{OUT} = 1.5\text{ A}$		0.2	0.45	V
	$V_{OUT} = 4.85\text{ V}$, $I_{OUT} = 10\text{ mA}$		50	150	mV
Peak current limit	$V_{OUT} = 0\text{ V}$	4	7	10	A
Overcurrent threshold		3	4	5.5	A
Current limit duty cycle	$V_{OUT} = 0\text{ V}$		3	5	%
Overcurrent time out, t_{ON}	$V_{OUT} = 0\text{ V}$	300	575	1200	μs
Reverse leakage current	$1\text{ V} < V_{IN} < V_{OUT}$ $V_{OUT} \leq 9\text{ V}$, at V_{OUT}		30	100	μA
Bias current at ADJ input			20	250	nA
Quiescent current	No load		400	650	μA
Shutdown threshold	At CT input	0.25	0.65		V
Quiescent current in shutdown	$V_{IN} = 9\text{ V}$		40	75	μA
UVLO	V_{IN} where V_{OUT} passes current	2.5	2.8	3	V

absolute maximum ratings over operating free-air temperature (unless otherwise noted)[†]

Input voltage

V_{IN} 9 V

CT -0.3 V to 3 V

ADJ -0.3 V to 9 V

Storage Temperature, T_{stg} -65°C to 150°C

Junction Temperature, T_J -55°C to 150°C

Lead Temperature (soldering, 10 seconds) 300°C

[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.



pin descriptions

ADJ: Adjust pin for the UCC283-ADJ version only. Feedback pin for the linear regulator. Program the output voltage with R1 connected from ADJ to GND and R2 connected from VOUT to ADJ. Output voltage is given by:

$$V_{OUT} = \frac{1.25 \text{ V} \times (R1 + R2)}{R1}$$

CT: Short-circuit timing capacitor and shutdown input for the UCC283-ADJ version. Pulling CT below 0.25 V turns off the regulator and places it in a low quiescent-current mode. A timing capacitor, C, from CT to GND programs the duration of the pulsed short-circuit on-time. On-time, t_{ON} , is approximately given by:

$$t_{ON} = 750 \text{ k} \times C$$

GND: Reference ground.

VIN: Input voltage. This pin must be bypassed with a low ESL/ESR 1- μ F or larger capacitor to GND. VIN can range from ($V_{OUT} + V_{DROPOUT}$) to 9 V. If VIN is reduced to zero while VOUT is held high, the reverse leakage from VOUT to VIN is less than 75 μ A.

VOUT: Regulated output voltage. A bypass capacitor is not required at VOUT, but may be desired for good transient response. The bypass capacitor must not exceed a maximum value in order to insure the regulator can start.

APPLICATION INFORMATION

overview

The UCC383 family of low dropout linear (LDO) regulators provide a regulated output voltage for applications with up to 3 A of load current. The regulators feature a low dropout voltage and short-circuit protection, making their use ideal for demanding high-current applications requiring fault protection.

short-circuit-protection

The UCC383 provides unique short-circuit protection circuitry that reduces power dissipation during a fault. When an overload situation is detected, the device enters a pulsed mode of operation at 3% duty cycle reducing the heat sink requirements during a fault. The UCC383 has two current thresholds that determine its behavior during a fault as shown in Figure 1. When the regulator current exceeds the **overcurrent threshold** for a period longer than t_{ON} , the UCC383 shuts off for a period (t_{OFF}) which is $32 \times t_{ON}$. During an overload, the regulator actively limits the maximum current to the **peak current limit** value. The peak current limit is nominally 3 A greater than the overcurrent threshold. The regulator continues in pulsed mode until the fault is cleared as illustrated in Figure 1.

UCC283-3, UCC283-5, UCC283-ADJ, UCC383-3, UCC383-5, UCC383-ADJ LOW-DROPOUT 3-A LINEAR REGULATOR FAMILY

SLUS215B – OCTOBER 1998 – REVISED FEBRUARY 2004

APPLICATION INFORMATION

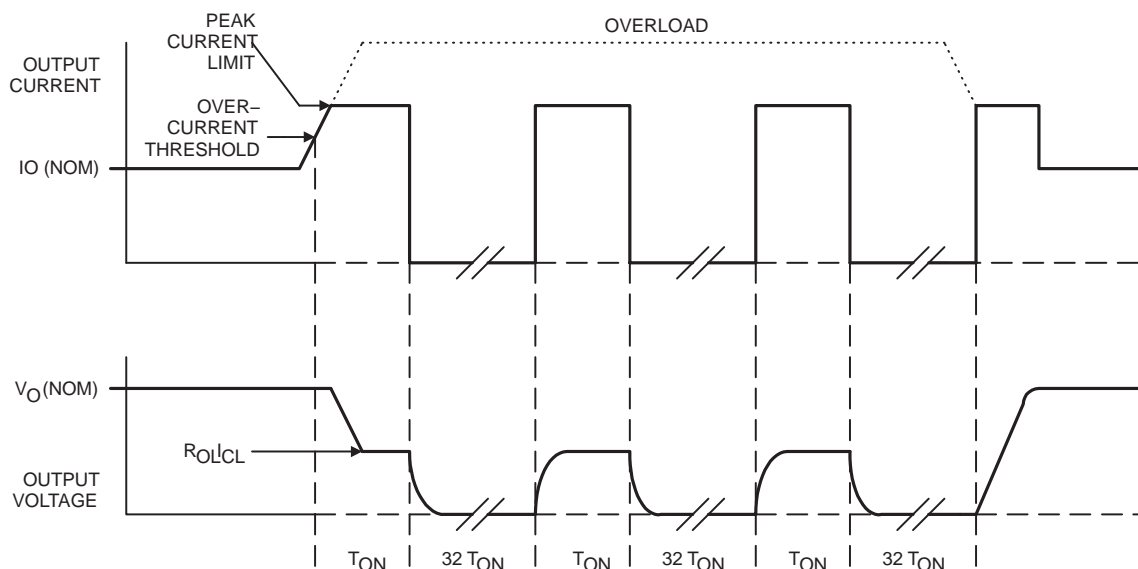


Figure 1. UCC383 Short-Circuit Timing

A capacitive load on the regulator's output appears as a short-circuit during start-up. If the capacitance is too large, the output voltage does not come into regulation during the initial t_{ON} period and the UCC383 enters pulsed mode operation. The peak current limit, t_{ON} period, and load characteristics determine the maximum value of output capacitor that can be charged. For a constant current load the maximum output capacitance is given as follows:

$$C_{OUT(max)} = (I_{CL} - I_{LOAD}) \times \frac{t_{ON}}{V_{OUT}} \text{ Farads} \quad (1)$$

For worst case calculations, the minimum values of on time (t_{ON}) and peak current limit (I_{CL}) should be used. The adjustable version allows the t_{ON} time to be adjusted with a capacitor on the CT pin:

$$t_{ON(adj)} = 750,000 \times C (\mu \text{ Farad}) \text{ microseconds} \quad (2)$$

For a resistive load (R_{LOAD}) the maximum output capacitor can be estimated from:

$$C_{OUT(max)} = \frac{t_{ON(sec)}}{R_{LOAD} \times \ln \left[\frac{1}{1 - \frac{V_{OUT}}{I_{CL} \times R_{LOAD}}} \right]} \text{ Farads} \quad (3)$$

APPLICATION INFORMATION

dropout performance

Referring to the *Block Diagram*, the dropout voltage of the UCC383 is equal to the minimum voltage drop (V_{IN} to V_{OUT}) across the N-channel MOSFET. The dropout voltage is dependent on operating conditions such as load current, input and load voltages, as well as temperature. The UCC383 achieves a low $R_{ds(on)}$ through the use of an internal charge-pump (V_{PUMP}) that drives the MOSFET gate. Figure 2 depicts typical dropout voltages versus load current for the 3.3-V and 5-V versions of the part, as well as the adjustable version programmed to 3.0 V.

Figure 3 depicts the typical dropout performance of the adjustable version with various output voltages and load currents.

Operating temperatures also affect the $R_{ds(on)}$ and dropout voltage of the UCC383. Figure 4 graphs the typical dropout for the 3.3-V and 5-V versions with a 3-A load over temperature.

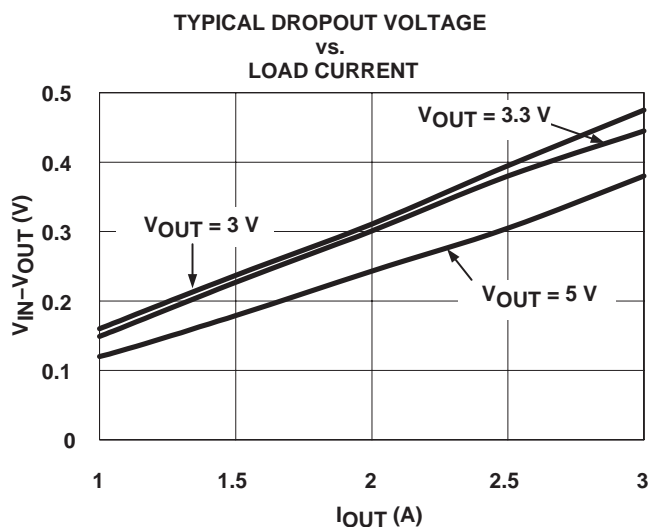


Figure 2

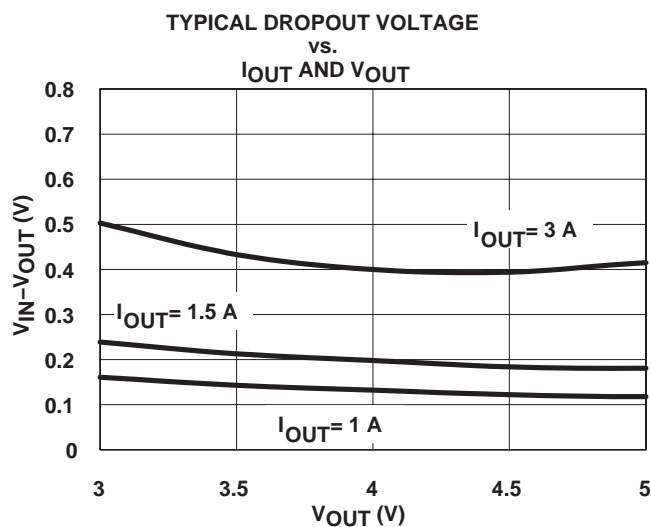


Figure 3

UCC283-3, UCC283-5, UCC283-ADJ, UCC383-3, UCC383-5, UCC383-ADJ LOW-DROPOUT 3-A LINEAR REGULATOR FAMILY

SLUS215B – OCTOBER 1998 – REVISED FEBRUARY 2004

APPLICATION INFORMATION

voltage programming and shutdown feature for adjustable version

A typical application circuit based on the UCC383 adjustable version is shown in Figure 5. The output voltage is externally programmed through a resistive divider at the ADJ pin.

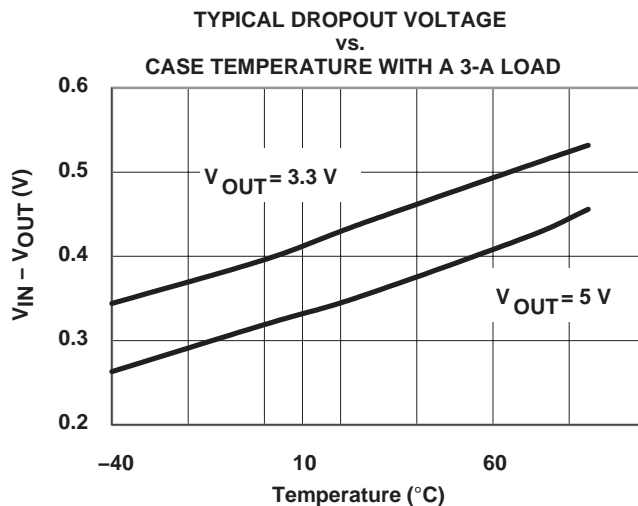


Figure 4

$$V_{OUT} = 1.25 \times \left(1 + \frac{R_2}{R_1}\right) \text{ Volts} \quad (4)$$

The maximum programmed output voltage is constrained by the 9-V absolute rating of the IC (this includes the charge pump voltage) and its ability to enhance the N-channel MOSFET. Unless the load current is below the 3-A rating of the device, output voltages above 7 V are not recommended. The minimum output voltage can be programmed down to 1.25 V. However, the input voltage must always be greater than the UVLO of the part.

The adjustable version includes a shutdown feature, limiting quiescent current to 40 μ A typical. The UCC383 is shut down by pulling the CT pin to below 0.25 V. As shown in Figure 5, a small logic level MOSFET or BJT transistor in parallel with the timing capacitor can be driven with a digital signal, putting the device in shutdown. If the CT pin is not pulled low, the IC internally pulls up the pin enabling the regulator. The CT pin should not be forced high, as this interferes with the short-circuit-protection feature. Selection of the timing capacitor is explained in *Short-Circuit-Protection*.

The adjustable version can be used in applications requiring remote voltage sensing (i.e. monitoring a voltage other than or not directly tied to the VOUT pin). This is possible since the inverting input of the error-voltage amplifier (see *Block Diagram*) is brought out to the ADJ pin.

APPLICATION INFORMATION

thermal design

The *Package Information* section of the *Power Supply Control Products Data Book, Volume 3* (Literature No. SLUD003) contains reference material for the thermal ratings of various packages. The section also includes an excellent article *Thermal Characteristics of Surface Mount Packages*, that is the basis of the following discussion.

Thermal design for the UCC383 family of linear regulators includes two modes of operation, normal and pulsed mode. In normal operation, the linear regulator and heat sink must dissipate power equal to the maximum forward voltage drop multiplied by the maximum load current. Assuming a constant current load, the expected heat rise at the regulator's junction can be calculated as follows:

$$t_{RISE}^{(\theta)} = P_{DISS} \times (\theta_{jc} + \theta_{ca}) \quad ^\circ\text{C} \quad (5)$$

Where theta, (θ) is thermal resistance and P_{DISS} is the power dissipated. The thermal resistance of both the TO-220 and TO-263 packages (junction to case) is 3°C per Watt. In order to prevent the regulator from going into thermal shutdown, the case to ambient theta must keep the junction temperature below 150°C. If the LDO is mounted on a 5-square inch pad of 1-ounce copper, for example, the thermal resistance from junction to ambient becomes 60°C per Watt. If a lower thermal resistance is required by the application, the device heat sinking would need to be improved.

When the UCC383 regulator is in pulsed mode due to an overload or short-circuit in the application, the maximum average power dissipation is calculated as follows:

$$P_{PULSE(avg)} = (V_{IN} - V_{OUT}) \times I_{CL} \times \frac{t_{ON}}{33 \times t_{ON}} \text{ Watts} \quad (6)$$

As seen in Equation 6, the average power during a fault is reduced dramatically by the duty cycle, allowing the heat sink to be sized for normal operation. Although the peak power in the regulator during the t_{ON} period can be significant, the thermal mass of the package generally keeps the junction temperature from rising unless the t_{ON} period is increased to tens of milliseconds.

ripple rejection

Even though the UCC383 family of linear regulators are not optimized for fast transient applications (Refer to the UC182 Fast LDO Linear Regulator), they do offer significant power supply rejection at lower frequencies. Figure 6 depicts ripple rejection performance in a typical application. The performance can be improved with additional filtering.

UCC283-3, UCC283-5, UCC283-ADJ, UCC383-3, UCC383-5, UCC383-ADJ
LOW-DROPOUT 3-A LINEAR REGULATOR FAMILY

SLUS215B – OCTOBER 1998 – REVISED FEBRUARY 2004

APPLICATION INFORMATION

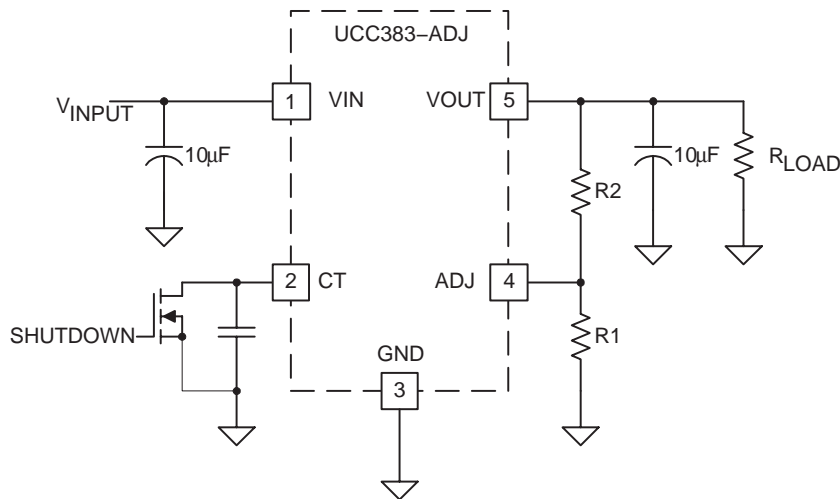


Figure 5. Typical Application for 5-Pin Adjustable Version

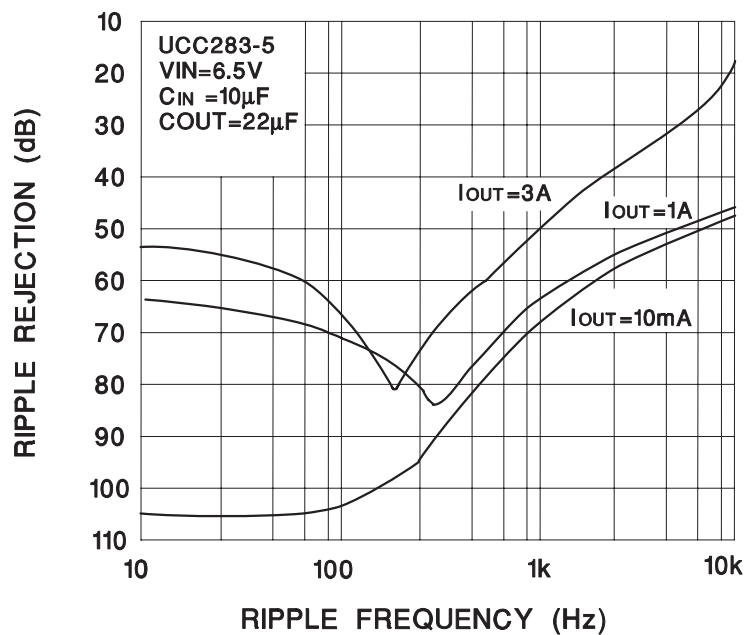


Figure 6. Ripple Rejection vs. Frequency

PACKAGING INFORMATION

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead/Ball Finish (6)	MSL Peak Temp (3)	Op Temp (°C)	Device Marking (4/5)	Samples
UCC283T-3	LIFEBUY	TO-220	KC	3	50	Green (RoHS & no Sb/Br)	CU SN	N / A for Pkg Type	-40 to 85	UCC283T-3	
UCC283T-3G3	LIFEBUY	TO-220	KC	3	50	Green (RoHS & no Sb/Br)	CU SN	N / A for Pkg Type	-40 to 85	UCC283T-3	
UCC283T-5	LIFEBUY	TO-220	KC	3	50	Green (RoHS & no Sb/Br)	CU SN	N / A for Pkg Type	-40 to 85	UCC283T-5	
UCC283T-5G3	LIFEBUY	TO-220	KC	3	50	Green (RoHS & no Sb/Br)	CU SN	N / A for Pkg Type	-40 to 85	UCC283T-5	
UCC283T-ADJ	ACTIVE	TO-220	KC	5	50	Green (RoHS & no Sb/Br)	CU SN	N / A for Pkg Type	-40 to 85	UCC283T-ADJ	Samples
UCC283TDKTTT-3	LIFEBUY	DDPAK/ TO-263	KTT	3	50	Green (RoHS & no Sb/Br)	CU SN	Level-2-260C-1 YEAR	-40 to 85	UCC283TD-3	
UCC283TDKTTT-3G3	LIFEBUY	DDPAK/ TO-263	KTT	3	50	Green (RoHS & no Sb/Br)	CU SN	Level-2-260C-1 YEAR	-40 to 85	UCC283TD-3	
UCC283TDKTTT-5	ACTIVE	DDPAK/ TO-263	KTT	3	50	Green (RoHS & no Sb/Br)	CU SN	Level-2-260C-1 YEAR	-40 to 85	UCC283TD-5	Samples
UCC283TDKTTT-ADJ	LIFEBUY	DDPAK/ TO-263	KTT	5	50	Green (RoHS & no Sb/Br)	CU SN	Level-2-260C-1 YEAR	-40 to 85	UCC283TD-ADJ	
UCC283TDKTTT-ADJG3	LIFEBUY	DDPAK/ TO-263	KTT	5	50	Green (RoHS & no Sb/Br)	CU SN	Level-2-260C-1 YEAR	-40 to 85	UCC283TD-ADJ	
UCC283TDTR-3	ACTIVE	DDPAK/ TO-263	KTT	3	500	Green (RoHS & no Sb/Br)	CU SN	Level-2-260C-1 YEAR	-40 to 85	UCC283TD-3	Samples
UCC283TDTR-ADJ	ACTIVE	DDPAK/ TO-263	KTT	5	500	Green (RoHS & no Sb/Br)	CU SN	Level-2-260C-1 YEAR	-40 to 85	UCC283TD-ADJ	Samples
UCC383T-3	LIFEBUY	TO-220	KC	3	50	Green (RoHS & no Sb/Br)	CU SN	N / A for Pkg Type	0 to 70	UCC383T-3	
UCC383T-3G3	LIFEBUY	TO-220	KC	3	50	Green (RoHS & no Sb/Br)	CU SN	N / A for Pkg Type	0 to 70	UCC383T-3	
UCC383T-5	LIFEBUY	TO-220	KC	3	50	Green (RoHS & no Sb/Br)	CU SN	N / A for Pkg Type	0 to 70	UCC383T-5	
UCC383T-ADJ	ACTIVE	TO-220	KC	5	50	Green (RoHS & no Sb/Br)	CU SN	N / A for Pkg Type	0 to 70	UCC383T-ADJ	Samples
UCC383TDKTTT-3	LIFEBUY	DDPAK/ TO-263	KTT	3	50	Green (RoHS & no Sb/Br)	CU SN	Level-2-260C-1 YEAR	0 to 70	UCC383TD-3	

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead/Ball Finish (6)	MSL Peak Temp (3)	Op Temp (°C)	Device Marking (4/5)	Samples
UCC383TDKTTT-3G3	LIFEBUY	DDPAK/ TO-263	KTT	3	50	Green (RoHS & no Sb/Br)	CU SN	Level-2-260C-1 YEAR	0 to 70	UCC383TD-3	
UCC383TDKTTT-5	ACTIVE	DDPAK/ TO-263	KTT	3	50	Green (RoHS & no Sb/Br)	CU SN	Level-2-260C-1 YEAR	0 to 70	UCC383TD-5	Samples
UCC383TDKTTT-ADJ	LIFEBUY	DDPAK/ TO-263	KTT	5	50	Green (RoHS & no Sb/Br)	CU SN	Level-2-260C-1 YEAR	0 to 70	UCC383TD-ADJ	
UCC383TDTR-3	ACTIVE	DDPAK/ TO-263	KTT	3	500	Green (RoHS & no Sb/Br)	CU SN	Level-2-260C-1 YEAR	0 to 70	UCC383TD-3	Samples
UCC383TDTR-ADJ	ACTIVE	DDPAK/ TO-263	KTT	5	500	Green (RoHS & no Sb/Br)	CU SN	Level-2-260C-1 YEAR	0 to 70	UCC383TD-ADJ	Samples
UCC383TDTR-ADJG3	ACTIVE	DDPAK/ TO-263	KTT	5	500	Green (RoHS & no Sb/Br)	CU SN	Level-2-260C-1 YEAR	0 to 70	UCC383TD-ADJ	Samples

(1) The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) **RoHS:** TI defines "RoHS" to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substance do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, "RoHS" products are suitable for use in specified lead-free processes. TI may reference these types of products as "Pb-Free".

RoHS Exempt: TI defines "RoHS Exempt" to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.

Green: TI defines "Green" to mean the content of Chlorine (Cl) and Bromine (Br) based flame retardants meet JS709B low halogen requirements of <=1000ppm threshold. Antimony trioxide based flame retardants must also meet the <=1000ppm threshold requirement.

(3) MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

(5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

(6) Lead/Ball Finish - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.

Important Information and Disclaimer: The information provided on this page represents TI's knowledge and belief as of the date that it is provided. TI bases its knowledge and belief on information provided by third parties, and makes no representation or warranty as to the accuracy of such information. Efforts are underway to better integrate information from third parties. TI has taken and continues to take reasonable steps to provide representative and accurate information but may not have conducted destructive testing or chemical analysis on incoming materials and chemicals. TI and TI suppliers consider certain information to be proprietary, and thus CAS numbers and other limited information may not be available for release.

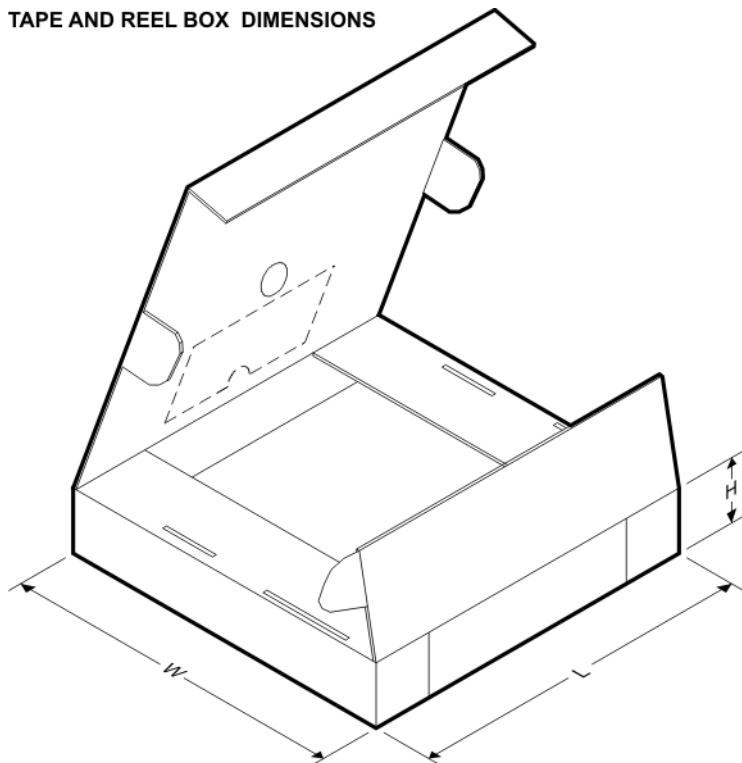
In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.

TAPE AND REEL INFORMATION


*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
UCC283TDKTTT-3	DDPAK/TO-263	KTT	3	50	330.0	24.4	10.6	15.6	4.9	16.0	24.0	Q2
UCC283TDKTTT-5	DDPAK/TO-263	KTT	3	50	330.0	24.4	10.6	15.6	4.9	16.0	24.0	Q2
UCC283TDKTTT-ADJ	DDPAK/TO-263	KTT	5	50	330.0	24.4	10.6	15.6	4.9	16.0	24.0	Q2
UCC283TDTR-3	DDPAK/TO-263	KTT	3	500	330.0	24.4	10.6	15.6	4.9	16.0	24.0	Q2
UCC283TDTR-ADJ	DDPAK/TO-263	KTT	5	500	330.0	24.4	10.6	15.6	4.9	16.0	24.0	Q2
UCC383TDKTTT-3	DDPAK/TO-263	KTT	3	50	330.0	24.4	10.6	15.6	4.9	16.0	24.0	Q2
UCC383TDKTTT-5	DDPAK/TO-263	KTT	3	50	330.0	24.4	10.6	15.6	4.9	16.0	24.0	Q2
UCC383TDKTTT-ADJ	DDPAK/TO-263	KTT	5	50	330.0	24.4	10.6	15.6	4.9	16.0	24.0	Q2
UCC383TDTR-3	DDPAK/TO-263	KTT	3	500	330.0	24.4	10.6	15.6	4.9	16.0	24.0	Q2
UCC383TDTR-ADJ	DDPAK/TO-263	KTT	5	500	330.0	24.4	10.6	15.6	4.9	16.0	24.0	Q2

TAPE AND REEL BOX DIMENSIONS

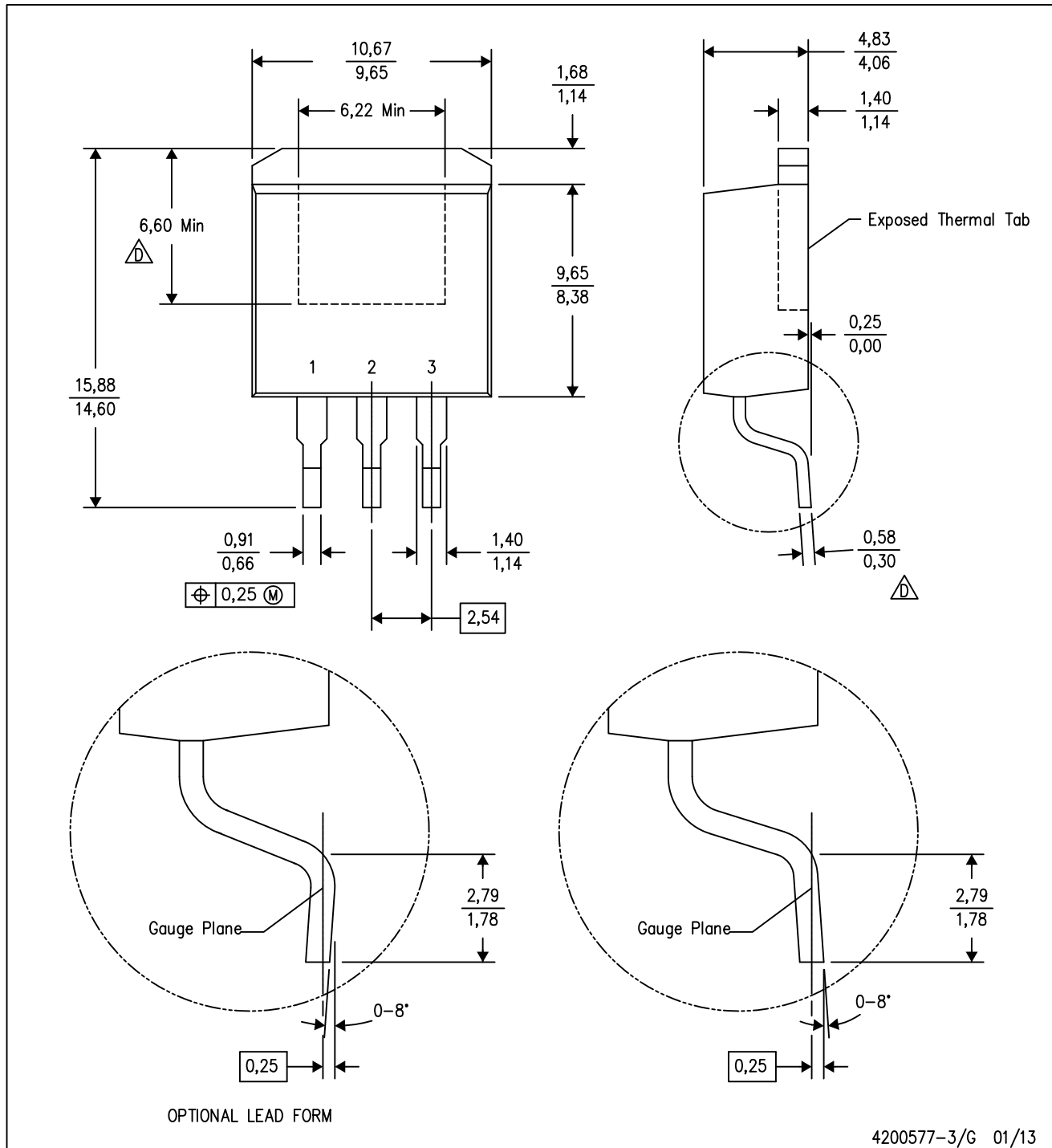


*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
UCC283TDKTTT-3	DDPAK/TO-263	KTT	3	50	367.0	367.0	45.0
UCC283TDKTTT-5	DDPAK/TO-263	KTT	3	50	367.0	367.0	45.0
UCC283TDKTTT-ADJ	DDPAK/TO-263	KTT	5	50	367.0	367.0	45.0
UCC283TDTR-3	DDPAK/TO-263	KTT	3	500	367.0	367.0	45.0
UCC283TDTR-ADJ	DDPAK/TO-263	KTT	5	500	367.0	367.0	45.0
UCC383TDKTTT-3	DDPAK/TO-263	KTT	3	50	367.0	367.0	45.0
UCC383TDKTTT-5	DDPAK/TO-263	KTT	3	50	367.0	367.0	45.0
UCC383TDKTTT-ADJ	DDPAK/TO-263	KTT	5	50	367.0	367.0	45.0
UCC383TDTR-3	DDPAK/TO-263	KTT	3	500	367.0	367.0	45.0
UCC383TDTR-ADJ	DDPAK/TO-263	KTT	5	500	367.0	367.0	45.0

KTT (R-PSFM-G3)

PLASTIC FLANGE-MOUNT PACKAGE

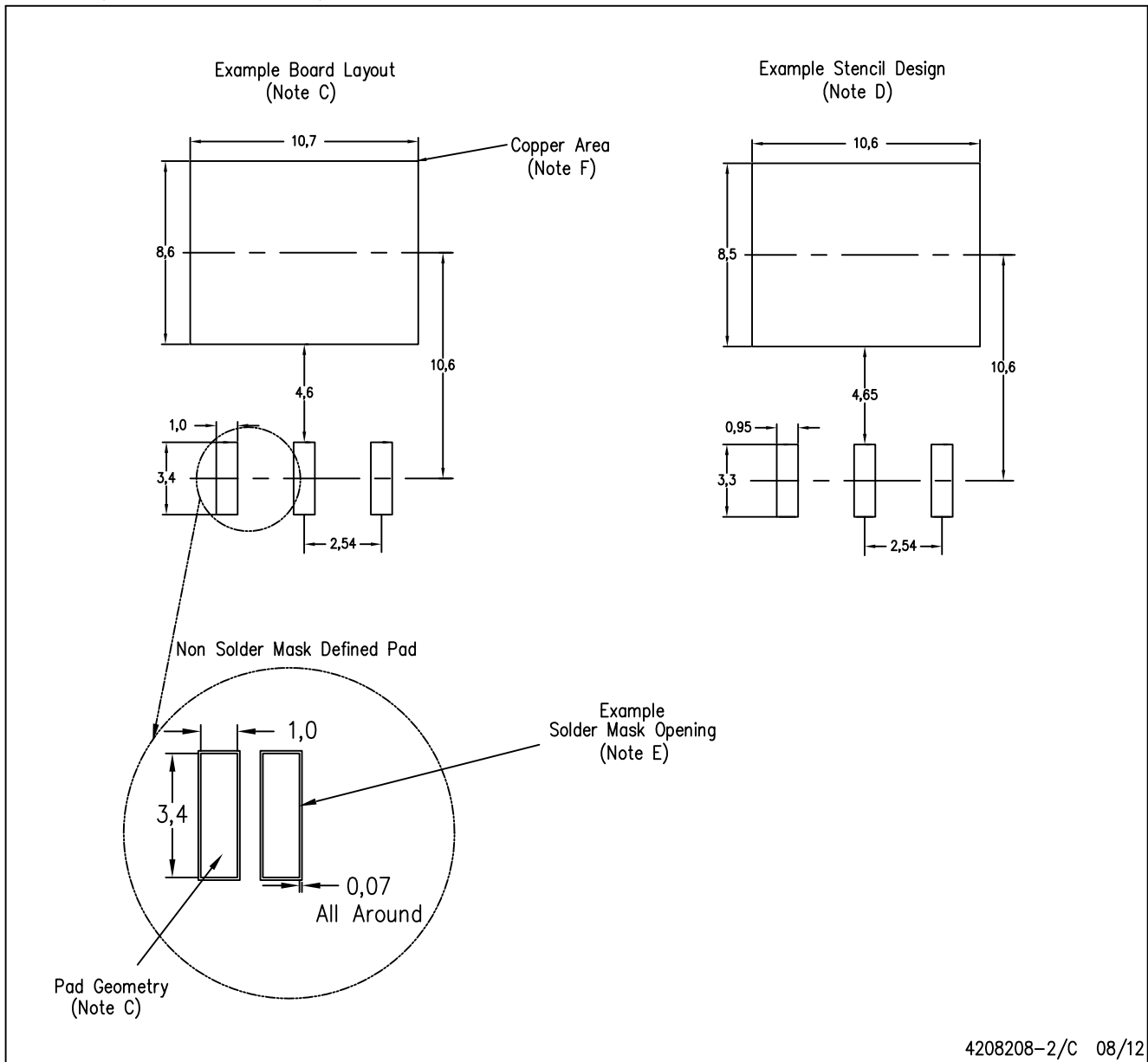


4200577-3/G 01/13

- NOTES:
- A. All linear dimensions are in millimeters.
 - B. This drawing is subject to change without notice.
 - C. Body dimensions do not include mold flash or protrusion. Mold flash or protrusion not to exceed 0.005 (0,13) per side.
- Falls within JEDEC TO-263 variation AA, except minimum lead thickness and minimum exposed pad length.

KTT (R-PSFM-G3)

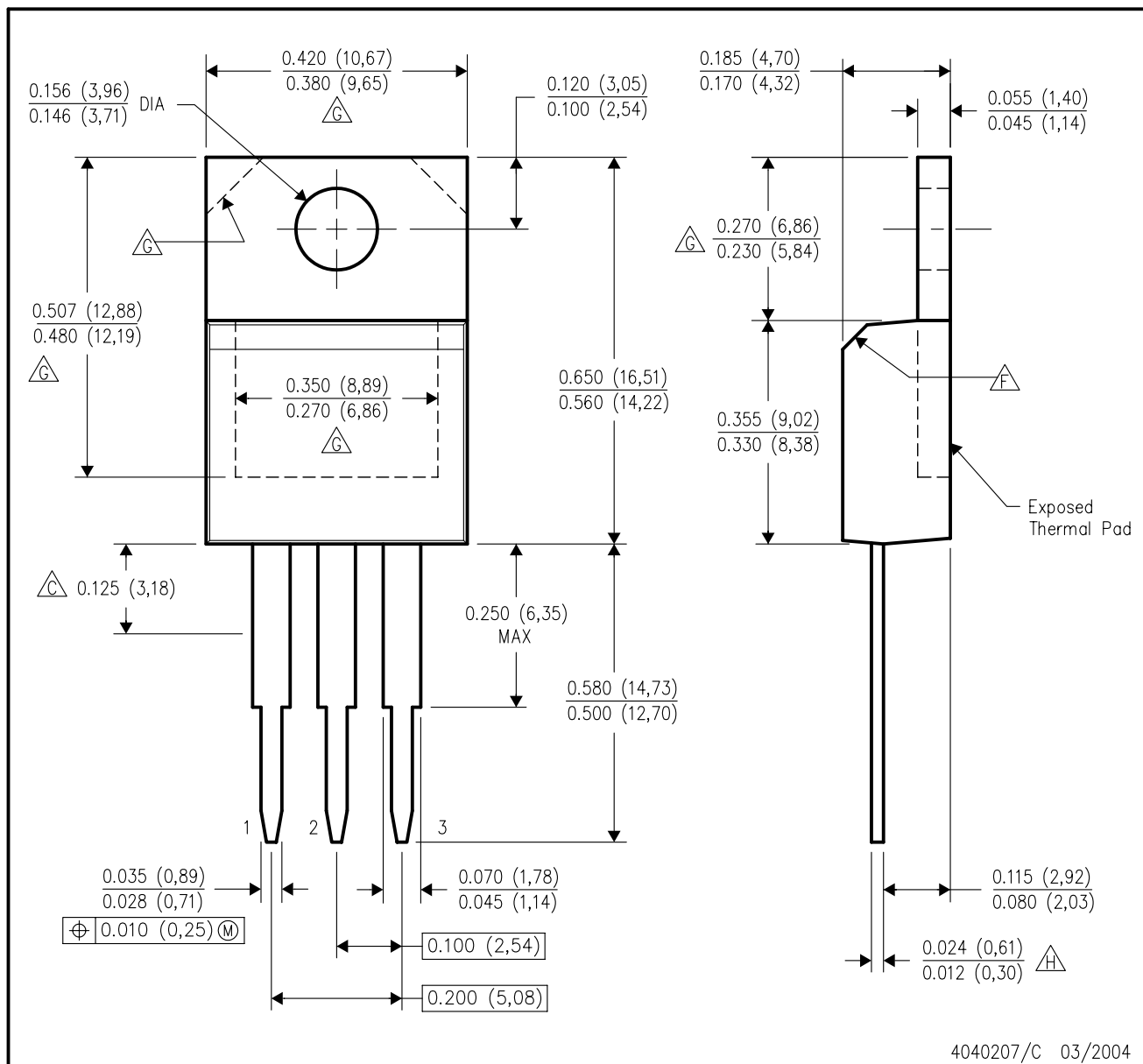
PLASTIC FLANGE-MOUNT PACKAGE



- NOTES:
- All linear dimensions are in millimeters.
 - This drawing is subject to change without notice.
 - Publication IPC-SM-782 is recommended for alternate designs.
 - Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release. Customers should contact their board assembly site for stencil design recommendations. Refer to IPC-7525.
 - Customers should contact their board fabrication site for solder mask tolerances between and around signal pads.
 - This package is designed to be soldered to a thermal pad on the board. Refer to the Product Datasheet for specific thermal information, via requirements, and recommended thermal pad size. For thermal pad sizes larger than shown a solder mask defined pad is recommended in order to maintain the solderable pad geometry while increasing copper area.

KC (R-PSFM-T3)

PLASTIC FLANGE-MOUNT PACKAGE



NOTES:

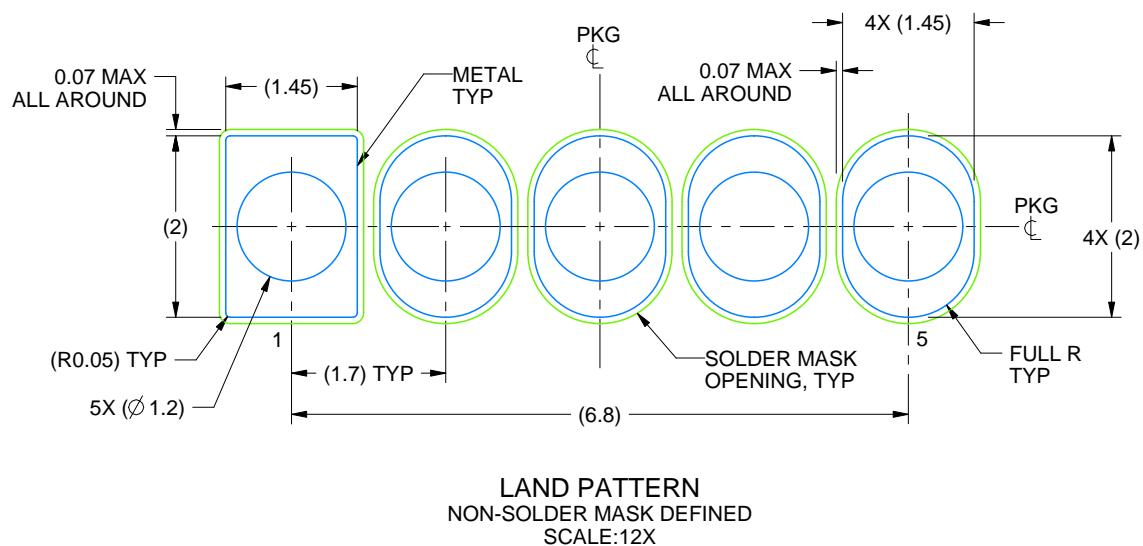
- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- C. Lead dimensions are not controlled within this area.
- D. All lead dimensions apply before solder dip.
- E. The center lead is in electrical contact with the mounting tab.
- F. The chamfer is optional.
- G. Thermal pad contour optional within these dimensions.
- H. Falls within JEDEC TO-220 variation AB, except minimum lead thickness.

EXAMPLE BOARD LAYOUT

KC0005A

TO-220 - 16.51 mm max height

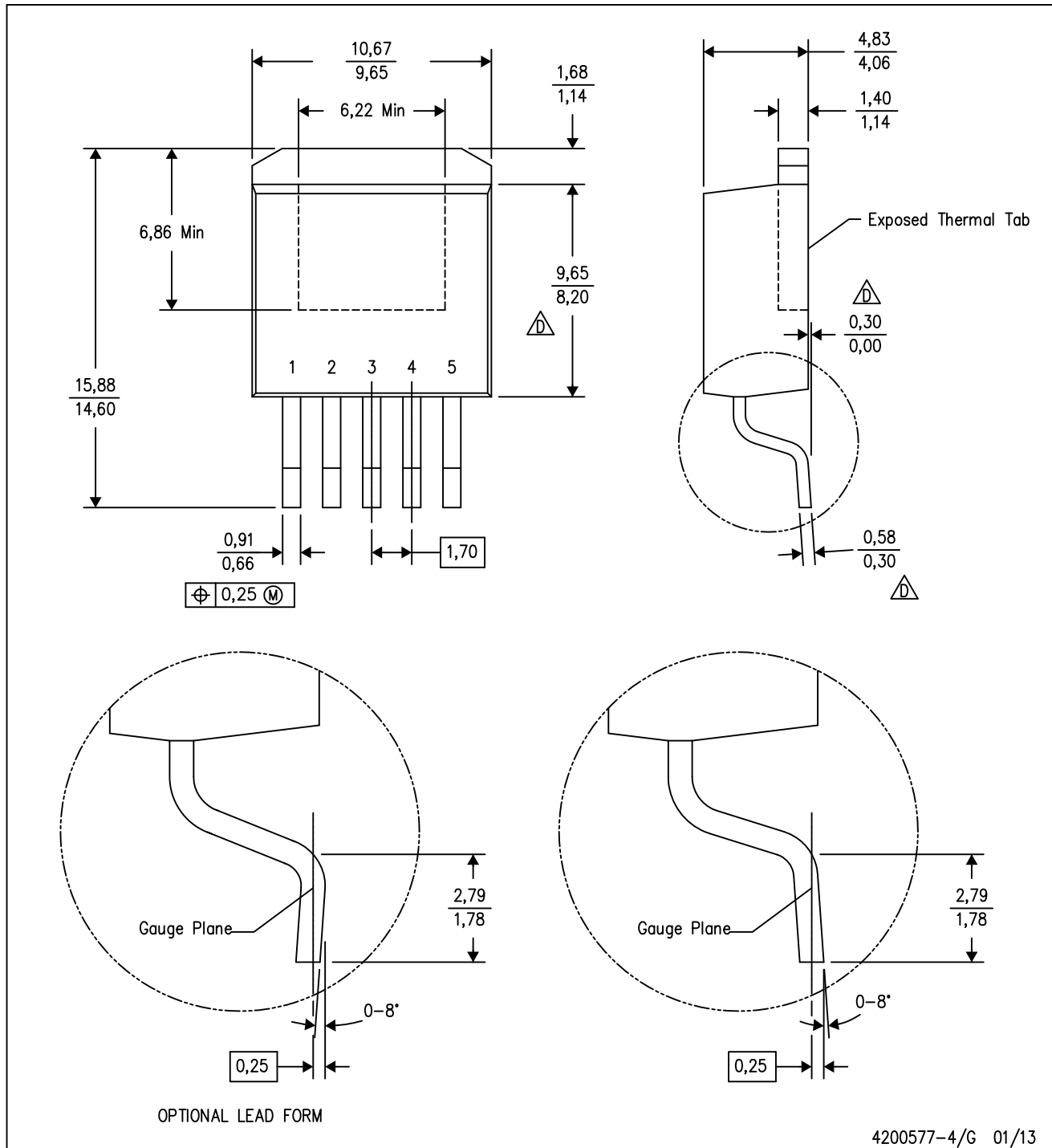
TO-220



4215009/A 01/2017

KTT (R-PSFM-G5)

PLASTIC FLANGE-MOUNT PACKAGE

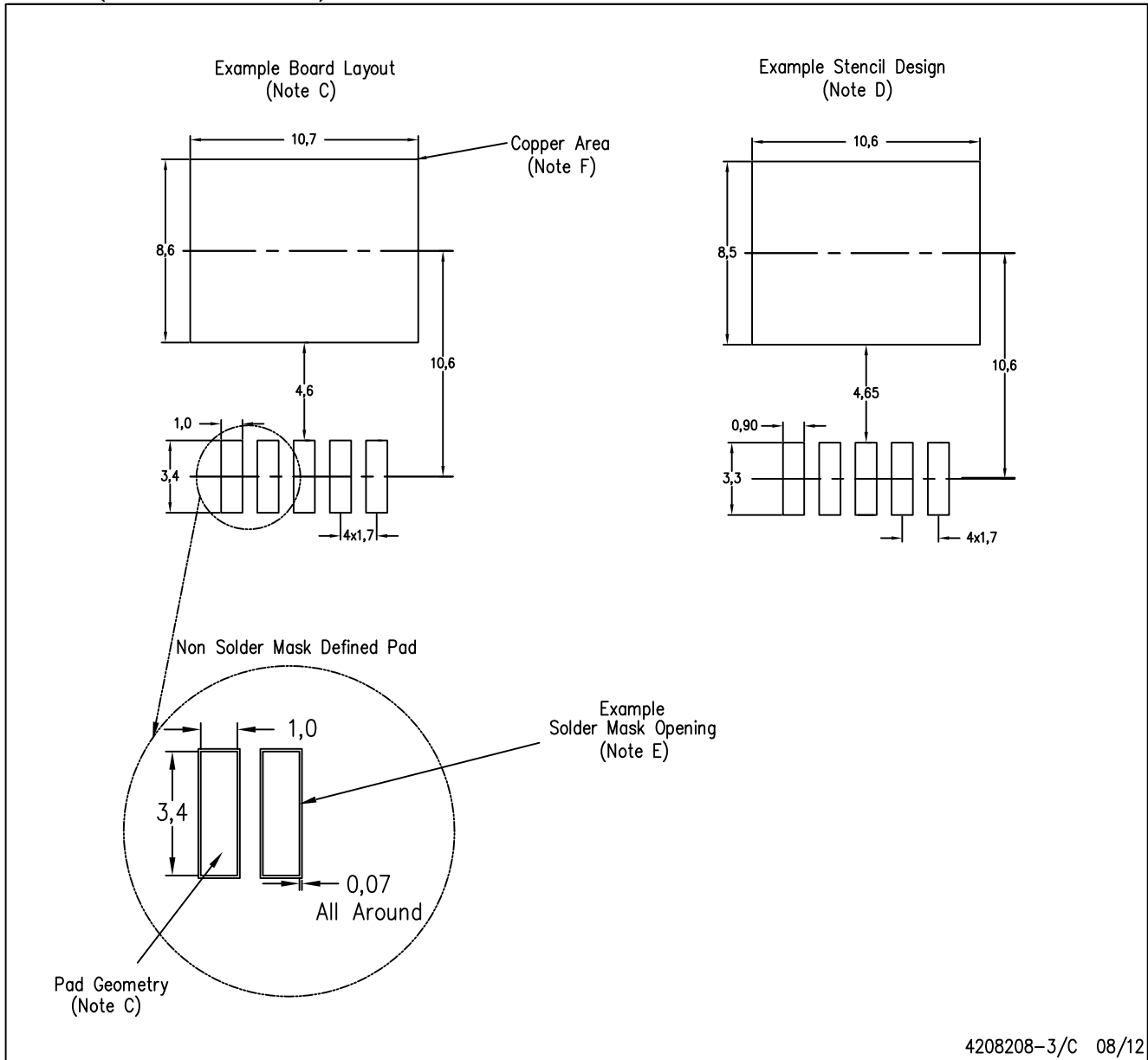


4200577-4/G 01/13

- NOTES:
- A. All linear dimensions are in millimeters.
 - B. This drawing is subject to change without notice.
 - C. Body dimensions do not include mold flash or protrusion. Mold flash or protrusion not to exceed 0.005 (0,13) per side.
- Falls within JEDEC TO-263 variation BA, except minimum lead thickness, maximum seating height, and minimum body length.

KTT (R-PSFM-G5)

PLASTIC FLANGE-MOUNT PACKAGE



- NOTES:
- A. All linear dimensions are in millimeters.
 - B. This drawing is subject to change without notice.
 - C. Publication IPC-SM-782 is recommended for alternate designs.
 - D. Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release. Customers should contact their board assembly site for stencil design recommendations. Refer to IPC-7525.
 - E. Customers should contact their board fabrication site for solder mask tolerances between and around signal pads.
 - F. This package is designed to be soldered to a thermal pad on the board. Refer to the Product Datasheet for specific thermal information, via requirements, and recommended thermal pad size. For thermal pad sizes larger than shown a solder mask defined pad is recommended in order to maintain the solderable pad geometry while increasing copper area.

IMPORTANT NOTICE

Texas Instruments Incorporated (TI) reserves the right to make corrections, enhancements, improvements and other changes to its semiconductor products and services per JESD46, latest issue, and to discontinue any product or service per JESD48, latest issue. Buyers should obtain the latest relevant information before placing orders and should verify that such information is current and complete.

TI's published terms of sale for semiconductor products (<http://www.ti.com/sc/docs/stdterms.htm>) apply to the sale of packaged integrated circuit products that TI has qualified and released to market. Additional terms may apply to the use or sale of other types of TI products and services.

Reproduction of significant portions of TI information in TI data sheets is permissible only if reproduction is without alteration and is accompanied by all associated warranties, conditions, limitations, and notices. TI is not responsible or liable for such reproduced documentation. Information of third parties may be subject to additional restrictions. Resale of TI products or services with statements different from or beyond the parameters stated by TI for that product or service voids all express and any implied warranties for the associated TI product or service and is an unfair and deceptive business practice. TI is not responsible or liable for any such statements.

Buyers and others who are developing systems that incorporate TI products (collectively, "Designers") understand and agree that Designers remain responsible for using their independent analysis, evaluation and judgment in designing their applications and that Designers have full and exclusive responsibility to assure the safety of Designers' applications and compliance of their applications (and of all TI products used in or for Designers' applications) with all applicable regulations, laws and other applicable requirements. Designer represents that, with respect to their applications, Designer has all the necessary expertise to create and implement safeguards that (1) anticipate dangerous consequences of failures, (2) monitor failures and their consequences, and (3) lessen the likelihood of failures that might cause harm and take appropriate actions. Designer agrees that prior to using or distributing any applications that include TI products, Designer will thoroughly test such applications and the functionality of such TI products as used in such applications.

TI's provision of technical, application or other design advice, quality characterization, reliability data or other services or information, including, but not limited to, reference designs and materials relating to evaluation modules, (collectively, "TI Resources") are intended to assist designers who are developing applications that incorporate TI products; by downloading, accessing or using TI Resources in any way, Designer (individually or, if Designer is acting on behalf of a company, Designer's company) agrees to use any particular TI Resource solely for this purpose and subject to the terms of this Notice.

TI's provision of TI Resources does not expand or otherwise alter TI's applicable published warranties or warranty disclaimers for TI products, and no additional obligations or liabilities arise from TI providing such TI Resources. TI reserves the right to make corrections, enhancements, improvements and other changes to its TI Resources. TI has not conducted any testing other than that specifically described in the published documentation for a particular TI Resource.

Designer is authorized to use, copy and modify any individual TI Resource only in connection with the development of applications that include the TI product(s) identified in such TI Resource. NO OTHER LICENSE, EXPRESS OR IMPLIED, BY ESTOPPEL OR OTHERWISE TO ANY OTHER TI INTELLECTUAL PROPERTY RIGHT, AND NO LICENSE TO ANY TECHNOLOGY OR INTELLECTUAL PROPERTY RIGHT OF TI OR ANY THIRD PARTY IS GRANTED HEREIN, including but not limited to any patent right, copyright, mask work right, or other intellectual property right relating to any combination, machine, or process in which TI products or services are used. Information regarding or referencing third-party products or services does not constitute a license to use such products or services, or a warranty or endorsement thereof. Use of TI Resources may require a license from a third party under the patents or other intellectual property of the third party, or a license from TI under the patents or other intellectual property of TI.

TI RESOURCES ARE PROVIDED "AS IS" AND WITH ALL FAULTS. TI DISCLAIMS ALL OTHER WARRANTIES OR REPRESENTATIONS, EXPRESS OR IMPLIED, REGARDING RESOURCES OR USE THEREOF, INCLUDING BUT NOT LIMITED TO ACCURACY OR COMPLETENESS, TITLE, ANY EPIDEMIC FAILURE WARRANTY AND ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, AND NON-INFRINGEMENT OF ANY THIRD PARTY INTELLECTUAL PROPERTY RIGHTS. TI SHALL NOT BE LIABLE FOR AND SHALL NOT DEFEND OR INDEMNIFY DESIGNER AGAINST ANY CLAIM, INCLUDING BUT NOT LIMITED TO ANY INFRINGEMENT CLAIM THAT RELATES TO OR IS BASED ON ANY COMBINATION OF PRODUCTS EVEN IF DESCRIBED IN TI RESOURCES OR OTHERWISE. IN NO EVENT SHALL TI BE LIABLE FOR ANY ACTUAL, DIRECT, SPECIAL, COLLATERAL, INDIRECT, PUNITIVE, INCIDENTAL, CONSEQUENTIAL OR EXEMPLARY DAMAGES IN CONNECTION WITH OR ARISING OUT OF TI RESOURCES OR USE THEREOF, AND REGARDLESS OF WHETHER TI HAS BEEN ADVISED OF THE POSSIBILITY OF SUCH DAMAGES.

Unless TI has explicitly designated an individual product as meeting the requirements of a particular industry standard (e.g., ISO/TS 16949 and ISO 26262), TI is not responsible for any failure to meet such industry standard requirements.

Where TI specifically promotes products as facilitating functional safety or as compliant with industry functional safety standards, such products are intended to help enable customers to design and create their own applications that meet applicable functional safety standards and requirements. Using products in an application does not by itself establish any safety features in the application. Designers must ensure compliance with safety-related requirements and standards applicable to their applications. Designer may not use any TI products in life-critical medical equipment unless authorized officers of the parties have executed a special contract specifically governing such use. Life-critical medical equipment is medical equipment where failure of such equipment would cause serious bodily injury or death (e.g., life support, pacemakers, defibrillators, heart pumps, neurostimulators, and implantables). Such equipment includes, without limitation, all medical devices identified by the U.S. Food and Drug Administration as Class III devices and equivalent classifications outside the U.S.

TI may expressly designate certain products as completing a particular qualification (e.g., Q100, Military Grade, or Enhanced Product). Designers agree that it has the necessary expertise to select the product with the appropriate qualification designation for their applications and that proper product selection is at Designers' own risk. Designers are solely responsible for compliance with all legal and regulatory requirements in connection with such selection.

Designer will fully indemnify TI and its representatives against any damages, costs, losses, and/or liabilities arising out of Designer's non-compliance with the terms and provisions of this Notice.