Header J1											
X1.1, X1.2	<<	VCC	1 T	2	VCC	>>	X1.1, X1.2				
χι.ι, χι.Δ		100	3	4	700		7(1.1, 7(1.2				
			5	6							
			7	8							
			9	10	SSP1 MOSI	>>	X1.10				
			11	12	SSP1 MISO	>>	X1.11				
			13	14	SSM1 SCK	>>	X1.9				
X1.6	<<	!CS ACC	15	16							
X1.7	<<	!CS GYRO	17	18							
X1.13	<<	!INT ACC	19	20							
			21	22							
			23	24							
			25	26							
			27	28							
			29	30							
			31	32							
			33	34							
			35	36							
			37	38							
			39	40							
			41	42							
			43	44							
			45	46							
			47	48							
			49	50							
			51	52							
			53	54							
			55	56							
			57	58	GND	>>	X1.3, SUB D9 P				
			59	60	GND	>>	X1.3, SUB D9 P				

			Head	er J10			
			1	2			
			3	4			
			5	6	GPIO3[0]	>>	LED Rot
			7	8	GPIO6[0]		LED Rot
			9	10	O1 100[0]		LLD Oldii
			11	12			
			13	14			
			15	16			
			17	18			
			19	20			
			21	22			
			23	24			
			25	26			
SUB D9 Pin2	<<	RS232 TX0	27	28			
SUB D9 Pin3	<<	RS232 RX0	29	30			
			31	32			
			33	34			
			35	36			
			37	38			
			39	40			
			41	42			
			43	44			
		_	45	46			
			47	48			
			49	50			
			51	52			
			53	54			·
			55	56			
			57	58			
			59	60			





