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Academic:

B. Tech in IT, Assam University Silchar, India (2012)

M. Tech in CSE, NIT Silchar, India (2014)

Ph. D. in CS, Assam University Silchar (2021)

NET qualified (Dec 2012, June 2013)

Professional Appointments:

Assistant Professor, NITS Mirza

Aug 2014 – Dec 2014

Assistant Professor, Assam University Silchar

Jan 2015- Till Date

Research Interest:

Computer Architecture, Machine Learning, Human Computer Interaction, Biometric Security, Theoretical Computer Science.

Publications:

1. **Roy B. R.** & Trivedi A. K., (**2014**) "Construction of fingerprint orientation field from minutia points," IEEE International Conference on Advanced Communications, Control and Computing Technologies, Ramanathapuram, 2014, pp. 1439-1442, doi: 10.1109/ICACCCT.2014.7019340.

2. **Roy B. R.**, Trivedi A. K., Yadav A. K. and Paul S., (**2016**) "An effective approach to estimate fingerprint orientation," 2nd IEEE International Conference on Advances in Computing, Communication, & Automation (ICACCA) (Fall), Bareilly, 2016, pp. 1-4, doi: 10.1109/ICACCAF.2016.7748997.

3. Das P., **Roy B.R.**, Das, S. & Paul S. (**2018**). A New Method Of Gray Image Coding/Decoding By Using Newly Constructed Shepard Kernel Based Fuzzy Transforms. 7. 97-107.

4. Das P., **Roy B.R.** & Paul S. (**2018**). Balanced Data Clustering Algorithm for Both Hard and Soft Clustering. International Journal of Computer Sciences and Engineering. 6. 176-183. 10.26438/ijcse/v6i2.176183.

5. Das P., **Roy B.R.** & Das, S. (**2018**). A Fast Global k-means Algorithm for Datasets having Streaming Behavior. International Journal of Computer Sciences and Engineering. 6. 84-91. 10.26438/ijcse/v6i2.8491.

6. Das P. & **Roy B.R.** (**2019**). SplitWays: An Efficient Replacement Policy for Larger Sized Cache Memory. 10.35940/ijeat.A1634.109119.

7. Das S., Das P., **Roy B.R**. (**2020**) Cloud Detection and Cloud Removal of Satellite Image—A Case Study. In: Sarma H., Bhuyan B., Borah S., Dutta N. (eds) Trends in Communication, Cloud, and Big Data. Lecture Notes in Networks and Systems, vol 99. Springer, Singapore. https://doi.org/10.1007/978-981-15-1624-5_6

8. Das P., **Roy B.R.** (**2020**) "Small-LRU: A Hardware Efficient Hybrid Replacement Policy" International Journal of Advanced Computer Science and Applications (IJACSA), 11(9). http://dx.doi.org/10.14569/IJACSA.2020.0110981

9. Das P., **Roy B.R.** (**2021**) A Categorical Study on Cache Replacement Policies for Hierarchical Cache Memory. In: Mandal J., Mukhopadhyay S., Roy A. (eds) Applications of Internet of Things. Lecture Notes in Networks and Systems, vol 137. Springer, Singapore. https://doi.org/10.1007/978-981-15-6198-6_19

10. Das P., **Roy B.R.** (**2021**) Reused-based Replacement Policy for Last Level Cache with Minimum Hardware Cost. In: Bora P.K., Nandi S., Laskar S. (eds) Emerging Technologies for Smart Cities. Lecture Notes in Electrical Engineering, vol 765, Springer, Singapore, p 161-170.