

EXERCISE 7

DA Converters

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1. OBJECTIVE

1 Objective

This laboratory exercise focus on the on the A/D converter with current sources, during the realization of the exercise we will understand the wayt that it works and how to implement it on Matlab,we then will see the diffenrece between Differential and Non Differential signal, and,yet again, obtain DNL and INL.

2 Exercise 1: Model of D/A converter realized with current sources

We will implement the formulas in Matlab and begin by obtaining the output linear voltage and plotting it:

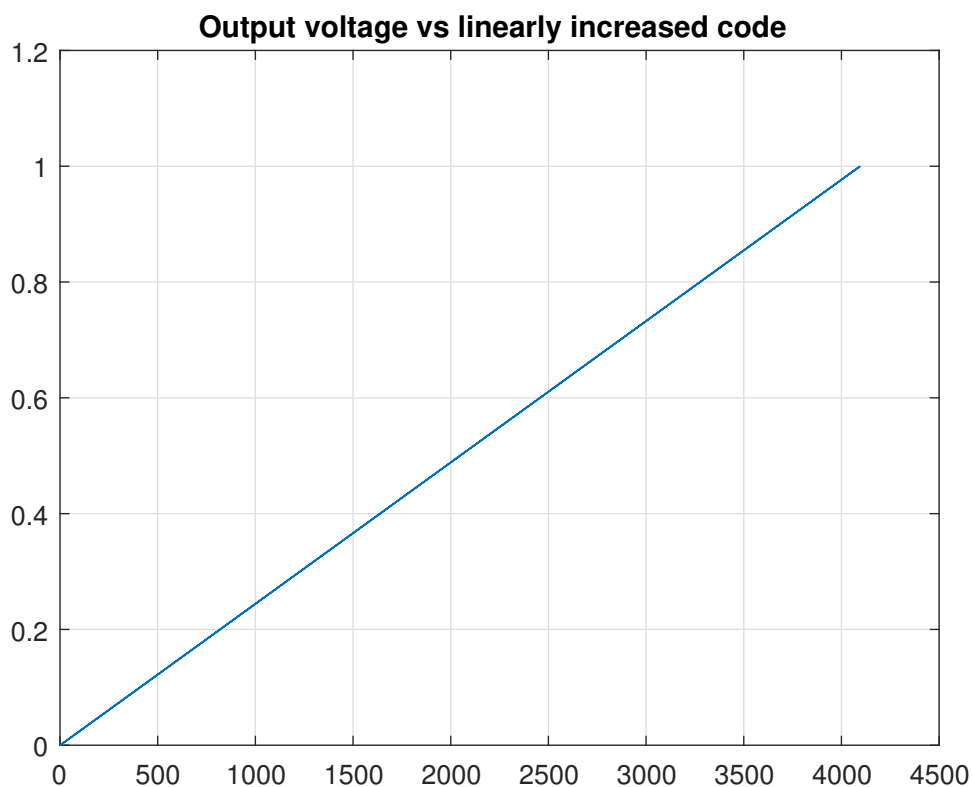


Figure 1: Output Voltage versus Input Code

2. EXERCISE 1: MODEL OF D/A CONVERTER REALIZED WITH CURRENT SOURCES

Then we will obtain DNL and INL as did in previous laboratory exercises and plot them:

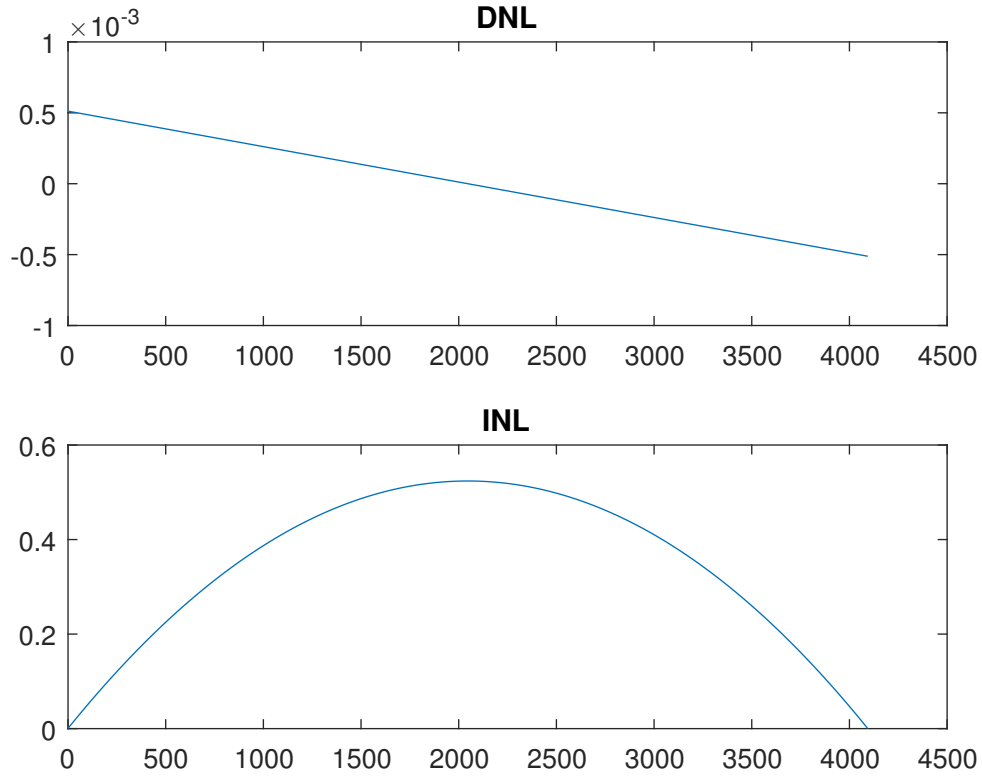


Figure 2: DNL and INL

We can see how DNL is a decreasing line, while INL is a parabole.

Finally, we obtain the output function $V_{out} \sin$ and the same signal delayed by π . Then we will plot the first signal and the difference between both.

2. EXERCISE 1: MODEL OF D/A CONVERTER REALIZED WITH CURRENT SOURCES

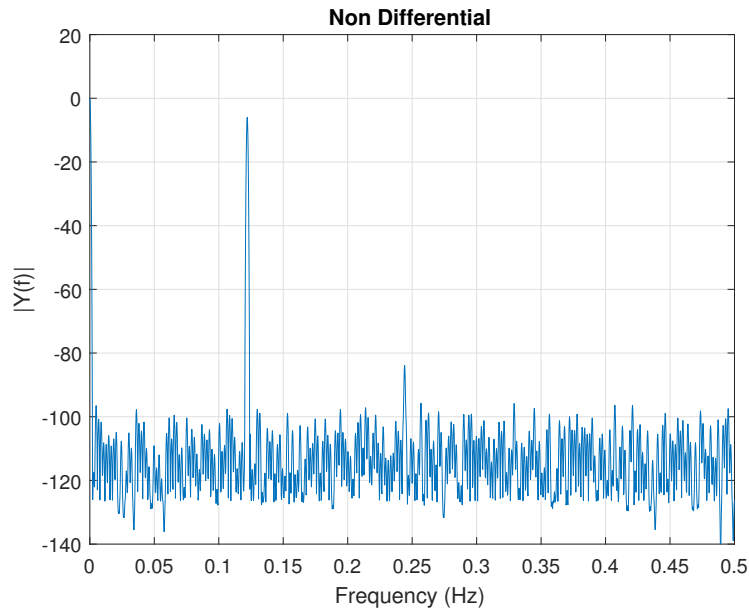


Figure 3: Non Differential

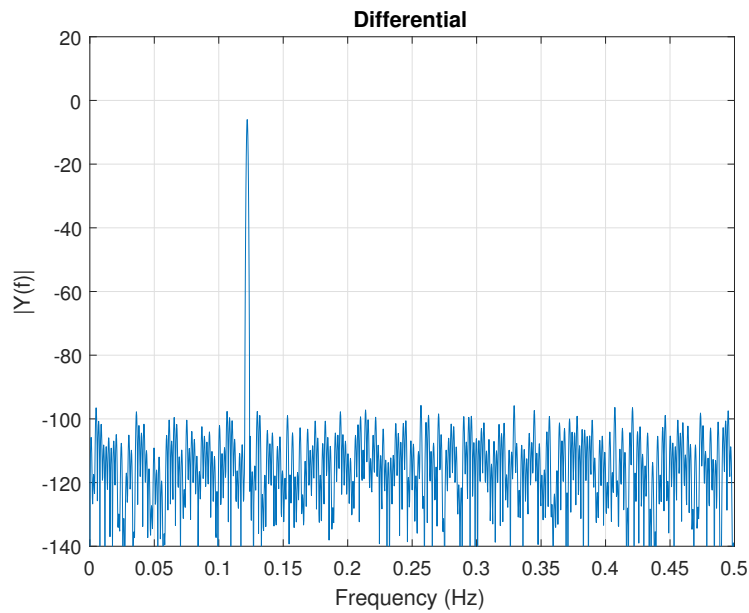


Figure 4: Differential

We can notice the difference between both modes, in non differential we have all the harmonics, while in the differential mode we delete all of them and only the first (the desired one) remains.

3 Appendix

```

1 clear all;
2 close all;
3 % We define all the variables
4 Fs=1;           % Sampling Freq
5 Vdd=1;          % Voltage input
6 B=12;           % Number of bits
7 RL=25;          % Load Resistor
8 Ron=100;        % Switch serial resistor
9 RU=200e6;       % Current source resistor
10 Iu=Vdd/(RL*(2^B-1));
11 Ron_n=Ron*ones(1,2^B);
12 RU_n=RU*ones(1,2^B);
13 Rn_n=Ron_n+RU_n;
14 In_n=Vdd./Rn_n + Iu;
15
16 x_lin=0:2^B-1;
17 % We obtain the Vout_lin
18 for i=1:2^B
19     Vout_lin(i)=sum(In_n(1:x_lin(i)))/(1/RL+sum(1./(Rn_n(1:x_lin
20         (i)))));
21 end
22 % Output voltage vs code
23 figure(1)
24 stairs(x_lin,Vout_lin);
25 grid on;
26 title('Output voltage vs linearly increased code')
27
28 % We define delta and get DNL and INL
29 delta=Vdd/(2^B-1);
30 DNL=(diff(Vout_lin)-delta)/delta;
31 for j=1:length(DNL)
32     INL(j)=sum(DNL(1:j));

```

3. APPENDIX

```

32 end
33
34 % We plot DNL and INL
35 figure(2)
36 subplot(2,1,1)
37 plot(x_lin(1:4095),DNL)
38 title('DNL')
39 subplot(2,1,2)
40 plot(x_lin(1:4095),INL)
41 title('INL')
42
43
44 % We now calculate Vout sin (Single)
45 n=0:2^B-1;
46 x_sin=(2^B-1)*(0.5*sin(2*pi*500./length(n).*n)+0.5);
47 for g=1:length(x_sin)
48     Vout_sin(g)=sum(In_n(1:x_sin(g)))/(1/RL+sum(1./Rn_n(1:
49         x_sin(g))));
50
51 end
52
53 fft_plot(Vout_sin,1,'lin','dB')
54 title('Non Differential')
55 ylim([-140 20])
56
57 % We now calculate Vout sin 180 (Diff)
58 x_sin_180=(2^B-1)*(0.5*sin(2*pi*500./length(n).*n+pi)+0.5);
59 for g=1:length(x_sin)
60     Vout_sin_180(g)=sum(In_n(1:x_sin_180(g)))/(1/RL+sum(1./Rn_n
61         (1:x_sin_180(g))));
62
63 end
64
65 fft_plot((Vout_sin-Vout_sin_180)/2,1,'lin','dB');
66 ylim([-140 20])
67 title('Differential')

```