

Seeking Embedded Software Development or Hardware Design Coop for Spring/Summer 2021.

## Work Experience

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### D3 Engineering: Software Engineer

Software Engineering technician at D3 Engineering, where I primarily worked with Embedded Linux and C development on Texas Instruments' TDA line of processors, for automotive vision applications. I managed the team for the development and execution of testing for the latest product. While working at D3, I was exposed to embedded software development on a large code base. I worked on both embedded Linux development, and embedded BIOS development on remote cores of a processor. I was able to hone and expand my skills in working as part of a development team. Demonstrated ability to efficiently work from home, due to COVID-19 Pandemic

**Rochester, NY**  
January --- July '20

### iD Tech Camps: Lead Instructor

*iD Tech Camps: William and Mary, PayPal Timonium, and American University*

Taught a variety of camps during two years at iD Tech camps. In addition to teaching, my second year I was promoted to Lead Instructor. As Lead, I was in charge of the other instructors, and responsible for parent interaction if need be, and administering medicine to students. This position gave me valuable experience in team/personnel management.

**Multiple Locations**  
June-August '18, '19

### RIT CE Department: Teaching Assistant

*RIT Computer Engineering Department*

Assisted in Lab for a variety of classes over the course of 3 years. Taught concepts to students, and helped students debug, problems.

**Rochester, NY**  
August '18-Present

## Education

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### Rochester Institute of Technology

Bachelor of Science: Computer Engineering, 5 year program

GPA: 3.04 - Dean's List Spring '19

Relevant Coursework:

- **Reconfigurable Computing (CMPE-660)**: Made complex synchronous designs on Nexys 4 DDR FPGA board. In Progress.
- **Interfacing Digital Electronics (CMPE-460)**: Used FRDM-K64 ARM board to interface with peripherals, to eventually build and program an autonomous race car. In Progress.
- **Digital Signal Processing (CMPE-480)**: Studied mathematical analysis of digital signals, and how to represent. In Progress.
- **Intro to Software Engineering (SWEN-261)**: Developed and practiced the ability to work in a team with other software developers, in an Agile work environment.

**Rochester, NY**  
Expected May '22

## Skills

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- **Languages**: C, VHDL, Arm Assembly, Python,  $\LaTeX$ , Bash, C++, Qt5, Java, Rust
- **Tools**: GNU/Linux tools and environment, Xilinx Vivado, Git, KiCad, Altera Quartus II, ModelSim
- **Hardware**: Soldering, Prototyping on breadboard, Hardware design on FPGA, Hardware Debugging
- **Professional Skills**: Public Speaking, Spanish (Semi Conversational), Team Management

## Projects

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### Pipelined MIPS Processor

Created each stage of a MIPS processor, individually tested these stages. The processor was modeled and tested using VHDL and then implemented on to Basys 3 FPGA. The processor was tested by calculating 10 elements of the Fibonacci Sequence.

**VHDL**  
January-May '19

## Organizations

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- **Engineering House**: Special Interest House at RIT. Positions Held: Secretary. Active October '17 - May '19
- **Computer Science House**: Special Interest House at RIT. Active August '17 - May '18
- **CodeRIT**: RIT's Hackathon Club. I assisted in planning BrickHack 2019