Seth Gower

sdg7234@rit.edu
github.com/SethGower
(757)553-0576
sethgower.com

Seeking Full Time employment for Digital Hardware Design. Starting June 2022.

Work Experience

Herrick Technology Laboratories: DSP Engineering Intern
Germantown, MD
February '21 - Present

Currently Employed at HTL as a DSP Engineering Intern. I work on Software Defined Radio (SDR) using FPGA technology to handle communication over high speed interfaces. Extensive work with Intel/Altera based platforms and tools. Use of Matlab for design verification.

D3 Engineering: Software Engineer
Rochester, NY
January - July '20

As an Embedded Software Engineer at D3 Engineering, I worked with Embedded Systems for Advanced Driver Assistance Systems (ADAS) applications, using Texas Instruments' TDA Line of Processors. In addition to embedded software, I wrote the test suite to perform automated production testing for a new product. During the COVID-19 Pandemic, I showed I was able to work effectively and productively in a remote capacity.

iD Tech Camps: Lead Instructor
Multiple Locations
June - August '18, '19

iD Tech Camps: William and Mary, PayPal Timonium, and American University
Taught a variety of camps during two years at iD Tech camps. In addition to teaching, my second year I was promoted to Lead Instructor. As Lead, I was in charge of the other instructors, and responsible for parent interaction if need be, and administering medicine to students. This position gave me valuable experience in team/personnel management.

Education

Rochester Institute of Technology
Rochester, NY
Expected May '22

Bachelor of Science: Computer Engineering, 5 year program
GPA: 3.11 - Dean's List: Spring '19, Fall '20
Relevant Coursework:

- Reconfigurable Computing (CMPE-660): Learned Advanced Synchronous Digital Design concepts targeting a Xilinx Nexys A7 (Previously Nexys 4 DDR) development board.
- Interfacing Digital Electronics (CMPE-460): Used FRDM-K64 ARM board to interface with peripherals, to eventually build and program an autonomous race car. In Progress.

Skills

- **Languages:** C, VHDL, Matlab, Arm Assembly, Python, LaTeX, Bash, C++, Qt5, Java, Rust
- **Tools:** GNU/Linux tools and environment, Git, KiCad, Altera Quartus Suite, ModelSim, Xilinx Vivado
- **Hardware:** Soldering, Prototyping on breadboard, Hardware design on FPGA, Hardware Debugging
- **Professional Skills:** Public Speaking, Team Management, Ability to Work productively in a remote capacity

Projects

Small Scale Autonomous Race Car
Embedded C
August - December '20

Using a NXP/Freescale FRDM-K64 Embedded Development board, myself and a partner assembled and programmed an autonomous race car to compete in RIT's IDE Cup. Used embedded C to design to interface with a line scan camera to control motors and servos to quickly traverse a randomly designed track.

Pipelined MIPS Processor
VHDL
January - May '19

Created each stage of a MIPS processor, individually tested these stages. The processor was modeled and tested using VHDL and then implemented on to Basys 3 FPGA. The processor was tested by calculating 10 elements of the Fibonacci Sequence.

Organizations

- **Engineering House:** Special Interest House at RIT. Positions Held: Secretary. Active October '17 - May '19
- **Computer Science House:** Special Interest House at RIT. Active August '17 - May '18