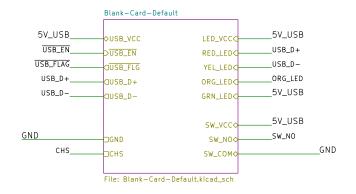
SLI Backplane Card Default Edge Connector

First 104 pins follow pumpkin CSK bus (Rev E) pins Some user and IO pins have been assigned by SLI (that are not used by any other functions)

	(that are not used t	Jy all	9 0tii	er run	ctions)
	Card Edge for H	SECE		0-01-	S-DV-A-K-TR
	SEP1	1		2	SEP2
	<pre>- Z_SOLAR_TLE - Z_SOLAR_RTN</pre>	<u>3</u> 5		6	+X_SOLAR_TLE +X_SOLAR_RTN
	-Z_SOLAR_RYN	7	-	8	+X_SOLAR_PWR
	H1-1	9		10	H1-2
	H2-1	11		12	H2-2
	H1-3	13	L	14	H1-4
	H2-3	15		16	H2-4
	H1-5 H2-5	17		18	H1-6 H2-6
	H1-7	19 21		20 22	H1-8
	H2-7	23	-	24	H2-8
	H1-9	25		26	H1-10
	H2-9	27		28	H2-10
	H1-11	29	L -	30	H1-12
	H2-11	31		32	H2-12
	H1-13 H2-13	33	┝ -	34	H1-14 H2-14
	RBF	35 37		36 38	H1-16
	H2-15	39		40	H2-16
	H1-17	41		42	H1-18
	H2-17	43		44	H2-18
	H1-19	45		46	H1-20
	H2-19	47		48	H2-20
	H1-21 H2-21	49		50	H1-22 H2-22
	H1 _ 23	51 53	-	52 54	H1 _ 24
	H2-23	55		56	H2-24
	H1-25	57		58	H1-26
	H2-25	59		60	H2-26
	H1-27	61	_ =	62	H1-28
	VCC_sys	63	┡ -	64	VCC_sys
	BUS_RESET GND	65 67		66 68	H1-30 GND
	H1-31	69		70	5V_USB
	AGND	71		72	GND
	H1-33	73		74	H1-34
	H2-33	75		76	H2-34
	H1-35			78	H1-36
	H2-35 H1-37	79 81		80 82	H2-36 H1-38
	H2-37	83	-	84	H2-38
	H1-39	85	[86	H1-40
	H2-39	87		88	H2-40
	SDA_SYS	89	L .	90	H1-42
	H2-41	91		92	H2-42
	SCL_SYS H2-43	93 95		94	H1-44
	H1-45	95 97		96 98	H2-44 H1-46
	H2-45	99		100	H2-46
End of CCK Don Di	SELF_TEST	101		102	USER_1
End of CSK Bus Pins	GND	103		104	GND
	USER_2	105		106	H1-50
	H2-49	107		108	H2-50
	H1-51 H2-51	109 111		110 112	H1-52 H2-52
	+Y_SOLAR_TLE	111	-	112	-X_SOLAR_TLE
			<u> </u>	116	-X_SOLAR_RTN
	+Y_SOLAR_RTN +Y_SOLAR_PWR	117		118	-X_SOLAR_PWR
	SEP3	119		120	SEP4
				l	
				•	

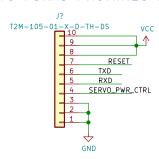
SLI Backplane Card Default Circuitry

USB 2.0 microB



NT? mainboard PC01 H2-6 SERVO_PWR_CTRL pull'd high by servo power regulator? mainboard PA19 H1-6 RESET openMV RST UART_2_Rx H1-19 TXD openMV MOSI NT? do we need to buffer the UART lines? UART_2_Tx H1-20 RXD openMV MISO

TO FORAS PROMINEO PAB



Sierra Lobo INC

Sheet: /
File: FP Interface Card.kicad_sch

Title: Blank Card for SLI Backplane

Size: B Date: 2021–11–05 Rev: A

KiCad E.D.A. kicad (6.0.0) Id: 1/2

