Think about some computational seenario a sort these computational needs according to which should be used? Microprocessor on microcontroller.

Microprocessor is a controlling unit of a micro-computer, on a small chip that enpublic of performing ALU (Asithmetic Logic unit) operation and commicriting with the other devices connected to it,

Gome popular example of the microprocernor are:

De Jostel core is, is, if

De AMD Athlon

Broadcom (Raspherry Pi)

=> Pentium

Mieroproeessor one not made for speedie task but they one required where task one complex and torcky like development of refusive and other application application that required high memory and where

input, output are not defined. It may also be called heart of a computer system.

A mieroeomputer made on a single semiconduite ehrip is called single chip mieroeomputer. Since, single chip mieroeomputer are generally used in control application, they also are colled mieroeomputer.

In other words, mierocontroller contains all the essential components of a microcomputer such as,

> # EPU # RAM # ROM # I/O lines

El What is the difference between Microprocesyn

A list of difference between Mieroprocessor and Mieroeuntroller are given below :

### Mieroproessor

- Mieroprocessor acts as a hart of computer system.
- opplientions.
- 3) It cannot be used in compact system therefore, mieroprocessor is inefficient
- at is mainly used in personal computers.

# Mierocontroller

- OMieroeontroller actsos a heart of embedded system.
- single task withhat on application.
  - compact. Therefore,
    microcontroller is more
    efficient.
- Det is used in machine, was thing machine, air. condition, MP3 player ets.

# 13 Explain Moore's Low.

Morre's low refers to Gordon Moore's perception that the number of transistors on a microchip doubles every two years.

In other words, Moore's low water that, we can expect that speed our computer to increase every couple of our computer to increase every couple of years.

D what is the difference between RISE and else processor.

as follows:

#### RISE

of instruction own present in the architecture.

#### RIJE

100,

- @ muliple formats, ove supported for specifying operands
- B e Is e supports
  Arroy.
- are voul

- & simple addressing formats are supported.
- BRISE does not sury.
  - 1) No condition codes