

Q1	<p>(a) What do you mean by Instruction and Instruction Set? What is ISA and Microarchitecture? Give examples if needed.</p> <p>(b) Briefly mention the meaning of the following MIPS instructions.</p> <div><p>(i) add \$s1,\$s2,\$s3</p><p>(ii) lw \$s1,20(\$s2)</p><p>(iii) andi \$s1,\$s2,10</p><p>(iv) sltu \$s1,\$s2,\$s3</p><p>(v) bne \$s1,\$s2,25</p></div>	2*5 = 10																												
Q2	<p>(a) Draw a block diagram for Von Neumann Architecture.</p> <p>(b) List the five classic components of a computer system.</p> <p>(c) What is the stored program concept?</p> <p>(d) State Amdahl's Law and Moore's law</p>	5*2 = 10																												
Q3	<p>A compiler designer is trying to decide between two code sequences for a computer. The hardware designers have supplied the following facts:</p> <table><tr><td></td><td colspan="3">CPI for each instruction class</td></tr><tr><td></td><td>A</td><td>B</td><td>C</td></tr><tr><td>CPI</td><td>1</td><td>2</td><td>3</td></tr></table> <p>For a high-level language statement, the compiler writer is considering two code sequences that require the following instruction counts:</p> <table><tr><td></td><td colspan="3">Instruction counts for each instruction class</td></tr><tr><td>Code Sequence</td><td>A</td><td>B</td><td>C</td></tr><tr><td>C-1</td><td>4</td><td>2</td><td>4</td></tr><tr><td>C-2</td><td>6</td><td>3</td><td>2</td></tr></table> <p>Which code sequence executes the most instructions? Which will be faster? What is the CPI for each sequence?</p>		CPI for each instruction class				A	B	C	CPI	1	2	3		Instruction counts for each instruction class			Code Sequence	A	B	C	C-1	4	2	4	C-2	6	3	2	5
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[Answer all the questions. A google form is provided to submit the answer script. You must submit a pdf document on the google form. You can use camscanner or other images to pdf converter tools. If the file size is too large, you need to compress the file. You can use this website <https://www.ilovepdf.com/>.

Format of your file should be: CSE311<your_reg_no>TT#01.pdf]