North East University Bangladesh (NEUB)

Department of Computer Science and Engineering

Mid Semester Examination 2022 Course code: CSE311 (Computer Architecture)

Total Marks: 30 | Time: 90 minutes

Answer any 3 questions.

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Q1	Microarchitecture? Give examples if needed. Please mention the Fields and Field size for R-type, I-type and J-type instructions. (b) Briefly mention the meaning of the following MIPS instructions. (i) add \$s1,\$s2,\$s3				5
1	(ii) lw \$s1,20(\$s2) (iii) andi \$s1,\$s2,10				
	(iv) sltu \$s1,\$s2,\$s3				
102	(v) bne \$s1,\$s2,25				
Q2	(a) Using 4-bit numbers using the multiplication algorithm, multiply 0010 _{two} * 0011 _{two} .				5
	(b) Find out the decimal equivalent of the following floating-point binary number. Also represent this number in 64bit format.				5
1	1 01111110 1000000000000000000000000000				
l	1 10111110 1000000000000000000000000000				
Q3	machine instruction?				5
	00af8020hex				5
	(b) In the following code segment, f, g, h, i, and j are variables. If the five variables f through j correspond to the five registers \$50 through \$54,				٠
1	what is the compiled MIPS code for this C if statement?				
1	if (i == j) f = g + h;				
		else f = g -	h;		
Q4	(a) A compiler designer is trying to decide between two code sequences for a computer. The hardware designers have supplied the following facts:				5
		CPI for each instruction class			
- 1		Α	В	С	
	CPI	1	2	3	
	For a high-level language statement, the compiler writer is considering two code sequences that require the following instruction counts: Instruction counts for each instruction class				
	Code	A	В	C	
1	Sequence				
	C-1	2	1	2	
	C-2	4	1	1	5
	Which code sequence executes the most instructions? Which will be faster? What is the CPI for each sequence?				3
	(b) Suppose we have two implementations of the same instruction set architecture. Computer A has a clock cycle time of 250 ps and a CPI of 2.0 for some programs, and computer B has a clock cycle time of 500 ps and a CPI of 1.2 for the same program. Which computer is faster for this program and by how much?				

(a) Draw a block diagram for Von Neumann Architecture. (b) List the five classic components of a computer system. 5 = (c) What is the stored program concept? 10 (d) Sate Moore's law. According to the trend in device scaling historically observed by Moore's Law, the number of transistors on a chip in 2025 should be how many times the number in 2015? (e) The eight great ideas in computer architecture are like ideas from other fields. Match the eight ideas from computer architecture, "Design for Moore's Law", "Use Abstraction to Simplify Design", "Make the Common Case Fast", "Performance via Parallelism", "Performance via Pipelining", "Performance via Prediction", "Hierarchy of Memories", and "Dependability via Redundancy" to the following ideas from other fields: A. Assembly lines in automobile manufacturing B. Suspension bridge cables C. Express elevators in buildings

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