North East University Bangladesh Department of est

Course Cade: CSE-321

Assignment & \$3

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1) Elaborate on the difference between Van Nauman arehitecture and hardvard Architecture?

The Difference between Von Neuman architecture & howard Architecture as follows;

Von Neuman

OUT mes are single and staring both data and instruction.

- 1) One common single path (bus) helps in the transfer of both instruction and data
- 394 requires two lock for executing orsingle instruction.

Hanvard

- 1 1) It uses two separate physical address for accoming physical address for storing and accessing both duta & instruction.
 - OH uses seperate buses for trunsfering both data and instruction.
 - 3 1- executives instruction ving only one eyelle

The Von Neuman architecture is a type of ligital computer architecture in whie the design follows the energh of the empoter with stoyed programs the tey stone the

data with the instruction data in the same memory.

The harvard architecture is a type of digital computer anchitecture in which the digital computer anchitecture in which the design tollows a basic concept of having seperate single but and seperate store for data a instruction.

2) Explain how register, works?

In computer architecture, the register are very hit computer memory which are used to exect programms and operation efficiently.

A register is a group of Hip-Hops, each stip-flop empable of storing one bit information. An n bit register has an Hip-flop and is copable of storing binnot information of n bits. The flip-flop and hold the binary information and goth control when and how new information is transferd into a register.

consider the following orithmentie mierooperation:

RB - RI+R2

The above startment instruction the data or controls of register R1 to be added to data of register R2 and the sum transferd to register R3.

3 what is the function of ALU in a processor ?.

An arithmentic Logie Unit (ALV) is a digital circuit in a processor to perform arithmentic and Logie aperation. An ALU performs basic arithmentic operation are addition. Substruction, multiplicution, and division. An ALU auto can performs Logie aperation are not, AND) of. An ALU represents the fordamental building block of the epu of a computer. Modern epus contains very powerful and complex ALUS.

@ what are the bus system necessary for a microcomputer system o

between computer microprocessor and the main memory. The bus provides a communication path for the data and control signals moving between the major components of the computer system. The system bus worke by combining the function of the three main busses, randy, the late, address and control busses.

The data bus, which is a bidrectional poth, which carries the actual data between the processor, many on the preripherals

the address bus is used to speedy memory western for the data being transferred.

The control bus carries the control, timing, and co-ordinator signals to manage the various function accross the system.