

# Presentation on: **Experiment 2**

## **Experiment Name**

Diode Circuit Analysis and Logic gates using diodes

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# Outline of this presentation

- ❑ Objective
- ❑ Theory
  - Apparatus Needed
  - Circuits
  - Procedure
- ❑ Report

## Objective:

The objective of this experiment is to analyse simple diode circuits and to build logic circuits using diodes and resistors.

## Theory:

To complete this lab need to read:-

- Lecture-1
- Lecture-2
- Lecture-3

## Apparatus Needed

- Trainer Board (Bread Board)
- Diodes
- Resistor
- DC Voltmeter
- DC Ammeter
- DC Power supply
- Function Generator
- Oscilloscope
- Connecting Wires

# Circuit

Here, I have attached 3 Figures of the circuit.

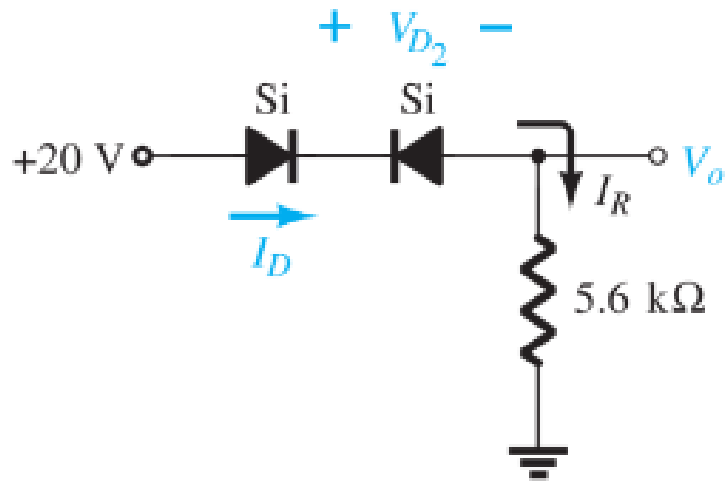


Figure 1 Circuit for diode analysis

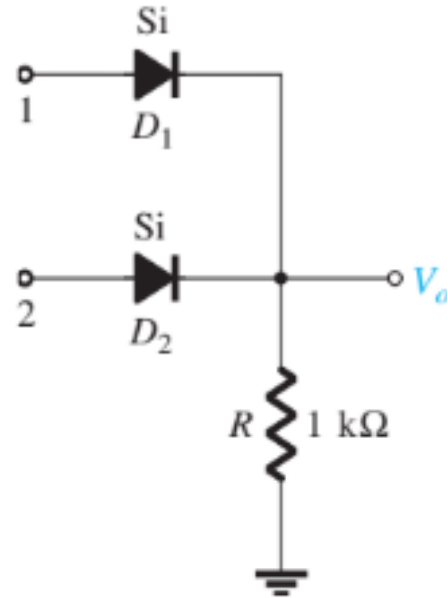


Figure 2 Positive Logic OR Gate

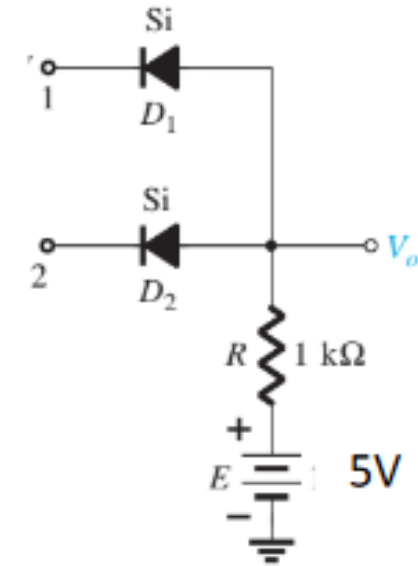


Figure 3 Positive Logic AND Gate

## Report:

1. Carefully Fill all the data for table 1, 2, 3.
2. Comment on the learnings from this LAB.

# END