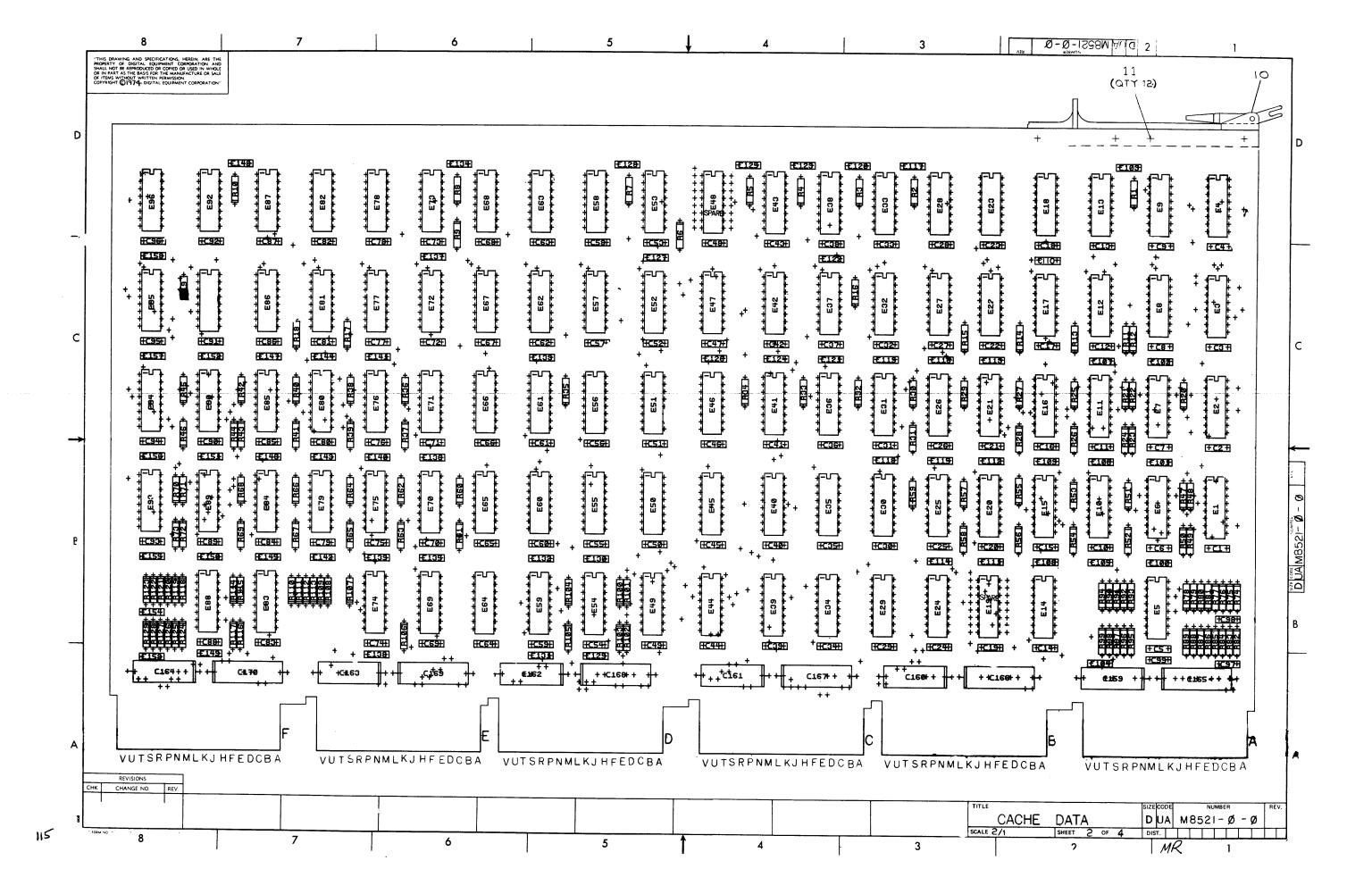
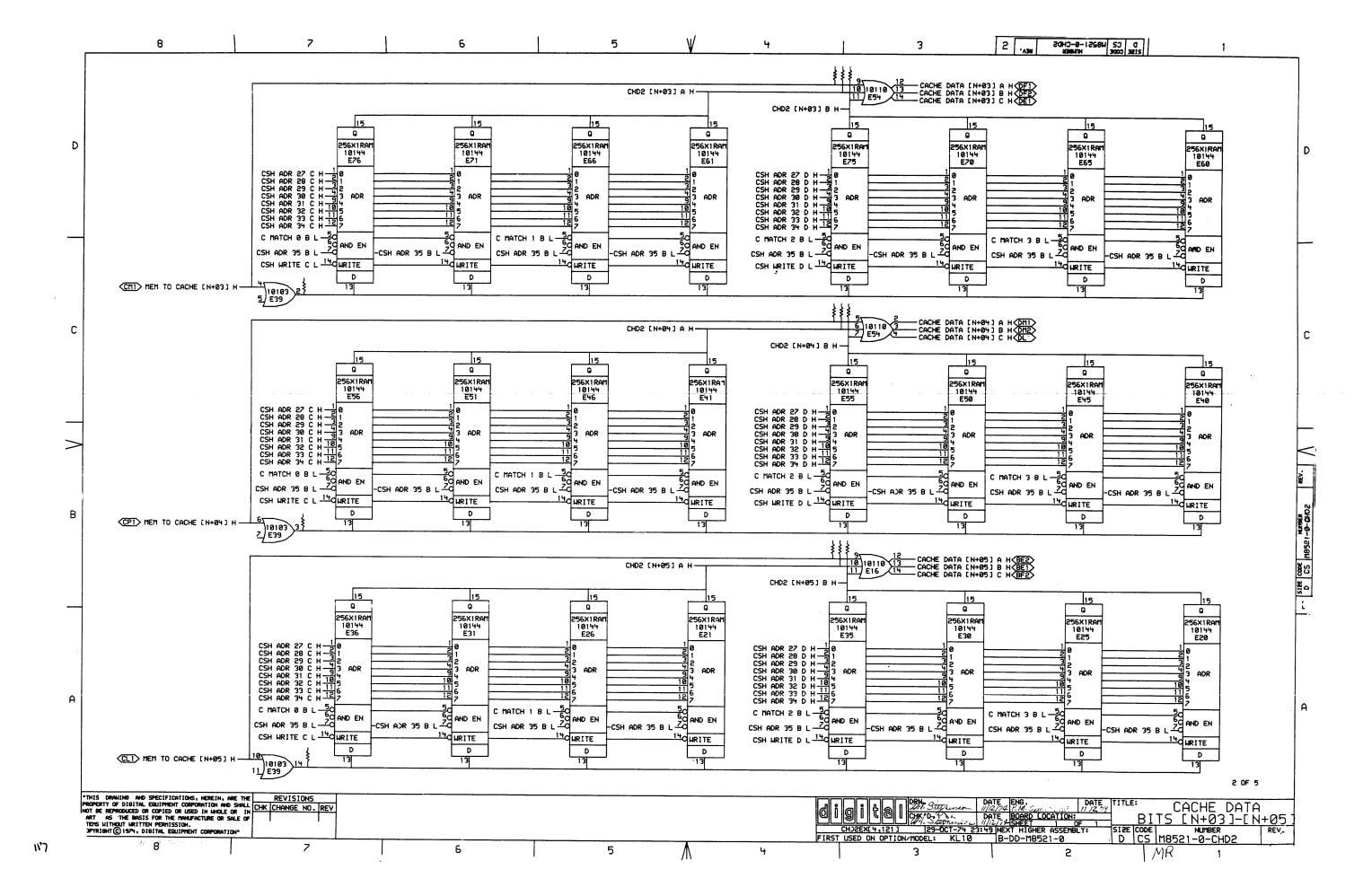
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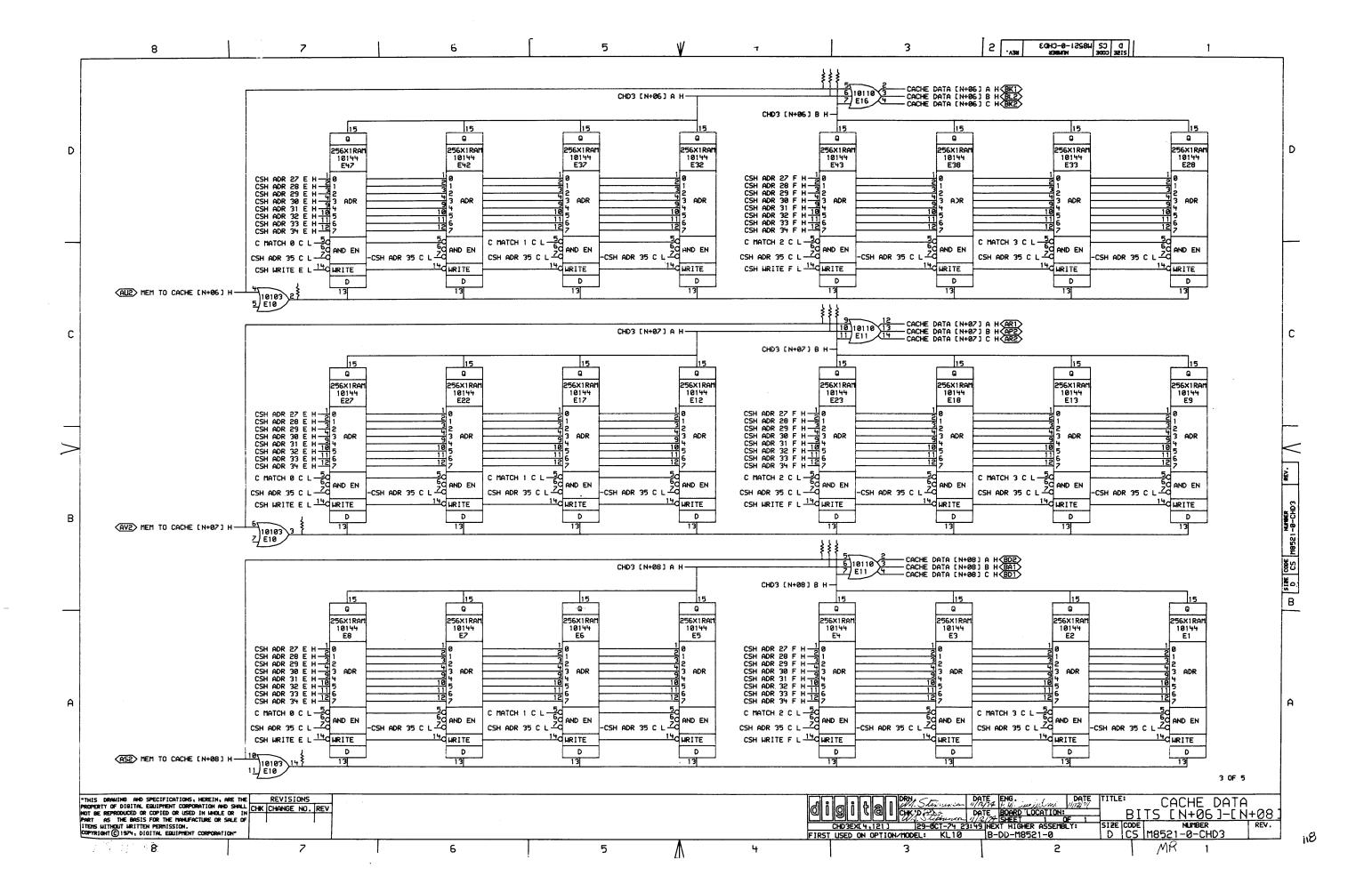


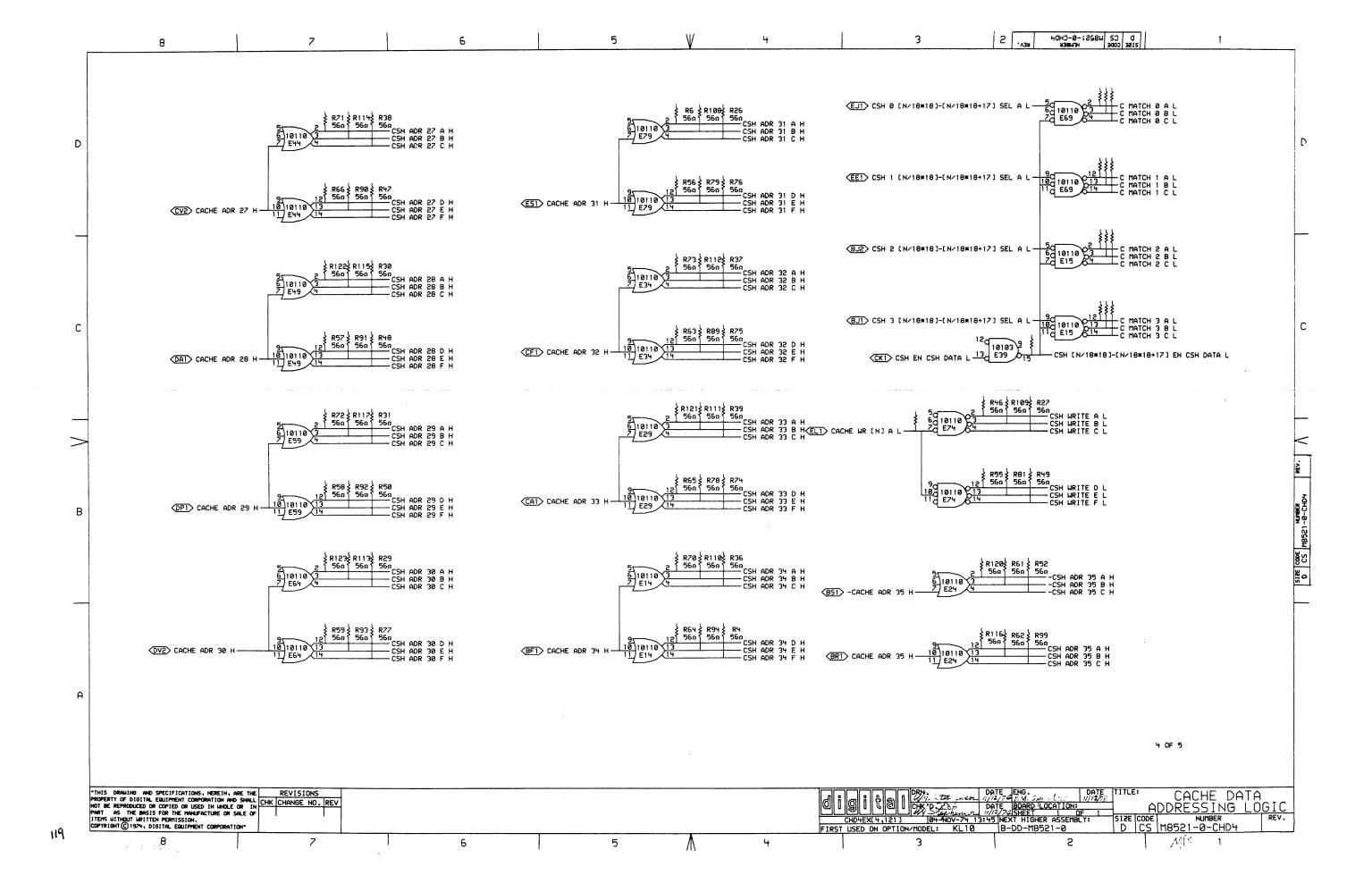


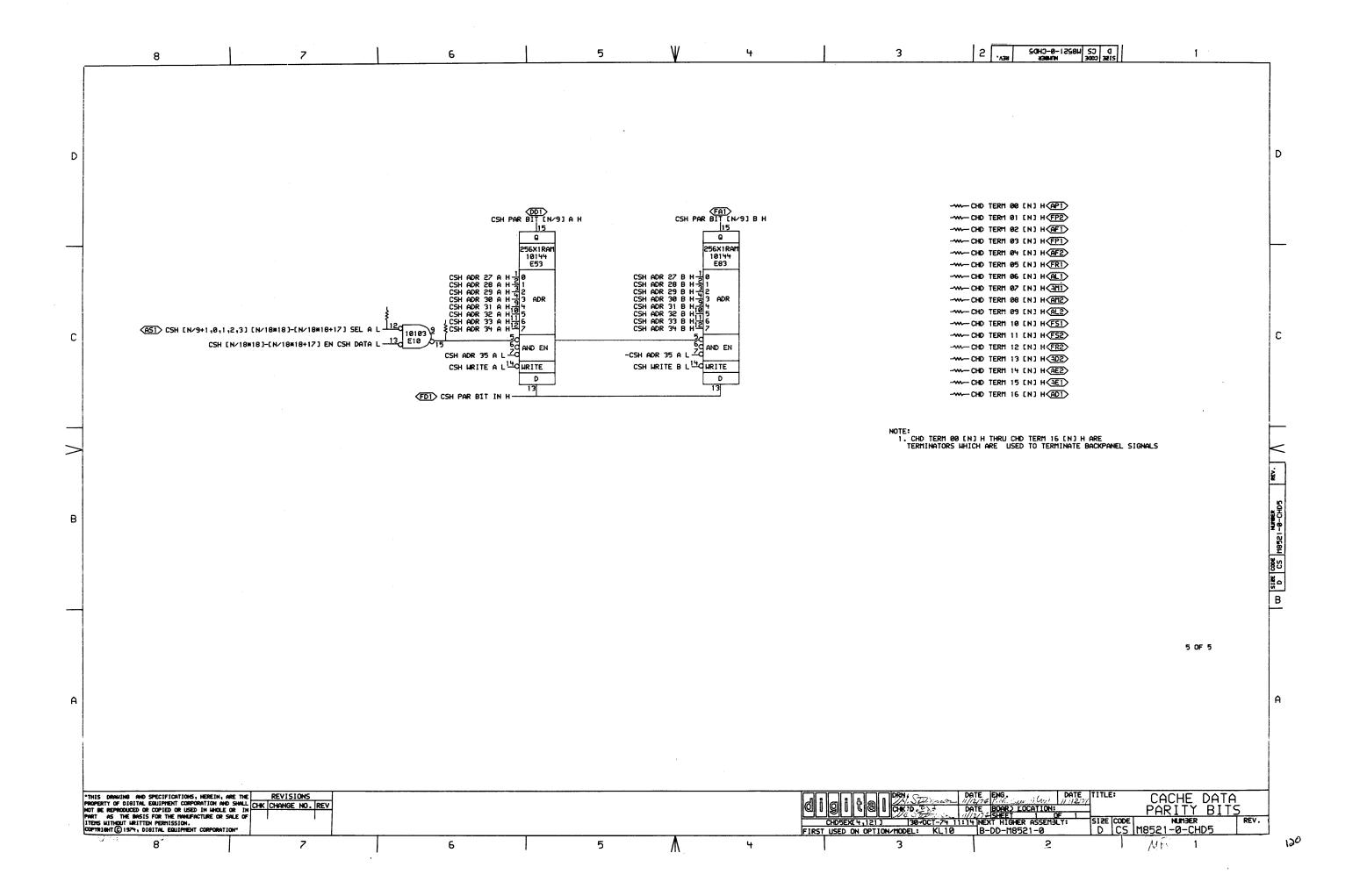
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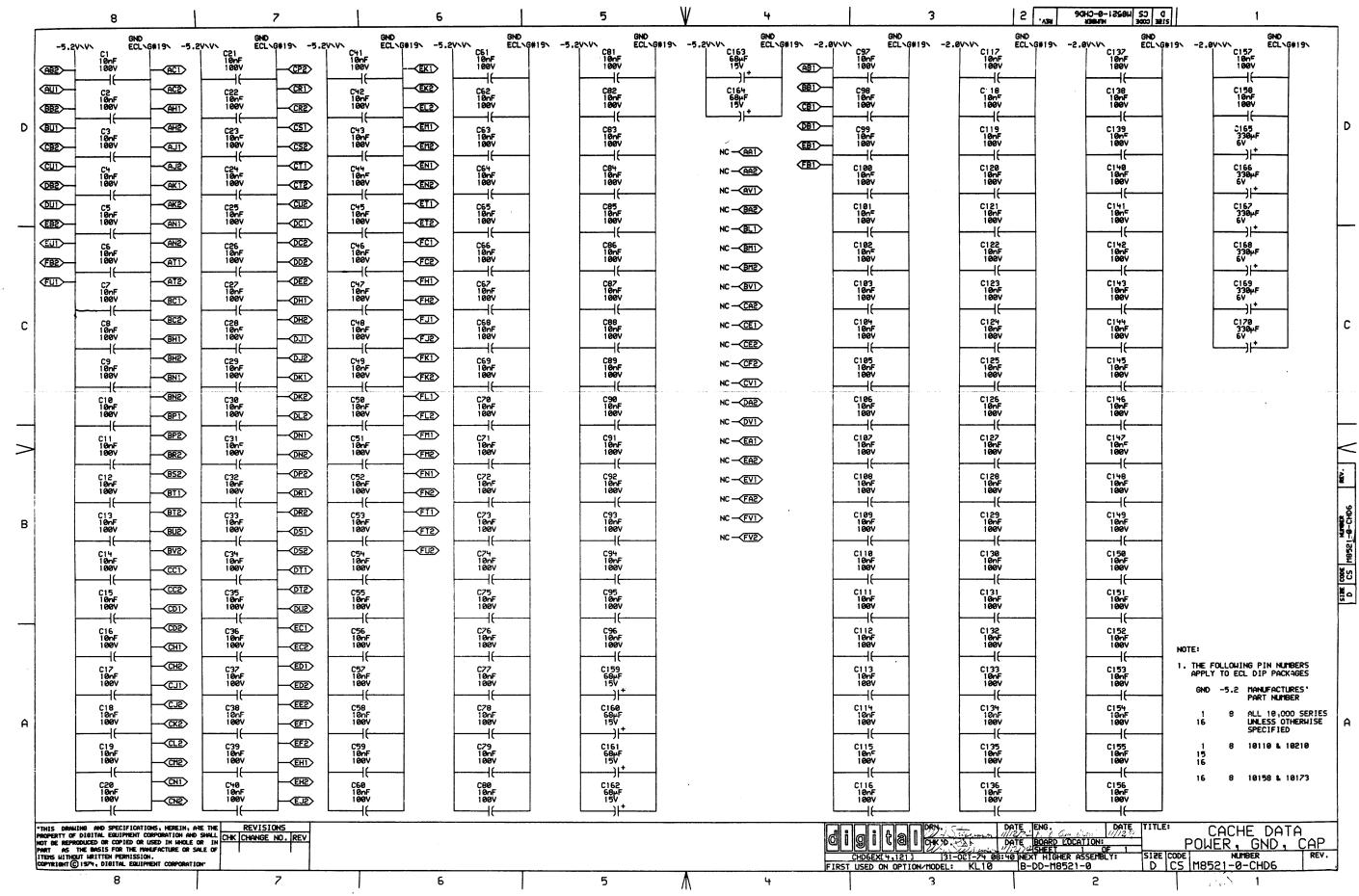
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			SINAL LOC PIN	SHOWN ON TERMINATES DRW NO SIGINAL  CHD4 -CACHE WR [N] A	RESISTOR SHOWN LOC PIN DRW H R 18 1 CHD	NO SIGINAL			
		R 80 1 CHD3 %E100 R 7 1 CHD5 %E100 R 2 1 CHD3 %E100	(15) R 98 1	CHD5 CHD TERM 00 [N] CHD5 CHD TERM 01 [N]	H R 41 1 CHD	1 CHD1 [N+02] B H			
		R 1 1 CHD3 %E100 R 33 1 CHD2 %E390	R 86 1	CHD5 CHD TERM 02 [N]	H R 102 1 CHD	S CHDS [N+03] B H			
		R 35 1 CHD2 %E390 R 34 1 CHD2 %E390	R 87 1	CHD5 CHD TERM 04 [N] CHD5 CHD TERM 05 [N]	H R 105 1 CHD	2 CHD2 [N+04] B H			
		R 9 1 CHD1 %E850 R 118 1 CHD1 %E850	14) R 88 1	CHD5 CHD TERM 06 [N] CHD5 CHD TERM 07 [N]	H R 53 1 CHD	2 CHD2 [N+05] B H			
		R 10 1 CHD1 %E850		CHD5 CHD TERM 08 [N] CHD5 CHD TERM 09 [N]	H R 14 1 CHD	3 CHD3 [N+06] B H			
		R 32 1 CHD4 -C MA	ATCH Ø B H R 126 1	CHD5 CHD TERM 10 [N]	H R 11 1 CHD	3 CHD3 [N+07] B H			
		R 19 1 CHD4 -C MA	ATCH 0 C H R 129 1 ATCH 1 A H R 128 1	CHD5 CHD TERM 11 [N] CHD5 CHD TERM 12 [N]	H R 106 1 CHD	4 -CSH [N/18*18]-[N/1	8*18+17] EN CSH DATA H		
		R 16 1 CHD4 -C MA	ATCH 1 B H R 83 1 ATCH 1 C H R 85 1	CHD5 CHD TERM 13 [N] CHD5 CHD TERM 14 [N]	H R 42 1 CHD	1 MEM TO CACHE [N+00]		LA	
		R 67 1 CHD4 -C MA	ATCH 2 A H R 84 1 ATCH 2 B H R 82 1	CHD5 CHD TERM 15 [N] CHD5 CHD TERM 16 [N]	H R 44 1 CHD	1 MEM TO CACHE [N+02]	н		
			ATCH 2 C H R 68 1 ATCH 3 A H R 69 1	CHD1 CHD1 [N+00] A H CHD1 CHD1 [N+00] B H	R 100 1 CHD R 101 1 CHD				
			ATCH 3 C H R 40 1	CHD1 CHD1 [N+01] A H CHD1 CHD1 [N+01] B H	R 23 1 CHD R 25 1 CHD	3 MEM TO CACHE [N+06]	н		
					R 24 1 CHD R 21 1 CHD				
		NOTE:							
		<ol> <li>ENTRIES ARE SORTED BY</li> <li>ALL TERMINATION RESIST ARE CONNECTED TO -2.0V</li> </ol>	ORS ARE 68Ω 1/4W 5% AND 'UNLESS OTHERWISE SPECIFIED						
		3. % INDICATES OUTPUT OF () INDICATES PIN NUMBE	DIP LOC AND R						
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Perty of Digital Equif Be reproduced or copi	TFICATIONS, HEREIN, APE THE REVIS PHENT CORPORATION AND SHALL CHK CHANGI IED OR USED IN MHOLE OR IN	SIONS E NO. REV				digita	DRN. C DATE 31-001-74 CHK D S 2 DATE DATE 1 06-NOV-74 87:19 NEX	ENG. DATE TITLE:  7. Y. Guy G 22 M/12/22  BOARD LOCATION:	CHACHE DATA TERMINATORS
T AS THE BASIS FOR MS WITHOUT WRITTEN PER	THE MONIFOCTIBE OF SOLE OF!	1				M8521[4,427 FIRST USED ON OPT	1   05-NOV-74 07:19 NEX 10N-MODEL: KL10 B-1	SHEET 1 OF 1 THIGHER ASSEMBLY: SIZE CODE DD-M8521-0 D CS	

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