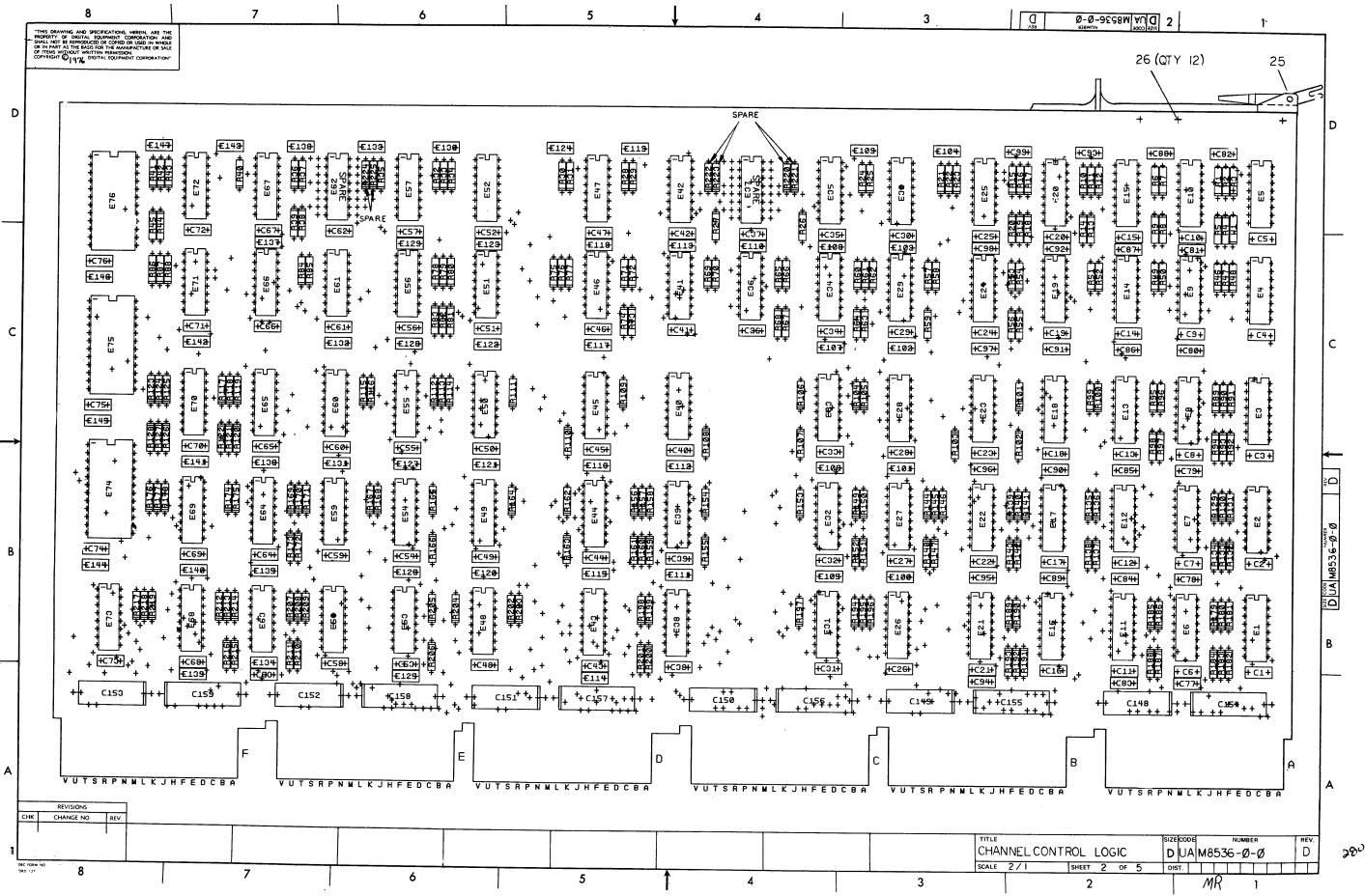
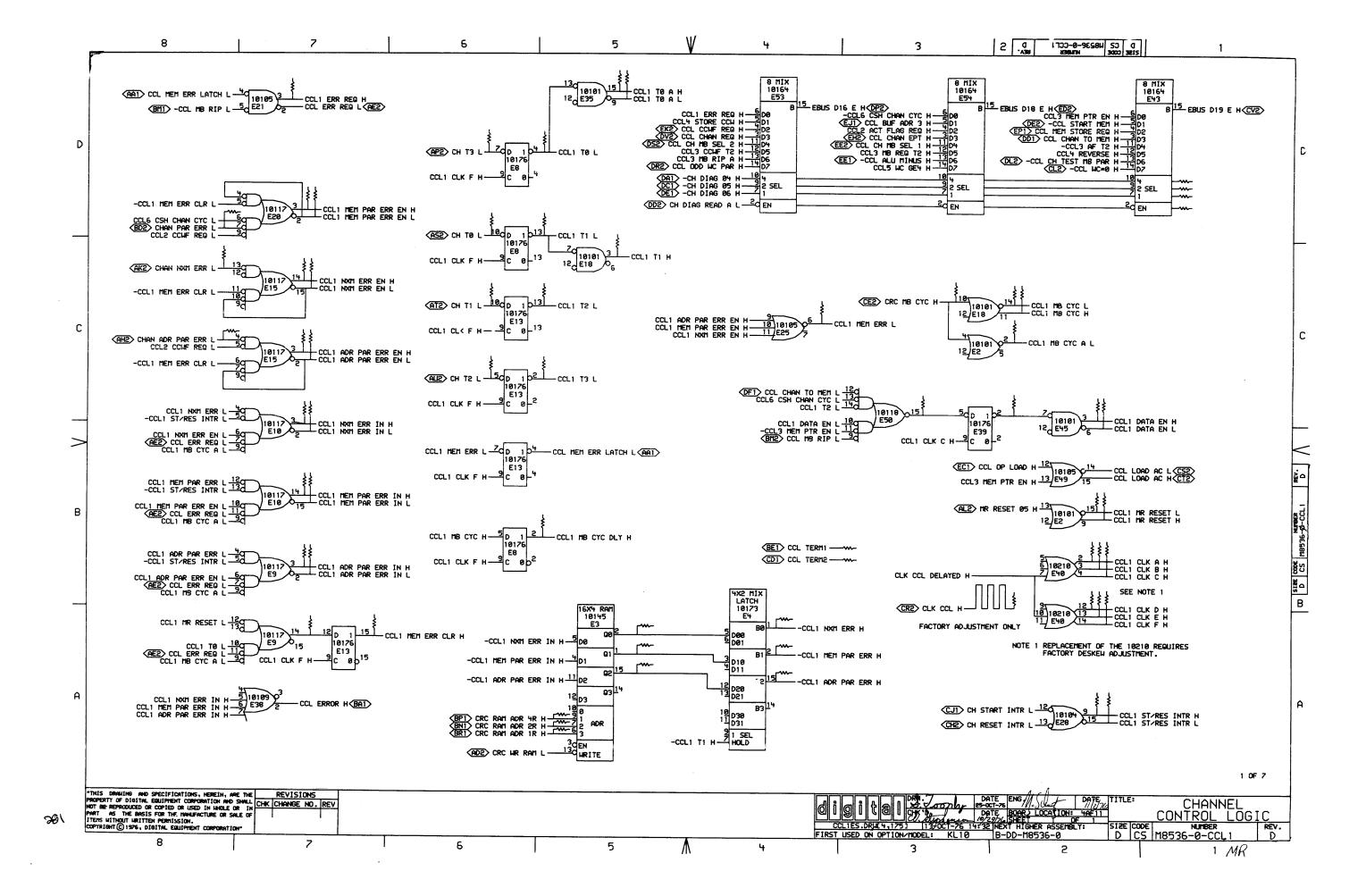
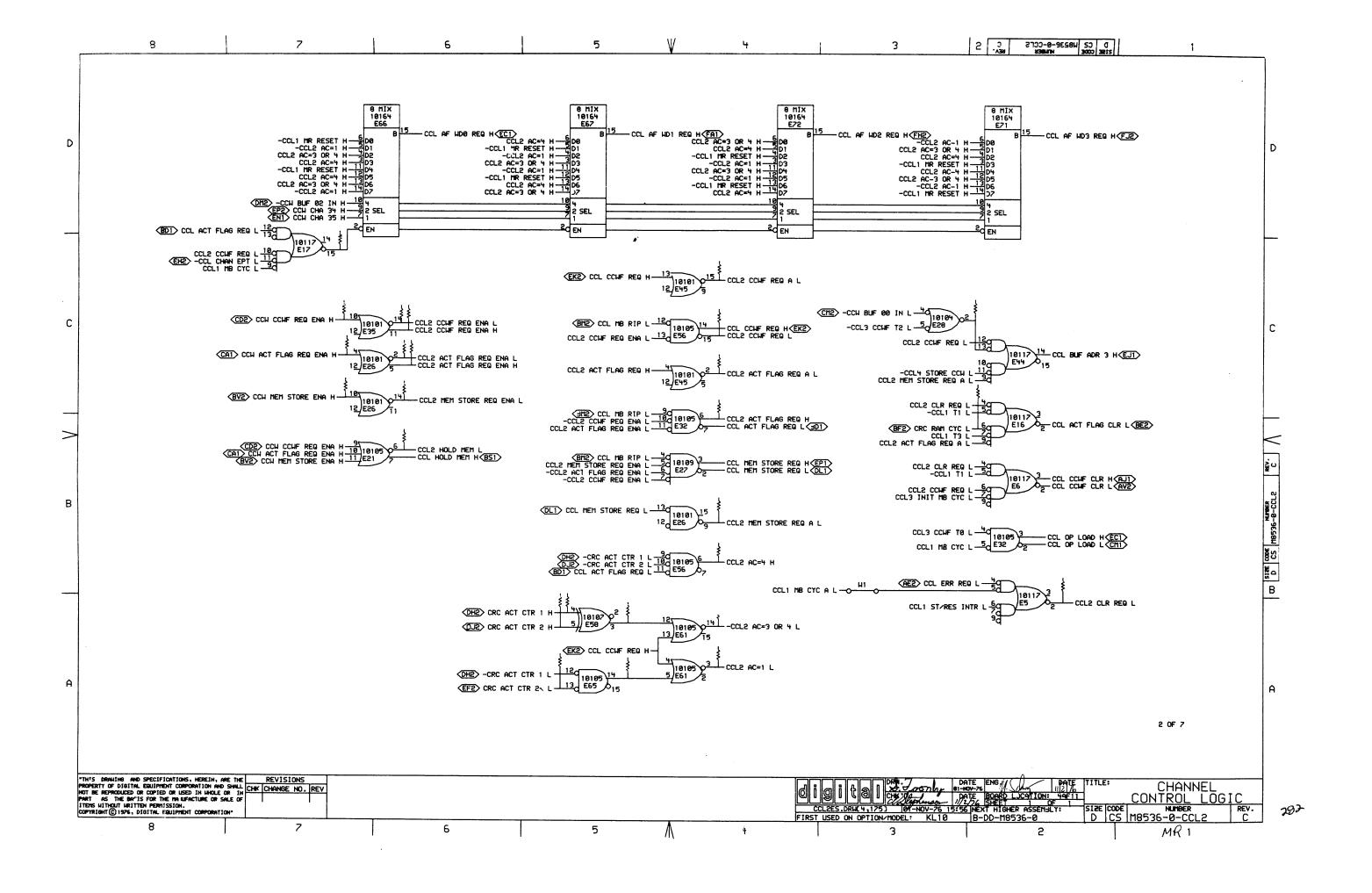
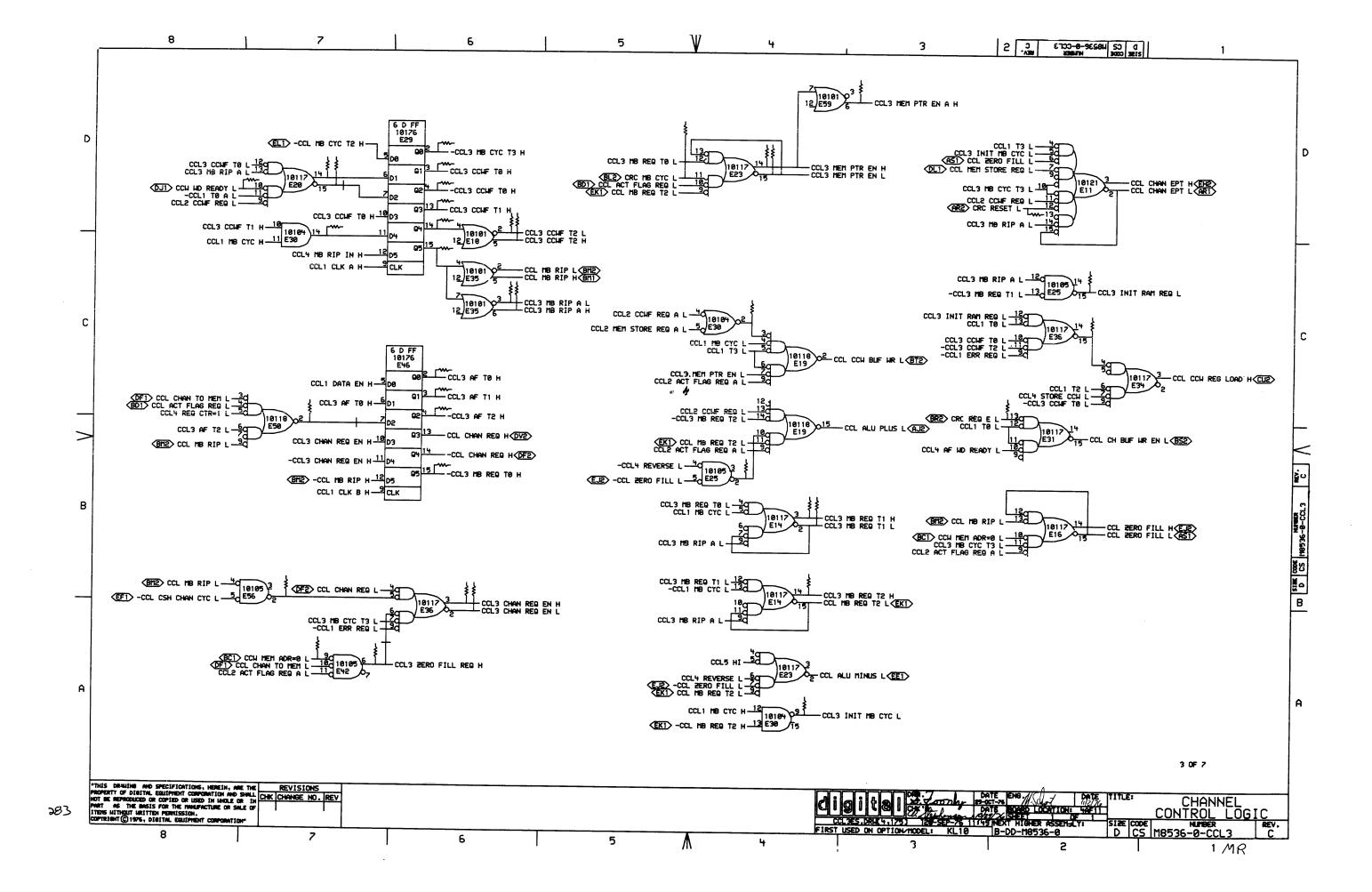
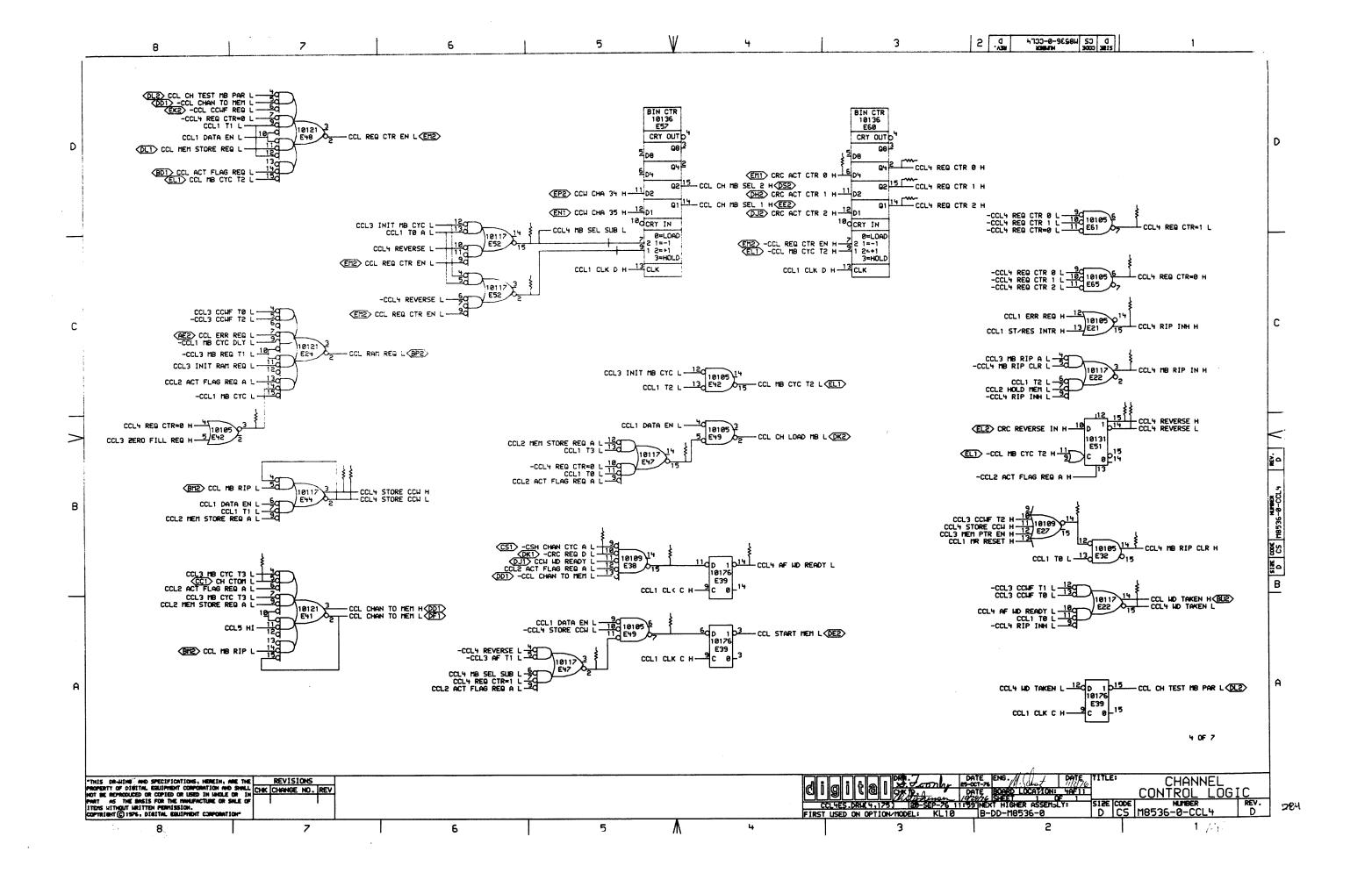
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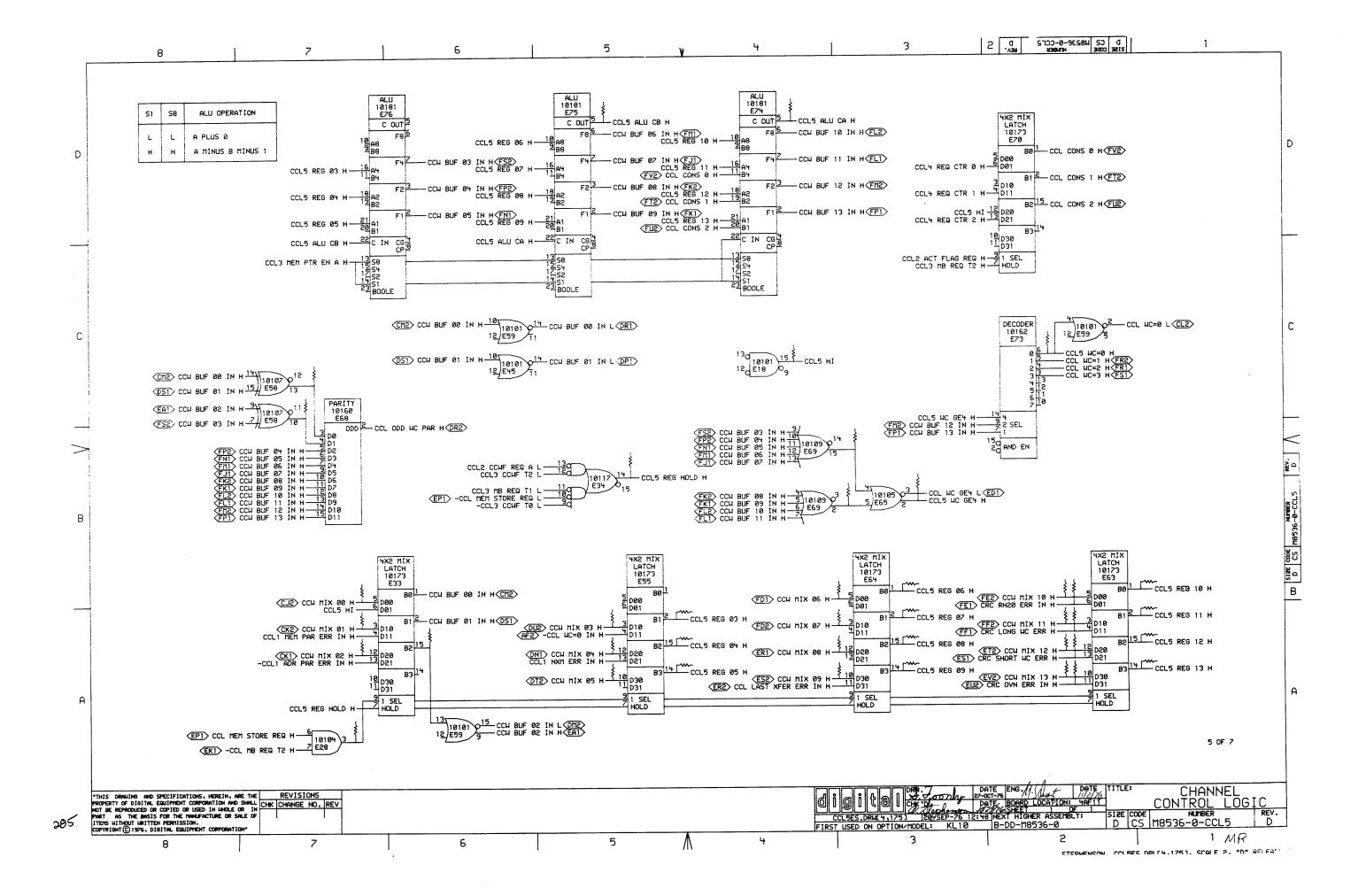


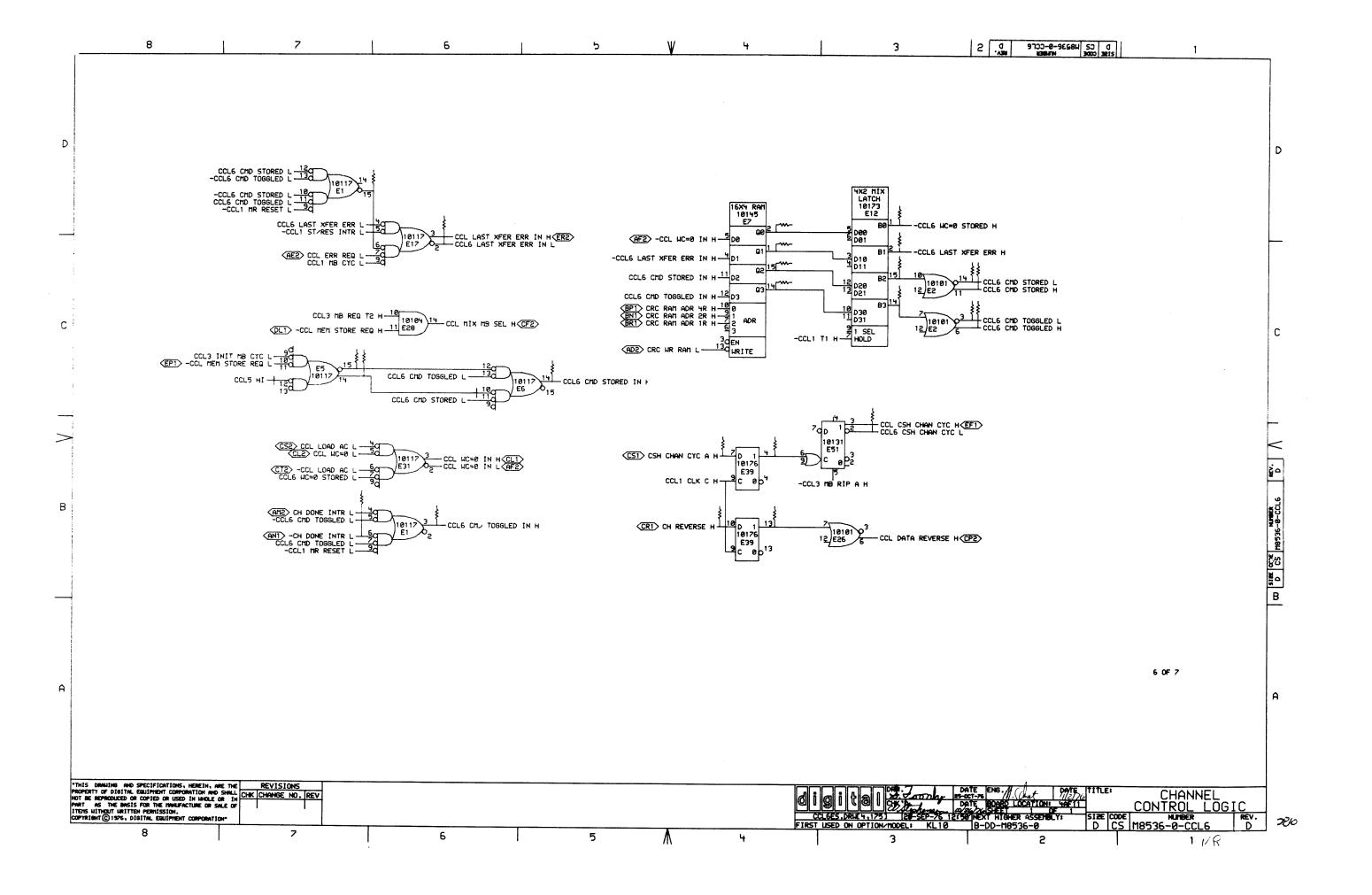


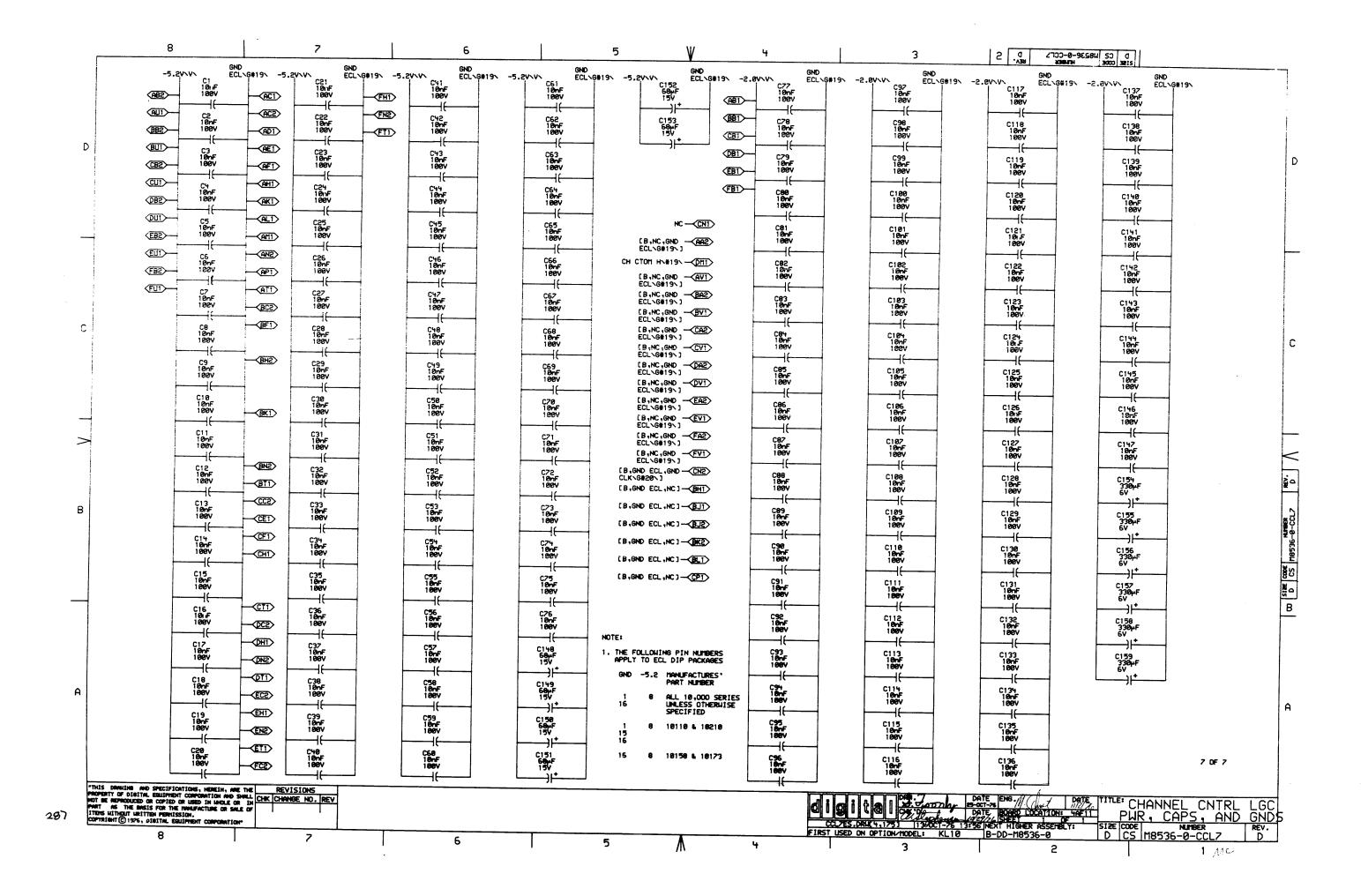












			- A38 N384FN 3000 3215
RESISTOR SHOWN ON VALUE TERMINATES	RESISTOR SHOWN ON VALUE TERMINATES	RESISTOR SHOWN ON VALUE TERMINATES	RESISTOR SHOWN ON VALUE TERMINATES
LOC(PIN) DRH# REF SIGNAL	LOC(PIN) DRW# REF SIGNAL	LOC(PIN) DRH# REF SIGNAL	LOC(PIN) DRH# REF SIGNAL
R140(1) CCL6 D6 58n %E1(15)	R138(1) CCL6 C4 680 %E7(14)	R28(1) CCL1 D5 68α -CCL1 T0 H	R80(1) CCL3 C6 68n -CCL3 MB RIP A H
R133(1) CCL6 C3 68n %E12(14) R132(1) CCL6 C3 68n %E12(15)	R137(1) CCL6 C4 68a %E7(15) R136(1) CCL6 D4 68a %E7(2)	R11(1) CCL1 D5 680 CCL1 T0 A H R34(1) CCL1 D5 680 -CCL1 T0 A H	R172(1) CCL3 D4 68α CCL3 MEM PTR EN H R55(1) CCL3 D4 68α -CCL3 MEM PTR EN H
R40(1) CCL2 C7 68n %E17(15)	R96(1) CCL1 A7 68n %E9(14)	R185(1) CCL1 C5 680 CCL1 T1 H	R45(1) CCL3 D3 680 CCL3 MEM PTR EN A H
R62(1) CCL3 D7 680 %E20(14)	R191(1) CCL1 B3 68n CCL TERM1	R203(1) CCL1 D5 680 -CCL1 T1 H	R69(1) CCL3 A7 680 CCL3 ZERO FILL REQ H
R63(1) CCL3 D7 680 %E20(15)	R196(1) CCL1 B3 680 CCL TERM2	R111(1) CCL1 C5 680 -CCL1 T2 H	R143(1) CCL4 B4 680 -CCL4 AF HD READY H
R51(1) CCL3 84 680 %E25(2)	R50(1) CCL1 A4 680 -CCL1 ADR PAR ERR H	R29(1) CCL1 C5 68n -CCL1 T3 H	R146(1) CCL4 B1 680 CCL4 MB RIP CLR H
R149(1) CCL4 B2 68n %E27(14)	R19(1) CCL1 C7 680 CCL1 ADR PAR ERR EN H	R39(1) CCL2 A4 680 -CCL2 AC=1 H	R57(1) CCL4 C1 680 CCL4 MB RIP IN H
R156(1) CCL2 C2 68n %E28(2)	R49(1) CCL1 C7 680 -CCL1 ADR PAR ERR EN H	R37(1) CCL2 A4 68x CCL2 AC=3 OR 4 H	R35(1) CCL4 C5 680 -CCL4 MB SEL SUB H
R210(1) CCL5 A7 68n %E28(3) R101(1) CCL3 D6 68n %E29(14)	R200(1) CCL1 B7 68α CCL1 ADR PAR ERR IN H R105(1) CCL1 B7 68α -CCL1 ADR PAR ERR IN H	R36(1) CCL2 B4 680 CCL2 AC=4 H R122(1) CCL2 B4 680 CCL2 ACT FLAG REQ H	R127(1) CCL4 D3 68Ω CCL4 REQ CTR Ø H R125(1) CCL4 D3 68Ω CCL4 REQ CTR 1 H
R26(1) CCL3 C6 680 %E29(15)	R58(1) CCL1 B2 68n CCL1 CLK A H	R79(1) CCL2 C4 680 -CCL2 ACT FLAG REQ A H	R125(1) CCL4 D3 68a CCL4 REQ CTR 1 H R120(1) CCL4 D3 68a CCL4 REQ CTR 2 H
R46(1) CCL1 A5 680 %E3(1)	R73(1) CCL1 B2 68a CCL1 CLK B H	R150(1) CCL2 C6 680 CCL2 ACT FLAG REQ ENA H	R204(1) CCL4 C1 680 CCL4 REQ CTR=0 H
R47(1) CCL1 A5 680 %E3(15)	R157(1) CCL1 B2 680 CCL1 CLK C H	R152(1) CCL2 C6 680 -CCL2 ACT FLAG REQ ENA H	R30(1) CCL4 D1 680 -CCL4 REQ CTR=1 H
R48(1) CCL1 A5 680 %E3(2)	R33(1) CCL1 B2 680 CCL1 CLK D H	R13(1) CCL2 C4 680 -CCL2 CCWF REQ H	R22(1) CCL4 B1 680 CCL4 REVERSE H
R59(1) CCL3 D7 68n %E30(14)	R109(1) CCL1 B2 680 CCL1 CLK E H	R25(1) CCL2 C4 680 -CCL2 CCWF REQ A H	R103(1) CCL4 B1 680 -CCL4 REVERSE H
R54(1) CCL3 C4 68n %E30(2)	R98(1) CCL1 B2 68n CCL1 CLK F H	R151(1) CCL2 C6 680 CCL2 CCHF REQ ENA H	R142(1) CCL4 C1 680 CCL4 RIP INH H
R171(1) CCL5 A6 680 %E33(15)	R77(1) CCL1 C2 680 CCL1 DATA EN H	R78(1) CCL2 C6 680 -CCL2 CCHF REQ ENA H	R148(1) CCL4 B7 680 CCL4 STORE CCH H
R66(1) CCL3 C2 68n %E36(15)	R202(1) CCL1 C2 680 -CCL1 DATA EN H	R189(1) CCL2 B2 680 -CCL2 CLR REQ H	R67(1) CCL4 B7 680 -CCL4 STORE CCH H
R161(1) CCL4 B5 68n %E38(15) R195(1) CCL6 B4 68n %E39(13)	R68(1) CCL1 D7 68Ω CCL1 ERR REQ H	R147(1) CCL2 B6 68a -CCL2 HOLD MEM H	R154(1) CCL4 B1 680 -CCL4 HD TAKEN H
R195(1) CCL6 B4 680 %E39(13) R110(1) CCL1 C2 680 %E39(2)	R21(1) CCL1 C2 68a CCL1 MB CYC H R153(1) CCL1 C2 68a -CCL1 MB CYC H	R162(1) CCL2 B4 680 -CCL2 MEM STORE REQ A H R144(1) CCL2 C6 680 -CCL2 MEM STORE REQ ENA H	R86(1) CCL5 D4 68Ω CCL5 ALU CA H R42(1) CCL5 D5 68Ω CCL5 ALU CB H
R81(1) CCL6 B4 68n %E39(4)	R5(1) CCL1 C2 68n -CCL1 MB CYC A H	R76(1) CCL3 C6 68α CCL3 AF T0 H	R121(1) CCL5 D5 680 CCL5 ALU CB H R121(1) CCL5 C4 680 CCL5 HÎ
R53(1) CCL4 B7 680 %E42(3)	R56(1) CCL1 B5 68a CCL1 MB CYC DLY H	R31(1) CCL3 C6 68n CCL3 AF T1 H	R44(1) CCL5 A5 680 CCL5 REG 03 H
R164(1) CCL4 B4 680 %E47(15)	R99(1) CCL1 C4 680 -CCL1 MEM ERR H	R198(1) CCL3 C6 680 -CCL3 AF T2 H	R43(1) CCL5 A5 68a CCL5 REG 04 H
R163(!) CCL4 A5 680 %E47(2)	R17(1) CCL1 A7 680 CCL1 MEM ERR CLR H	R64(1) CCL3 D6 680 CCL3 CCWF T0 H	R41(1) CCL5 A5 680 CCL5 REG 05 H
R160(1) CCL4 A5 68a %E49(7)	R1(1) CCL1 A4 680 -CCL1 MEM PAR ERR H	R10(1) CCL3 D6 680 -CCL3 CCHF T0 H	R123(1) CCL5 B3 68a CCL5 REG 06 H
R184(1) CCL6 C7 68n %E5(14)	R28(1) CCL1 D7 680 CCL1 MEM PAR ERR EN H	R139(1) CCL3 D6 68Ω CCL3 CCHF T1 H	R124(1) CCL5 A3 68n CCL5 REG 87 H
R179(1) CCL6 C7 680 %E5(15)	R8(1) CCL1 D7 680 -CCL1 MEM PAR ERR EN H	R205(1) CCL3 D6 68Ω CCL3 CCWF T2 H	R88(1) CCL5 A3 680 CCL5 REG 08 H
R158(1) CCL1 C3 68n %E58(15)	R199(1) CCL1 B7 680 CCL1 MEM PAR ERR IN H	R60(1) CCL3 D6 68n -CCL3 CCWF T2 H	R87(1) CCL5 A3 68Ω CCL5 REG 09 H
R75(1) CCL3 B7 68n %E50(2) R32(1) CCL4 C5 68n %E52(2)	R89(1) CCL1 B7 68α -CCL1 MEM PAR ERR IN H R145(1) CCL1 B2 68α CCL1 MR RESET H	R74(1) CCL3 B6 680 CCL3 CHAN REQ EN H R71(1) CCL3 B6 680 -CCL3 CHAN REQ EN H	R176(1) CCL5 B1 68a CCL5 REG 10 H R177(1) CCL5 A1 68a CCL5 REG 11 H
R79(1) CCL3 B7 68a %E56(2)	R38(1) CCL1 B2 680 -CCL1 MR RESET H	R71(1) CCL3 B6 680 -CCL3 CHAN REQ EN H R4(1) CCL3 A4 680 -CCL3 INIT MB CYC H	R177(1) CCL5 A1 680 CCL5 REG 11 H R178(1) CCL5 A1 680 CCL5 REG 12 H
R218(1) CCL5 C7 680 %E58(10)	R6(1) CCL1 A4 680 -CCL1 NXM ERR H	R65(1) CCL3 C2 680 -CCL3 INIT RAM REQ H	R128(1) CCL5 A1 680 CCL5 REG 13 H
R219(1) CCL5 C7 68n %E58(13)	R15(1) CCL1 C7 68a CCL1 NXM ERR EN H	R187(1) CCL3 D6 680 -CCL3 MB CYC T3 H	R215(1) CCL5 B5 68a CCL5 REG HOLD H
R84(1) CCL2 A5 68n %E58(3)	R7(1) CCL1 C7 680 -CCL1 NXM ERR EN H	R52(1) CCL3 B6 68Ω -CCL3 MB REQ TØ H	R217(1) CCL5 B3 68n CCL5 HC GE4 H
R85(1) CCL2 A5 68a %E65(14)	R114(1) CCL1 C7 680 CCL1 NXM ERR IN H	R23(1) CCL3 B4 68n CCL3 MB REQ T1 H	R170(1) CCL5 C2 68Ω CCL5 HC=0 H
R117(1) CCL5 B3 68n %E69(15)	R91(1) CCL1 C7 680 -CCL1 NXM ERR IN H	R61(1) CCL3 B4 68n -CCL3 MB REQ T1 H	R182(1) CCL6 C2 68n CCL6 CMD STORED H
R119(1) CCL5 B3 68n %E69(2)	R2(1) CCL1 A2 68a CCL1 ST/RES INTR H	R126(1) CCL3 B4 680 CCL3 MB REQ T2 H	R180(1) CCL6 C2 680 -CCL6 CMD STORED H
R135(1) CCL6 C4 680 %E7(1)	R3(1) CCL1 A2 68Ω -CCL1 ST∕RES INTR H	R206(1) CCL3 C6 680 CCL3 MB RIP A H	R134(1) CCL6 C5 68n CCL6 CMD STORED IN H
NOTE:			
1. ALL TERMINATORS HAVE PIN THO CONNECTED TO	-2.8V AND		
ARE 5% 1/HIATT UNLESS OTHERHISE SPECIFIED 2. ENTRIES ARE SORTED BY SIGNAL NAME	LIOV MID		
3. % INDICATES OUTPUT OF DIP LOC AND			
() INDICATES PIN NUMBER			
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S WITHOUT WRITTEN PERMISSION. RIGHT © 1976, DIGITAL EQUIPMENT COMPORATION"		\$85361.DRU(4,175) [2560CT-76.1]	7:23 NEXT HIGHER ASSEMBLY: SIZE CODE NUMBER B-DD-M8536-0 D CS M8536-0-RES
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0 CC 48236-0-RES C C S 2125 CODE MINDER

8 7 6 5 D C2 48239-9-6E2 2355 0005 3 2 .3 REA. S 1 D RESISTOR SHOWN ON VALUE LOC(PIN) DRW# REF TERMINATES RESISTOR SHOWN ON LOC(PIN) DRW# REF TERMINATES VALUE R181(1) CCL6 C2 68Ω CCL6 CMD TOGGLED H R108(1) CCL1 B2 CLK CCL H R186(1) CCL6 -CCL6 CMD TOGGLED H R115(1) CCL4 D3 68a CRC ACT CTR 8 H R129(1) CCL6 68₽ CCL6 CMD TOGGLED IN H R82(1) CCL2 A5 680 CRC ACT CTR 1 H R16(1) CCL6 B3 68Ω -CCL6 CSH CHAN CYC H R83(1) CCL2 A5 68Ω CRC ACT CTR 2 H R141(1) CCL6 C3 -CCL6 LAST XFER ERR H R118(1) CCL2 68Ω -CRC ACT CTR 2R H CCL6 D6 -CCL6 LAST XFER ERR IN H R213(1) CCL5 A2 68Ω CRC LONG HC ERR H R197(1) -CCL6 NC=0 STORED H R131(1) CCL1 C3 68n CRC MB CYC H R193(1) CCLS C7 CCH ACT FLAG REQ ENA H R102(1) CCL3 D5 680 -CRC MB CYC H R24(1) CCL2 CZ CCW CCWF REQ ENA H R208(1) A2 68Ω CRC OVN ERR IN H R27(1) CCL3 68Ω 87 -CCW MEM ADR=0 H R98(1) CRC RAM ADR 1R H R192(1) CCLS **C**7 68Ω CCW MEM STORE ENA H R93(1) CCL1 R107(1) CCL5 B7 CCM MIX 88 H R92(1) CCL 1 85 R186(1) CCL5 A7 68Ω CCW MIX 01 H R190(1) CCL2 C5 -CRC RAM CYC H R104(1) CCL5 AZ 680 CCM MIX 02 H R201(1) CCL4 85 CRC REQ D H C R116(1) CCL5 A5 68Ω CCM MIX 83 H R194(1) CCL3 C2 -CRC REQ E H CCL5 A5 R112(1) CCM MIX 84 H R188(1) CCL3 D2 58g -CRC RESET H R113(1) CCL5 A5 CCW MIX 05 H R216(1) CCL5 B2 689 CRC RH20 ERR IN H R175(1) CCL5 B3 CCW MIX 06 H R209(1) CCL5 A2 68g CRC SHORT HC ERR H R174(1) CCL5 63 680 CCU MIX 82 H R159(1) CCL6 B4 68p CSH CHAN CYC A H R169(1) CCL5 A3 680 CCU MIX 98 H R173(1) CCL5 A3 CCM MIX 09 H R214(1) CCL5 B2 CCH MIX 10 H R212(1) CCL5 A2 68₀ CCW MIX 11 H R207(1) CCL5 A2 68Ω CCM MIX 15 H \geq R211(1) CCL5 A2 680 CCU MIX 13 H CCL3 D7 -CCW WD READY H R72(1) CCL4 B2 -CH CTOM H R165(1) CCL1 D1 -CH DIAG 04 H R166(1) CCL1 D1 680 -CH DIAG 25 H R168(1) CCL1 D1 -CH DIAG Ø6 H В R167(1) CCL1 D1 -CH DIAG READ A H R183(1) CCL6 B7 68Ω CH DONE INTR H R155(1) CCL6 84 68Ω CH REVERSE H R94(1) CCL1 D6 68α -CH TØ H SIRE CS R95(1) CCL1 -CH T1 H R100(1) CCL1 C6 -CH TZ H CCL1 D6 68Ω R97(1) -CH T3 H В K12(1) CCL1 C8 68g -CHAN ADR PAR ERR H R9(1) CCL1 C8 68Ω -CHAN NXM ERR H R18(1) CCL1 D8 68n -CHAN PAR ERR H NOTE: () INDICATES PIN NUMBER "THIS DRAWING AND SPECIFICATIONS, HEREIM, ARE THE REVISIONS PROPERTY OF DIGITAL EQUIPMENT CORPORATION AND SWALL CHK CHANGE NO. REV NOT BE REPRODUCED OR COPIED OR USED IN HADLE OR IN PART AS THE BASIS FOR THE MANUFACTURE OR SALE OF ITEMS WITHOUT WRITTEN PERMISSION.

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LOGIC TERMINATORS

NUMBER | REV. SIZE CODE NUMBER
D CS M8536-0-RES 8 5 3

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