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D

D

C

C

B

B

A

A

A	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
B	BCC22C															
C	(6000)															
D	BCC22C	M	M	M	M	M	M	M	M	M	S A	M	M	M	M	
E	(6000)	SHARP														
F																

REV.

NUMBER

SIZE CODE

CMU

KL1Ø-Ø-BAYI

FIRST USED ON OPTION/MODEL		QTY.	DESCRIPTION		PART NO.	ITEM NO.
KL1Ø			PARTS LIST			
DIMENSIONAL TOLERANCE			DRN	DATE		
DIMENSIONS ARE MILLIMETERS UNLESS OTHERWISE SPECIFIED			CHKD	DATE		
MILLIMETERS	INCHES	ANGLES	ENG	DATE		
XXX = ±0,10	XXX = ±.006	±0° 30'	PROJ. ENG.	DATE		
XX = ±0,5	XX = ±.02		PROD.	DATE		
X = ±2,	X = ±.1					
THIRD ANGLE PROJECTION	REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY ✓		NEXT HIGHER ASSY.			
	MATERIAL	---	D-U-A-KL1Ø-Ø-Ø	SIZE CODE	NUMBER	REV.
	FINISH	---	SCALE ---	CMU	KL1Ø-Ø-BAYI	
			SHEET 1 OF 1	DIST.		

REVISIONS	CHANGE NO.	REV.
CHK		

4

3

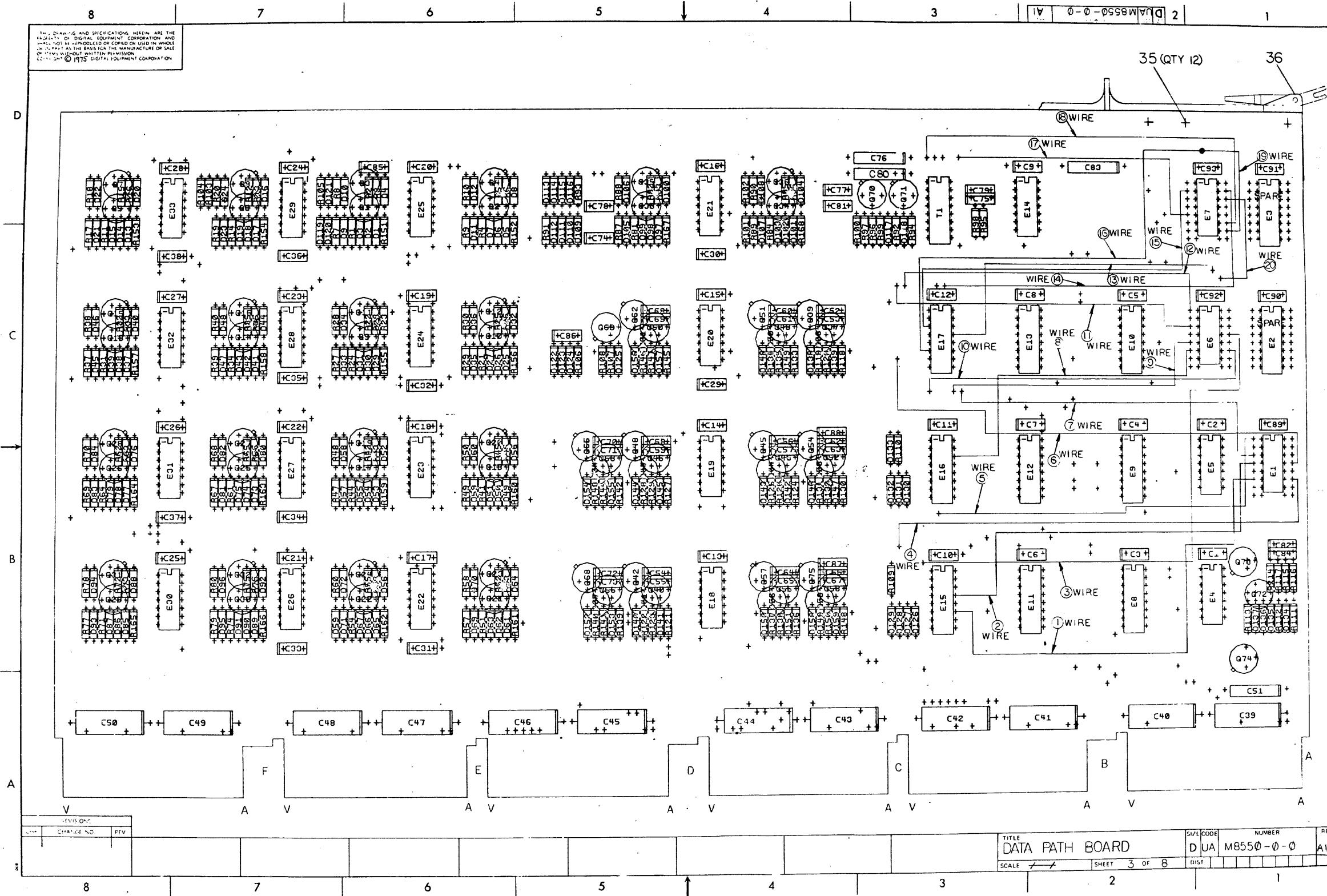
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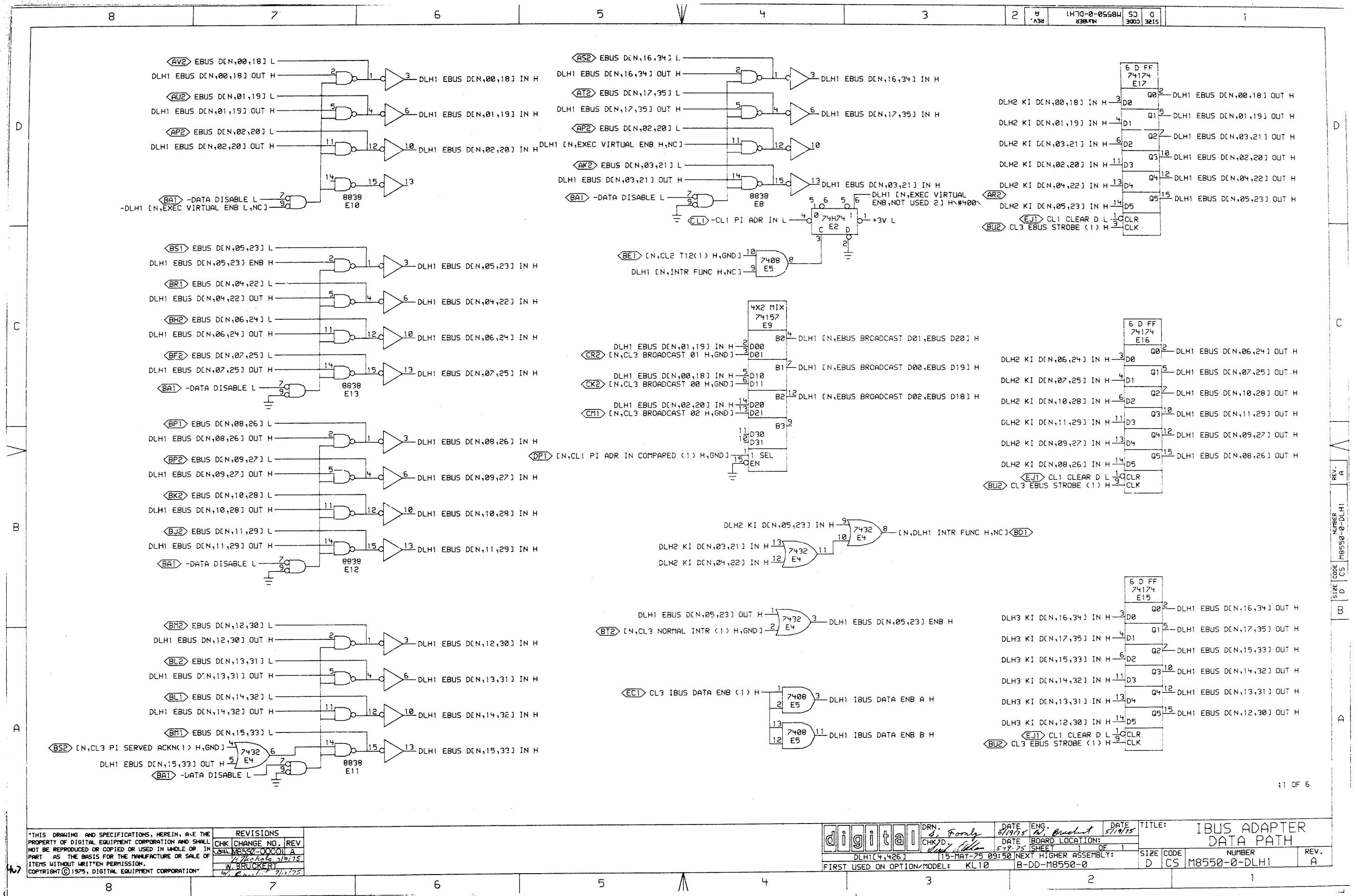
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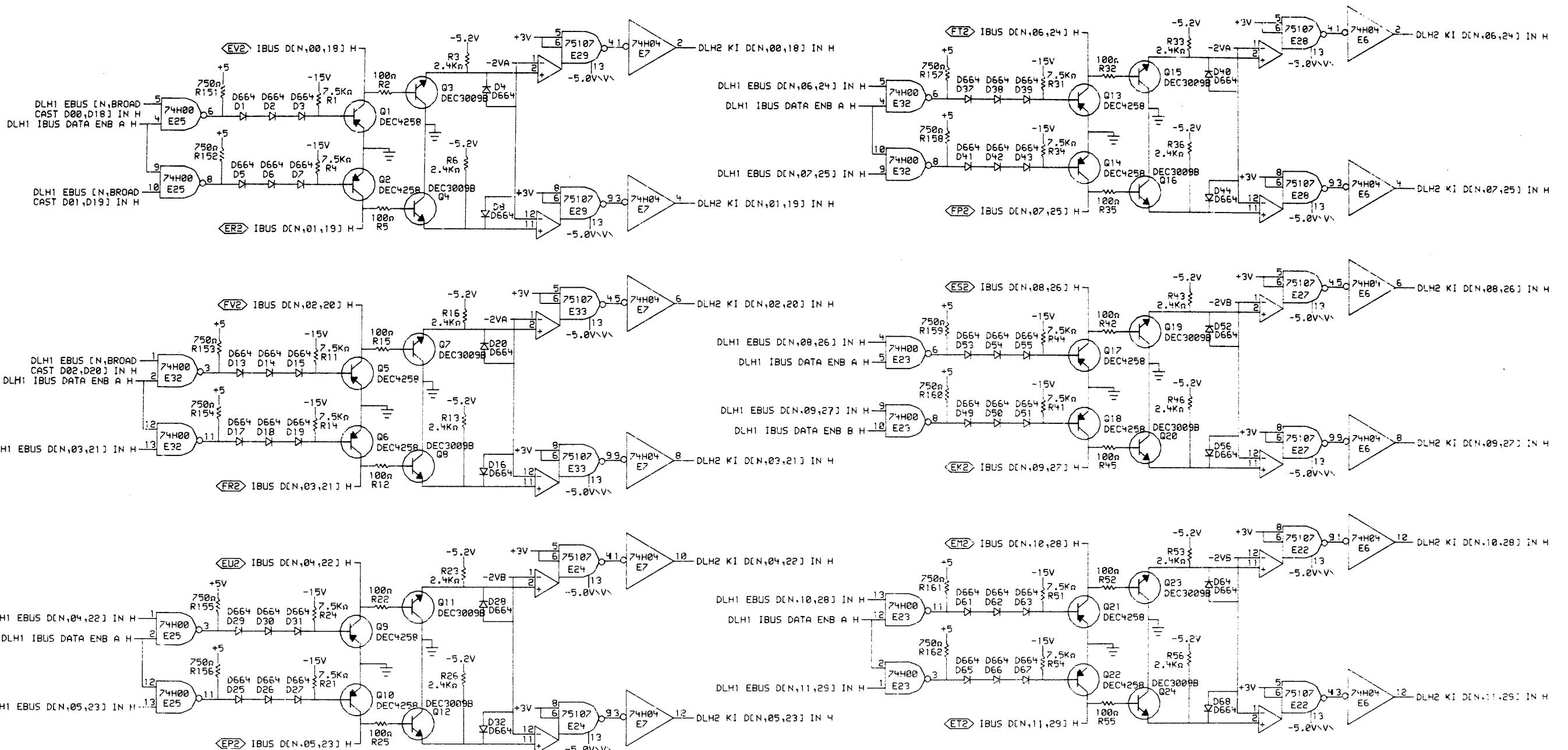
464

CUSTOMER PRINT SET	MFG SET	REVISION CONTROL SHEET			
		DRAWING NO	NO OF SHT	DESCRIPTION	OPTION NO/FILE DATE
		-		MODULE REVISION	A B E
		D-UA-M8550-0-0	8	DATA PATH BOARD	- A AI
		D-CS-M8550-0-DLH1	1	IBUS ADAPTER DATA PATH	- A A
		D-CS-M8550-0-DLH2	1	IBUS ADAPTER DATA PATH	- A A
		D-CS-M8550-0-DLH3	1	IBUS ADAPTER DATA PATH	- A A
		D-CS-M8550-0-DLH4	1	IBUS ADAPTER DATA PATH	- A A
		D-CS-M8550-0-DLH5	1	IBUS ADAPTER DATA PATH	- A A
		D-CS-M8550-0-DLH6	1	IBUS ADAPTER POWER AND GROUND	- A A
		K-CO-M8550-0-4	1	DATA PATH BOARD (CALDEC DATA BASE)	B B R
		D-AH-M8550-0-5	4	DATA PATH BOARD	A A A
		B-MH-M8550-0-6	1	MODULE ECO HISTORY	- - -
		5010519 P00-M8550-00	-	ETCH CIRCUIT BOARD PROCESS SHEET (REF ONLY)	B B - - NEW LAYOUT REF SHT 3
CUSTOMER PRINT SET CODES	X = PRINT OF DOCUMENT INCLUDED IN PRINT SET C = INCLUDES ALL PRINTS INDICATED ON DOCUMENT S = CONFIDENTIAL AUTHORIZED SIGNATURE REQUIRED			ECO NO ORIG 00001 00002 00003	
465				TITLE DATA PATH BOARD	SHEET 2 OF 3 B DD M8550-0 REV C

NJR





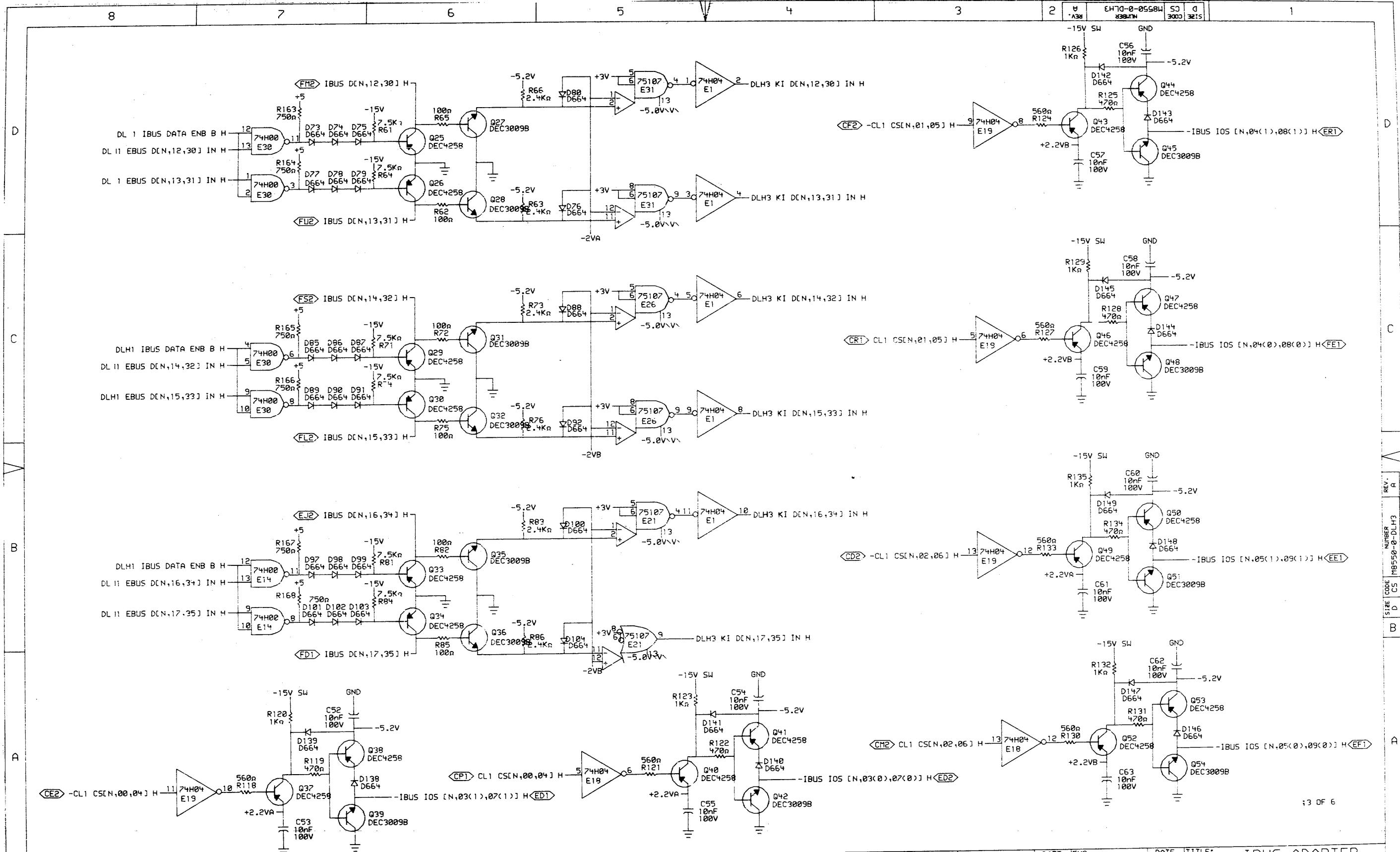


12 OF 6

REVISIONS

HK	CHANGE NO.	R
M9550-00001		
S. Nichols 7-9		
W. BRUCKERT		

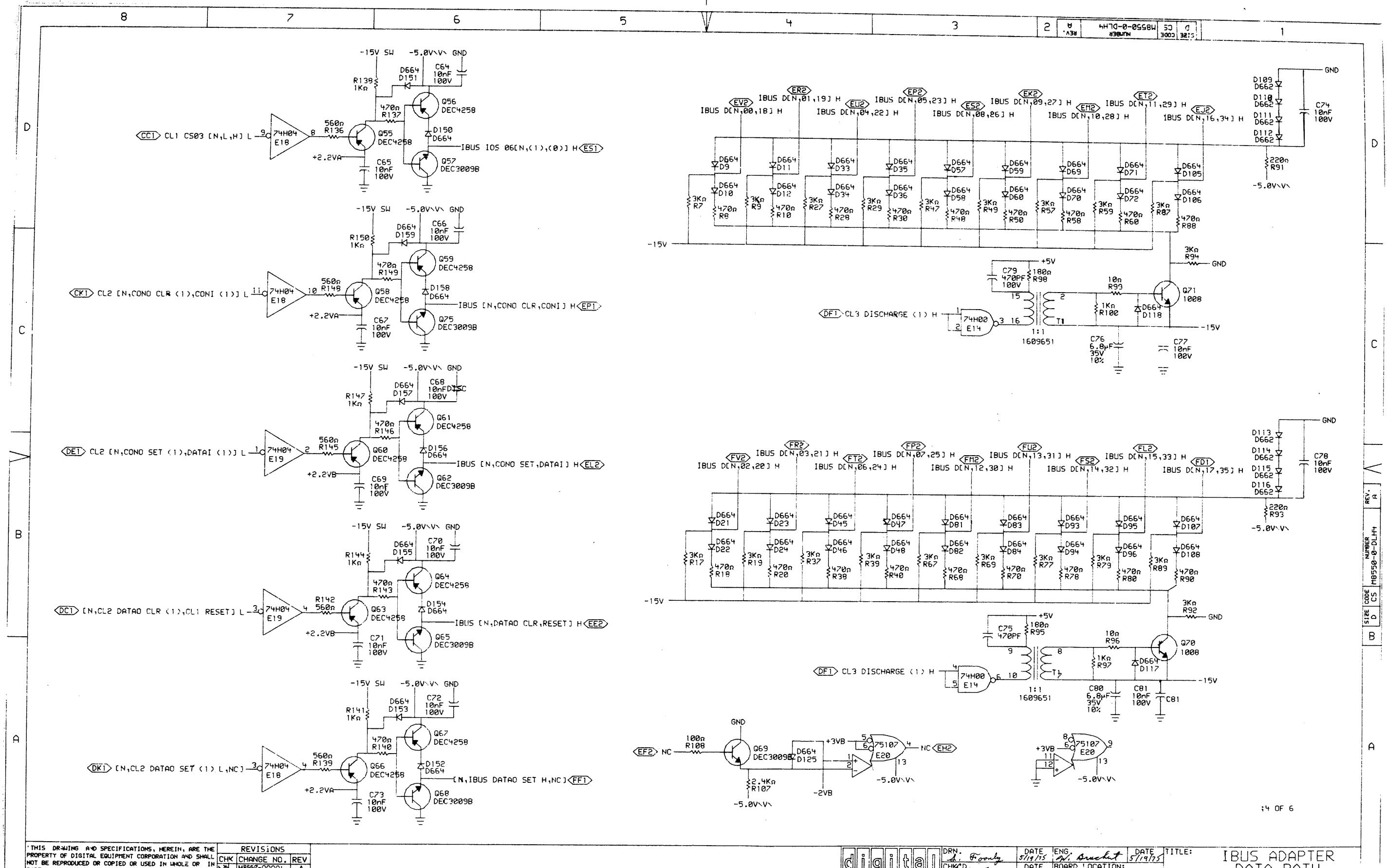
digital	DRN. <u>J. Foonly</u>	DATE <u>5/19/75</u>	ENG. <u>Mr. Bushell</u>	DATE <u>5/19/75</u>	TITLE: IBUS ADAPTER DATA PATH		
CHGD	DATE <u>5-19-75</u>	BOARD LOCATION:		SHEET <u>1</u> OF <u>1</u>			
DLH2C4,426J		NEXT HIGHER ASSEMBLY:		SIZE <u>D</u>	CODE <u>CS</u>	NUMBER <u>M8550-0-DLH2</u>	REV. <u>A</u>
FIRST USED ON OPTION/MODEL: KL10 B-DD-M8550-0							



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THE HALL IN E OF	RE
CHIK	CH
	M.G.
	B
	W.B.

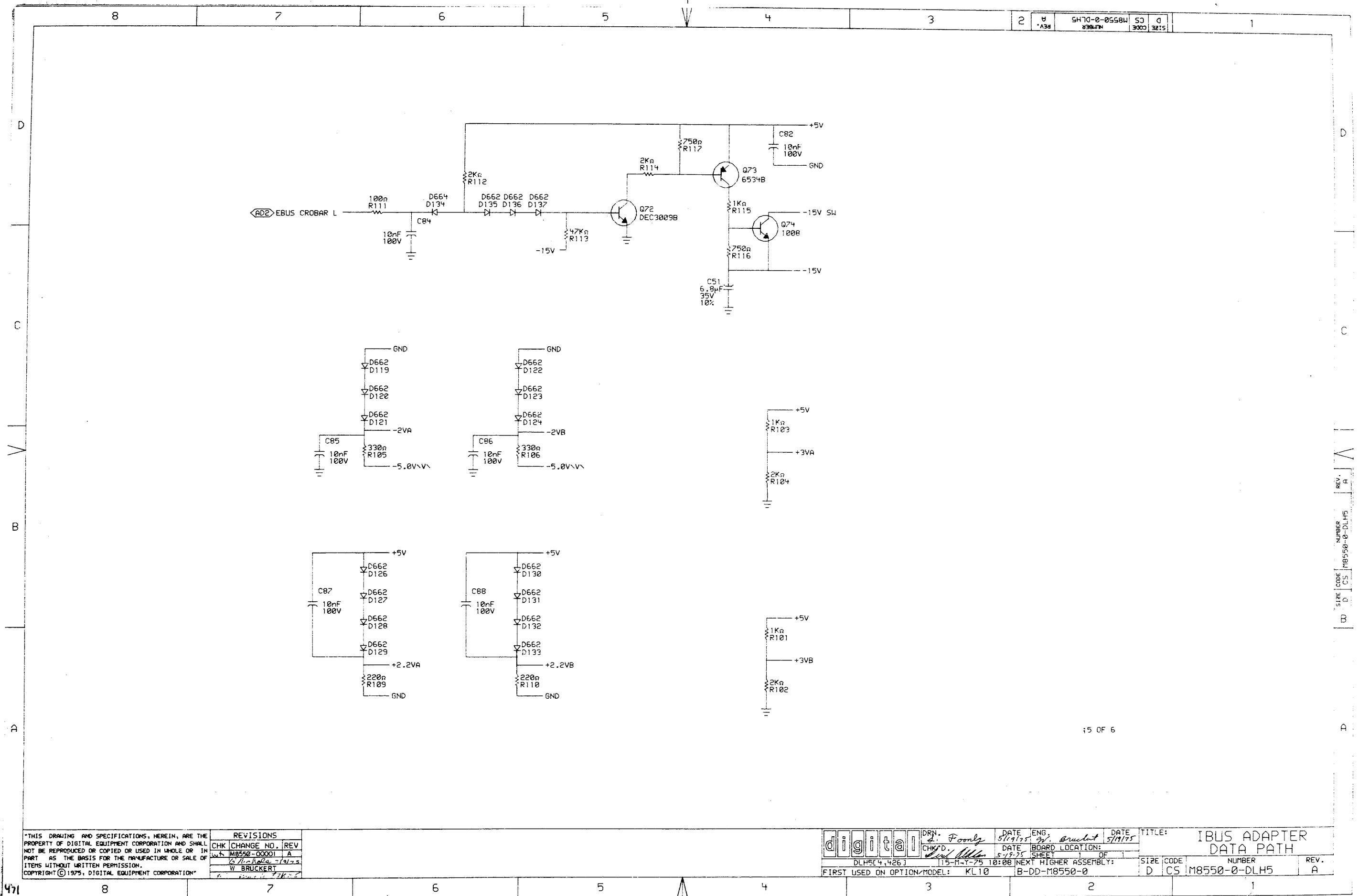
digital	DRN- J. Foomly	DATE 5/19/75	ENG W. Crandall	DATE 5/19/75	TITLE: I BUS ADAPTER DATA PATH	
CHK'D. C. J. C.	DATE 5-19-75	BOARD LOCATION: SHEET 1 OF 1				
DLH3C4,4263	15-MAY-75 09:52	NEXT HIGHER ASSEMBLY: B-DD-M8550-0	SIZE D	CODE CS	NUMBER M8550-0-DLH3	REV. A
FIRST USED ON OPTION/MODEL: KL10						

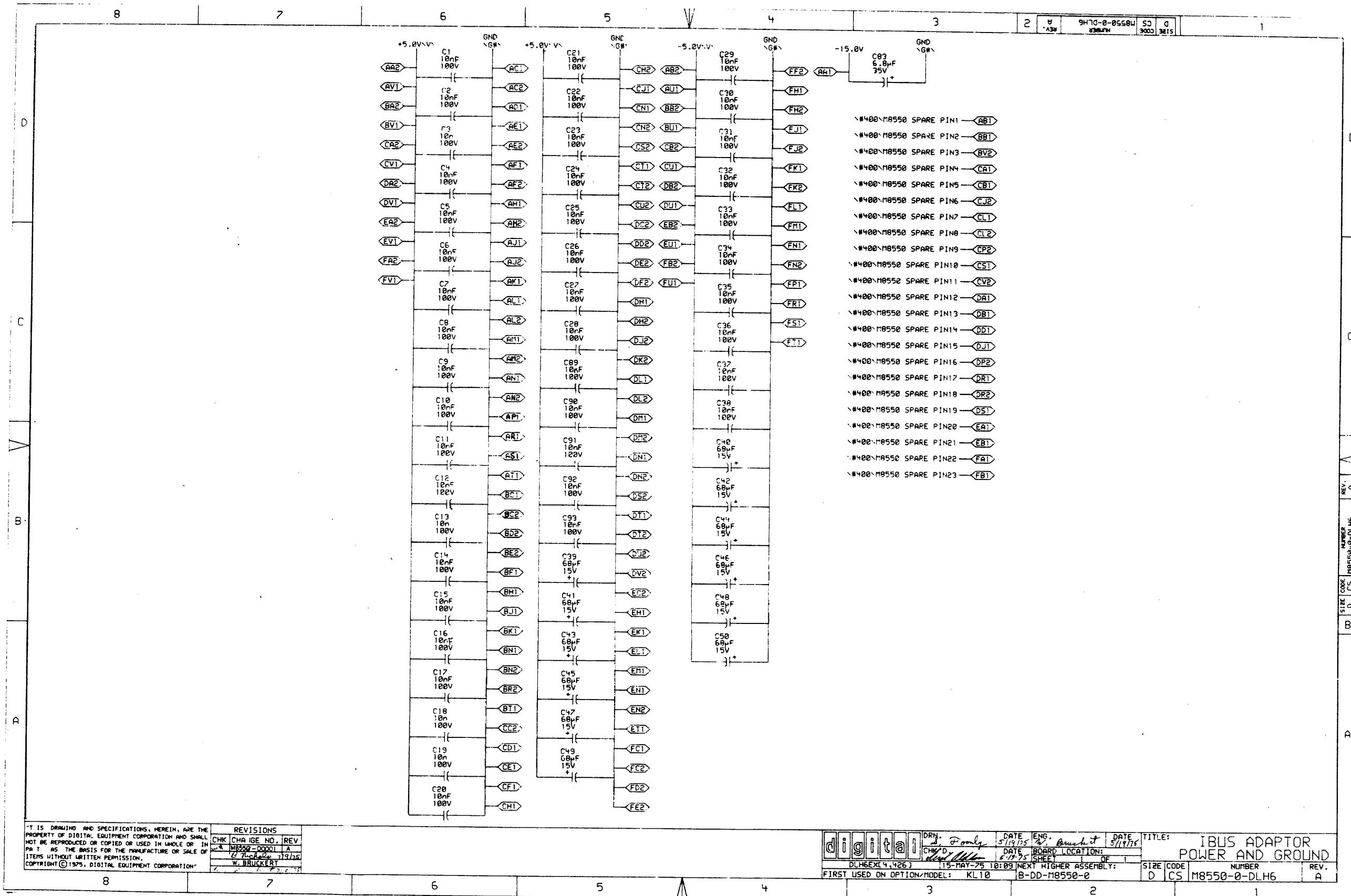


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REVISION	
CHK	CHANGE NO.
W.H.	M8550-0000
S. Nichols	
W. BRUCKER	

digitaI	DRN. <i>51975</i>	DATE 5/19/75	ENG. <i>ni buckt</i>	DATE 5/19/75	TITLE: I BUS ADAPTER DATA PATH
CHGD. <i>See Below</i>	DATE 5-19-75	BOARD LOCATION:	SHEET 1	OF 1	
DLH4[4,426]	15-M-Y-75 10:07	NEXT HIGHER ASSEMBLY:			
FIRST USED ON OPTION/MODEL:	KL10	B-DD-M8550-0			





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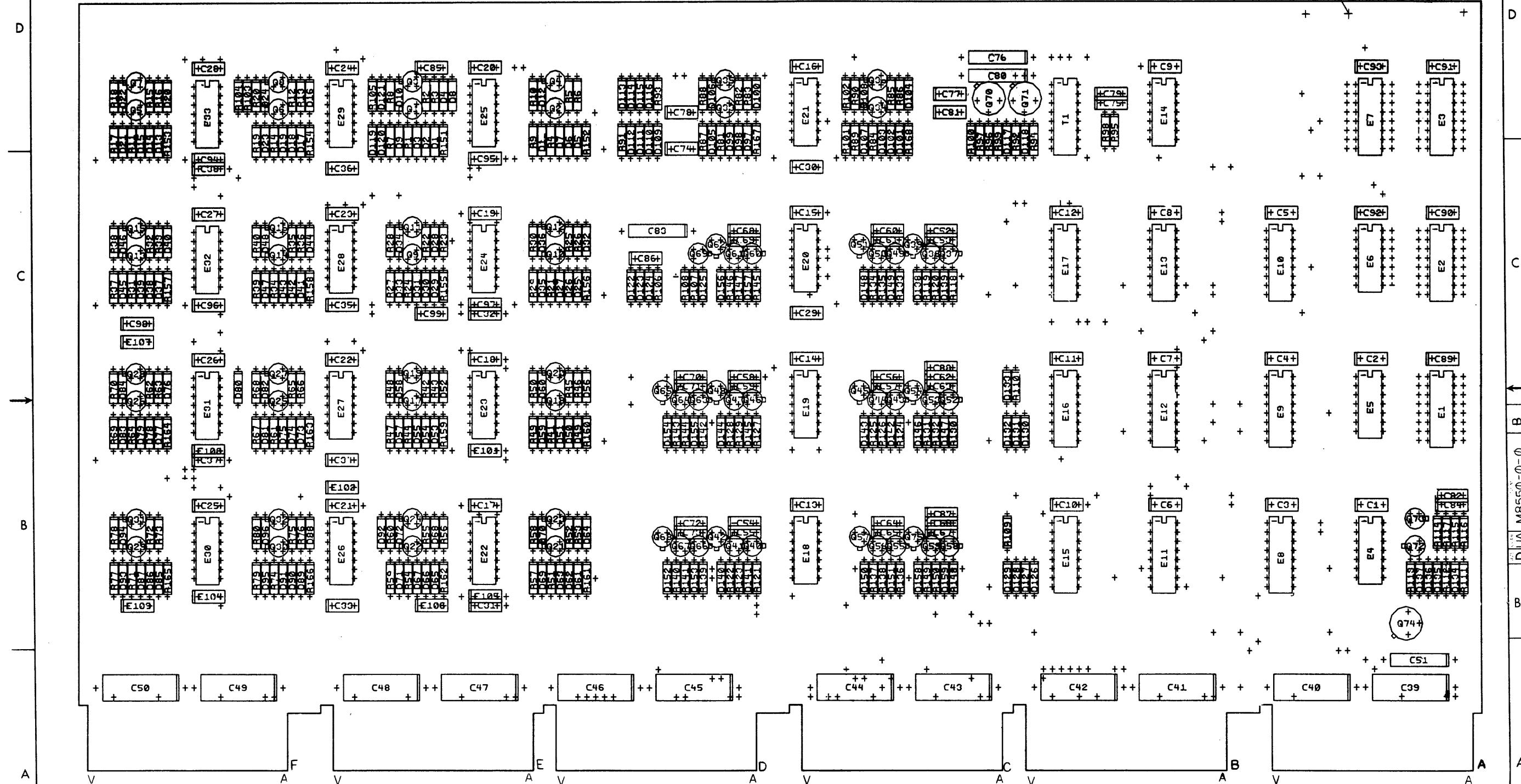
REVISIONS		
CHK	CHANGE NO.	REV
M8550-00001	A	
W. BRUCKERT	7-19-75	

DRN:	DATE:	ENG.:	DATE:
Fonly	5/19/75	Bruce J	5/19/75
CHG'D:	DATE:	BOARD LOCATION:	
W. BRUCKERT	5/19/75	SHEET 1 OF 1	
DL46EX(4,426)		NEXT HIGHER ASSEMBLY:	
15-MAY-75 10:09		SIZE CODE NUMBER REV.	
FIRST USED ON OPTION/MODEL: KL10		D CS M8550-0-DLH6 A	

CUSTOMER PRINT SET						REVISION CONTROL SHEET												
MFG SET	DRAWING NO	NO OF SHT	DESCRIPTION	OPTION NO/FILE DATE	REVISIONS													
			MODULE REVISION		C													
	D-UA-M8550-0-0	6	DATA PATH BD		B													
	D-CS-M8550-0-DLHI	1	BUS ADAPTER DATA PATH		B													
	D-CS-M8550-0-DLH2	1	BUS ADAPTER DATA PATH		B													
	D-CS-M8550-0-DLH3	1	BUS ADAPTER DATA PATH		B													
	D-CS-M8550-0-DLH4	1	BUS ADAPTER DATA PATH		B													
	D-CS-M8550-0-DLH5	1	BUS ADAPTER DATA PATH		B													
	D-CS-M8550-0-DLH6	1	BUS ADAPTER POWER AND GROUND		B													
	K-CO-M8550-0-4	1	DATA PATH BOARD CALDEC DATA BASE		C													
	D-AH-M8550-0-5	4	DATA PATH BOARD		B													
	B-MH-M8550-0-6	1	MODULE ECO HISTORY		B													
	5010519 POO-M8550-00		ETCH CIRCUIT BOARD PROCESS SHEET (REF ONLY)		C													
CUSTOMER PRINT SET CODES	X = PRINT OF DOCUMENT INCLUDED IN PRINT SET C = INCLUDES ALL PRINTS INDICATED ON DOCUMENT S = CONFIDENTIAL AUTHORIZED SIGNATURE REQUIRED				ECO NO 00002													
403						TITLE DATA PATH BOARD	SHEET 3 OF 3	B	SIZE CODE DD	NUMBER M8550-0	REV C	NIR						

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AND SPECIFICATIONS HERIN ARE THE



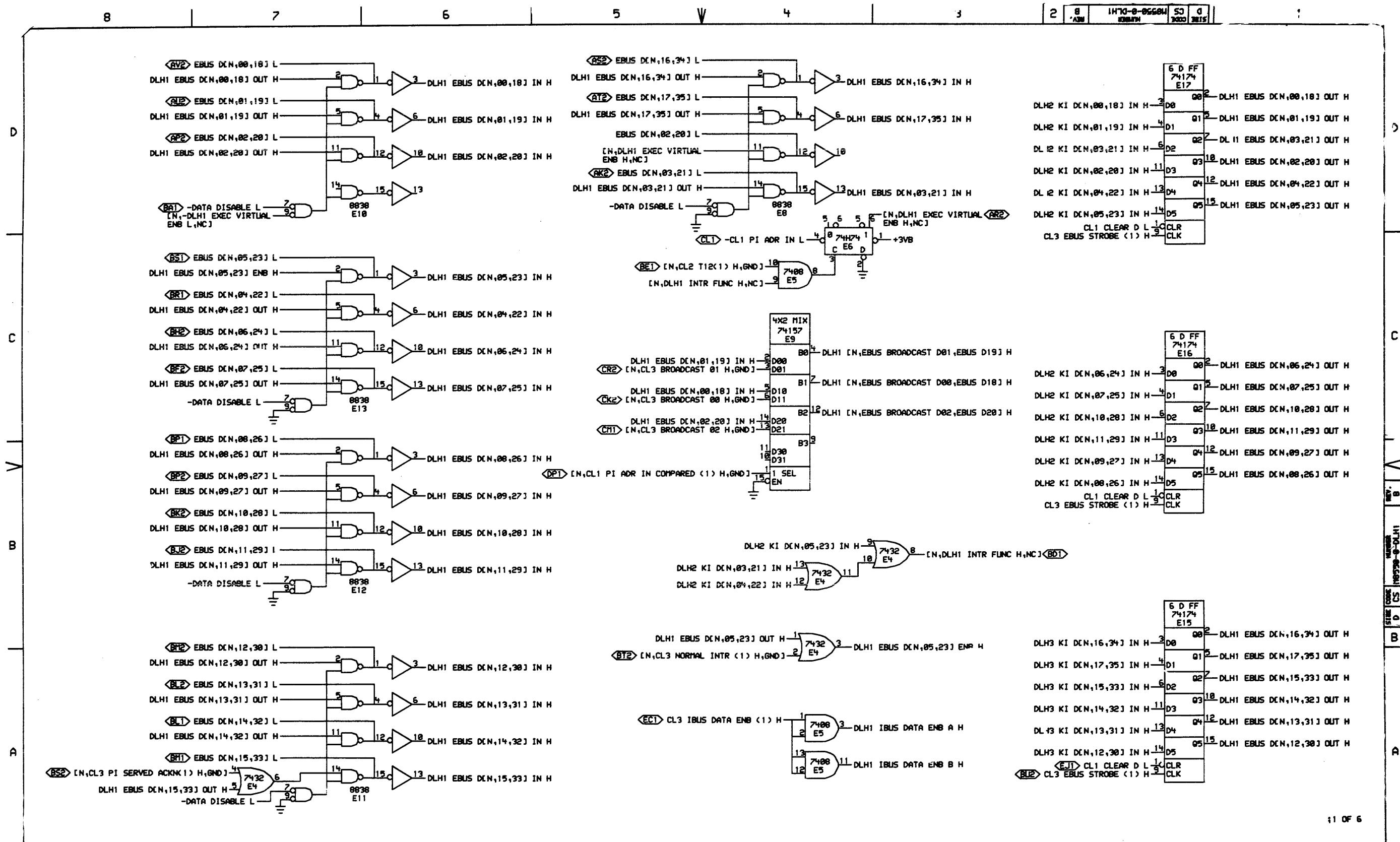
REVISIONS

TITLE DATA PATH BOARD

DRAWING NUMBER

ZE	CODE	NUMBER	REV
Q	UA	M8550-0-0	B

DIST. _____

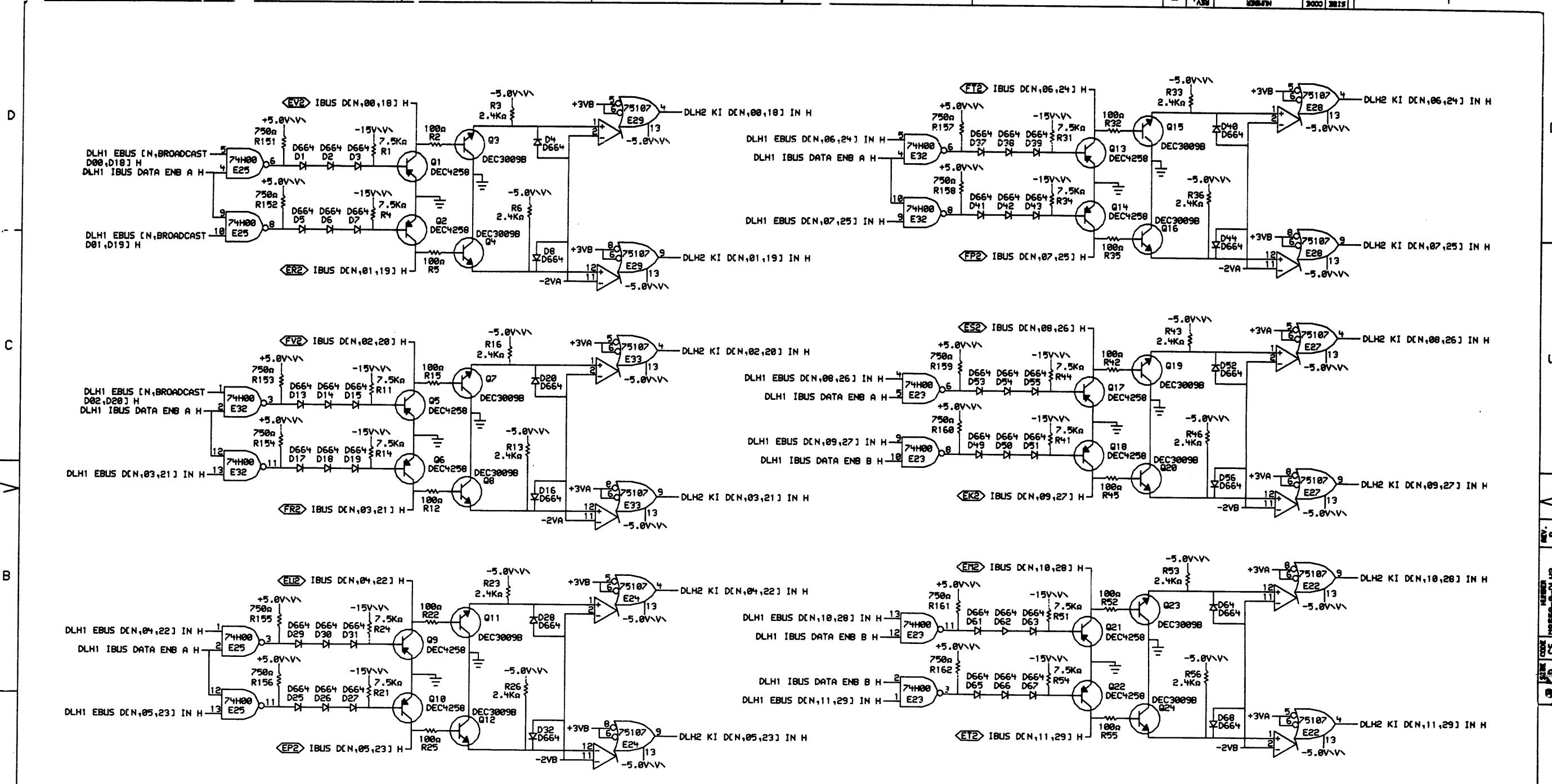


11 OF 6

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REVISION	
CHK	CHANGE NO.
	00002
BRUCKE	

DIGITAL	DRN: 7- <i>Scansy</i>	DATE: 20-JUN-75	ENG. BY: R. H. C. F. & P.	DATE: 9/11/75	TITLE: IBUS ADAPTER DATA PATH			
CHK'D A. F. T. B.	DATE: 30-JUN-75	BOARD LOCATION:	SHEET 1 OF 1	SIZE D	CODE CS	NUMBER M8550-0-DLH1	REV. B	
DLH1R4,426		12-JUN-75 10:32	NEXT HIGHER ASSEMBLY: B-DD-M8550-0					
FIRST USED ON OPTION MODEL: KL10								



2 OF 6

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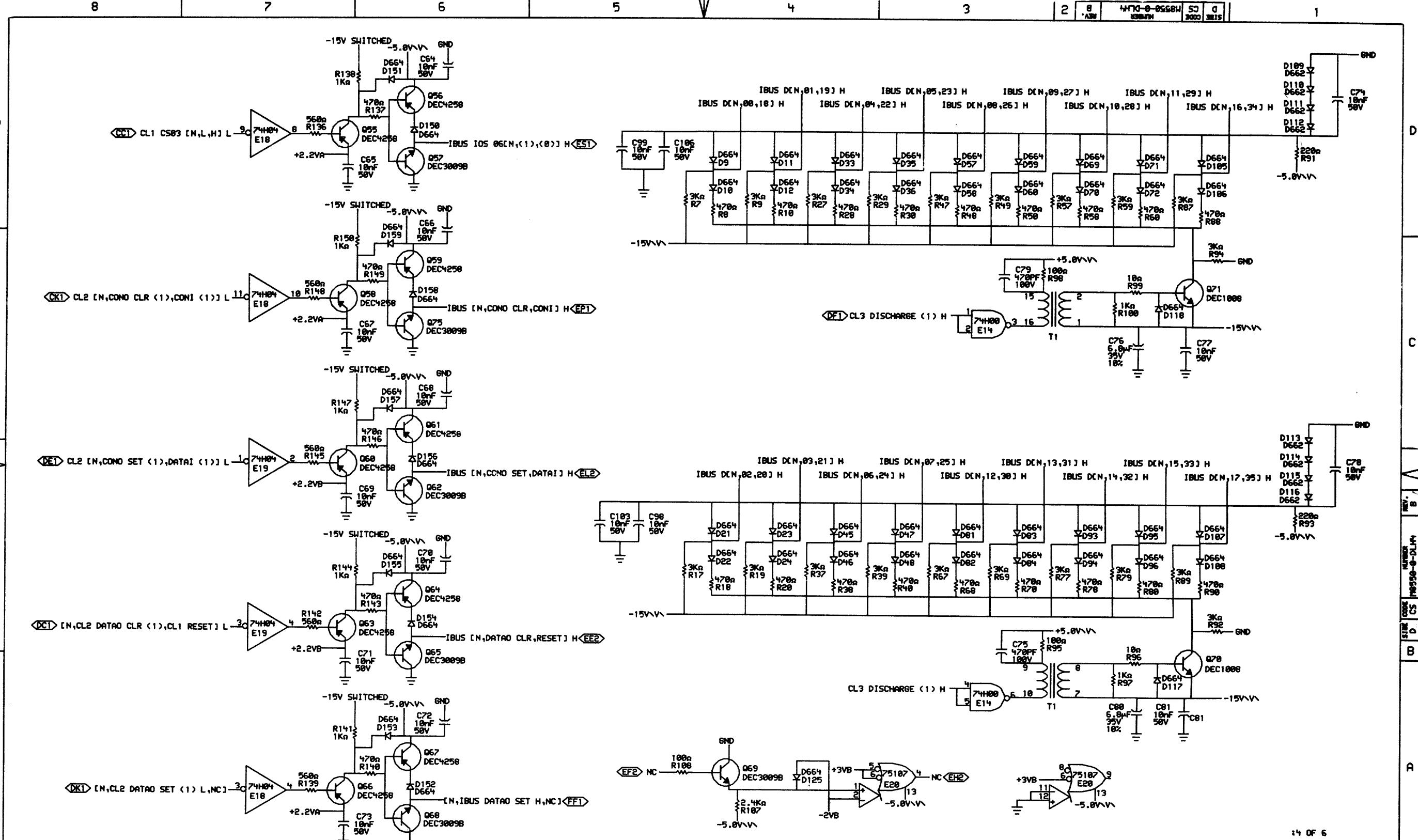
REVISIONS		
CHG	CHANGE NO.	REV

00002 B

BRUCKERT

2/16/75

digital	DRA. J. <i>[Signature]</i>	DATE 27-JUN-75	ENG. <i>[Signature]</i>	DATE 07-JUL-75	TITLE: IBUS ADAPTER DATA PATH
PK10	P&M <i>[Signature]</i>	DATE 27-JUN-75	BOARD LOCATIONS	SHEET 1 OF 1	
DLH2RL4,426	27-JUN-75 10:43	FIRST USED ON OPTION/MODEL: KL10	B-DD-M8550-0	SIZE D	CODE CS M8550-0-DLH2
				REV. B	



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REVISIONS		
CHG	CHANGE NO.	REV

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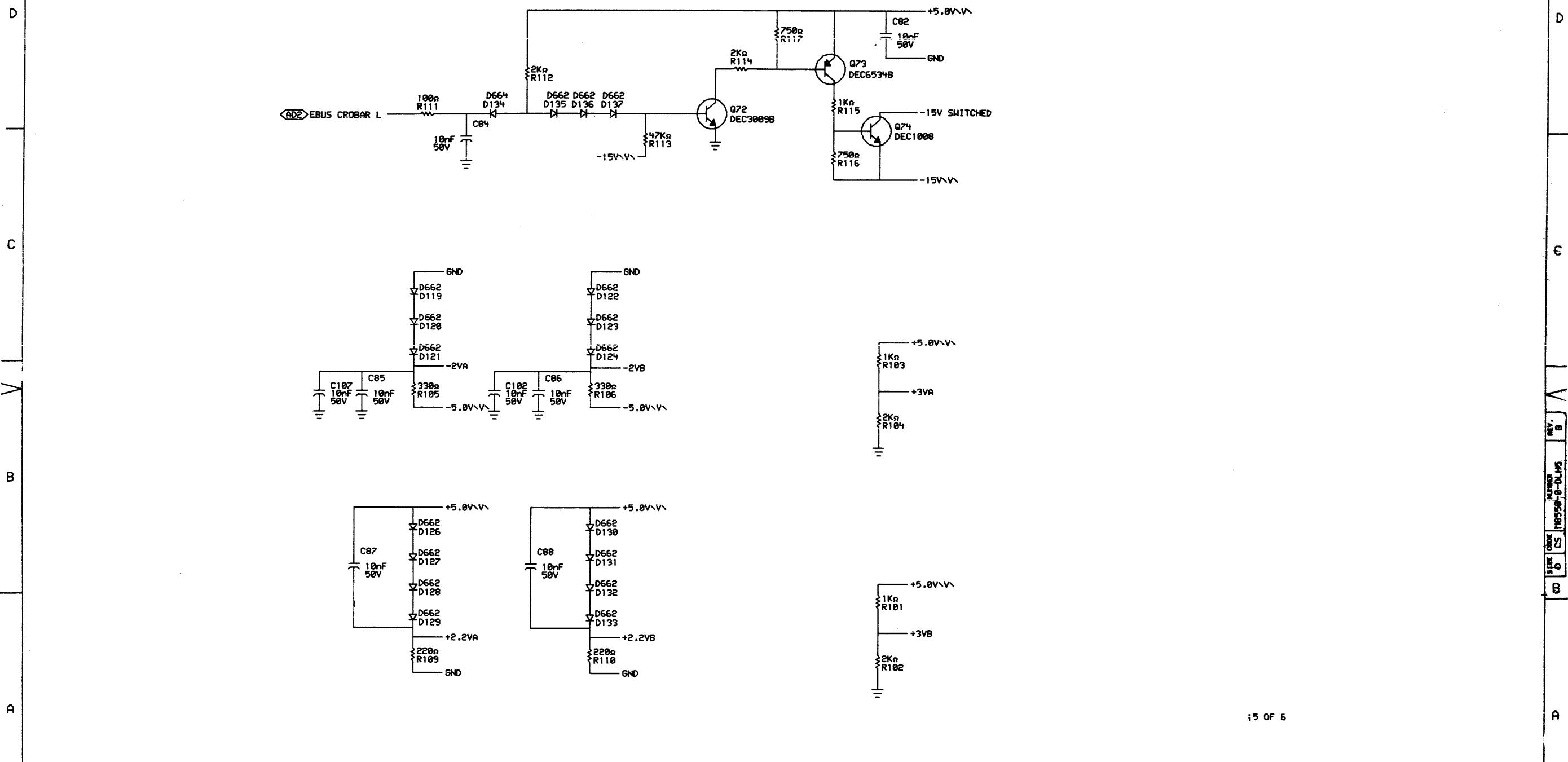
ERUCKERT

7/16

digital	DRW. 7	DATE 07-JUN-75	ENG. W. B.	DATE 01-JUL-75	TITLE: IBUS ADAPTER DATA PATH
CHK 9	100	DATE 07-JUN-75	BOARD LOCATION:	SHEET 1 OF 1	
DLHML4.4261	27-JUN-75 10:51	NEXT HIGHER ASSEMBLY:			
FIRST USED ON OPTION/MODEL: KL10	B-DD-M8550-0	SIZE D	CODE CS	NUMBER M8550-0-DLH4	REV. B

8 7 6 5 4 3 2 1

SH-550-0-DLH5
REV. B
M8550-0-DLH5
NUMBER 3000
SIZE CS D
1



15 OF 6

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CHK	CHANGE NO.	REV.						
N	00002	B						
BRUCKERT								

479

digital DRW. [Signature] DATE 27-JUN-75 ENG. B-1 DATE 27-JUN-75

CHK [Signature] DATE 27-JUN-75 BOARD LOCATION: 1 OF 1

DLH550-4261 127-JUN-75 10:56 NEXT HIGHER ASSEMBLY: 1 OF 1

FIRST USED ON OPTION/MODEL: KL10 B-DD-M8550-0

SIZE D	CODE CS	NUMBER M8550-0-DLH5	REV. B
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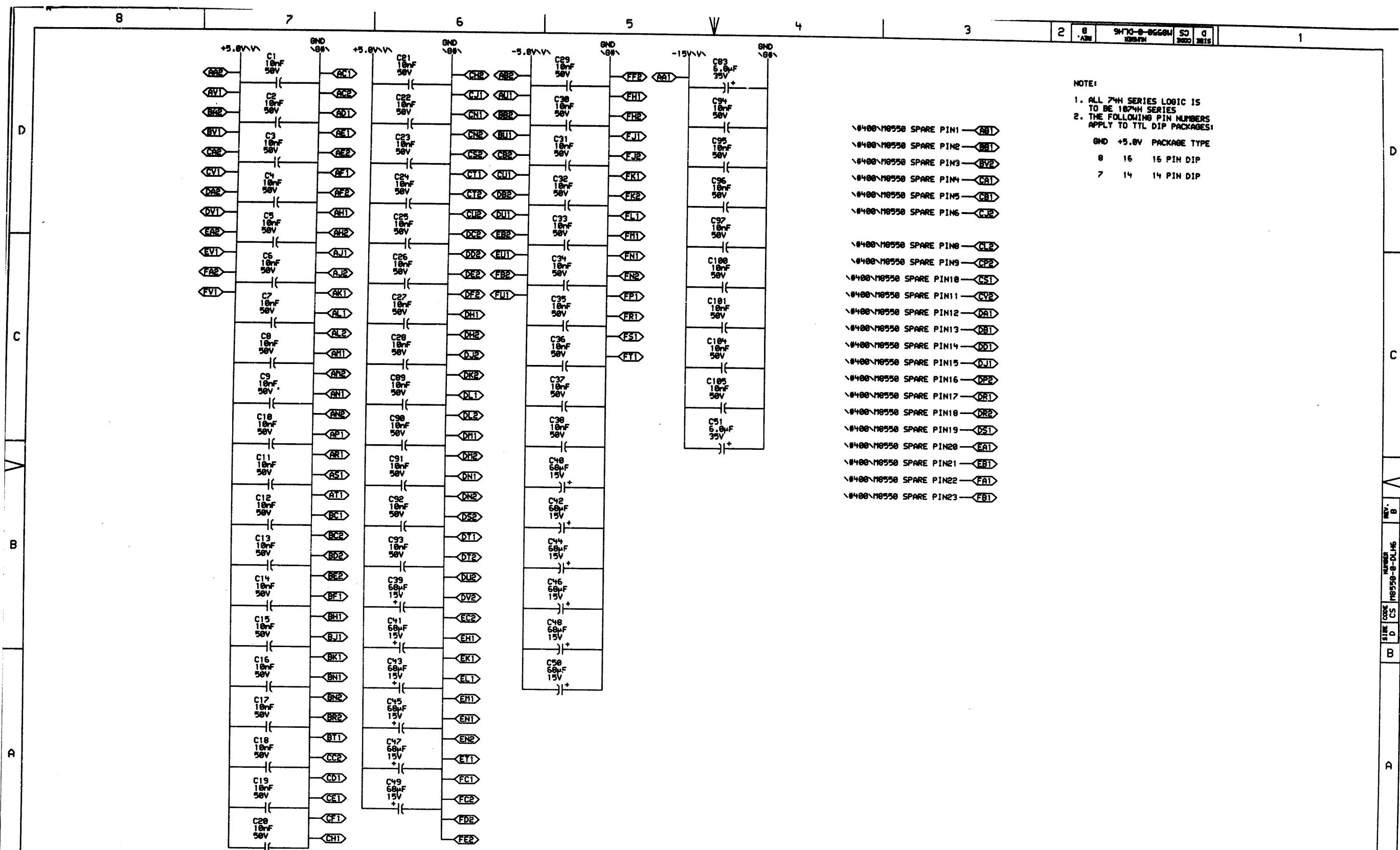
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REVISIONS		
CHK	CHANGE NO.	RE
DX	00002	B
	BRUCKERT	

digital	DRN: <i>J-1000</i>	DATE: <i>27-JUN-73</i>	ENG. <i>B-1000</i>	DATE: <i>27-JUN-73</i>	TITLE: IBUS ADAPTOR POWER AND GROUND
DLH6R(4,426)	CHK'D: <i>John</i>	DATE: <i>27-JUN-73</i>	BOARD LOCATION: <i>1</i>	SHEET <i>1</i> OF <i>1</i>	
FIRST USED ON OPTION/MODEL: K110 B-DD-M8550-0					SIZE CODE: D CS NUMBER: M8550-0-DI H6 REV. B
NEXT HIGHER ASSEMBLY:					

REVISION CONTROL SHEET			
DRAWING NO	NO OF SHT	DESCRIPTION	OPTION NO/FILE DATE
-	-	MODULE REVISION	A B C
D-UA-M8551-Ø-Ø	5	CONTROL BD FOR KL10 I/O ADAPTER	- A B
D-CS-M8551-Ø-CL1		I/O BUS ADAPTER CONTROL INTERFACE	- A B
D-CS-M8551-Ø-CL2	1	I/O BUS ADAPTER CONTROL INTERFACE	- A A
D-CS-M8551-Ø-CL3	1	I/O BUS ADAPTER CONTROL INTERFACE	- A B
D-CS-M8551-Ø-CL4	1	I/O BUS ADAPTER CONTROL INTERFACE	- A B
D-CS-M8551-Ø-CL5	1	I/O BUS ADAPTER CAPS	- A A
K-CO-M8551-Ø-4	1	CONTROL BD FOR KL10 I/O ADAPTER	B B B
D-AH-M8551-Ø-5	2	CONTROL BD FOR KL10 I/O ADAPTER	AAA
F-MD-7606166-Ø-Ø	6	MULTI-LAYER QUAD PANEL DATA	# # #
B-MH-M8551-Ø-6	1	MODULE ECO HISTORY	- A B
5010518	-	ETCH CIRCUIT BOARD	B B B
P00-M8551-ØØ		PROCESS SHEET (REF ONLY)	-

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C = INCLUDES ALL PRINTS INDICATED ON DOCUMENT
S = CONFIDENTIAL AUTHORIZED SIGNATURE REQUIRED

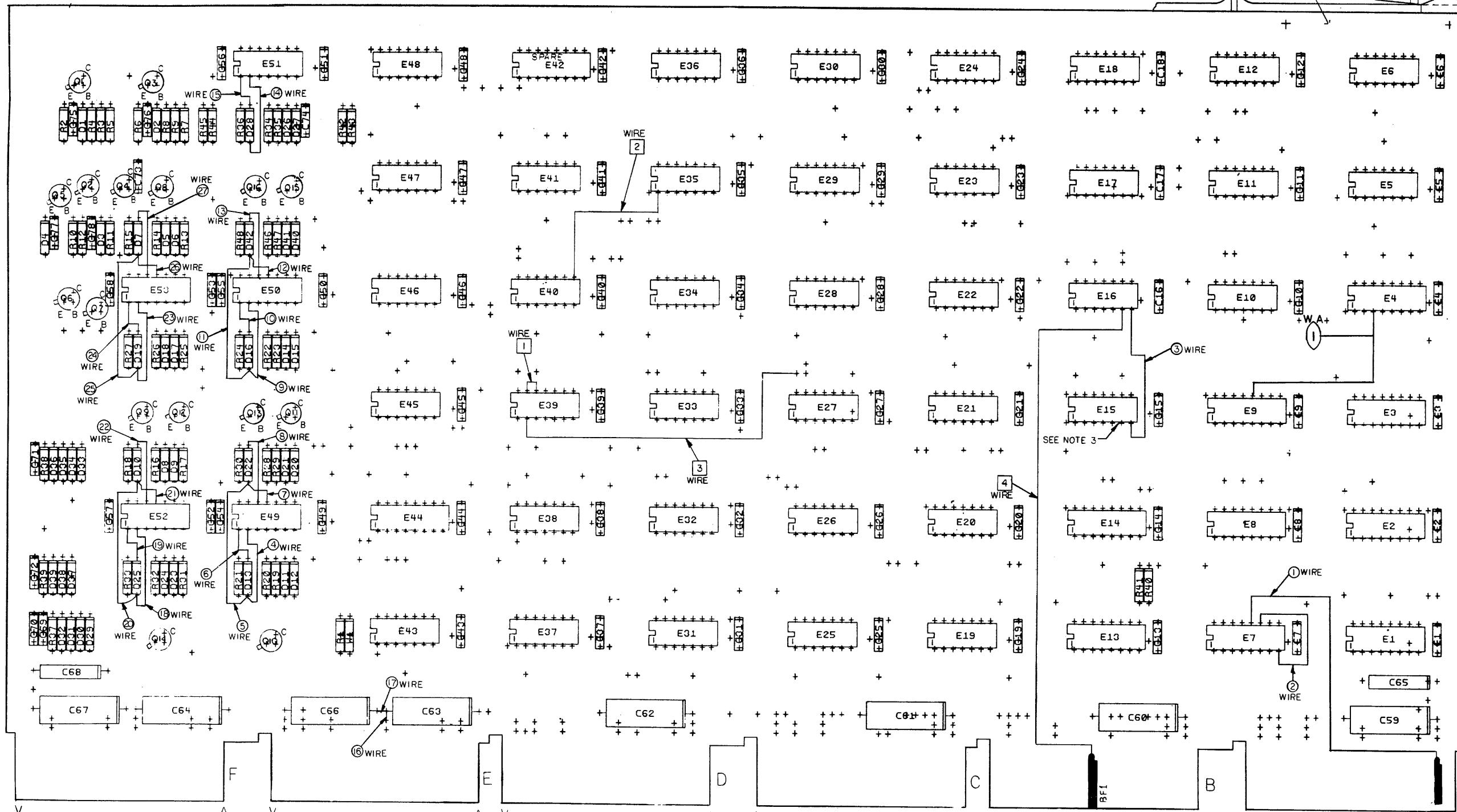
ECO NO	00001	00002
ORIG		

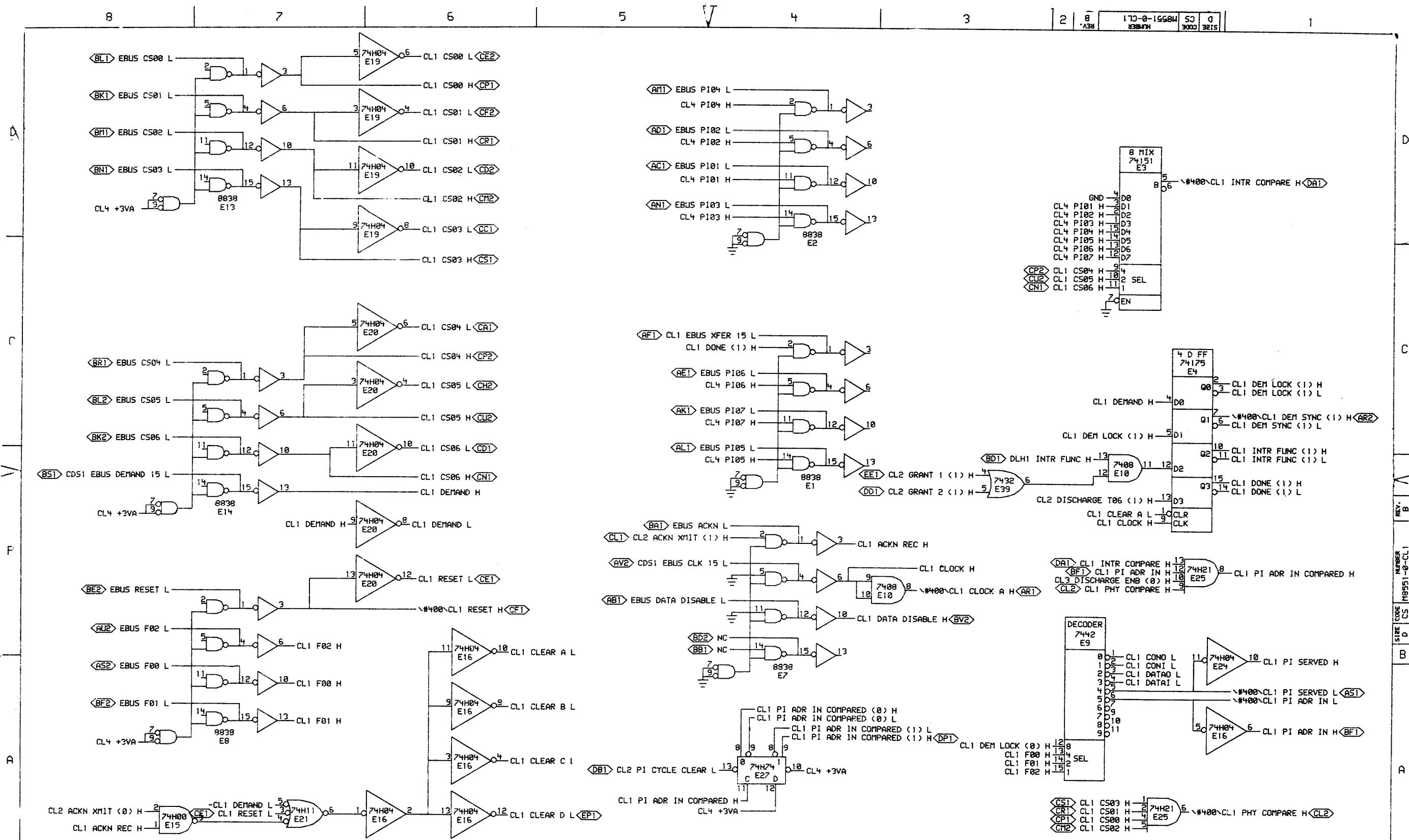
TITLE		SIZE	CODE	NUMBER
CONTROL BD FOR KL10 I/O ADAPTER	SHEET 2 OF 2	B	DD	M8551-Ø

451

MR

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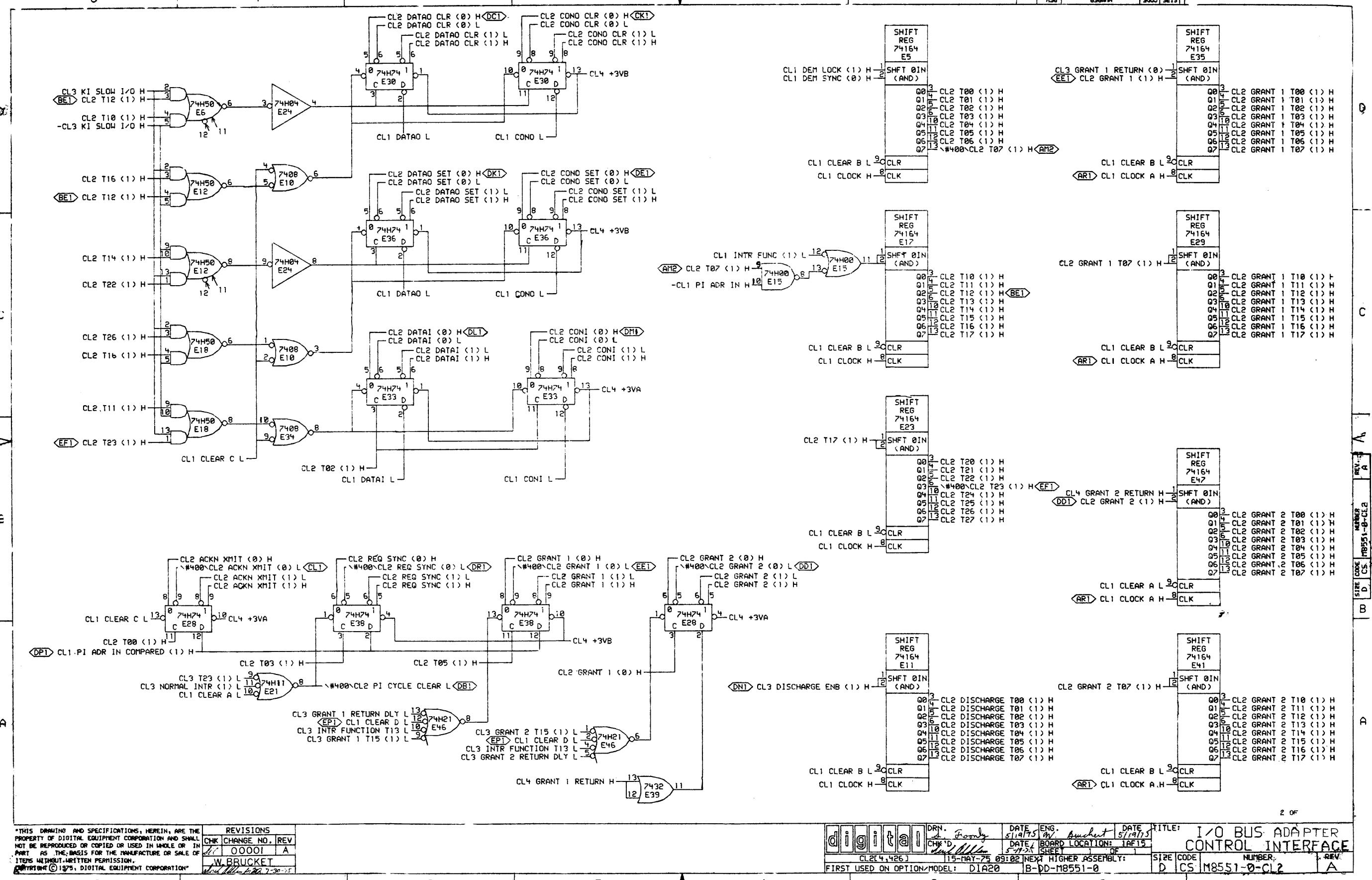


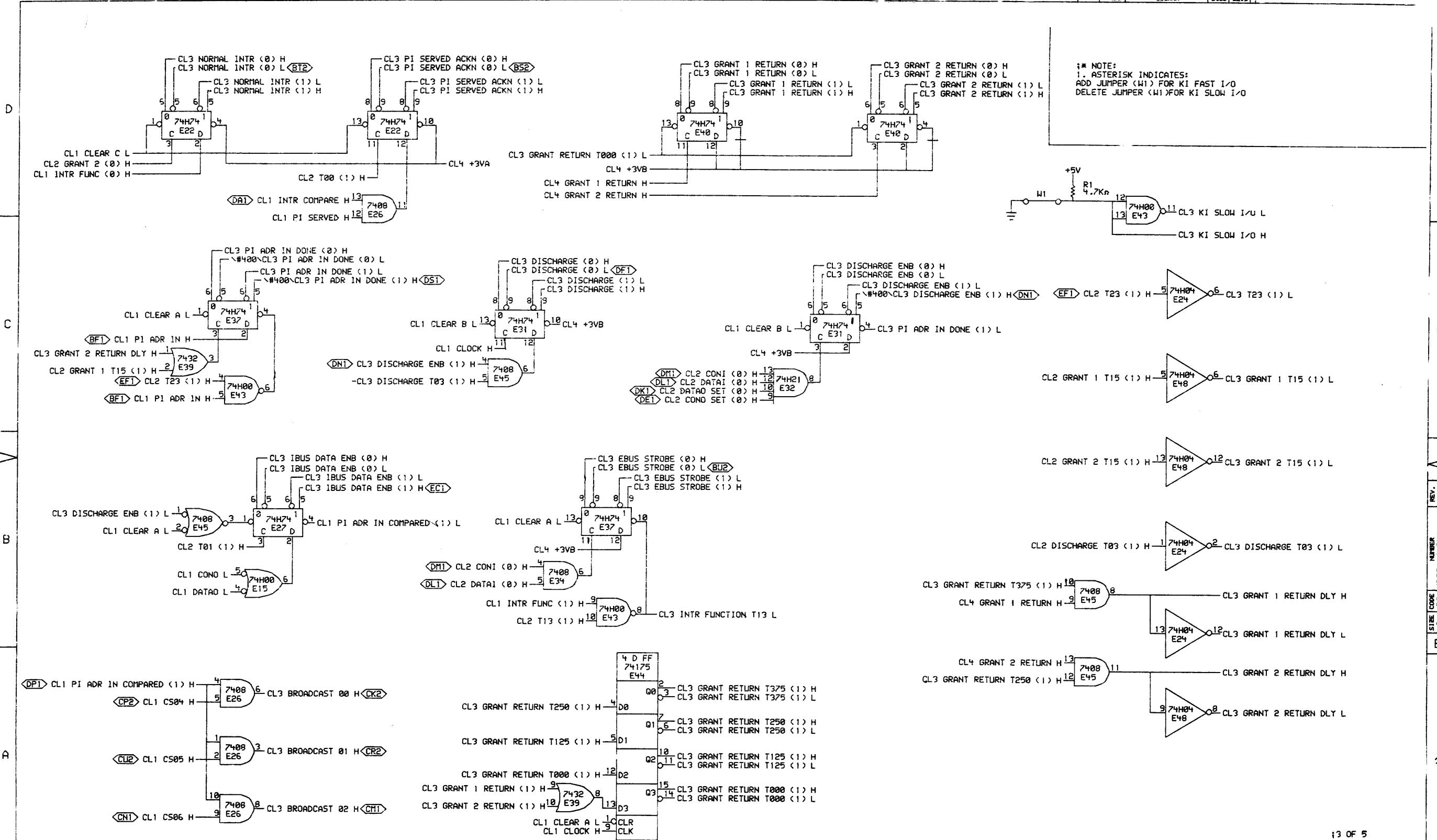


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REVISIONS		M8551-00002 B
CHK	CHANGE NO.	REV.
		11-10-75
		W. BRUCKERT
		11-10-75

digital		DRW. <i>S. Farley</i>	DATE 24-OCT-75	ENG. <i>W. Bruckert</i>	DATE 11-10-75	TITLE: I/O BUS ADAPTER CONTROL INTERFACE	
CHK ID	DATE	11-10-75	BOARD LOCATION: 1A615	SHEET 1 OF 1	REV. B		
CLIC4,4261	11-10-75	13:02	NEXT HIGHER ASSEMBLY: B-DD-M8551-0			SIZE CODE D CS M8551-0-CL1	REV. B
FIR T USED ON OPTION/MODEL: DIA20							





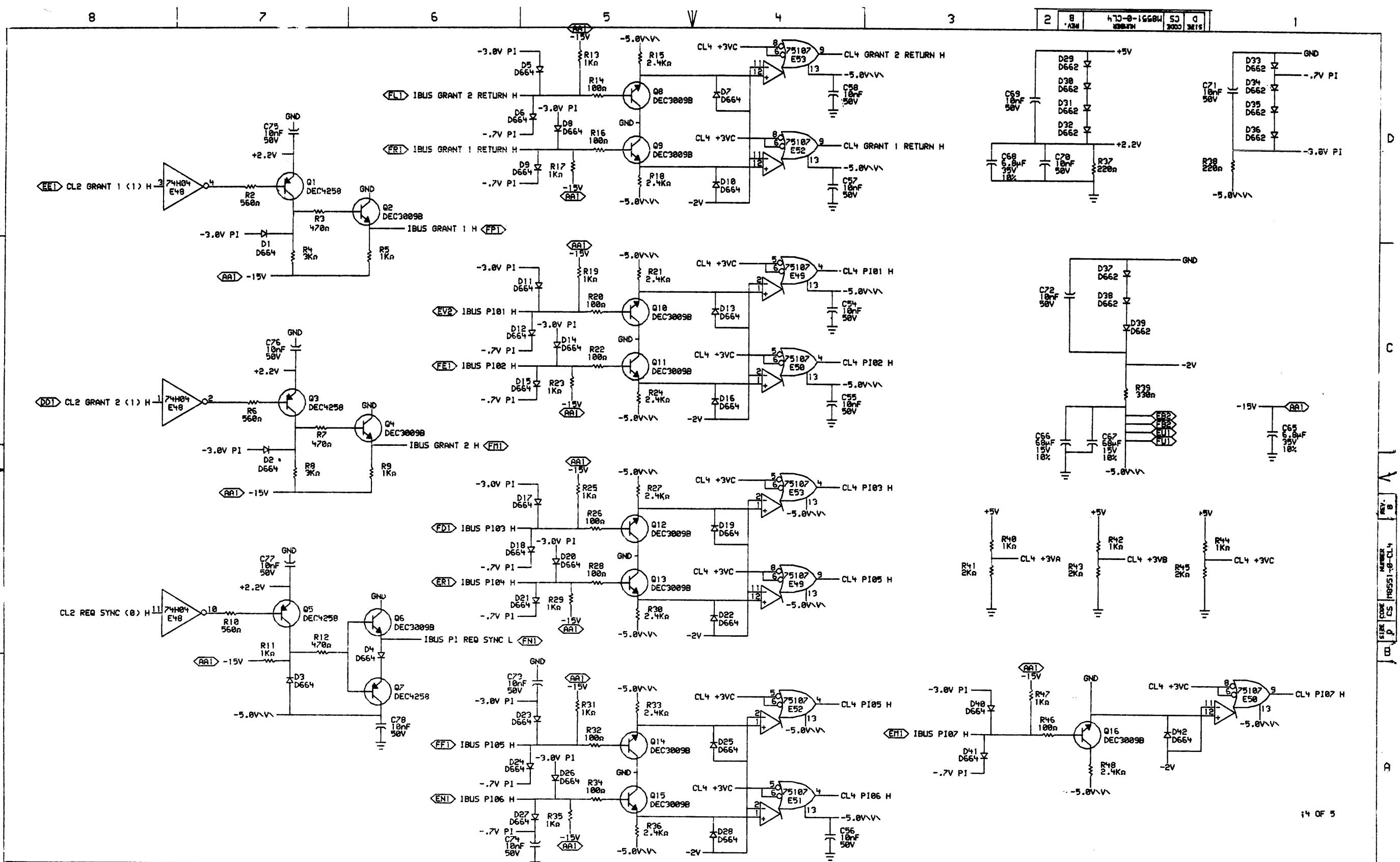
13 OF 5

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REVISIONS	M8551-0002 B
CHG. CHANGE NO.	REV
	24-OCT-75
	W.BRUCKERT
	Lead Master 10/12/12

SIZE CODE	CS M8551-0-CL3	REV.
D	CS M8551-0-CL3	B
1		
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REVISION
CHK CHANGE NO

di

g i t a
CL4[4,426]

DRN. 7
X-1000
CHK'D
Christopher
27-OCT-

DATE
27-OCT-73
DATE
11/14/75
75 09:20 NEXT

NG
W. Burkhardt
DARD LOCATION
HEET 1
HIGHER ASSEM

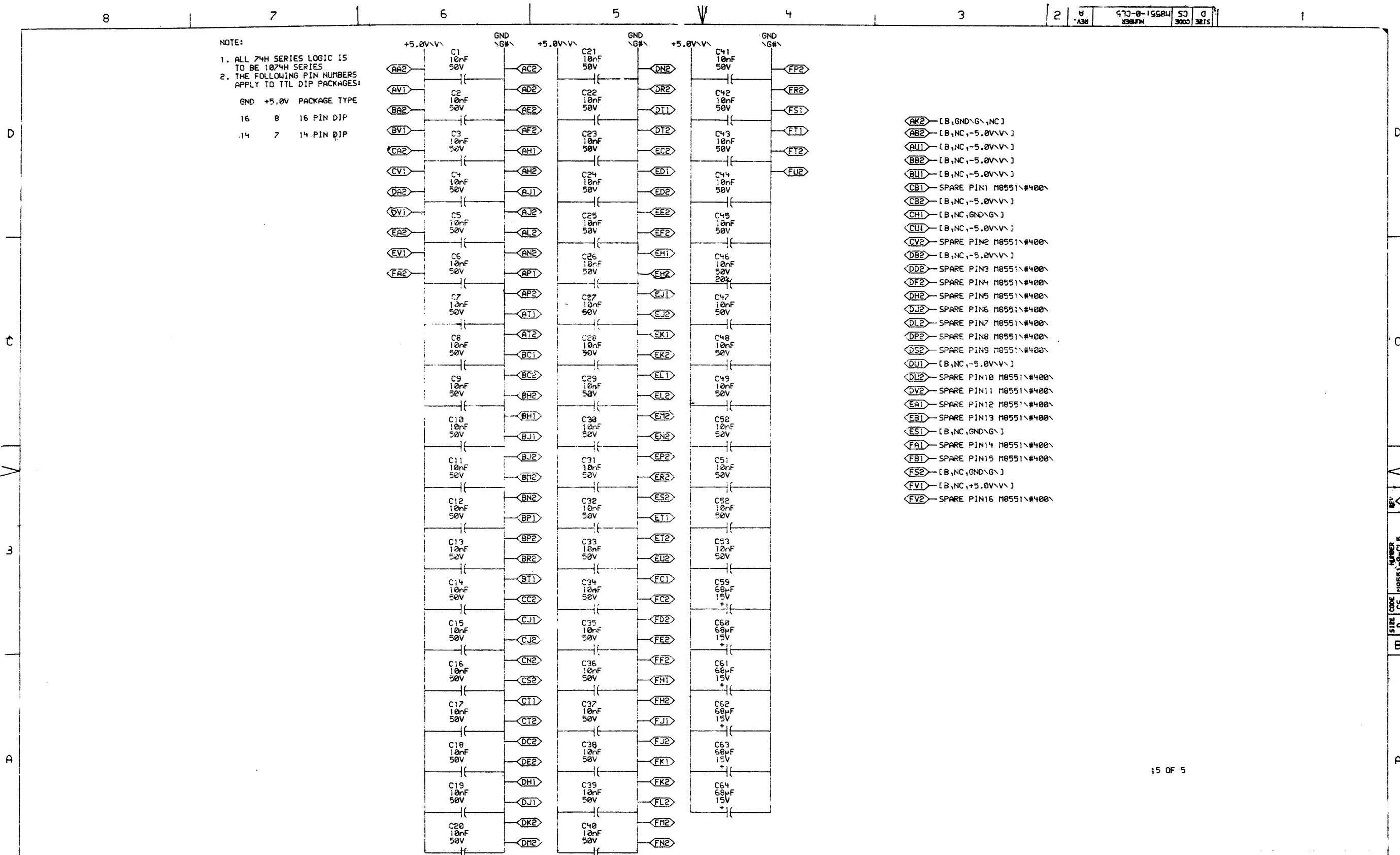
DATE 11/11/75 TITLE
: 1AF15
OF 1
LY: SIZE

I/O E
CODE

BUS ADAP
TDL INTELLI
NUMBER

APTER
RFACE

8 7 6 5 4 3 2 1



5 OF 5

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REVISIONS

CHG CHANGE NO. REV

00001 A

W. BRUCKET

487

8 7 5 4 3 2 1

REVISIONS

CHG D. Fooley DATE 5/19/75 ENG. W. Bruckert DATE 5/19/75
CHANGE NO. REV. SHEET 1 OF 1

CL5EX4,426) DATE 16-MAY-75 11:22 BOARD LOCATION: 1AF15
FIRST USED ON OPTION/MODEL: D1A20 SHEET 1 OF 1

SIZE CODE NUMBER REV.

D CS M8551-0-CL5 A

SLOT	H17		H13		H14		H15
	1	2	1	2	1	2	1
+ A	+	+	+	+	+	+	A
B +	+	+	+	+	+	+	B +
+ C	+	+	+	+	+	+	C
D +	+	+	+	+	+	+	D +
+ E	+	+	+	+	+	+	E
F +	+	+	+	+	+	+	F +
+ H	+	+	+	+	+	+	H
J +	+	+	+	+	+	+	J +
+ K	+	+	+	+	+	+	K
L +	+	+	+	+	+	+	L +
+ M	+	+	+	+	+	+	M
N +	+	+	+	+	+	+	N +
+ P	+	+	+	+	+	+	P
R +	+	+	+	+	+	+	R +
+ S	+	+	+	+	+	+	S
T +	+	+	+	+	+	+	T +

SLOT	J14		J15		J16		J17
	2	1	2	1	2	1	2
+ A	+	+	+	+	+	+	A
B +	+	+	+	+	+	+	B +
+ C	+	+	+	+	+	+	C
D +	+	+	+	+	+	+	D +
+ E	+	+	+	+	+	+	E
F +	+	+	+	+	+	+	F +
+ H	+	+	+	+	+	+	H
J +	+	+	+	+	+	+	J +
+ K	+	+	+	+	+	+	K
L +	+	+	+	+	+	+	L +
+ M	+	+	+	+	+	+	M
N +	+	+	+	+	+	+	N +
+ P	+	+	+	+	+	+	P
R +	+	+	+	+	+	+	R +
+ S	+	+	+	+	+	+	S
T +	+	+	+	+	+	+	T +

IBUS CONI L → IJ14P1 → IJ16P2
 IBUS CONO CLR L → IJ12R2 → IJ15R1
 IBUS CONO SET L → IJ13R1 → IJ14R2
 IBUS D00 H → IJ12A1 → IJ14A2
 IBUS D01 H → IJ12A2 → IJ15A1
 IBUS D02 H → IJ12B2 → IJ15B1
 IBUS D03 H → IJ13B1 → IJ15B2
 IBUS D04 H → IJ13A2 → IJ16A1
 IBUS D05 H → IJ14A1 → IJ16A2
 IBUS D06 H → IJ14B1 → IJ16B2
 IBUS D07 H → IJ14B2 → IJ17B1
 IBUS D08 H → IJ15A1 → IJ17A2
 IBUS D09 H → IJ12C1 → IJ14C2
 IBUS D10 H → IJ12C2 → IJ15C1
 IBUS D11 H → IJ12D2 → IJ15D1
 IBUS D12 H → IJ13D1 → IJ15D2
 IBUS D13 H → IJ13C2 → IJ15C1
 IBUS D14 H → IJ14C1 → IJ16C2
 IBUS D15 H → IJ14D1 → IJ16D2
 IBUS D16 H → IJ14D2 → IJ17D1
 IBUS D17 H → IJ15C1 → IJ17C2
 IBUS D18 H → IJ12E1 → IJ14E2
 IBUS D19 H → IJ12E2 → IJ15E1
 IBUS D20 H → IJ12F2 → IJ15F1
 IBUS D21 H → IJ13F1 → IJ15F2
 IBUS D22 H → IJ13E2 → IJ16E1
 IBUS D23 H → IJ14E1 → IJ16E2
 IBUS D24 H → IJ14F1 → IJ16F2
 IBUS D25 H → IJ14F2 → IJ17F1
 IBUS D26 H → IJ15E1 → IJ17E2
 IBUS D27 H → IJ12H1 → IJ14H2
 IBUS D28 H → IJ12H2 → IJ15H1
 IBUS D29 H → IJ11J2 → IJ15J1
 IBUS D30 H → IJ13J1 → IJ15J2
 IBUS D31 H → IJ13H2 → IJ16H1
 IBUS D32 H → IJ14H1 → IJ16H2
 IBUS D33 H → IJ14J1 → IJ16J2
 IBUS D34 H → IJ14J2 → IJ17J1
 IBUS D35 H → IJ15H1 → IJ17H2

IBUS DATA1 L → IJ13P2 → IJ16P1
 IBUS DATA0 CLR L → IJ12P1 → IJ14P2
 IBUS DATA0 SET L → IJ12P2 → IJ15P1
 IBUS DR SPLIT H → IJ13L1 → IJ15L2
 IBUS GRANT 1 H → IJ12S2
 IBUS GRANT 2 H → IJ15S1
 IBUS GRANT 2 RETURN H → IJ17P2
 IBUS IOS 03 H → IJ13K2 → IJ15K1
 IBUS IOS 03 L → IJ14K1 → IJ16K2
 IBUS ICS 01 H → IJ14L1 → IJ16L2
 IBUS IOS 04 L → IJ14L2 → IJ17L1
 IBUS IOS 05 H → IJ15K1 → IJ17K2
 IBUS IOS 05 L → IJ12M1 → IJ15M2
 IBUS IOS 06 H → IJ12N2 → IJ15N1
 IBUS IOS 06 L → IJ12N2 → IJ15N1
 IBUS IOS 07 H → IJ13M1 → IJ15N2
 IBUS IOS 07 L → IJ13M2 → IJ15M1
 IBUS IOS 08 H → IJ14M1 → IJ16M2
 IBUS IOS 08 L → IJ14M1 → IJ16N2
 IBUS IOS 09 H → IJ14N2 → IJ17N1
 IBUS IOS 09 L → IJ15M1 → IJ17M2
 IBUS PI REQ SYNC L → IJ12S1 → IJ14S2
 IBUS PI01 H → IJ12T2 → IJ15T1
 IBUS PI02 H → IJ13T1 → IJ15T2
 IBUS PI03 H → IJ13S2 → IJ15S1
 IBUS PI04 H → IJ14S1 → IJ16S2
 IBUS PI05 H → IJ14T1 → IJ16T2
 IBUS PI06 H → IJ14T2 → IJ17T1
 IBUS PI07 H → IJ15S1 → IJ17S2
 IBUS RDI DATA H → IJ14R2 → IJ17R1
 IBUS RDI PULSE L → IJ14R1 → IJ15R2
 IBUS RESERVE L → IJ12K2 → IJ15K1
 IBUS RESET L → IJ12K1 → IJ14K2

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REVISIONS

CHK	CHANGE NO.	REV.
-----	------------	------

DATE: 17-OCT-75 ENG: Niemietz REV: KL10 I/O BUS
 TITLE: CONNECTOR
 CHK: Johnson SHEET: 1 OF 1
 FIRST USED ON OPTION/MODEL: KL10 SIZE CODE: D NUMBER: IC KL10-0-IBUS
 488

SLOT	H01		H02		H03		H04		SLOT	H05		H07		H08		H09	
PIN SIDE	1	2	1	2	1	2	1	2	PIN SIDE	2	1	2	1	2	1	2	
	+ A	+	+	+	+	+	+	+		+ A	+	+	+	+	+	+	+
	B	+	+	+	+	+	+	+		B	+	+	+	+	+	+	+
	C	+	+	+	+	+	+	+		C	+	+	+	+	+	+	+
	D	+	+	+	+	+	+	+		D	+	+	+	+	+	+	+
	E	+	+	+	+	+	+	+		E	+	+	+	+	+	+	+
	F	+	+	+	+	+	+	+		F	+	+	+	+	+	+	+
	H	+	+	+	+	+	+	+		H	+	+	+	+	+	+	+
	J	+	+	+	+	+	+	+		J	+	+	+	+	+	+	+
	K	+	+	+	+	+	+	+		K	+	+	+	+	+	+	+
	L	+	+	+	+	+	+	+		L	+	+	+	+	+	+	+
	M	+	+	+	+	+	+	+		M	+	+	+	+	+	+	+
	N	+	+	+	+	+	+	+		N	+	+	+	+	+	+	+
	P	+	+	+	+	+	+	+		P	+	+	+	+	+	+	+
	R	+	+	+	+	+	+	+		R	+	+	+	+	+	+	+
	S	+	+	+	+	+	+	+		S	+	+	+	+	+	+	+
	T	+	+	+	+	+	+	+		T	+	+	+	+	+	+	+

KBUS0		KBUS1		KBUS2		KBUS3	
KBUS"X" ADR ACK (NT) L	1H01A1	1H05A2	1J03A2	1J09A1			
KBUS"X" ADI ACK (T) L	1H04C1	1H05C2	1J12C1				
KBUS"X" D00 L	1H01K1	1H05K2	1J03K1				
KBUS"X" D01 L	1H01K2	1H07K1	1J08K1	1J09K2			
KBUS"X" D02 L	1H01L2	1H07L1	1J04L1	1J09L2			
KBUS"X" D03 L	1H02L1	1H07L2	1J06L2	1J10L1			
KBUS"X" D04 L	1H02K2	1H08K1	1J05K1	1J10K2			
KBUS"X" D05 L	1H02K1	1H08K2	1J11K1				
KBUS"X" D06 L	1H02L1	1H08L2	1J11L2				
KBUS"X" D07 L	1H03L2	1H08L1	1J06L1	1J11L2			
KBUS"X" D08 L	1H04K1	1H08K2	1J12K1				
KBUS"X" D09 L	1H01M1	1H06M2	1J03M2	1J09M1			
KBUS"X" D10 L	1H01M2	1H07M1	1J04M1	1J09M2			
KBUS"X" D11 L	1H02N1	1H07N1	1J09N2				
KBUS"X" D12 L	1H02N1	1H07N2	1J10N1				
KBUS"X" D13 L	1H02M2	1H08M1	1J05M1	1J10M2			
KBUS"X" D14 L	1H03M1	1H08M2	1J05M2	1J11M1			
KBUS"X" D15 L	1H03N1	1H08N2	1J05N2	1J11N1			
KBUS"X" D16 L	1H03N2	1H09N1	1J06N1	1J11N2			
KBUS"X" D17 L	1H04M1	1H09M2	1J06M2	1J12M1			
KBUS"X" D18 L	1H01P1	1H05P2	1J03P2	1J09P1			
KBUS"X" D19 L	1H01P2	1H07P1	1J06P1	1J09P2			
KBUS"X" D20 L	1H01R2	1H07R1	1J04R1	1J09R2			
KBUS"X" D21 L	1H02R1	1H07R2	1J04R2	1J10R1			
KBUS"X" D22 L	1H02P2	1H08P1	1J05P1	1J10P2			
KBUS"X" D23 L	1H03P1	1H08P2	1J05P2	1J11P1			
KBUS"X" D24 L	1H03R1	1H08R2	1J05R2	1J11R1			
KBUS"X" D25 L	1H03R2	1H09R1	1J06R1	1J11R2			
KBUS"X" D26 L	1H04P1	1H09P2	1J06P2	1J12P1			
KBUS"X" D27 L	1H01S1	1H06S2	1J03S2	1J09S1			
KBUS"X" D28 L	1H01S2	1H07S1	1J04S1	1J09S2			
KBUS"X" D29 L	1H01T2	1H07T1	1J04T1	1J09T2			
KBUS"X" D30 L	1H02T1	1H07T2	1J10T1				
KBUS"X" D31 L	1H02S2	1H08S1	1J05S1	1J10S2			
KBUS"X" D32 L	1H03S1	1H08S2	1J05S2	1J11S1			
KBUS"X" D33 L	1H03T1	1H08T2	1J05T1	1J11T1			
KBUS"X" D34 L	1H03T2	1H09T1	1J06T1	1J11T2			
KBUS"X" D35 L	1H04S1	1H09S2	1J05S2	1J12S1			
KBUS"X" DATA WARNING L	1H04A1	1H03J2	1J05A2	1J12A1			
KBUS"X" IGN PAR PULSE L	1H03J1	1H05J2	1J06J1	1J11J2			
KBUS"X" MADR 14 L	1H02C2	1H08C1	1J05C1	1J10C2			
KBUS"X" MADR 15 L	1H03C1	1H08C2	1J05C2	1J11C1			
KBUS"X" MADR 16 L	1H03D1	1H08D2	1J05D2	1J11D1			
KBUS"X" MADR 17 L	1H03D2	1H09D1	1J06D1	1J11D2			
KBUS"X" MADR 18 L	1H03B2	1H09B1	1J05B1	1J11B2			
KBUS"X" MADR 19 L	1H01C1	1H05C2	1J03C2	1J09C1			
KBUS"X" MADR 20 L	1H01D2	1H07D1	1J04D1	1J09D2			
KBUS"X" MADR 21 L	1H04H1	1H09H2	1J06H2	1J12H1			
KBUS"X" MADR 22 L	1H01E1	1H07E2	1J03E2	1J09E1			
KBUS"X" MADR 23 L	1H01E2	1H07E1	1J06E1	1J09E2			
KBUS"X" MADR 24 L	1H01F2	1H07F1	1J04F1	1J09F2			
KBUS"X" MADR 25 L	1H02F1	1H07F2	1J07F2	1J10F1			
KBUS"X" MADR 26 L	1H02E2	1H08E1	1J05E1	1J10E2			
KBUS"X" MADR 27 L	1H03E1	1H08E2	1J05E2	1J11E1			
KBUS"X" MADR 28 L	1H03F1	1H08F2	1J05F2	1J11F1			
KBUS"X" MADR 29 L	1H03F2	1H09F1	1J06F1	1J11F2			
KBUS"X" MADR 30 L	1H04E1	1H08E2	1J06E2	1J12E1			
KBUS"X" MADR 31 L	1H01H1	1H06H2	1J03H2	1J09H1			
KBUS"X" MADR 32 L	1H01H2	1H07H1	1J04H1	1J09H2			
KBUS"X" MADR 33 L	1H01J2	1H07J1	1J04J1	1J09J2			
KBUS"X" MADR 34 L	1H02J1	1H07J2	1J04J2	1J10J1			
KBUS"X" MADR 35 L	1H02H2	1H08H1	1J05H1	1J10H2			
KBUS"X" MADR RD RQ L	1H03H1	1H06H2	1J05H2	1J11H1			
KBUS"X" MADR WR RQ L	1H03J1	1H08J2	1J05J2	1J11J1			
KBUS"X" MBD PAR L	1H02B1	1H07B2	1J04B2	1J10B1			
KBUS"X" MBI SEQ RQ L	1H02D1	1H07D2	1J04D2	1J10D1			
KBUS"X" RD RS L	1H01A2	1H07A1	1J04A1	1J09A2			
KBUS"X" RQ CYC FAST L	1H03A1	1H08A2	1J05A2	1J11A1			

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	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
A	11	(1)	(2)	(3)	(4)	(5)	(6)	(7)	(8)	(9)	(10)	(11)	(12)	(13)	(14)
B	11	(1)	(2)	(3)	(4)	(5)	(6)	(7)	(8)	(9)	(10)	(11)	(12)	(13)	(14)
C	11	(1)	(2)	(3)	(4)	(5)	(6)	(7)	(8)	(9)	(10)	(11)	(12)	(13)	(14)
D	11	(1)	(2)	(3)	(4)	(5)	(6)	(7)	(8)	(9)	(10)	(11)	(12)	(13)	(14)
E	11	(1)	(2)	(3)	(4)	(5)	(6)	(7)	(8)	(9)	(10)	(11)	(12)	(13)	(14)
F	11	(1)	(2)	(3)	(4)	(5)	(6)	(7)	(8)	(9)	(10)	(11)	(12)	(13)	(14)
G	11	(1)	(2)	(3)	(4)	(5)	(6)	(7)	(8)	(9)	(10)	(11)	(12)	(13)	(14)
H	11	(1)	(2)	(3)	(4)	(5)	(6)	(7)	(8)	(9)	(10)	(11)	(12)	(13)	(14)
I	11	(1)	(2)	(3)	(4)	(5)	(6)	(7)	(8)	(9)	(10)	(11)	(12)	(13)	(14)
J	11	(1)	(2)	(3)	(4)	(5)	(6)	(7)	(8)	(9)	(10)	(11)	(12)	(13)	(14)
K	11	(1)	(2)	(3)	(4)	(5)	(6)	(7)	(8)	(9)	(10)	(11)	(12)	(13)	(14)
L	11	(1)	(2)	(3)	(4)	(5)	(6)	(7)	(8)	(9)	(10)	(11)	(12)	(13)	(14)
M	11	(1)	(2)	(3)	(4)	(5)	(6)	(7)	(8)	(9)	(10)	(11)	(12)	(13)	(14)
N	11	(1)	(2)	(3)	(4)	(5)	(6)	(7)	(8)	(9)	(10)	(11)	(12)	(13)	(14)
O	11	(1)	(2)	(3)	(4)	(5)	(6)	(7)	(8)	(9)	(10)	(11)	(12)	(13)	(14)
P	11	(1)	(2)	(3)	(4)	(5)	(6)	(7)	(8)	(9)	(10)	(11)	(12)	(13)	(14)
Q	11	(1)	(2)	(3)	(4)	(5)	(6)	(7)	(8)	(9)	(10)	(11)	(12)	(13)	(14)
R	11	(1)	(2)	(3)	(4)	(5)	(6)	(7)	(8)	(9)	(10)	(11)	(12)	(13)	(14)
S	11	(1)	(2)	(3)	(4)	(5)	(6)	(7)	(8)	(9)	(10)	(11)	(12)	(13)	(14)
T	11	(1)	(2)	(3)	(4)	(5)	(6)	(7)	(8)	(9)	(10)	(11)	(12)	(13)	(14)
U	11	(1)	(2)	(3)	(4)	(5)	(6)	(7)	(8)	(9)	(10)	(11)	(12)	(13)	(14)
V	11	(1)	(2)	(3)	(4)	(5)	(6)	(7)	(8)	(9)	(10)	(11)	(12)	(13)	(14)
W	11	(1)	(2)	(3)	(4)	(5)	(6)	(7)	(8)	(9)	(10)	(11)	(12)	(13)	(14)
X	11	(1)	(2)	(3)	(4)	(5)	(6)	(7)	(8)	(9)	(10)	(11)	(12)	(13)	(14)
Y	11	(1)	(2)	(3)	(4)	(5)	(6)	(7)	(8)	(9)	(10)	(11)	(12)	(13)	(14)
Z	11	(1)	(2)	(3)	(4)	(5)	(6)	(7)	(8)	(9)	(10)	(11)	(12)	(13)	(14)

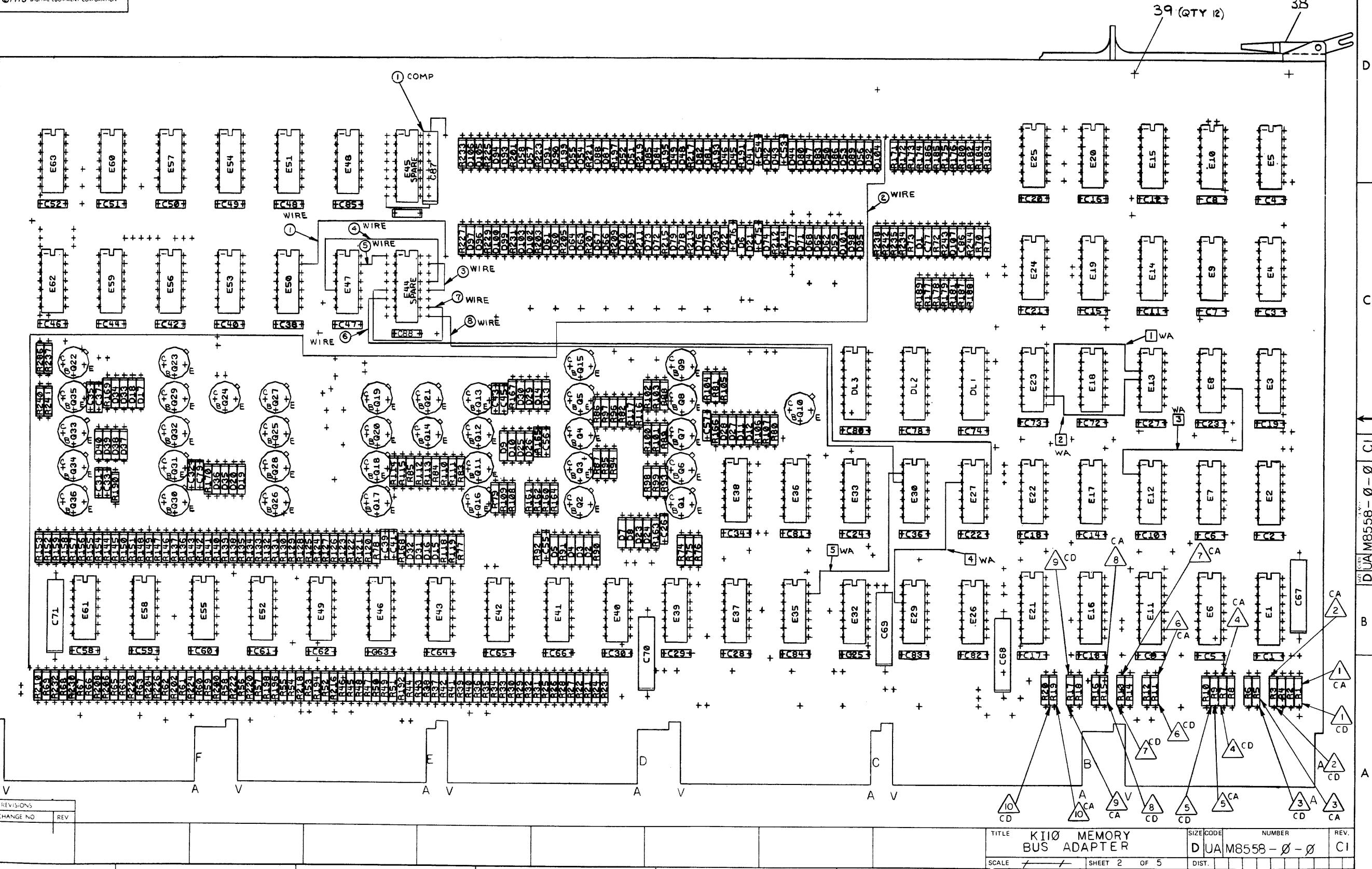
REV.	CHANGE NO.	REVISIONS
006		

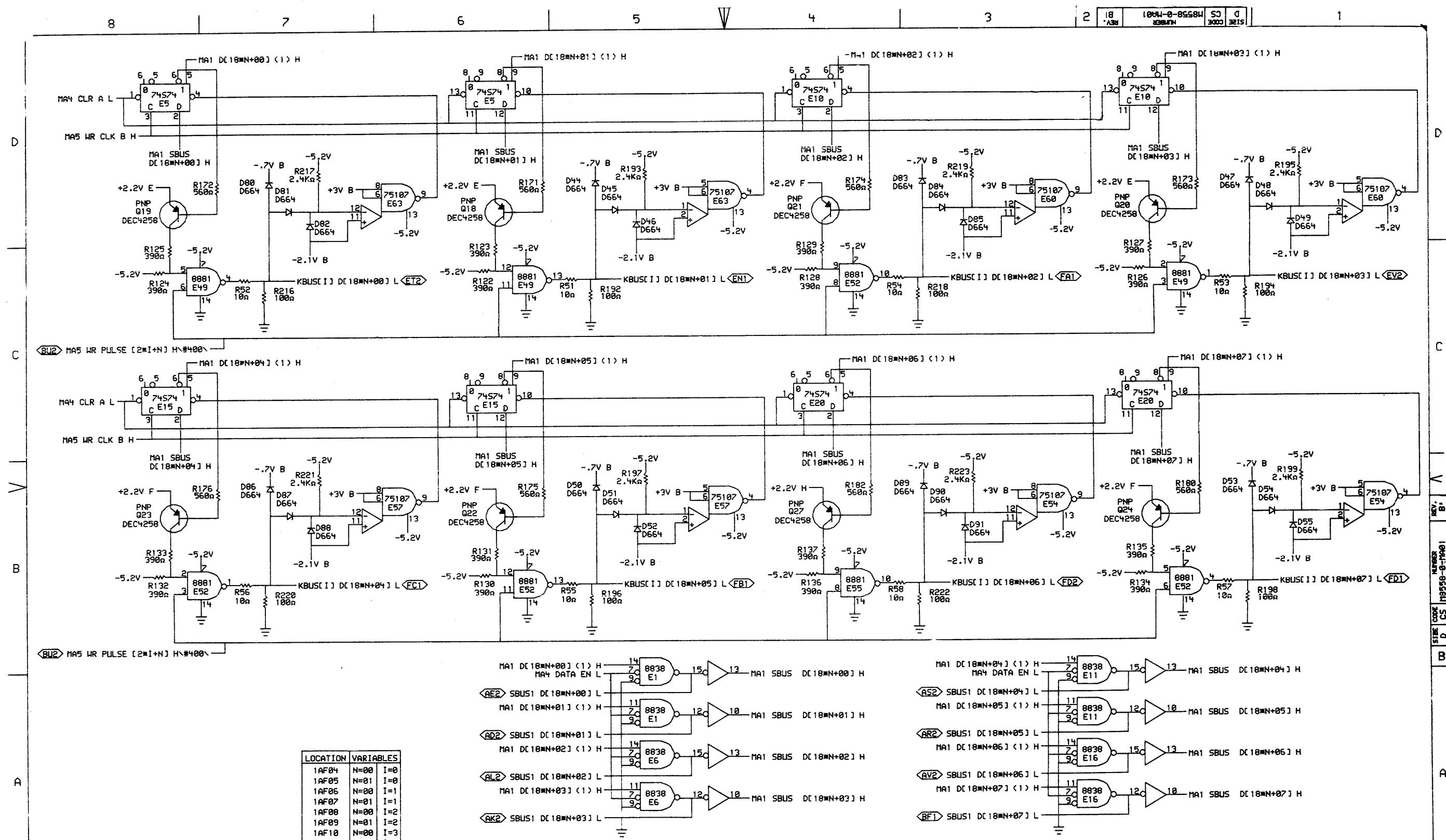
FIRST USED ON OPTION/MODEL		QTY.	DESCRIPTION	PART NO.	ITEM NO.	
KL1Ø			PARTS LIST			
DIMENSIONAL TOLERANCE		DRN.	DATE 3-5-75	 <p style="text-align: center;">TITLE MODULE UTILIZATION BAY 1</p>		
DIMENSIONS ARE <u>MILLIMETERS</u> <u>INCHES</u> UNLESS OTHERWISE SPECIFIED		CHK'D.	DATE 5-5-75			
MILLIMETERS	INCHES	ANGLES	ENG.			DATE 3-5-75
XXX = ±0,10 XX = ±0,5 X = ±2,	.XXX = ±.005 .XX = ±.02 .X = ±.1	±° 30'	PROJ. ENG.			DATE
			PROD.			DATE
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY ✓			NEXT HIGHER ASSY.			
THIRD ANGLE PROJECTION		MATERIAL	D-VA-KL1Ø-3-3	SIZE/CODE	NUMBER	REV.
		FINISH	SCALE	C MU	KL1Ø-Ø-BAY1	
			SHEET 1 OF 1	DIST.		

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CUSTOMER PRINT SET				REVISION CONTROL SHEET				
MFG SET	DRAWING NO	NO OF SHT	DESCRIPTION	OPTION NO/FILE DATE	REVISIONS			
	-		MODULE REVISION	A B B C C				
	D-U-A-M8558-0-0	5	KI10 MEMORY BUS ADAPTER	. A B C C I				
	D-CS-M8558-0-MA01	1	KI10 MEMORY BUS ADAPTER	A A B B I				
	D-CS-M8558-0-MA02	1	KI10 MEMORY BUS ADAPTER	A A B B I				
	D-CS-M8558-0-MA03	1	KI10 MEMORY BUS ADAPTER	A A B B I				
	D-CS-M8558-0-MA04	1	KI10 MEMORY ADAPTER	A A B B I				
	D-CS-M8558-0-MA05	1	KI10 MEMORY BUS ADAPTER	A A B B I				
	D-CS-M8558-0-MA06	1	KI10 MEM BUS ADPT POWER AND GND	A A B B I				
	K-C0-M8558-0-4	1	KI10 MEMORY BUS ADAPTER	B B B B B				
	D-AH-M8558-0-5	4	KI10 MEMORY BUS ADAPTER	A A A A A				
	B-MH-M8558-0-6	1	MODULE ECO HISTORY	- REF	REF			
	5010640	-	ETCH CIRCUIT BOARD	B B B B B				
	POO-M8558-00		PROCESS SHEET (REF ONLY)	- - - -				
					SHEET 3			
					REWORK VERSION REAYOUT VERSION			
CUSTOMER PRINT SET CODES	X = PRINT OF DOCUMENT INCLUDED IN PRINT SET C = INCLUDES ALL PRINTS INDICATED ON DOCUMENT S = CONFIDENTIAL AUTHORIZED SIGNATURE REQUIRED			ECO NO ORIG 00001 00002 00003 MR004				
491				TITLE KI10 MEMORY BUS ADAPTER	SHEET 2 OF 3	SIZE CODE B DD	NUMBER M8558-0	REV D

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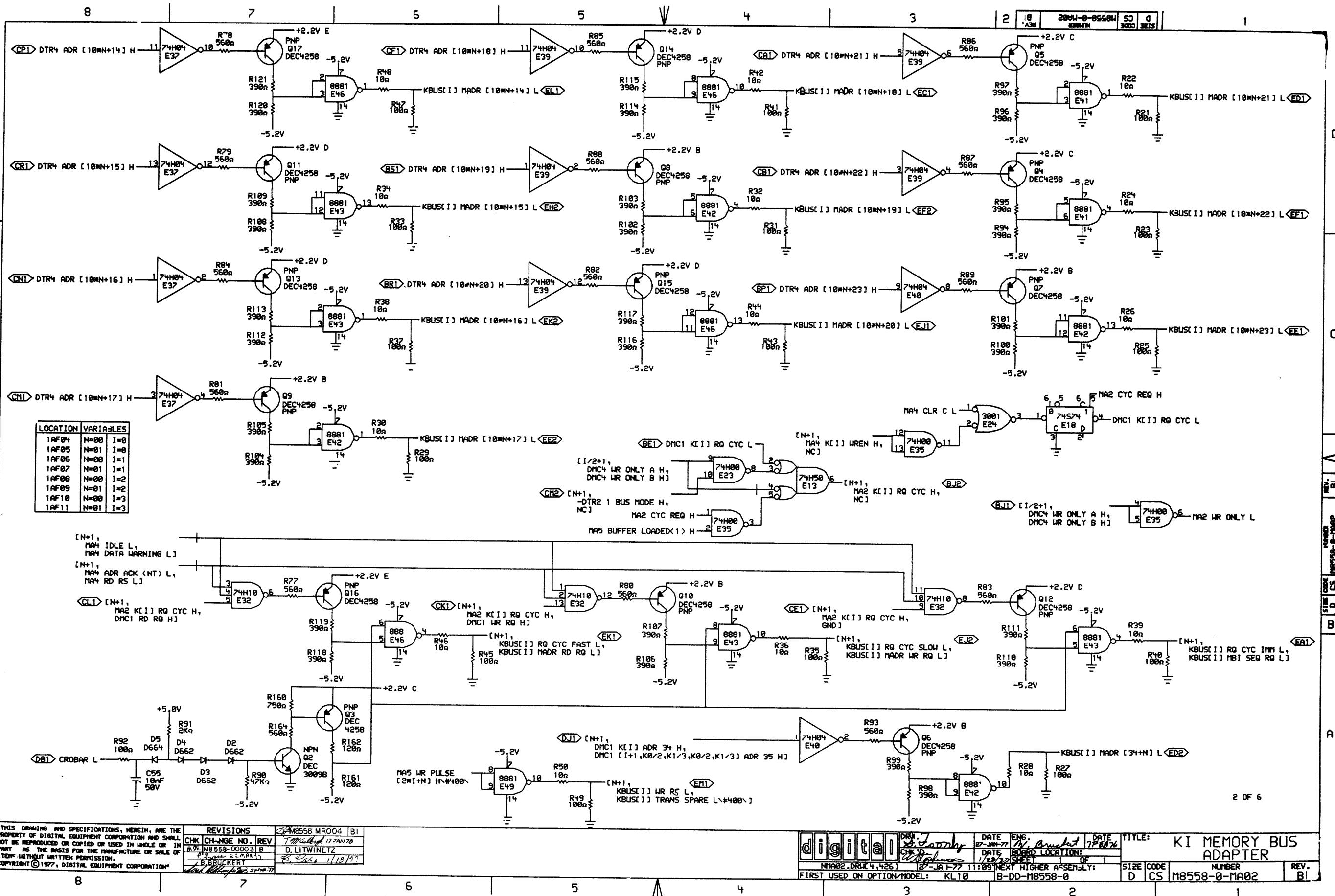




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CHK	CHANGE NO.	REV	D.LITWINETZ		
A.V	M8558-00003	B	P.C. 1/18/78		
B.BICKERT			P.C. 1/18/78		

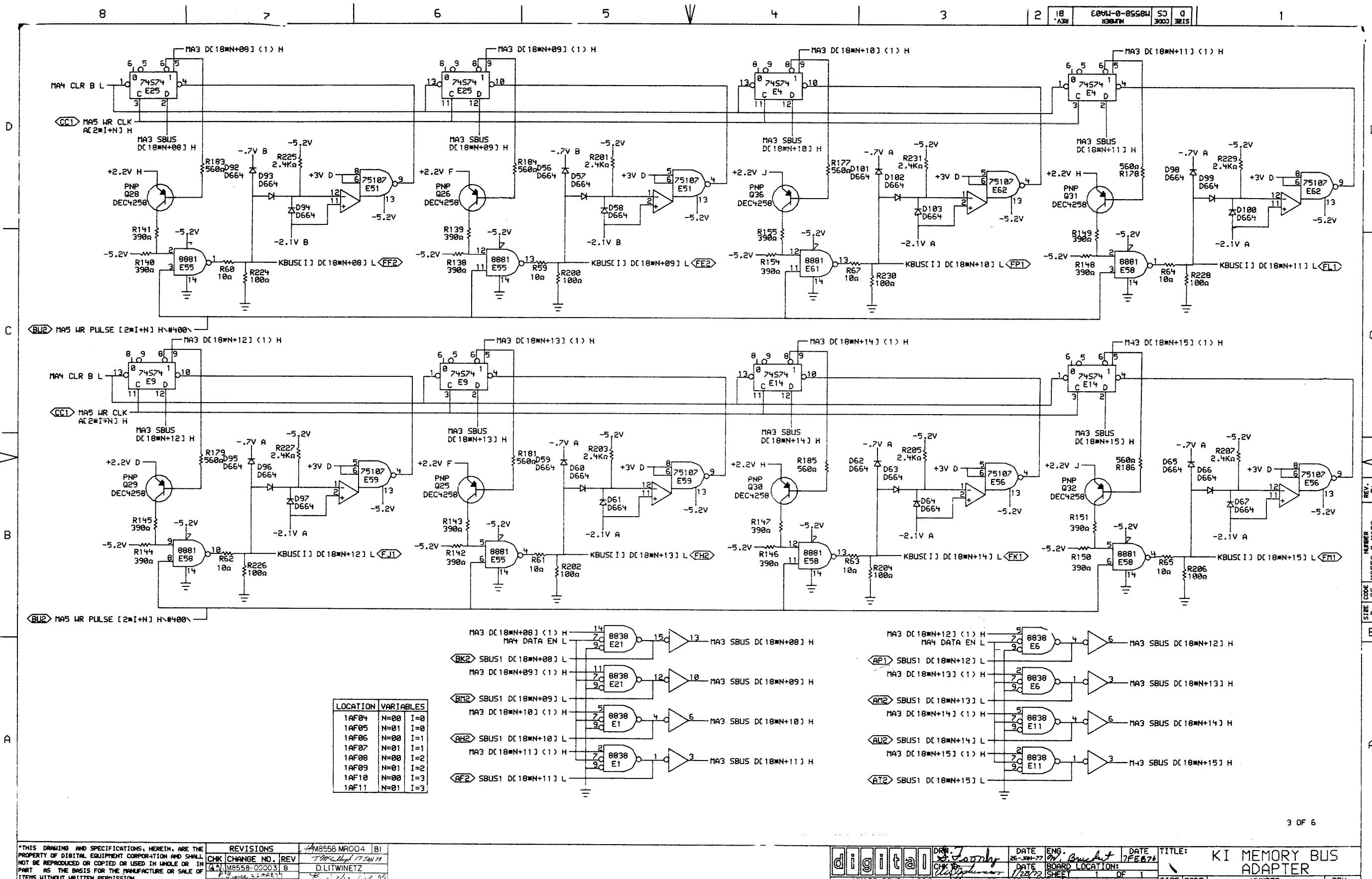
digital	DRW. <i>S. Jourdy</i>	DATE 27-JAN-77	ENG. <i>M. Brundage</i>	DATE 1 FEB 77	TITLE: KI MEMORY BUS ADAPTER		
CHK ID: <i>123456</i>	DATE <i>1/25/77</i>	BOARD LOCATION:					
NMA01.DRWL4,426] 26-JAN-77 16:58		NEXT HIGHER ASSEMBLY:					
FIRST USED ON OPTION-MODEL: KL10		B-DD-M8558-0					
SIZE D	CODE CS	NUMBER M8558-0-MA01		REV. B1			

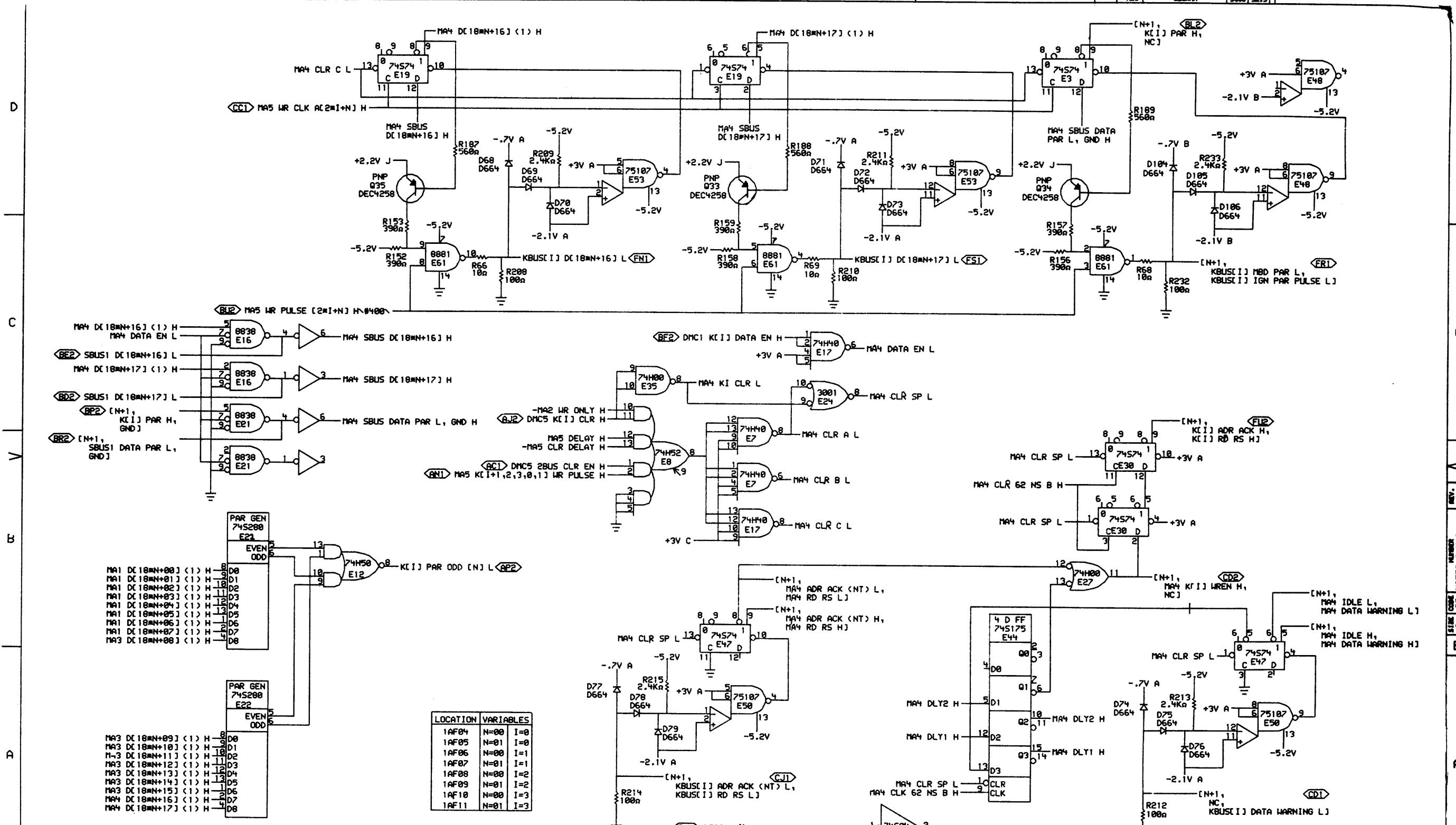


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REV. B1
M8558 MRO04
CHG-NCE NO. REV. T. McAllister 17 JAN 78
D. LITWINETZ
E. BRUCKERT
DATE 1-22-77
L. Pato 1/18/77

DATE 27-JAN-77 ENG. 11-Bruckert DATE 7-80-76
CIRCUIT 27-JAN-77 BOARD LOCATION:
NIA82.DR4E,4261 SHEET 1 OF 1
FIRST USED ON OPTION/MODEL: KL10
B-DD-18558-0
SIZE CODE D CS NUMBER M8558-0-MA02
REV. B1





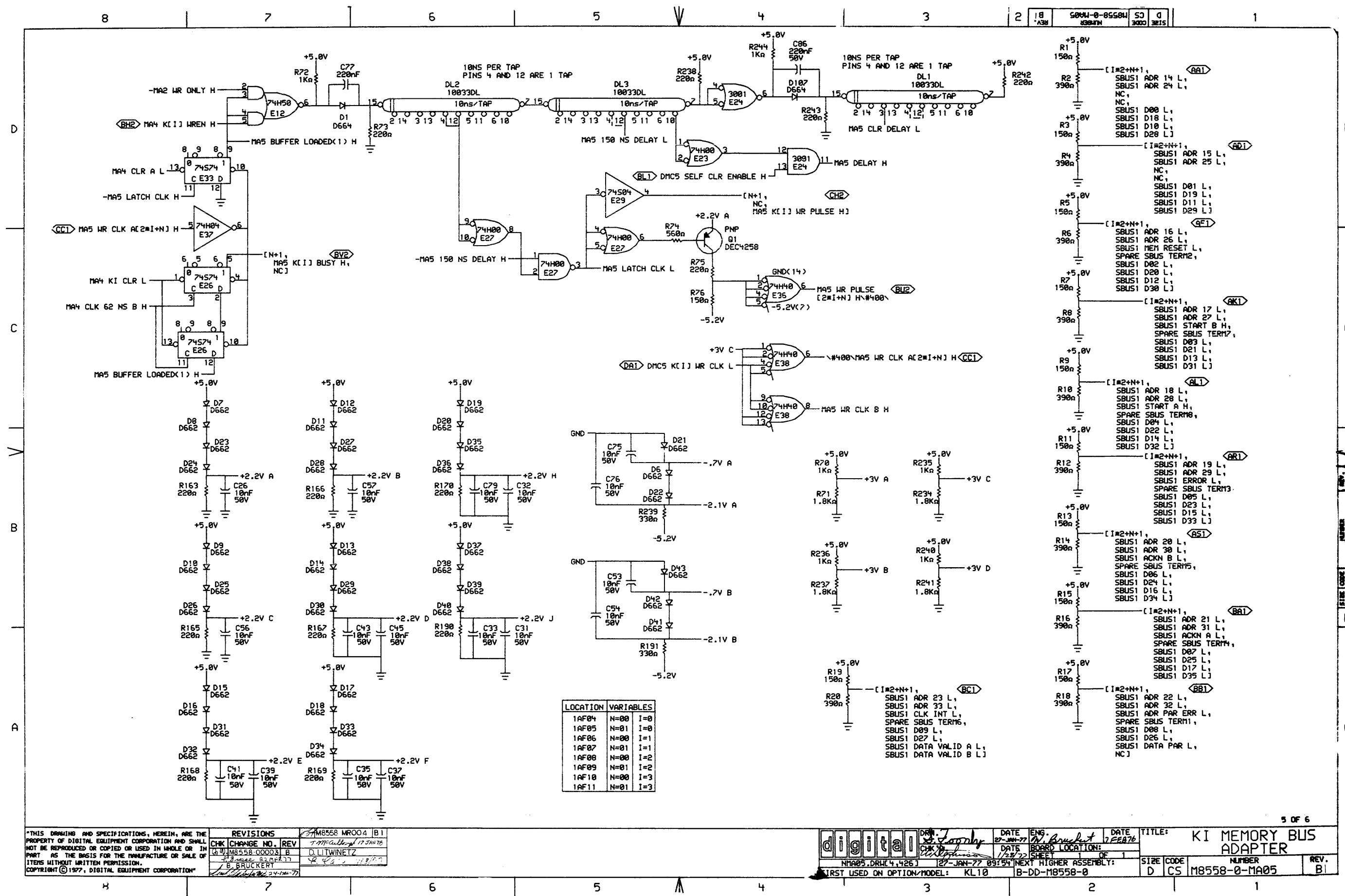
4 OF 6

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REVISIONS		
CH.	CH-1	REV. B1
IGE NO.	74M8558 MR004	74M8558 MR004
REV.	74M8558-00003	74M8558-00003

D. LITWINETZ
B. BRUCKERT
S. L. 1/18/77

digital		DRW. <i>J. Farley</i>	DATE 27-JAN-77	ENG. <i>M. Breslau</i>	DATE 7FEB76	TITLE: KI MEMORY ADAPTER
CHK ID	74M8558	DATE 27-JAN-77	BOARD LOCATION: 1/29/77	SHEET: 1 OF 1		
ITEM NO.	74M8558-0-MA04	ITEM NO. 74M8558-0-MA04				
FIRST USED ON OPTION/MODEL:	KL10	REV. B	SIZE D	CODE CS	NUMBER M8558-0-MA04	REV. B1



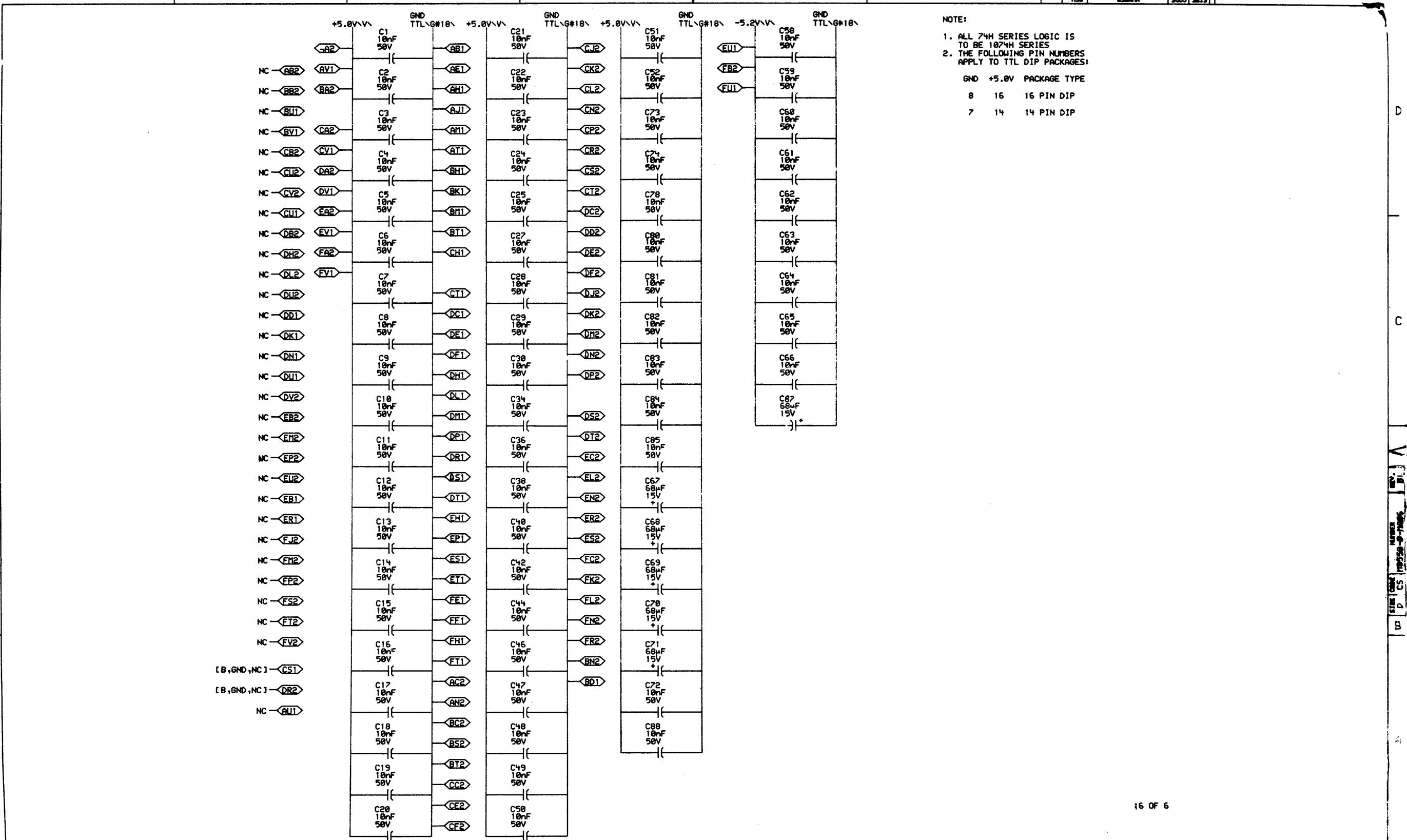
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REVISIONS			STM8558 MR004
CHK	CHANGE NO.	REV	TMCULLAGH 17 JAN
G	STM8558-00003	B	D. LITWINETZ
	P. J. GREE	S2.MR004	R. G. 1/17/91
	B. BRUCKERT		
	<i>initials</i>		



DATE 27-JAN-77	ENG. <i>M. Brumfield</i>	DATE 7 FEB 77
DATE 1/28/77	BOARD LOCATION: SHEET 1 OF 1	
09:54 NEXT HIGHER ASSEMBLY: P-000-M9558-2		

TITLE: KI MEMORY BUS
ADAPTER



NOTE:

- ALL 74H SERIES LOGIC IS TO BE 107H SERIES
- THE FOLLOWING PIN NUMBERS APPLY TO TTL DIP PACKAGES:

GND +5.8V PACKAGE TYPE

8 16 16 PIN DIP

7 14 14 PIN DIP

6 OF 6

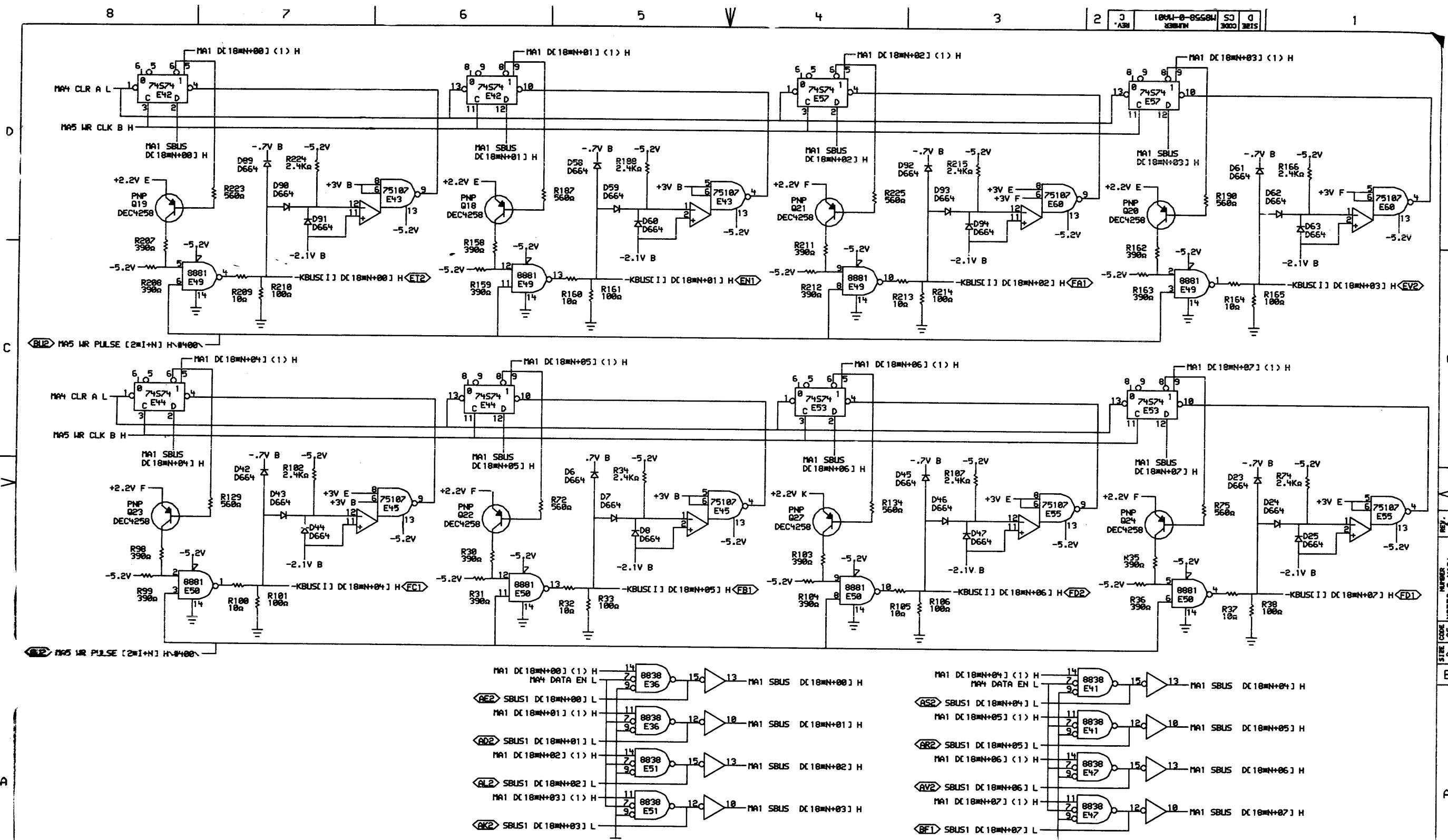
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REVISONS M8558 MRO04 B1
DATE 02-FEB-75
CMM NGE NO. REV 740003 B D. LITWINETZ
58-00003 B
M8558-0 MA06
BUCKERT
20-100-111
20-100-111
20-100-111

digital DR C. Smith DATE 02-FEB-75 ENG. M. Brant DATE 7-FEB-76
CMM DATE 28-JAN-77 BOARD LOCATION: SHEET 1 OF 1
58-00003 B DD-MB558-0 MA06 EX.DRIV 4,426
FIRST USED ON OPTION-MODEL: KL10 NEXT HIGHER ASSEMBLY: B-DD-MB558-0
SIZE CODE NUMBER REV.
D CS M8558-0-MA06 B

463

CUSTOMER PRINT SET		REVISION CONTROL SHEET					REVISIONS											
MFG	SET	DRAWING NO	NO OF SMT	DESCRIPTION	OPTION NO/FILE DATE	REVISIONS												
						NEW LAYOUT VERSION												
		-		MODULE REVISION														
		D-UA-M8558-0-0	5	KI10 MEMORY BUS ADAPTER														
		D-CS-M8558-0-MA01	1	KI10 MEMORY BUS ADAPTER														
		D-CS-M8558-0-MA02	1	KI10 MEMORY BUS ADAPTER														
		D-CS-M8558-0-MA03	1	KI10 MEMORY BUS ADAPTER														
		D-CS-M8558-0-MA04	1	KI10 MEMORY ADAPTER														
		D-CS-M8558-0-MA05	1	KI10 MEMORY BUS ADAPTER														
		D-CS-M8558-0-MA06	1	KI MEM BUS ADPT POWER AND GND														
		K-CO-M8558-0-4	1	KI10 MEMORY BUS ADAPTER														
		D-AH-M8558-0-5	1	KI10 MEMORY BUS ADAPTER														
		B-MH-M8558-0-6	1	MODULE ECO HISTORY														
		5010640	-	ETCH CIRCUIT BOARD PROCESS SHEET (REF ONLY)														
		P00-M8558-0-0																
CUSTOMER PRINT SET CODES		X = PRINT OF DOCUMENT INCLUDED IN PRINT SET C = INCLUDES ALL PRINTS INDICATED ON DOCUMENT S = CONFIDENTIAL AUTHORIZED SIGNATURE REQUIRED					ECO NO											
499							MRO04											
						TITLE	KL10 MEMORY BUS ADAPTER			SHEET 3 OF 3	SIZE CODE	NUMBER	REV					
											B DD	M8558-0	D					



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REV. E/C: 1/18/77

REVISIONS

CHK

CHANGE NO.

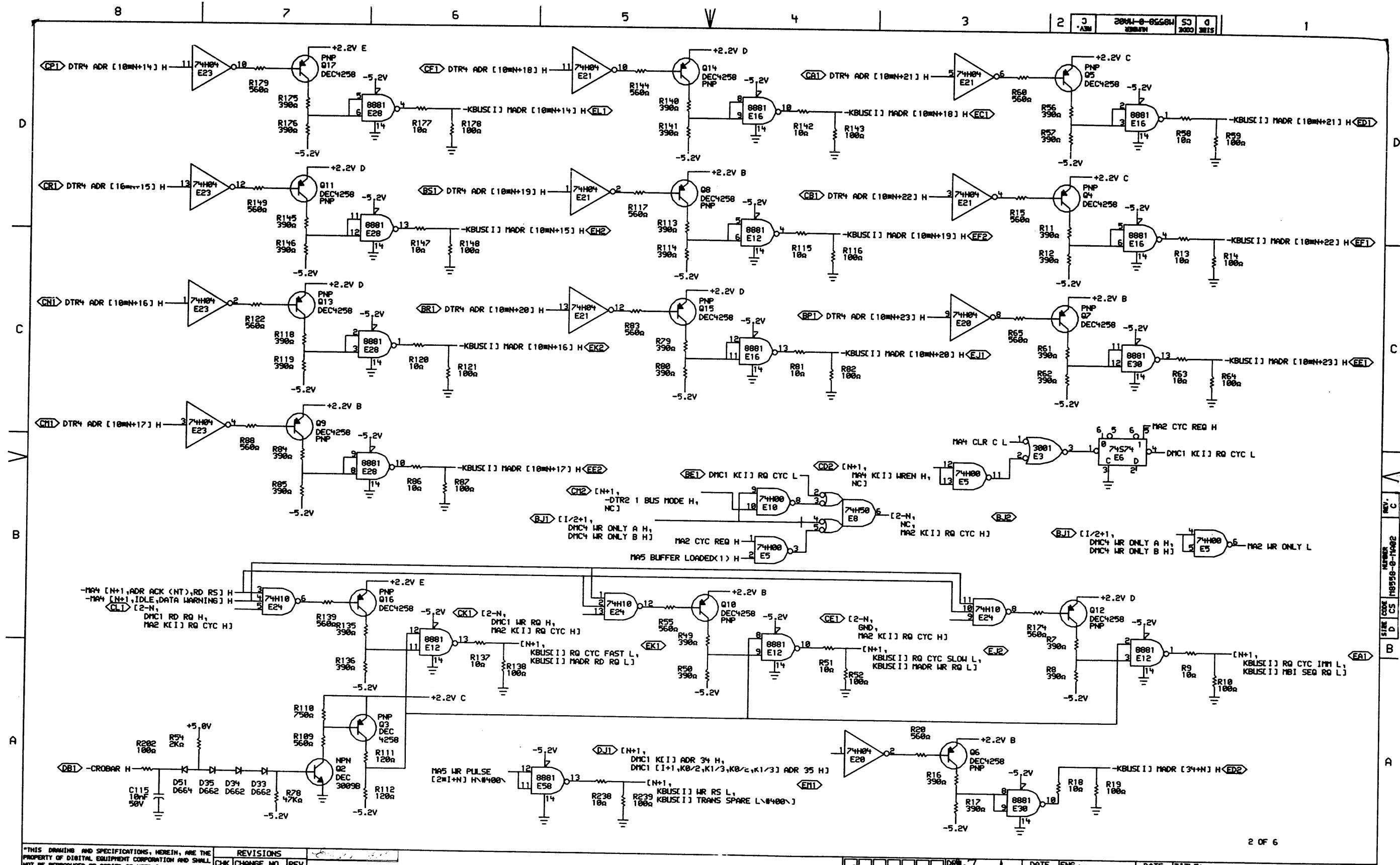
REV.

M8558 MR009 C

T. Muller 16 JAN 78

D. LITWINETZ

REVISIONS		REV.	DATE	ENG.	DATE	TITLE:
CHK	CHANGE NO.	REV.	15-AUG-77	J. J. Jordon	7/20/77	KI MEMORY BUS
M8558 MR009	C			CHK'D 2-22-77		ADAPTER
T. Muller	16 JAN 78			DATE	BOARD LOCATION:	
D. LITWINETZ				10/18/77	SHEET 1 OF 1	
FIRST USED ON OPTION/MODEL:		KL10	B-DD-M8558-0			
SIZE	CODE	NUMBER				
D	CS	M8558-0-MA01	REV.	C		

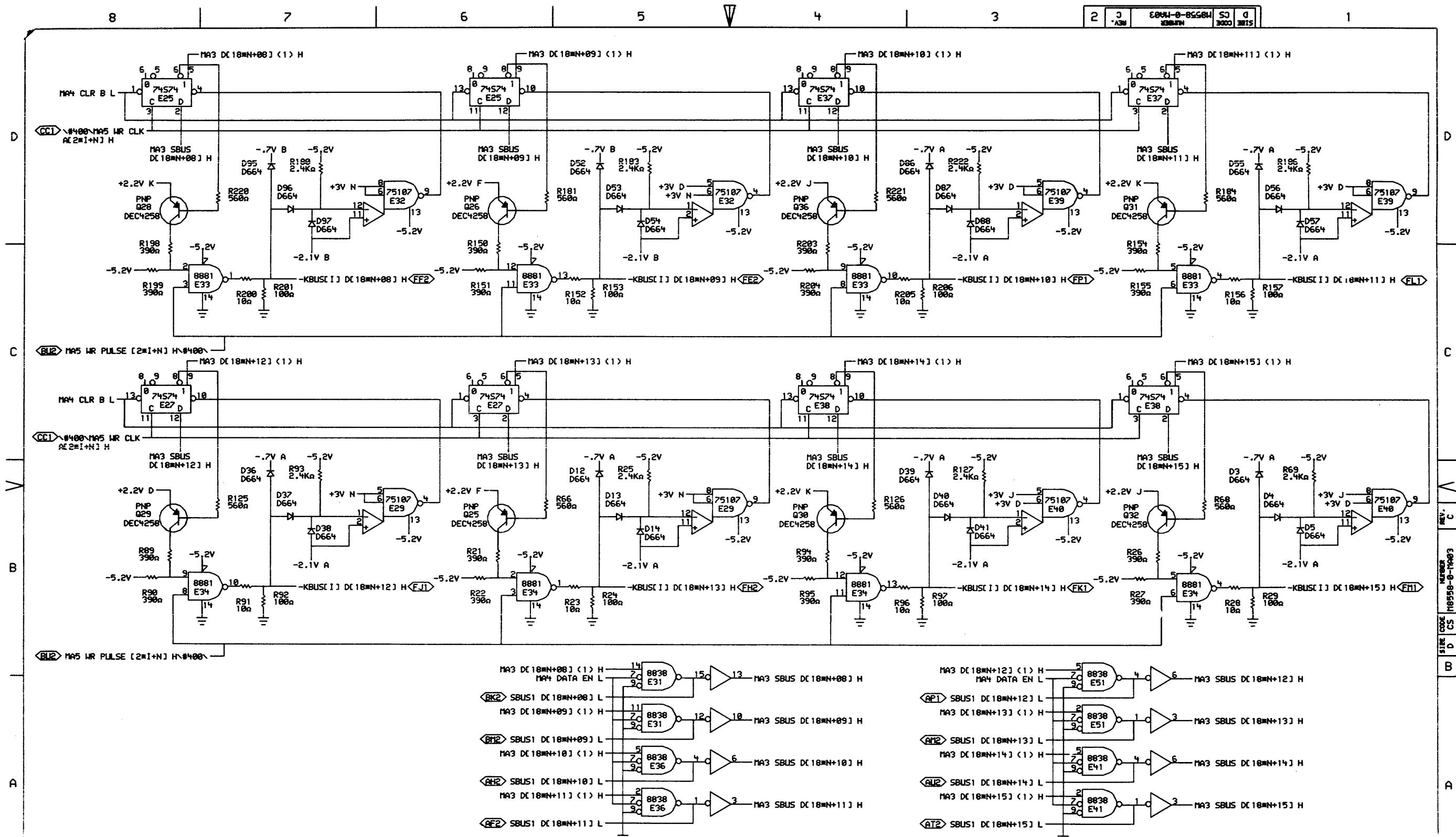


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REVISIONS		
CHG	CHANGE NO.	REV.

IN CHARGE
M8558 MRC
T.M. Culbong
D. LITWINE

digital	DRW-1000	DATE 10-AUG-77	ENG <i>Stewart</i>	DATE 7/1/77	TITLE: KI MEMORY BUS ADAPTER			
CHP-1000		BOARD LOCATION: <i>81022</i>		SHEET 1 OF 1	SIZE D	CODE CS	NUMBER M8558-A-MAR2	REV. C
MASTER DRW 4.6211 10-AUG-77 12:58 NEXT HIGHER ASSEMBLY:								
FIRST USED ON OPTION MODEL: KL10			B-DD-M8558-0					



3 OF 6

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REVISIONS
CHG CHANGE NO. REV
MB558 VR009 C
TAKC 17 JAN 76
D. LITWINETZ

SBUS1 DE 18#N+08J L
MA3 DE 18#N+09J (1) H
SBUS1 DE 18#N+09J L
MA3 DE 18#N+10J (1) H
SBUS1 DE 18#N+10J L
MA3 DE 18#N+11J (1) H
SBUS1 DE 18#N+11J L
MA3 DE 18#N+12J (1) H
SBUS1 DE 18#N+12J L
MA3 DE 18#N+13J (1) H
SBUS1 DE 18#N+13J L
MA3 DE 18#N+14J (1) H
SBUS1 DE 18#N+14J L
MA3 DE 18#N+15J (1) H
SBUS1 DE 18#N+15J L

8838 E31 150 13 MA3 SBUS DE 18#N+08J H
8838 E51 4 6 MA3 SBUS DE 18#N+12J H
8838 E51 1 3 MA3 SBUS DE 18#N+13J H
8838 E41 4 6 MA3 SBUS DE 18#N+14J H
8838 E41 1 3 MA3 SBUS DE 18#N+15J H

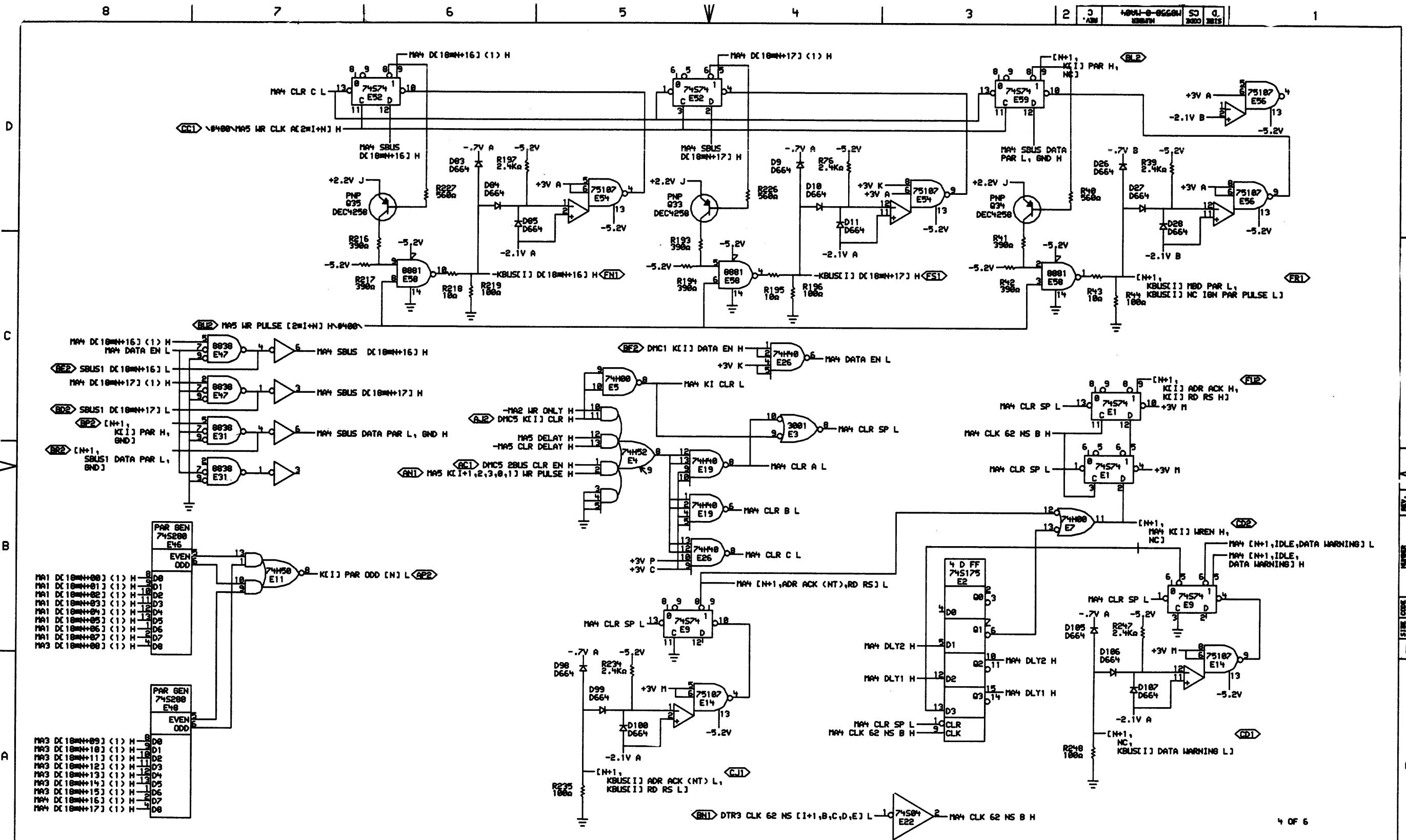
8838 E31 150 13 MA3 SBUS DE 18#N+08J H
8838 E51 4 6 MA3 SBUS DE 18#N+12J H
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8838 E41 4 6 MA3 SBUS DE 18#N+14J H
8838 E41 1 3 MA3 SBUS DE 18#N+15J H

8838 E31 150 13 MA3 SBUS DE 18#N+08J H
8838 E51 4 6 MA3 SBUS DE 18#N+12J H
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8838 E41 4 6 MA3 SBUS DE 18#N+14J H
8838 E41 1 3 MA3 SBUS DE 18#N+15J H

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8838 E41 1 3 MA3 SBUS DE 18#N+15J H

503



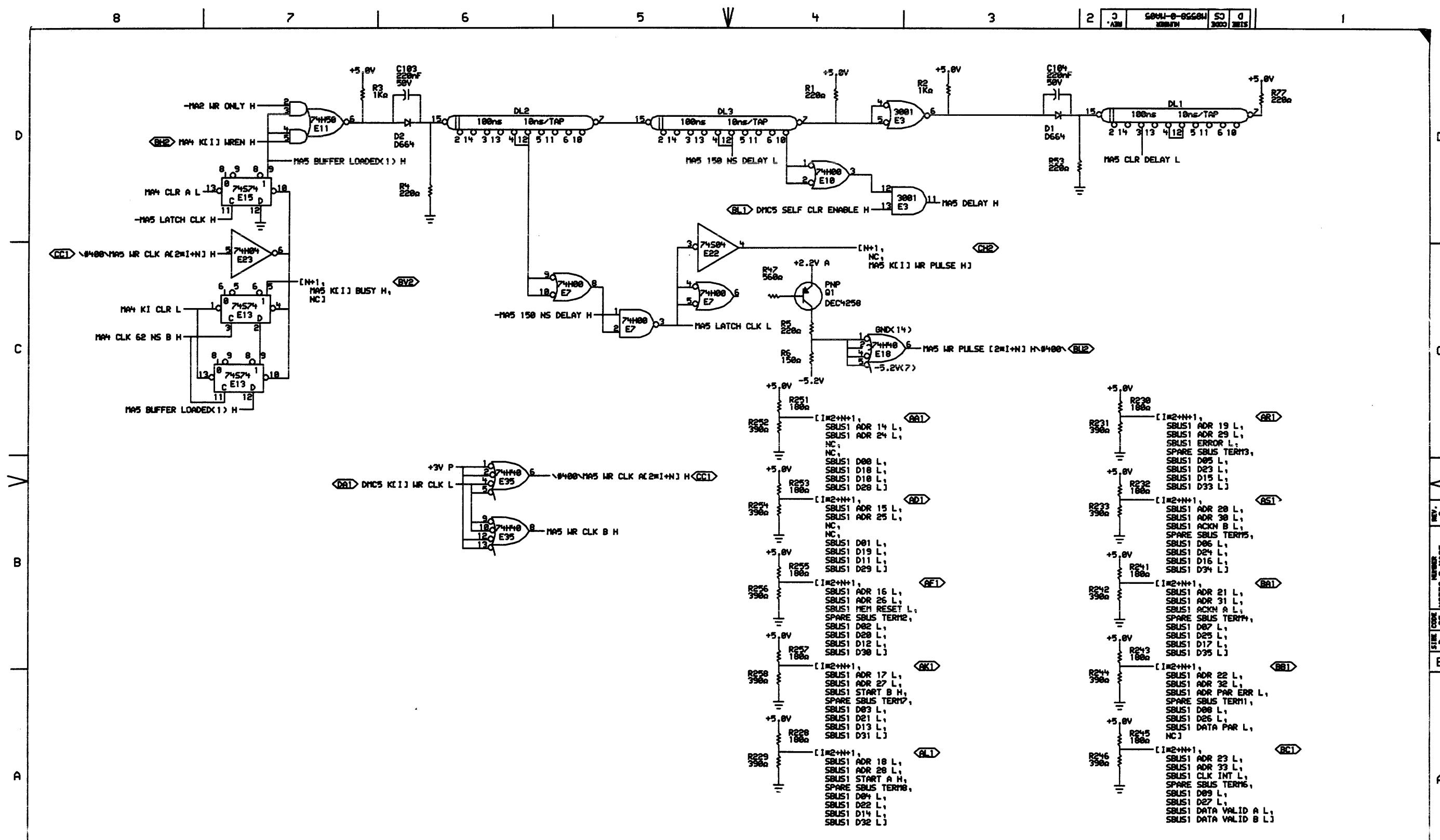
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REVISIONS
E.C. - 11/18/77

CHK	CHANGE NO.	REV
	M8558 MRC009	C
TMC	17 JAN 78	

D. LITWINETZ

DRW. [Signature]	DATE 15-AUG-77	ENG. [Signature]	DATE 15-AUG-77	TITLE: KI MEMORY ADAPTER	
CMC	DATE 15-AUG-77		15-AUG-77	BOARD LOCATION: SHEET 1 OF 1	
MARSHER.DRW(4.581)	NEXT HIGHER ASSEMBLY:		15-AUG-77	SIZE CODE NUMBER REV.	
FIRST USED ON OPTION/MODEL: KL10	B-DD-M8558-0	CS M8558-0-MA04	C		

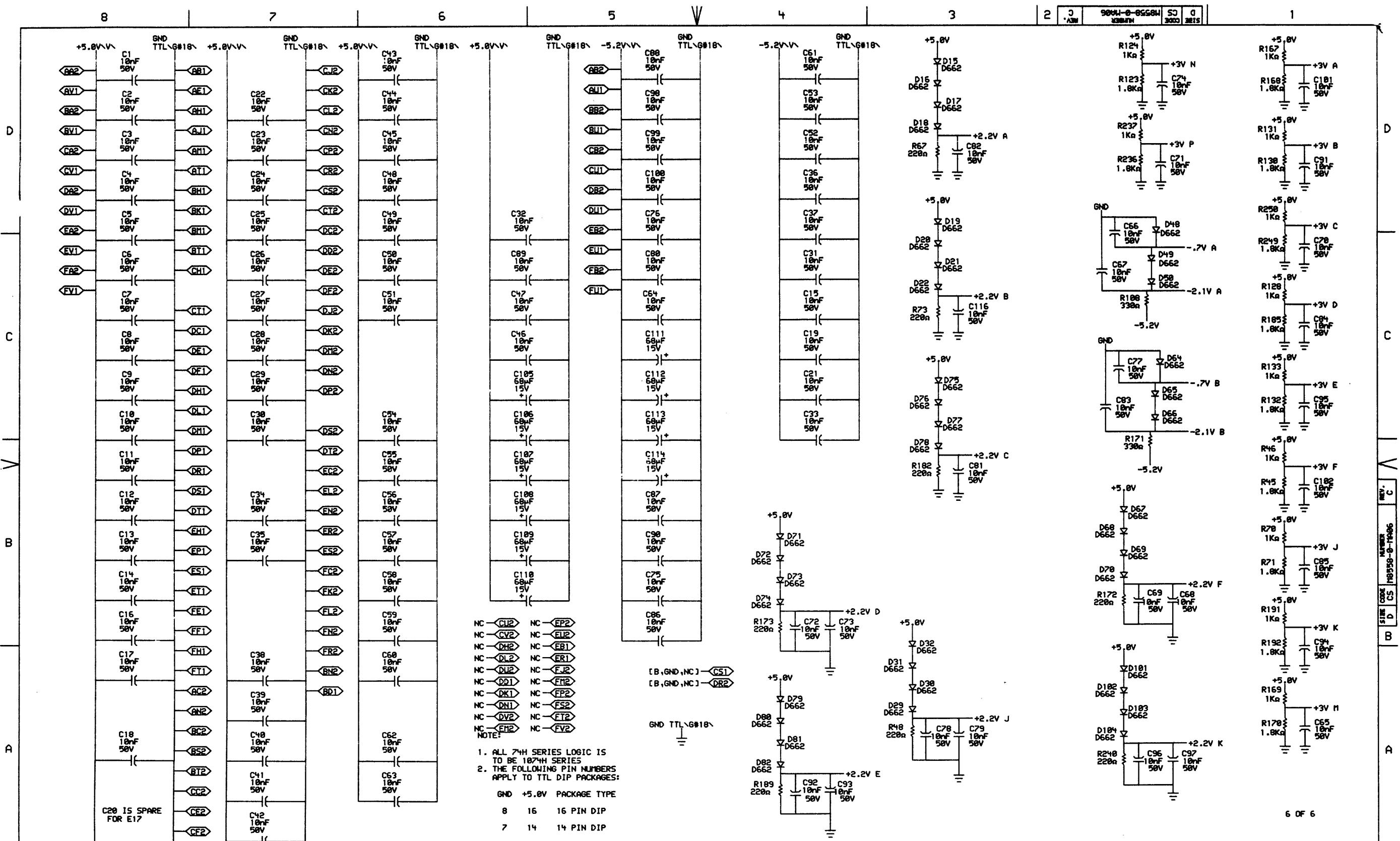


5 of 5

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VISIONS *E. Lacy 11/18/77*

digital	DRW- 1400-100 C100-100	DATE RE-AUG-77	ENS 1000-9	DATE 1/30/77	TITLE: KI MEMORY BUS ADAPTER
			SPEED LOCATION:		
HARDER.DRW 4.621	13-1000-1000	13125	NEXT HIGHER ASSEMBLY:		
FIRST USED ON OPTION-NODEL: KL10		B-DD-M8558-0	SIZE	CODE	NUMBER
			D	CS	M8558-0-MA05
					REV.
					C



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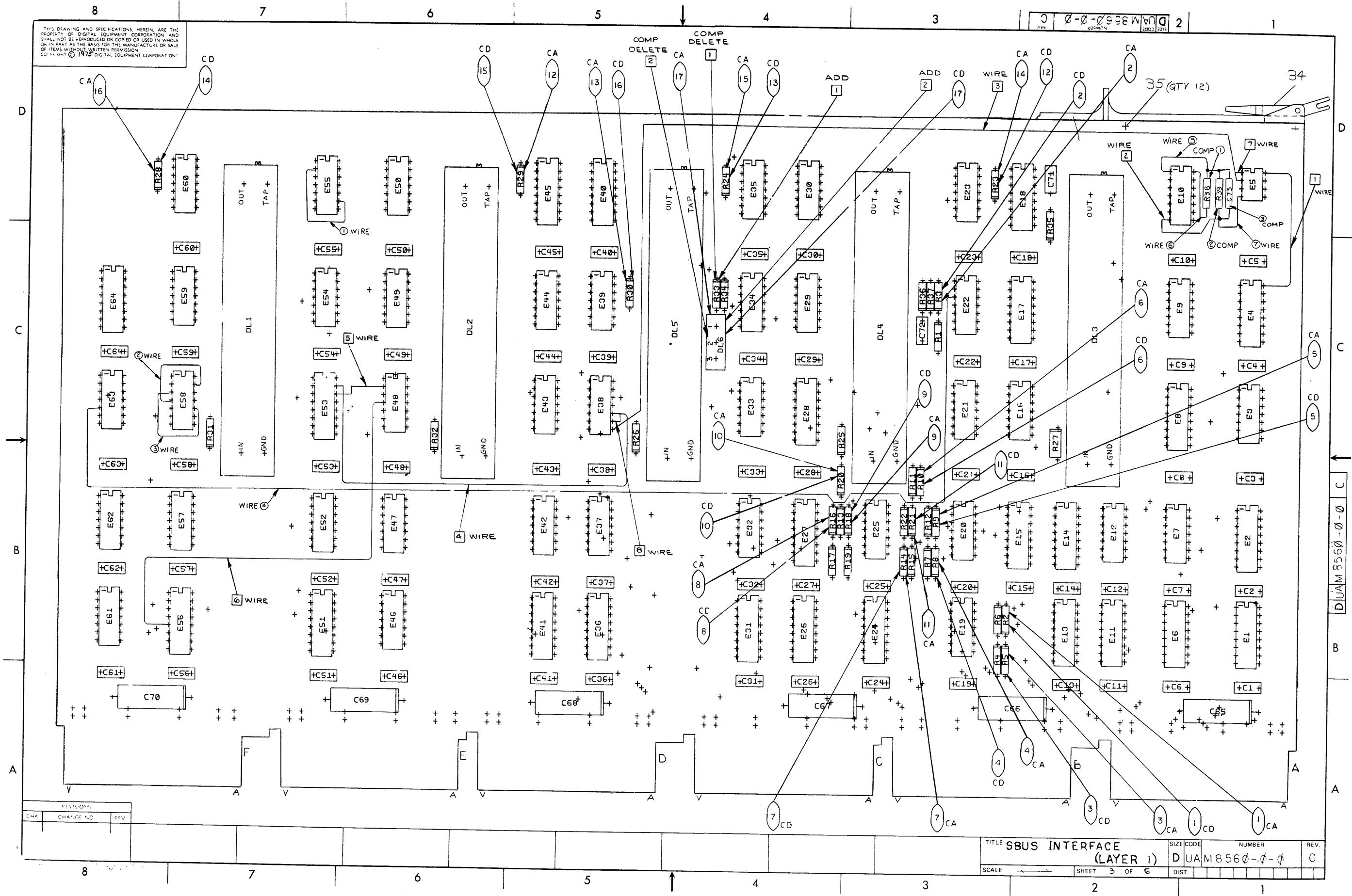
REVISIONS *E. Calvo 11/8/77*

CHK	CHANGE NO.	REV
M8558 NR009	C	
T.Wallengren	17-AUG-77	
D.LITWINETZ		

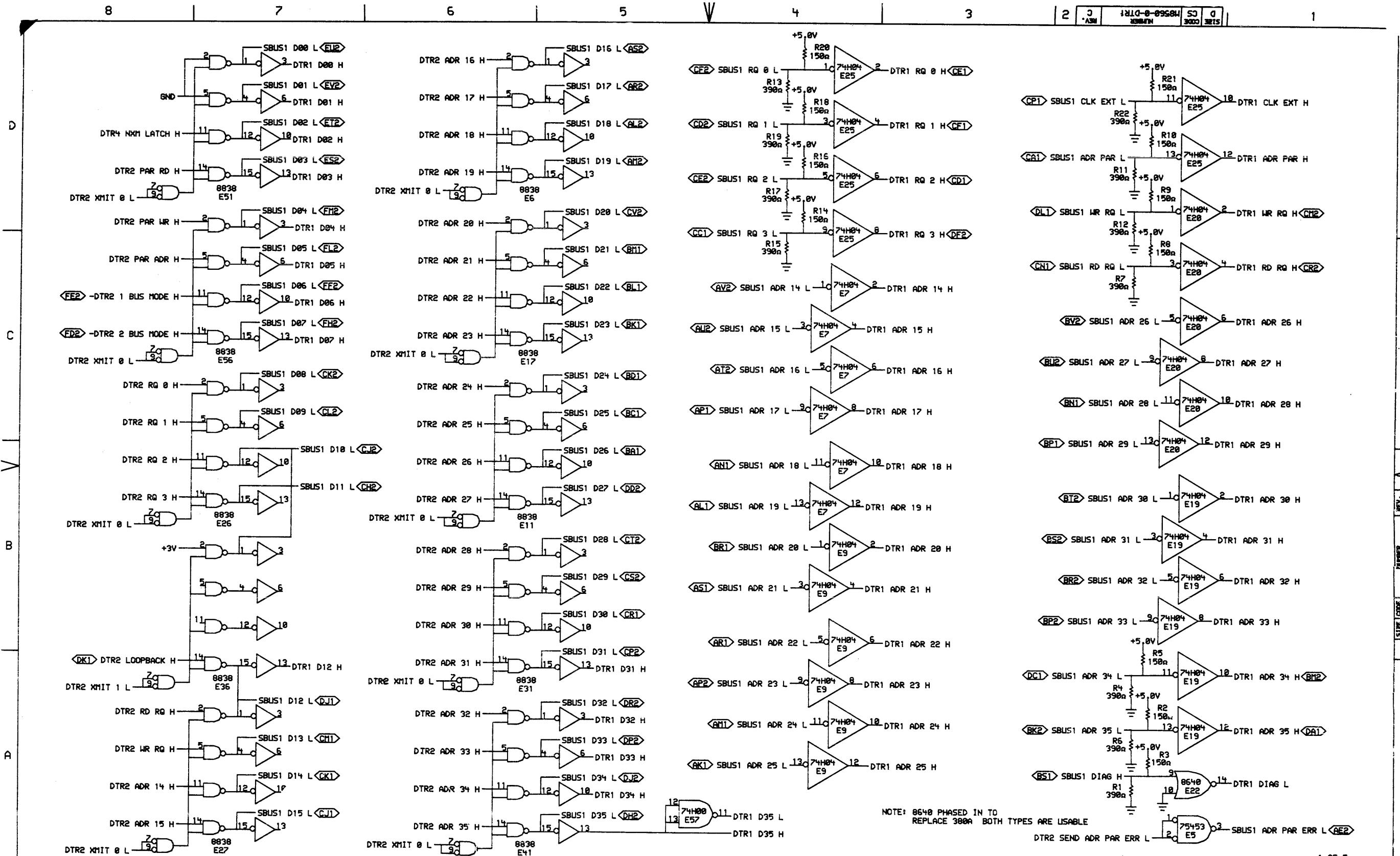
DATE: 15-AUG-77	ENG: <i>J. Journe</i>	DATE: 7/30/77
CHK'D: <i>E. Calvo</i>	BOARD LOCATION: <i>Sheet 1 of 1</i>	TITLE: KI MEMORY BUS ADAPTER
MAGNETIC DRIV4,621 05-AUG-77 14:11 NEXT HIGHER ASSEMBLY: KL10		SIZE: D
FIRST USED ON OPTION MODEL: B-DD-19558-0		CODE: CS
NUMBER: M8558-0-MA06		REV: C

S06

CUSTOMER PRINT SET					REVISION CONTROL SHEET																			
MFG SET	T S	DRAWING NO	NO OF SHT	DESCRIPTION	OPTION NO/FILE DATE	REVISIONS																		
		-		MODULE REVISION		A	B	C	C															
		D-UA-M8560-0-0	6	SBUS INTERFACE		-	A	B	C															
		D-CS-M8560-0-DTR1	1	DMA TIMING AND SBUS TRANCEIVERS		-	A	B	C															
		D-CS-M8560-0-DTR2	1	DMA TIMING AND SBUS TRANCEIVERS		-	A	A	A															
		D-CS-M8560-0-DTR3	1	DMA TIMING AND SBUS TRANCEIVERS		-	A	B	B															
		D-CS-M8560-0-DTR4	1	DMA TIMING AND SBUS TRANCEIVERS		-	A	A	B															
		D-CS-M8560-0-DTR5	1	DMA TIMING,POWER AND GND		-	A	A	A															
		K-CO-M8560-0-4	1	SBUS INTERFACE (CALDEC DATA BASE)		B	B	B	B															
		D-AH-M8560-0-5	4	SBUS INTERFACE		A	A	A	A															
		B-MH-M8560-0-6	1	MODULE ECO HISTORY		-	REF	=	-															
		5010655 POO-M8560-00	-	ETCH CIRCUIT BOARD PROCESS SHEET (REF ONLY)		B	B	B	B															
						-	-	-	-															
CUSTOMER PRINT SET CODES	X	= PRINT OF DOCUMENT INCLUDED IN PRINT SET				ECO NO	ORIG	00001	00002	00003														
	C	= INCLUDES ALL PRINTS INDICATED ON DOCUMENT																						
	S	= CONFIDENTIAL AUTHORIZED SIGNATURE REQUIRED																						
507							TITLE	SBUS INTERFACE				SHEET 2 OF 2	B	DD	M8560-0		NUMBER	REV	G					



508



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JULY 1977 BY DECP

REVISIONS		
CHG	CHANGE NO.	REV
-	M8560-00003	C

B.BRUCKET
JULY 1977 BY DECP

digit@DECP	DATE 10-AUG-77	ENG. No. 1147	DATE 10-AUG-77	TITLE: DMA TIMING AND SBUS TRANCEIVERS
DTR1EX, DTR2, 4, 5, 6, 7, 8	DATE 10-AUG-77	BOARD LOCATION: 1AF83	DATE 10-AUG-77	SHEET 1 OF 1
FIRST USED ON OPTION/MODEL: DMA20	SIZE D	CODE CS	NUMBER M8560-0-DTR1	REV. C
B-DD-M8560-0				

509

JULY 1977 BY DECP

8

7

6

5

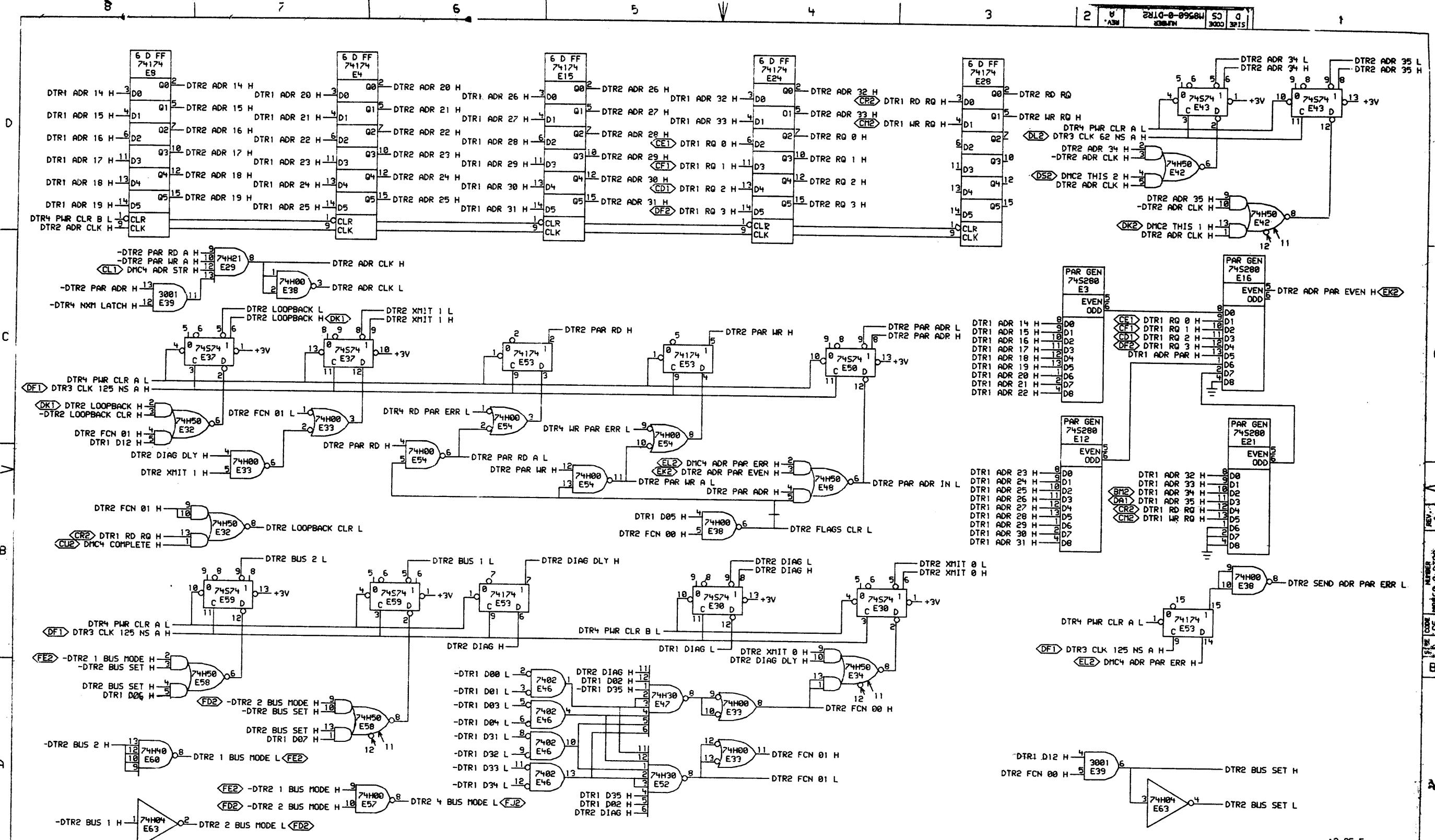
4

3

2

1

1 OF 5



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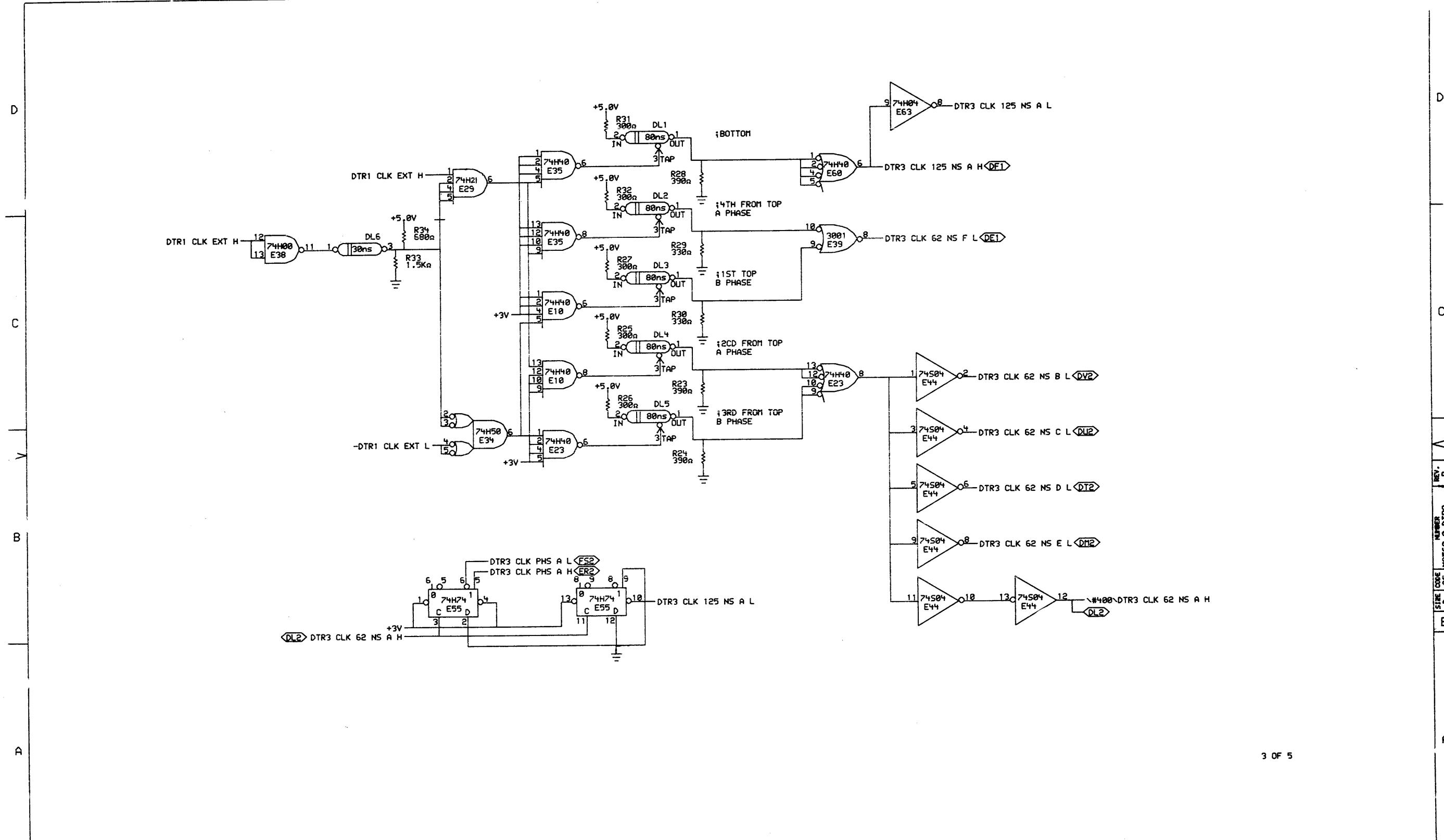
REVISONS

CHG. CHANGE NO. REV

07H M8560-0-DTR2 A

W. BRUCKERT 7/75

digital	DRA	DATE	ENG.	DATE	TITLE:
	S. Lourley	5/23/75	J. Brundt	6/19/75	DMA TIMING AND
	C. J. Stephen	5/23/75	JAFB3		SBUS TRANSCEIVERS
DTR2EX4,426	12 MAY 75 08:33	NEXT HIGHER ASSEMBLY:			
FIRST USED ON OPTION/MODEL:	D1420	B-DD-M8560-0	SIZE	CODE	NUMBER
			D	CS	M8560-0-DTR2
			A		

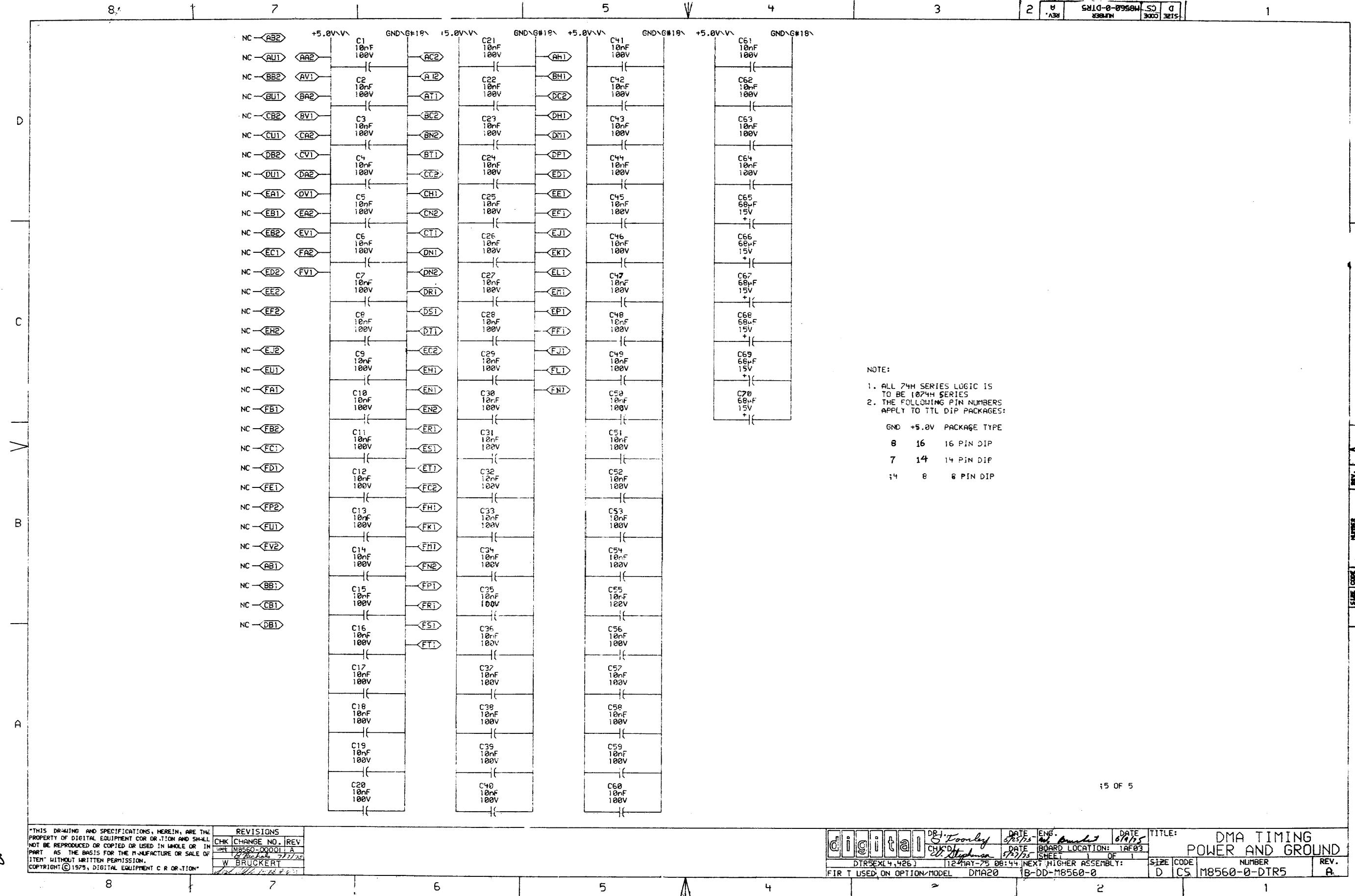


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REVISIONS	
CHK	CHANGE NO.
G/A	REV
1.0	000021

L. MARTIN
B. BRUCKERT
1/25/77

digital DRW 7/20/77 DATE 24-JAN-77 ENG. Buckert DATE 24-JAN-77
 CHK 74S04 BOARD LOCATION: 1AF03
 1/25/77 SHEET 1 OF 1
 DTR3EX.DRWE4,6231 [24 JAN 77 18:09 NEA] OTHER ASSEMBLY:
 FIRST USED ON OPTION/MODEL: DMA20 3560-0
 SIZE CODE D CS NUMBER M8560-0-DTR3 REV. B

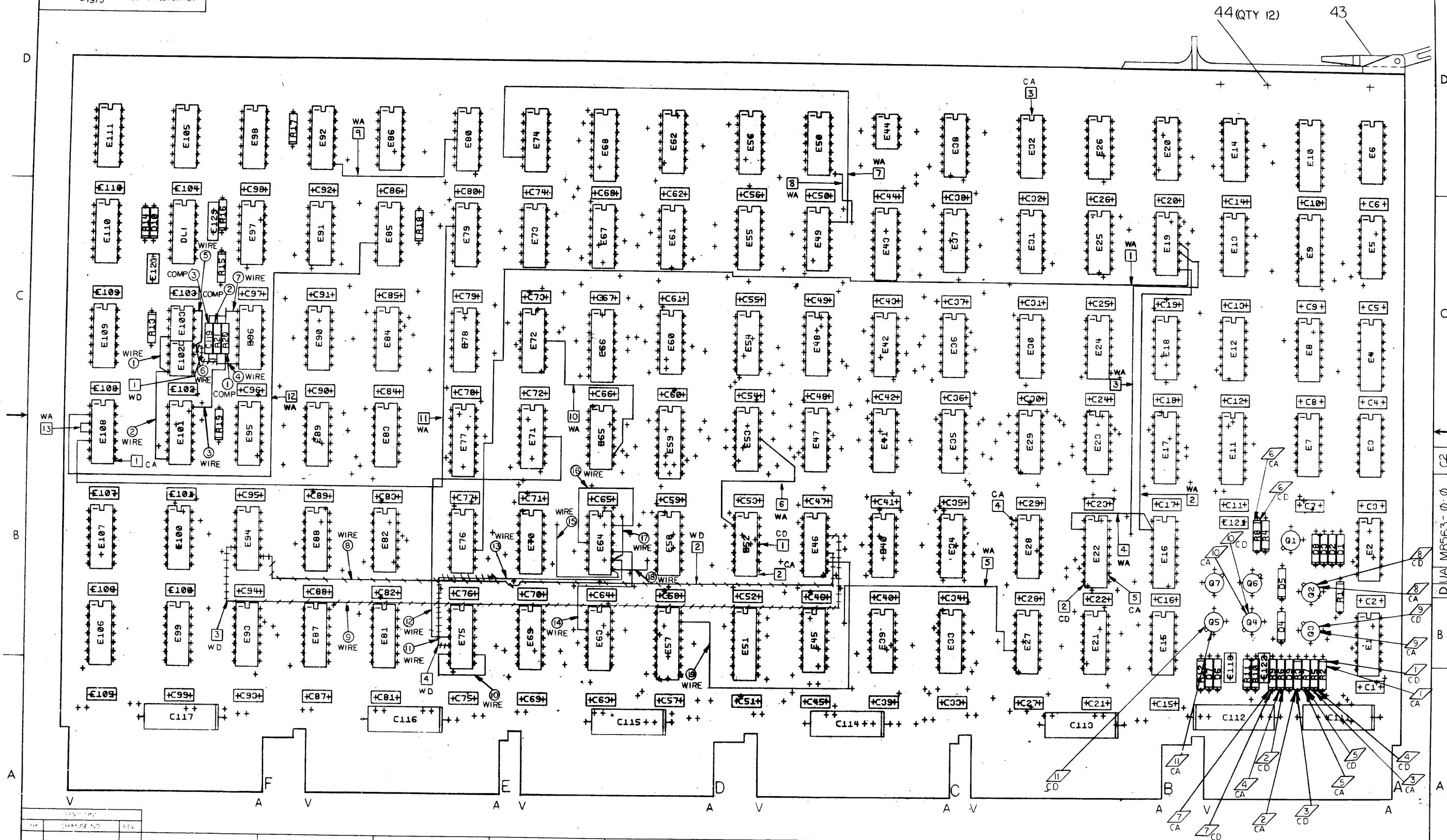


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C = INCLUDES ALL PRINTS INDICATED ON DOCUMENT
S = CONFIDENTIAL AUTHORIZED SIGNATURE REQUIRED

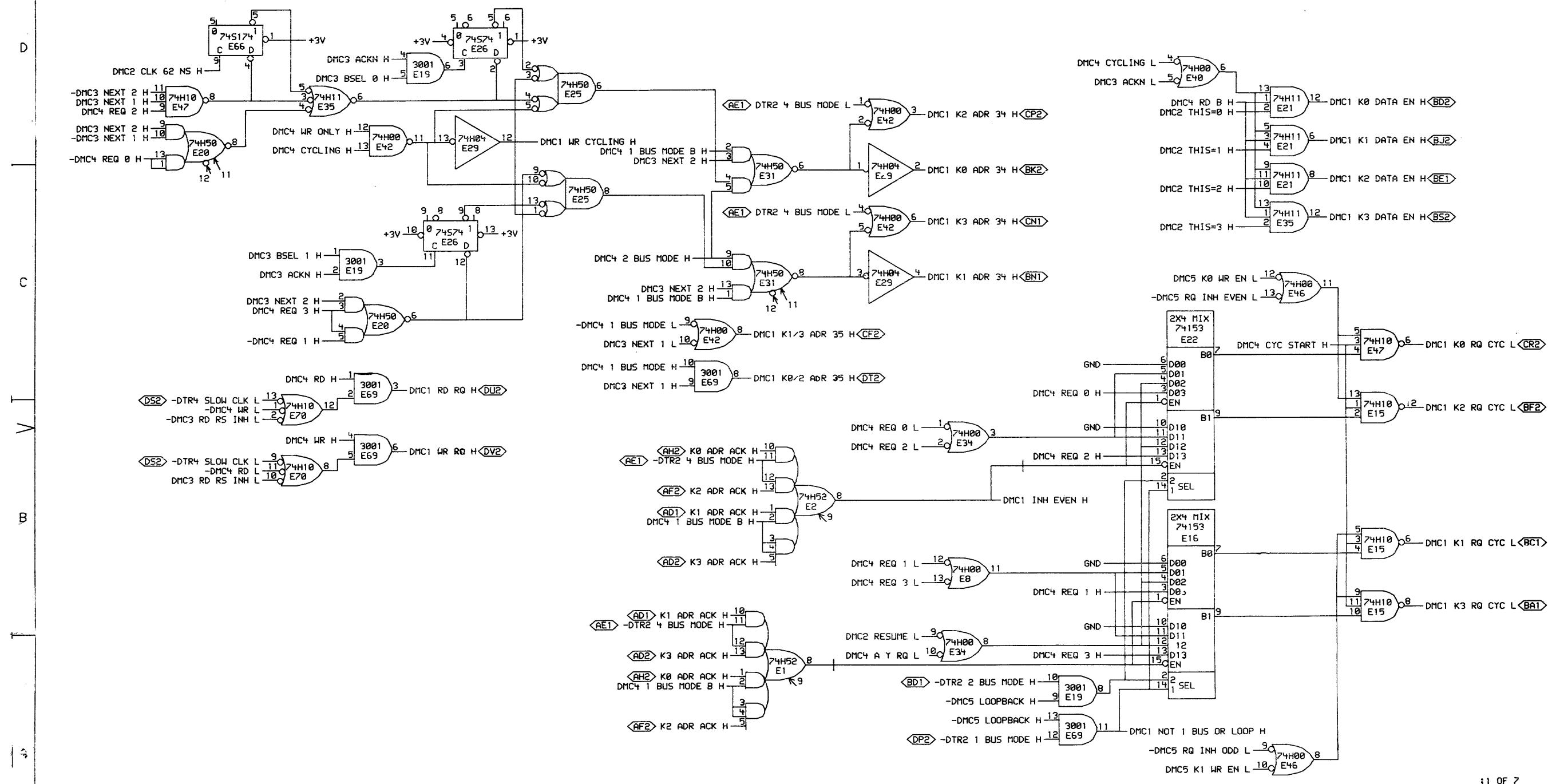
ECO NO	ORIG	00001	00001A	00002	00003	00004	00005

TITLE	SHEET 2 OF 3	SIZE CODE	NUMBER	REV
DMA2Ø BOARD #2	B DD	M8563-Ø	E	

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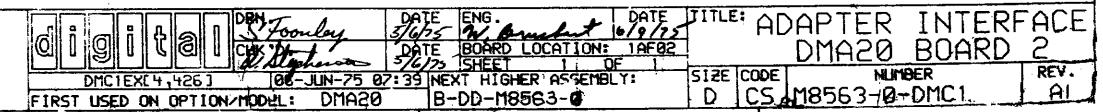


TITLE	SIZE CODE		NUMBER	REV.
DMA2Ø BOARD #2	D	UA	M8563-Ø-Ø	C2
SCALE	1	SHEET	3 OF 8	DISP.



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REVISIONS		
CHK	CHANGE NO.	REV
	M8563-00001	A
	W. BRUCKERT	
<i>Start date for rev 1-29-75</i>		



D

D

C

C

V

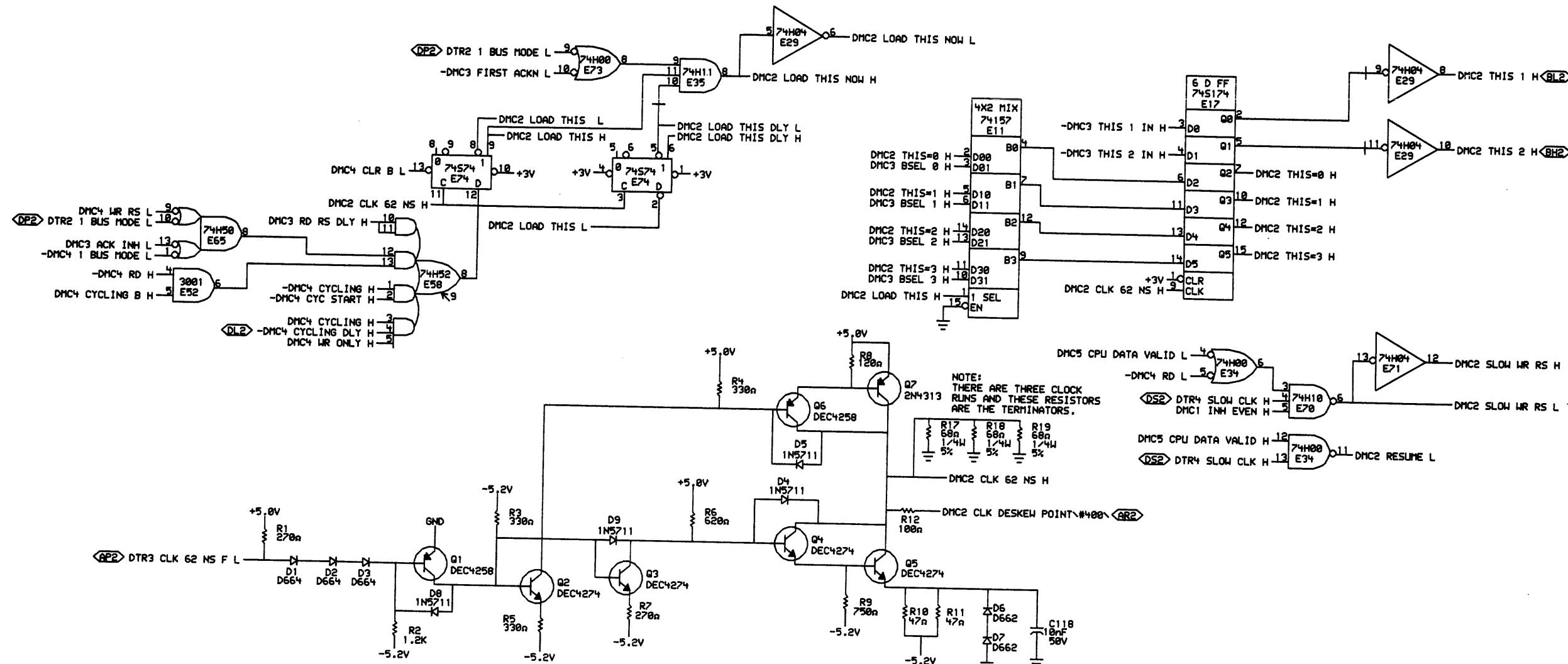
V

B

B

A

A



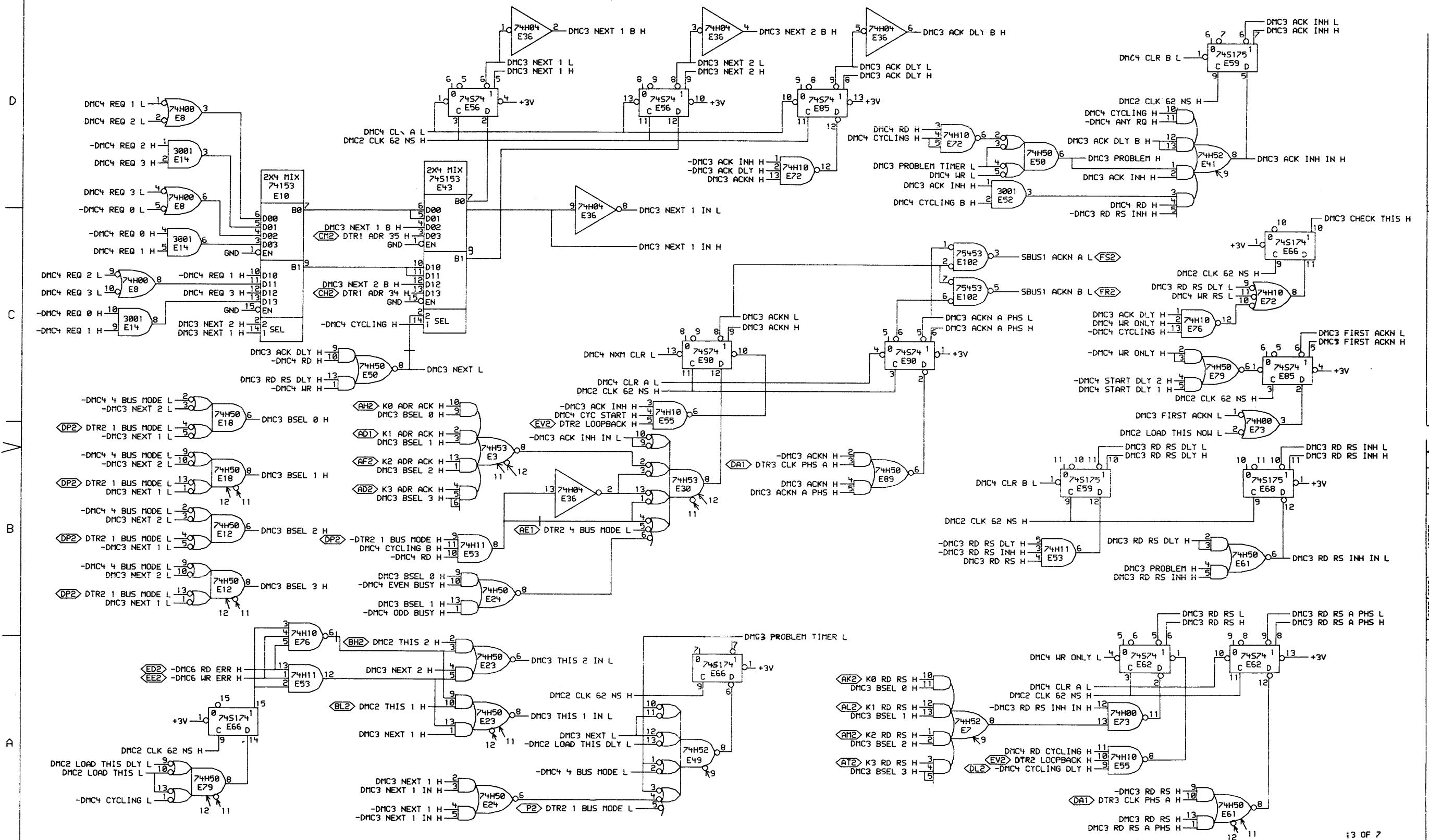
12 OF 7

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REVISIONS		
CHK	CHANGE NO.	REV

M8563-00003 B2
W. BRUCKERT 15-APR-77

drawn by [Signature]	date 15-APR-77	eng. [Signature]	date 15-APR-77	title: BUS SELECTION	
checked by [Signature]	date 15-APR-77	board location: TAFB2	sheet 1 of 1		dma20
FIRST USED ON OPTION/MODEL: DMA20		size code D CS M8563-0-DMC2		rev. B2	number
B-DD-118563-0					

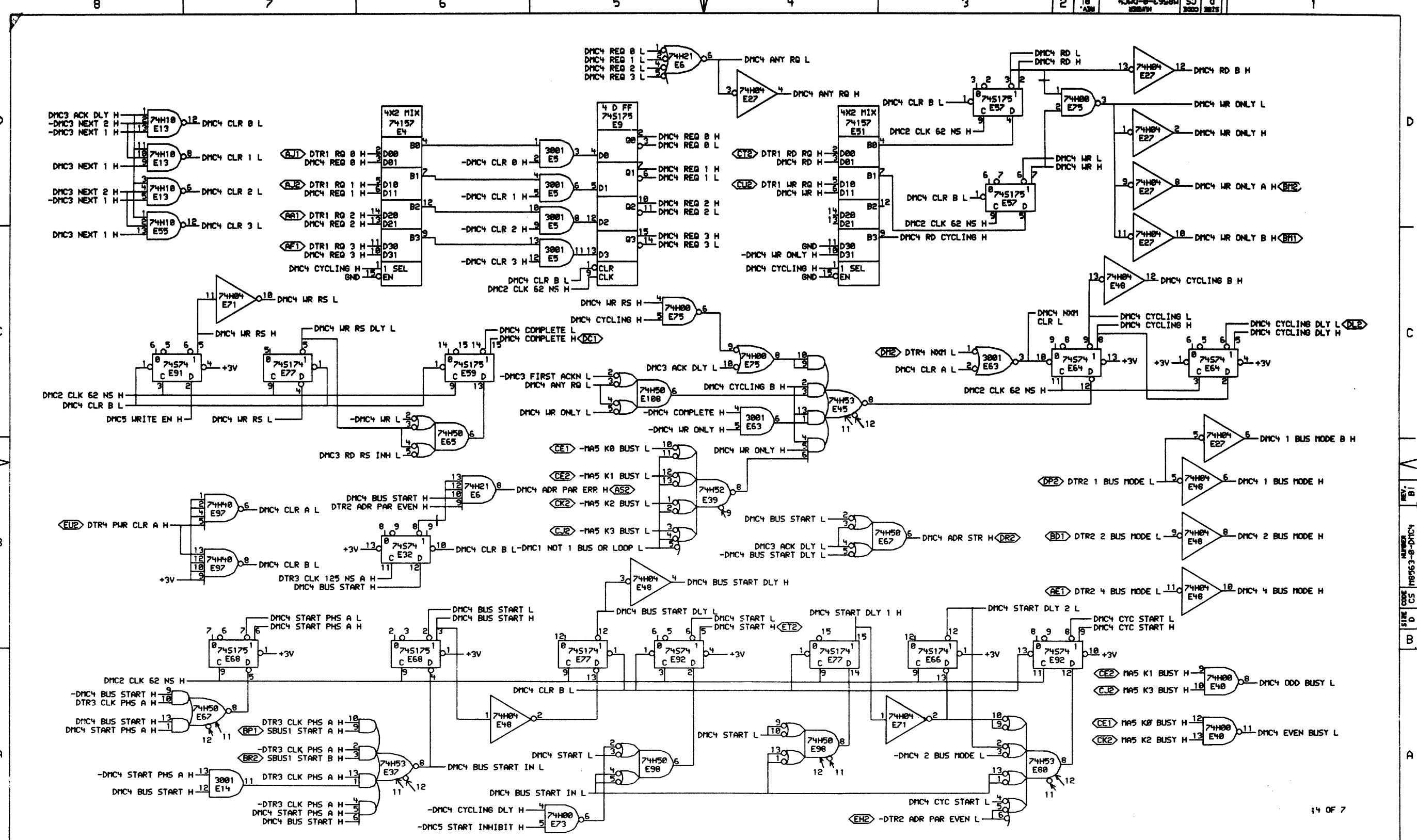


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REVISIONS			M8563-00004 L
CHK	CHANGE NO.	REV	W.BRUCKERT
IN OF	M 8563-00004	A	<i>1/24/64 fm 1-2</i>
	W. BRUCKERT		

digital	DRN: <i>J. Fooley</i>	DATE: <i>5/10/76</i>	ENG.: <i>R. Condit</i>	DATE: <i>6/17/75</i>	TITLE: ADAPTER INTERFACE DMA20 BOARD 2
	CH/D: <i>J. Stephens</i>	DATE: <i>5/10/76</i>	BOARD LOCATION: <i>1A0B2</i>		
			SHEET: <i>1</i>	OF: <i>1</i>	
DMC3EXI4,426J			105 JUN-75 07:42 NEXT HIGHER ASSEMBLY:		
FIRST USED ON OPTION/MODEL: DMA20			B-DD-M8563-0		
	SIZE	CODE	NUMBER		REV.
	D	CS	M8563-0-DMC3		A1

3 OF

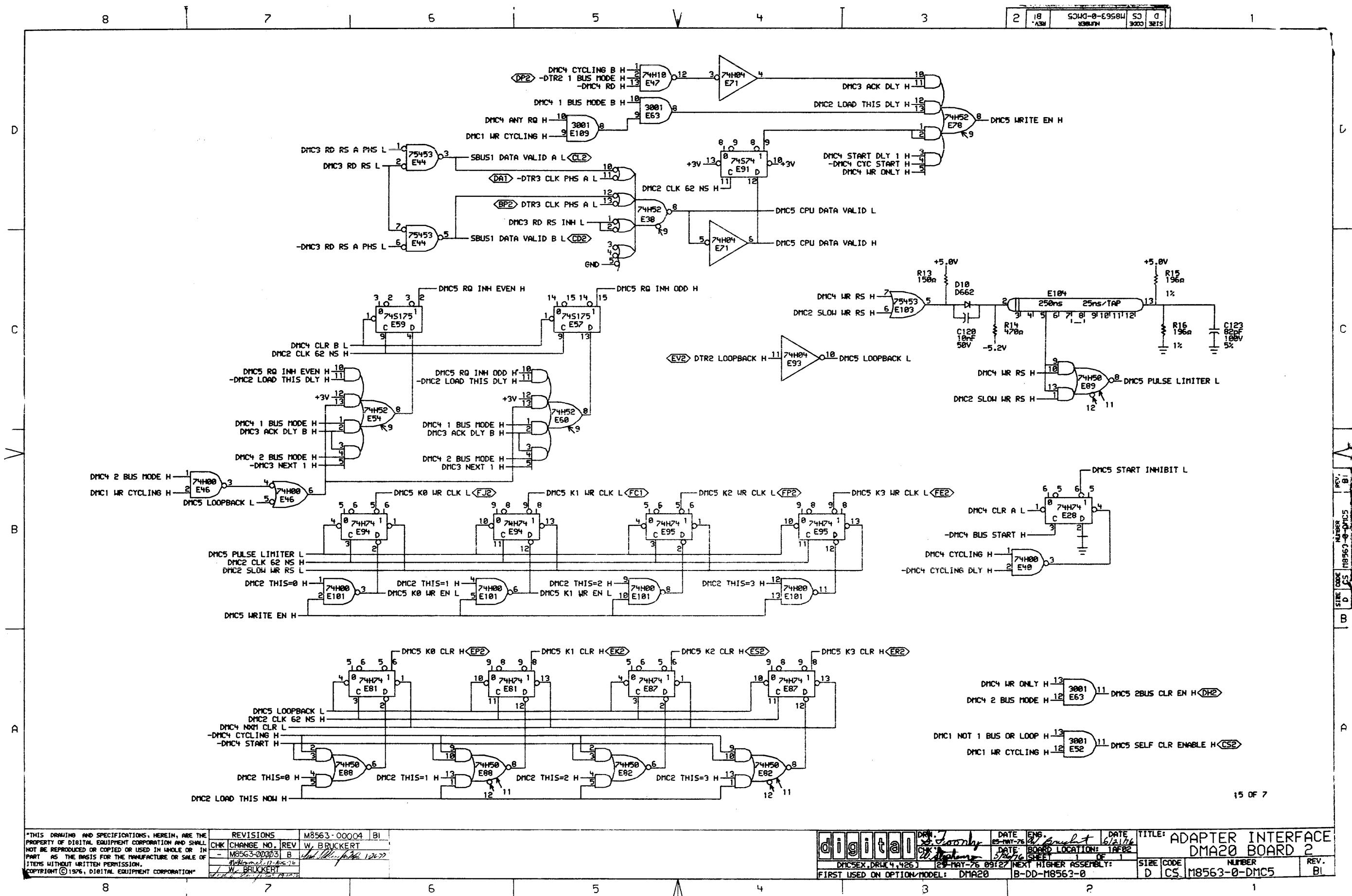


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REVISIONS	
CHK	CHANGE NO.
-	M8563-00073
	W. BRUCKERT

M8563 - 00
REV W. BRUCKE
B *Ende 1944*

digital	DRA 7 <i>X. Jorby</i>	DATE 25-MAY-76	ENG. <i>M. Bruckart</i>	DATE 6/2/76	TITLE: ADAPTER INTERFACE DMA20 BOARD 2		
CHK D <i>W. Stephan</i>	DATE 5/26/76	BOARD LOCATION: 1A1F02			SHEET 1	OF 1	
DMC4EX.DRW(4,426)		25-MAY-76 09:25	NEXT HIGHER ASSEMBLY:				
FIRST USED ON OPTION/MODEL: DMA20		B-DD-M8563-0					
		SIZE D	CODE CS	NUMBER M8563-0-DMC4			REV. B1

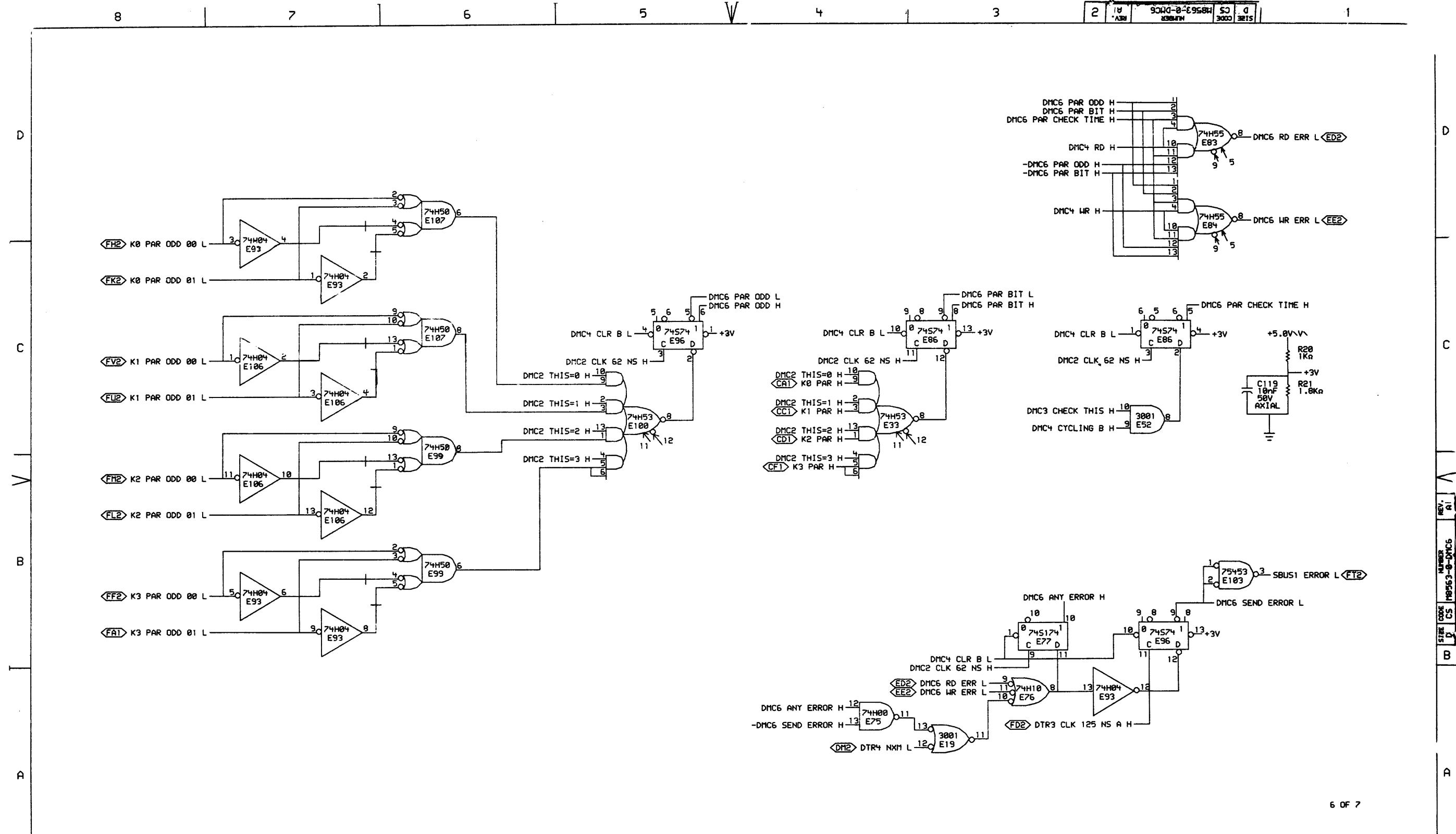


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REVISIONS	M8563-00004 B1
CHK CHANGE NO.	W. BRUCKERT
- M8563-00003 B	17-AUG-76
W. BRUCKERT	17-AUG-76

DRW. <i>J. J. Farley</i>	DATE 6/2/76	ENS. <i>A. Bruckert</i>	DATE 6/2/76	TITLE: ADAPTER INTERFACE
CKX <i>Farley</i>	DATE 6/2/76	BOARD LOCATION: TAFCB	SHEET 1 OF 1	DMA20 BOARD 2
DMC5EX.DR14.426J	29 MAY 76 09:27	NEXT HIGHER ASSEMBLY:		
FIRST USED ON OPTION/MODEL: DMA20	B-DD-M8563-0	SIZE CODE D CS M8563-0-DMC5	NUMBER	REV. B1

520



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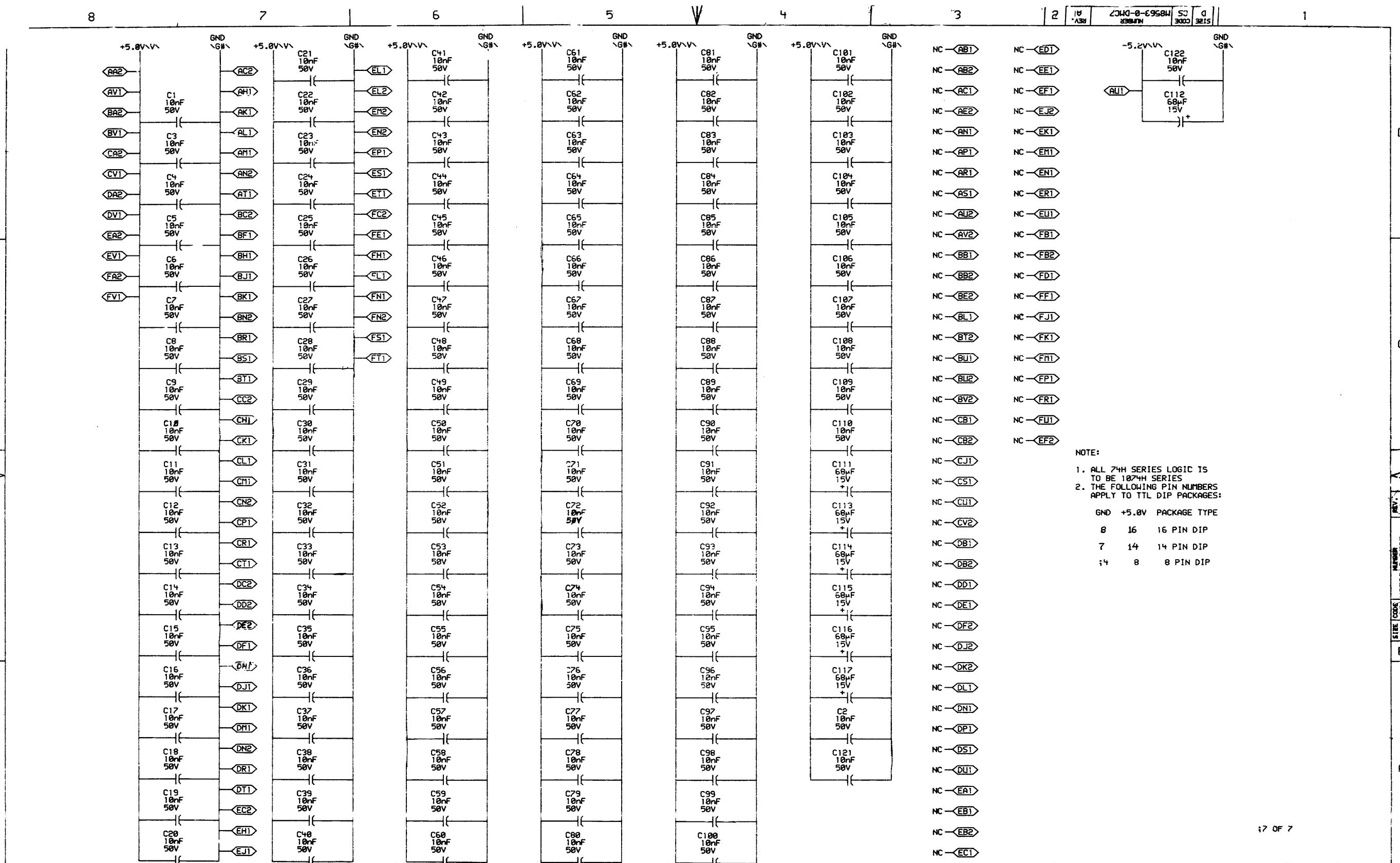
REVISIONS			M8563 - 00004
CHK	CHANGE NO.	REV	W. BRUCKURT
	M8563 - 00001	A	<i>check w/ me</i>
	W. BRUCKERT		



DRN S. foonly DATE 5/25 ENG. W. Brubat DATE 5/25
 CHK'D W. Schepers DATE 5/25 BOARD LOCATION: 1AF00
 SHEET 1 OF 1
 106-JUN-75 09:08 NEXT HIGHER ASSEMBLY:
 ON MODEL: DMDA29 R-DD-MR563-A

TITLE: ADAPTER INTERFACE DMA20 BOARD 2			
SIZE	CODE	NUMBER	REV.
D	CS	M8563-0-DMC6	A1

8 7 6 5 4 3 2 1



- NOTE:
1. ALL 74H SERIES LOGIC IS TO BE 1074H SERIES
 2. THE FOLLOWING PIN NUMBERS APPLY TO TTL DIP PACKAGES:

GND	+5.0V	PACKAGE TYPE
		16 PIN DIP
8	16	16 PIN DIP
7	14	14 PIN DIP
14	8	8 PIN DIP

17 OF 7

SIZE CODE: M8563-0-DMC7
REV. A1

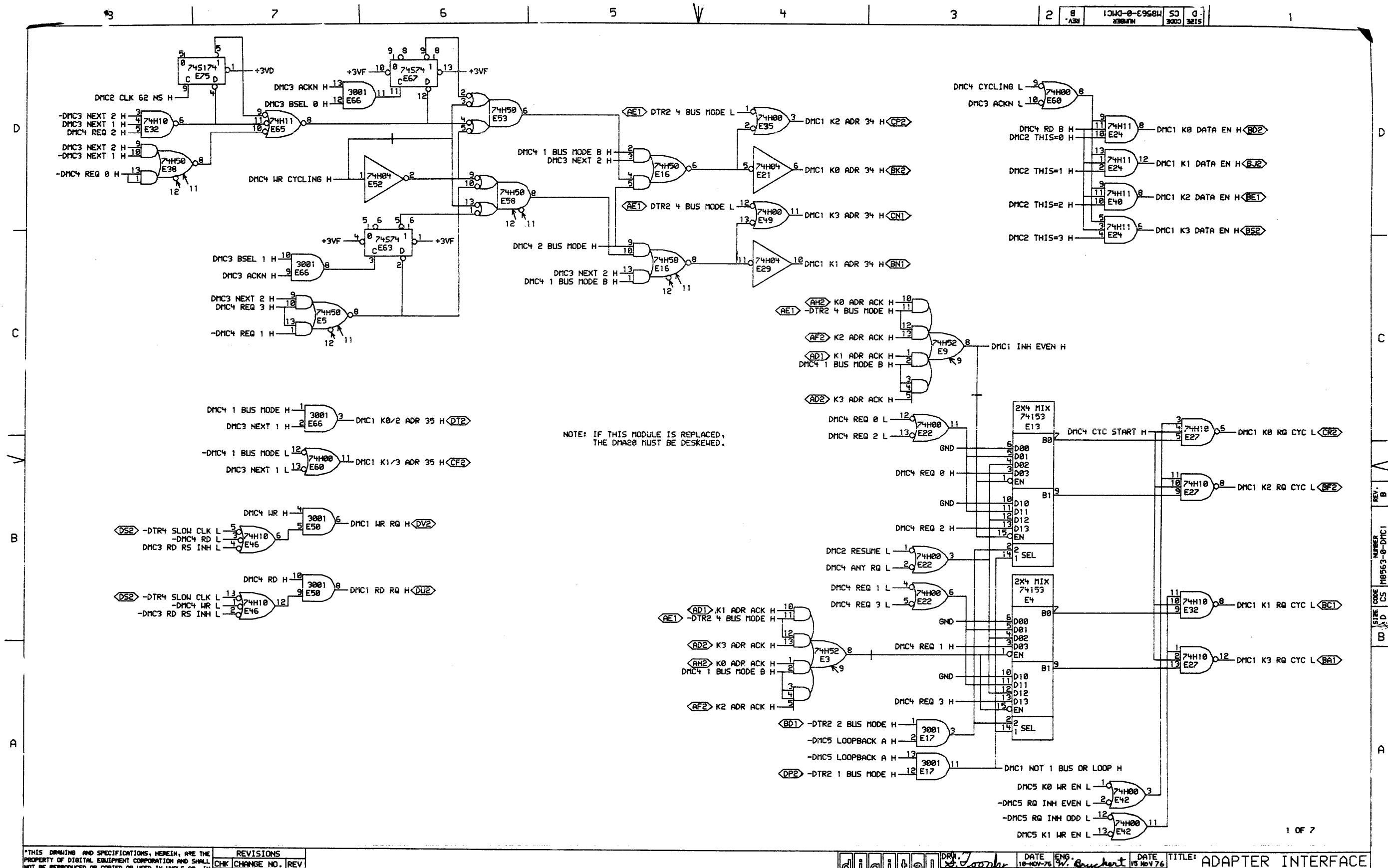
522

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REVISIONS M8563-00004 (A1)
CHG CHANGE NO. REV. W.BRUCKERT
M8563-00001 A *W.BRUCKERT 6/26/75*
W.BRUCKERT

DRN: J. Goodley DATE: 5/6/75 ENG: *W. Bruckert* DATE: 5/6/75
CHG: 1/2 SHEET: 1 OF 1 BOARD LOCATION: 1AF02
DMC7EXI(4,426) 105 JUN-75 09:09 NEXT HIGHER ASSEMBLY: 1
FIRST USED IN OPTION/MODEL: DMA20 B-DD-M8563-0
SIZE CODE NUMBER REV.
D CS M8563-0-DMC7 A1

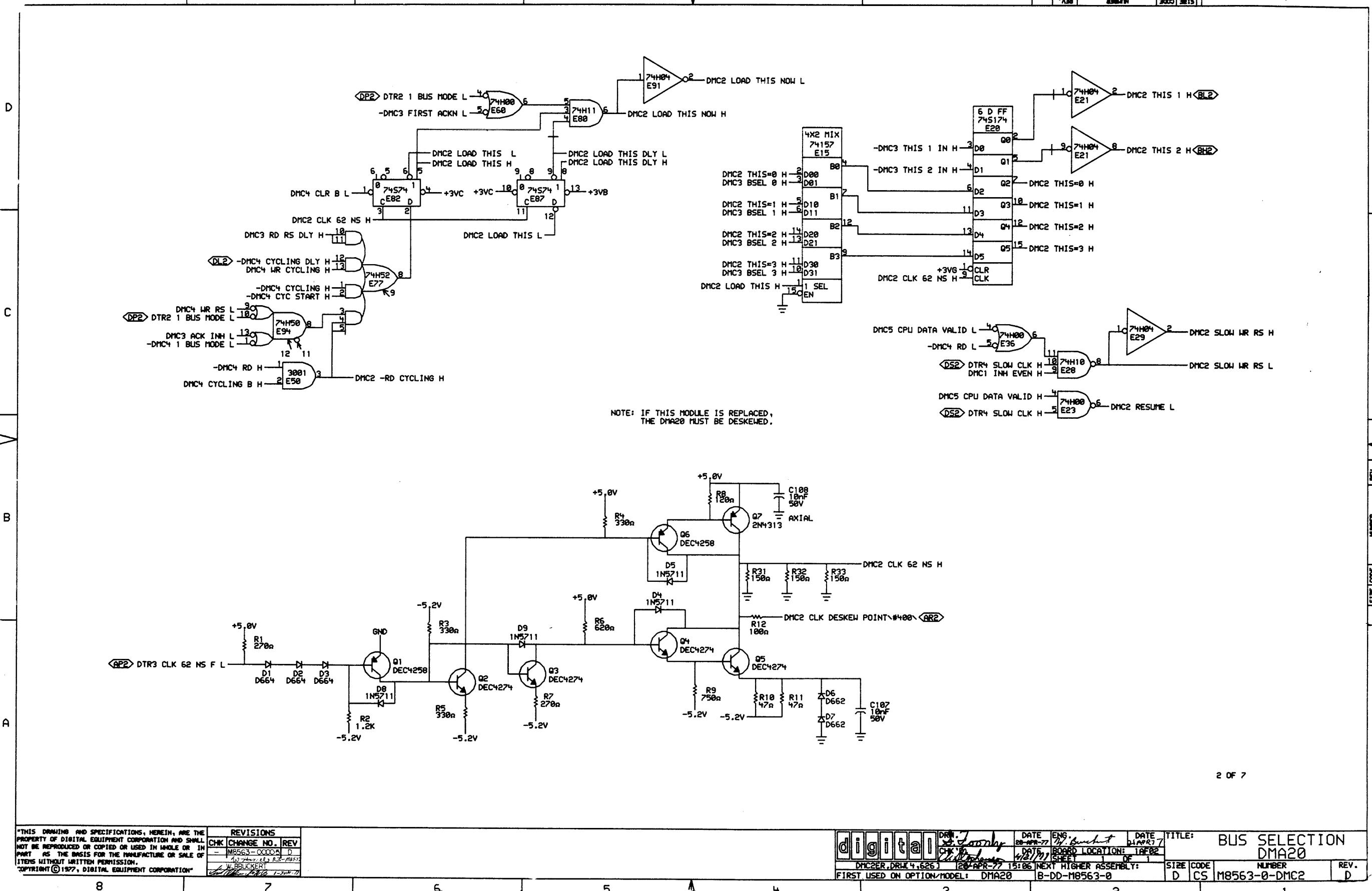
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C = INCLUDES ALL PRINTS INDICATED ON DOCUMENT
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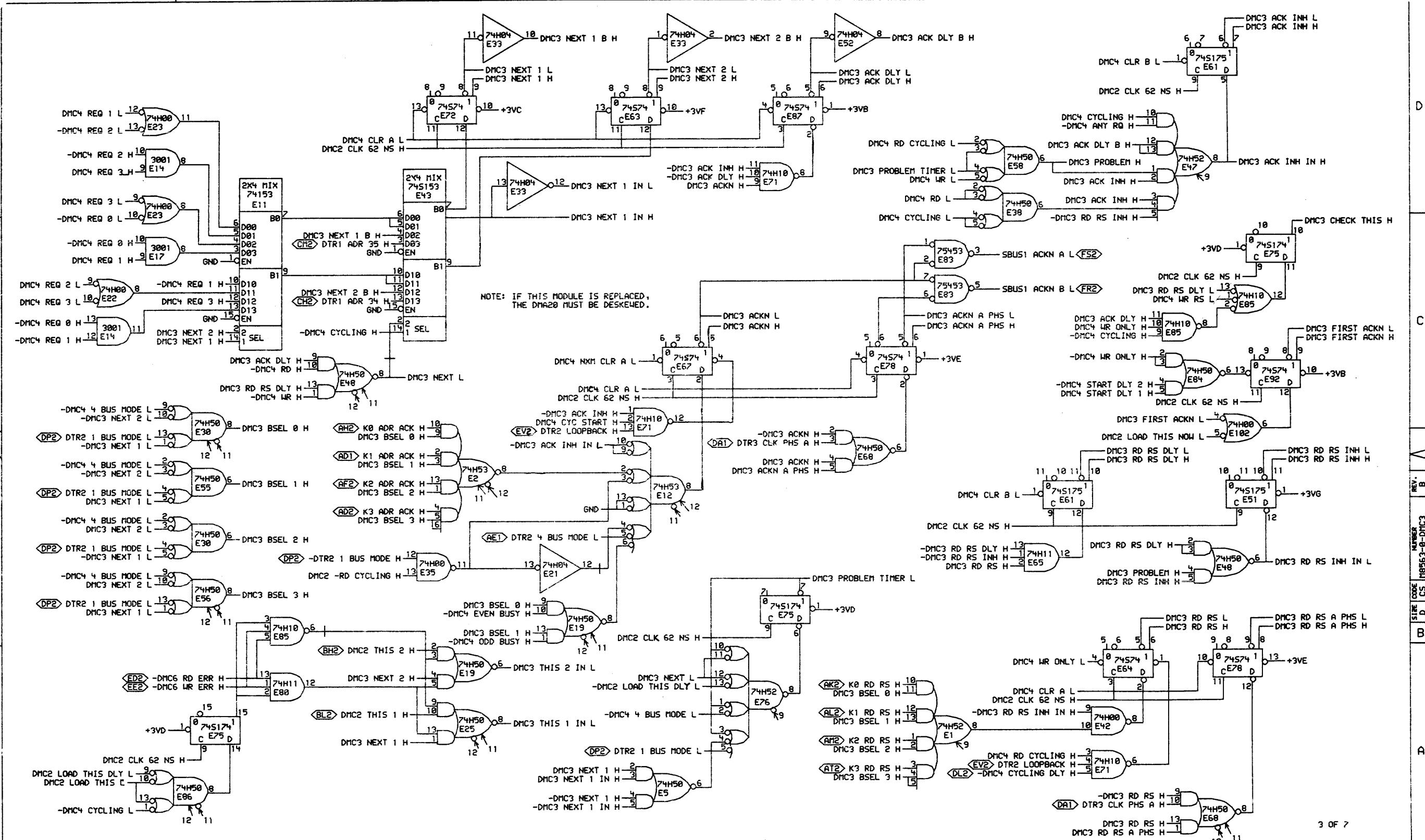


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THE HALL	REVISIONS		
	CHK	CHANGE NO.	RE

digital	DRW: <i>J. Foosby</i>	DATE: 16-NOV-76	ENG: <i>Bruce Hart</i>	DATE: 15 NOV 76	TITLE: ADAPTER INTERFACE		
CH:	DATE: <i>16-Nov-76</i>	BOARD LOCATION: 1AF02		DMA20 BOARD 2			
<i>J. Foosby</i>	16-Nov-76	SHEET 1 OF 1		SIZE: D	CODE: CS	NUMBER: M8563-0-DMC1	REV: B
DMC1ER.DRW[4,56] 169-NOV-76 11:01 NEXT HIGHER ASSEMBLY:							
FIRST USED ON OPTION MODEL: DMA20 B-DD-M8563-0							

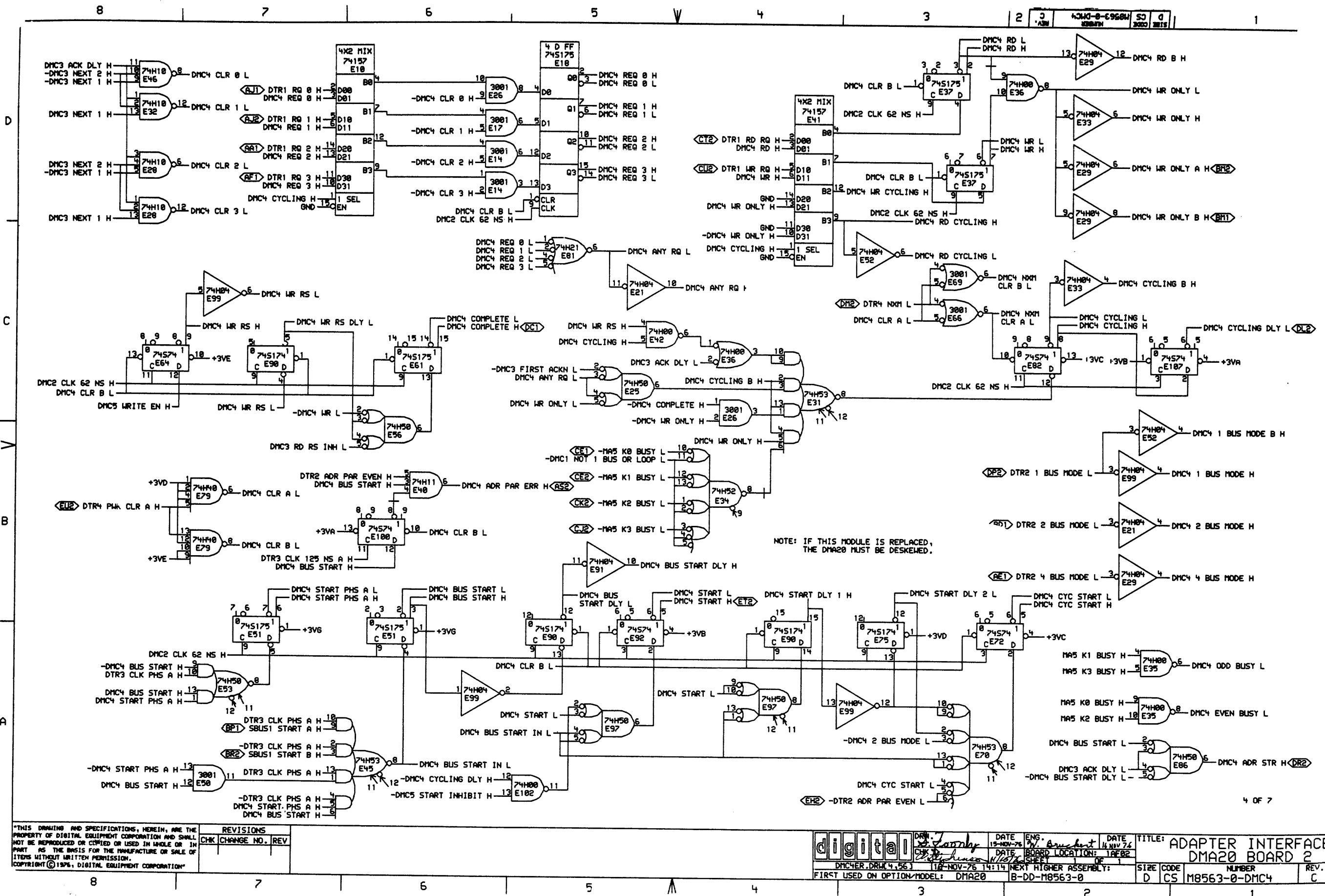




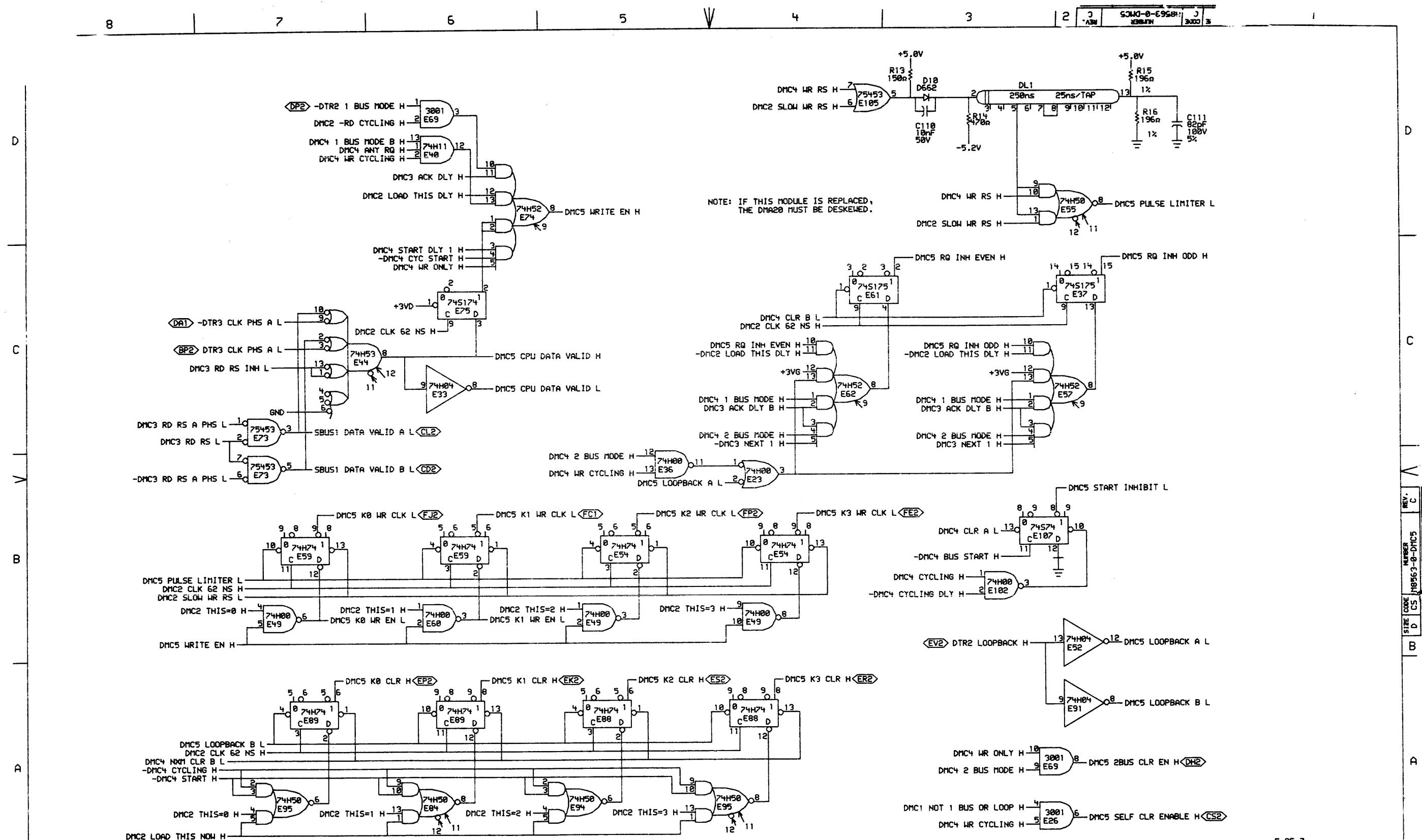
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REVISIONS
CHK CHANGE NO. REV

DRW 7
DATE 11-NOV-76 ENG. 16-Nov-76 DATE 16-Nov-76
C. J. S. C. J. S. C. J. S. C. J. S.
DATE BOARD LOCATIONS: TAPE2
C. J. S. C. J. S. C. J. S. C. J. S.
FIRST USED ON OPTION/MODEL: DMA20 B-DD-M8563-0
SIZE CODE NUMBER D CS M8563-0-DMC3 REV. B



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5 OF 7

S29
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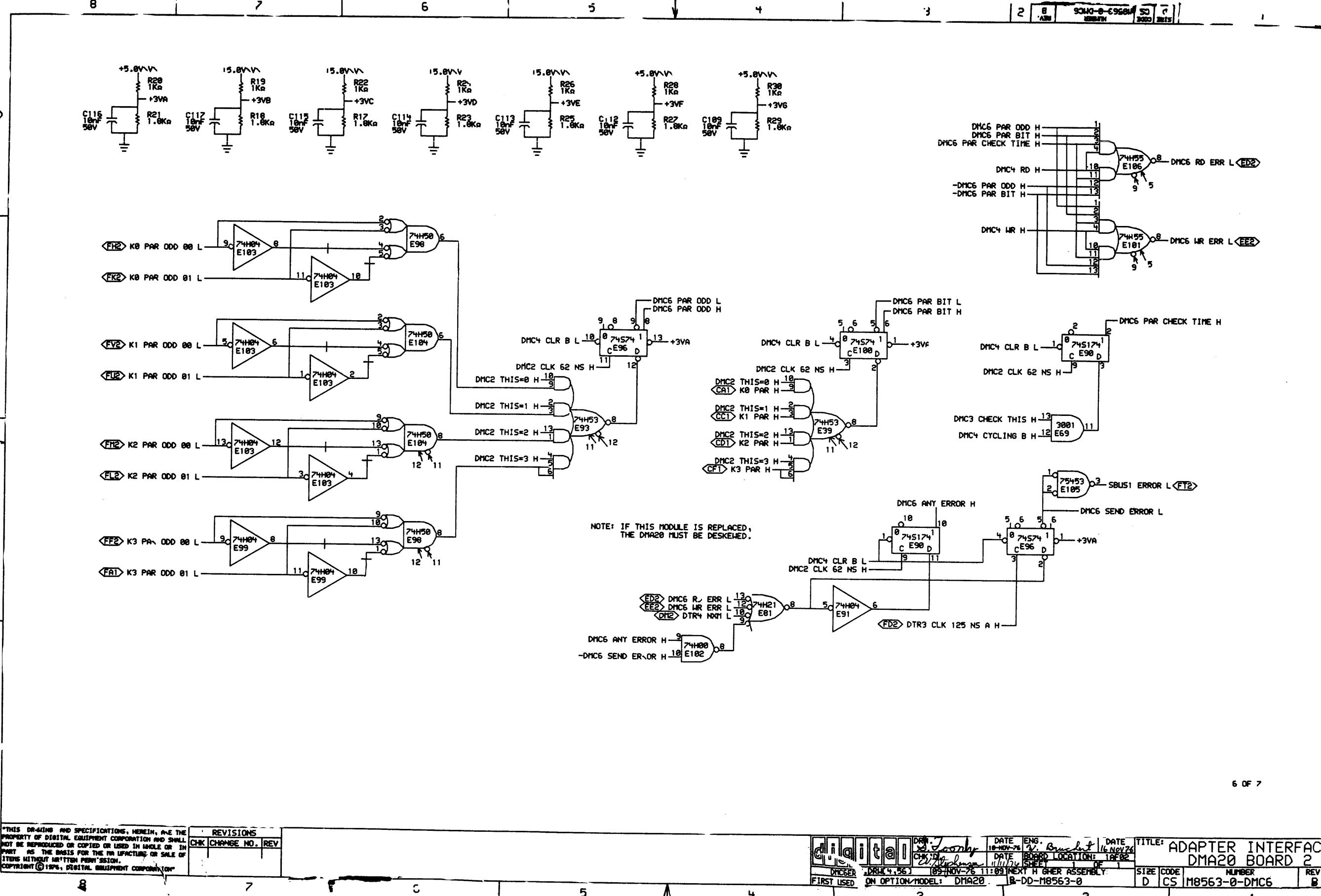
REVISIONS

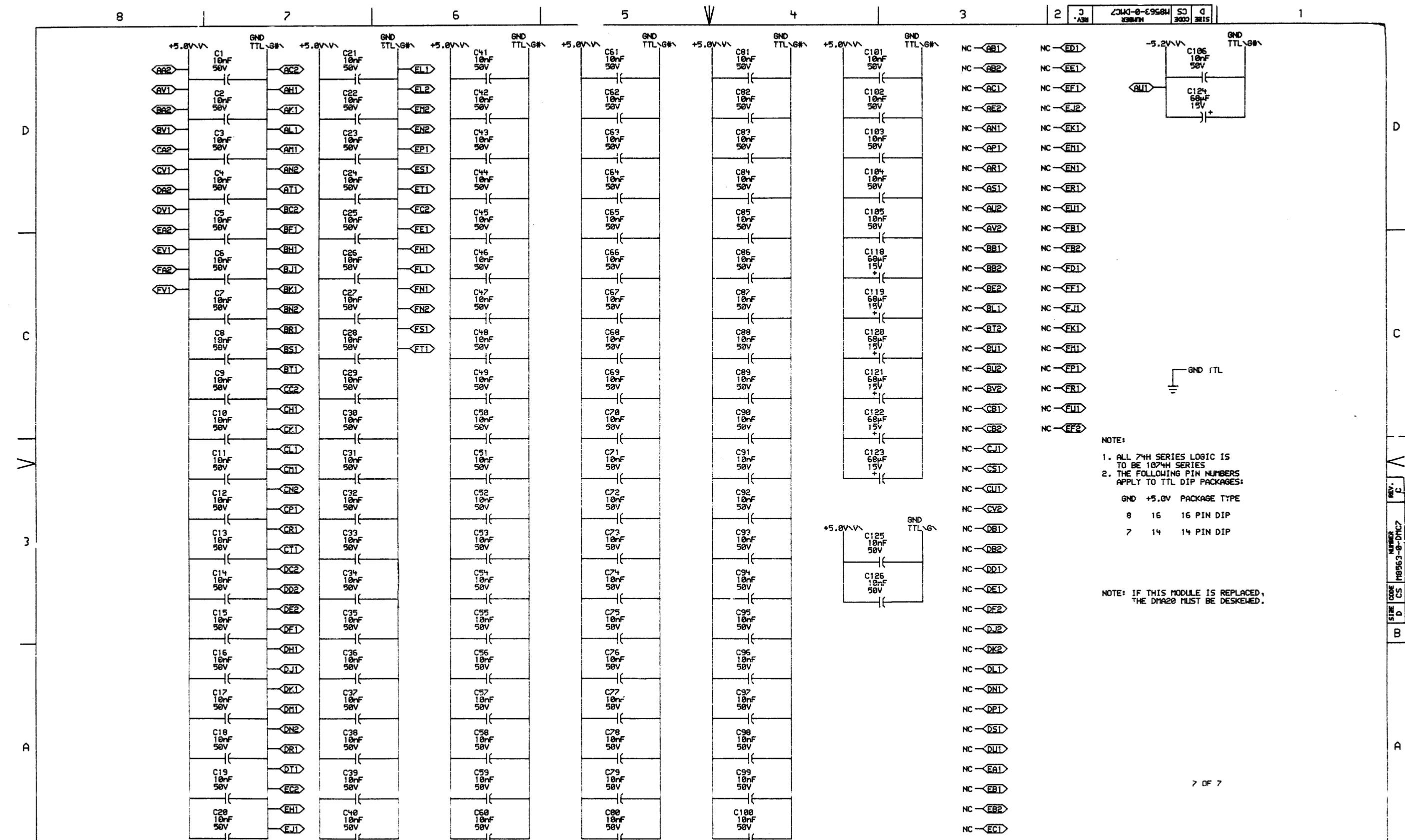
CHK

CHANGE NO.

REV

DR. 7000
DATE: 18-NOV-76
ENG: J. Brundrett
DATE: 16-NOV-76
CIRCUIT DESIGNER: DATE: 16-NOV-76
BOARD LOCATION: TAFB2
SHEET 1 OF 1
FIRST USED ON OPTION/MODEL: DMA20
NEXT HIGHER ASSEMBLY: B-DD-M8563-0
SIZE D CS NUMBER M8563-0-DMC5
REV. C





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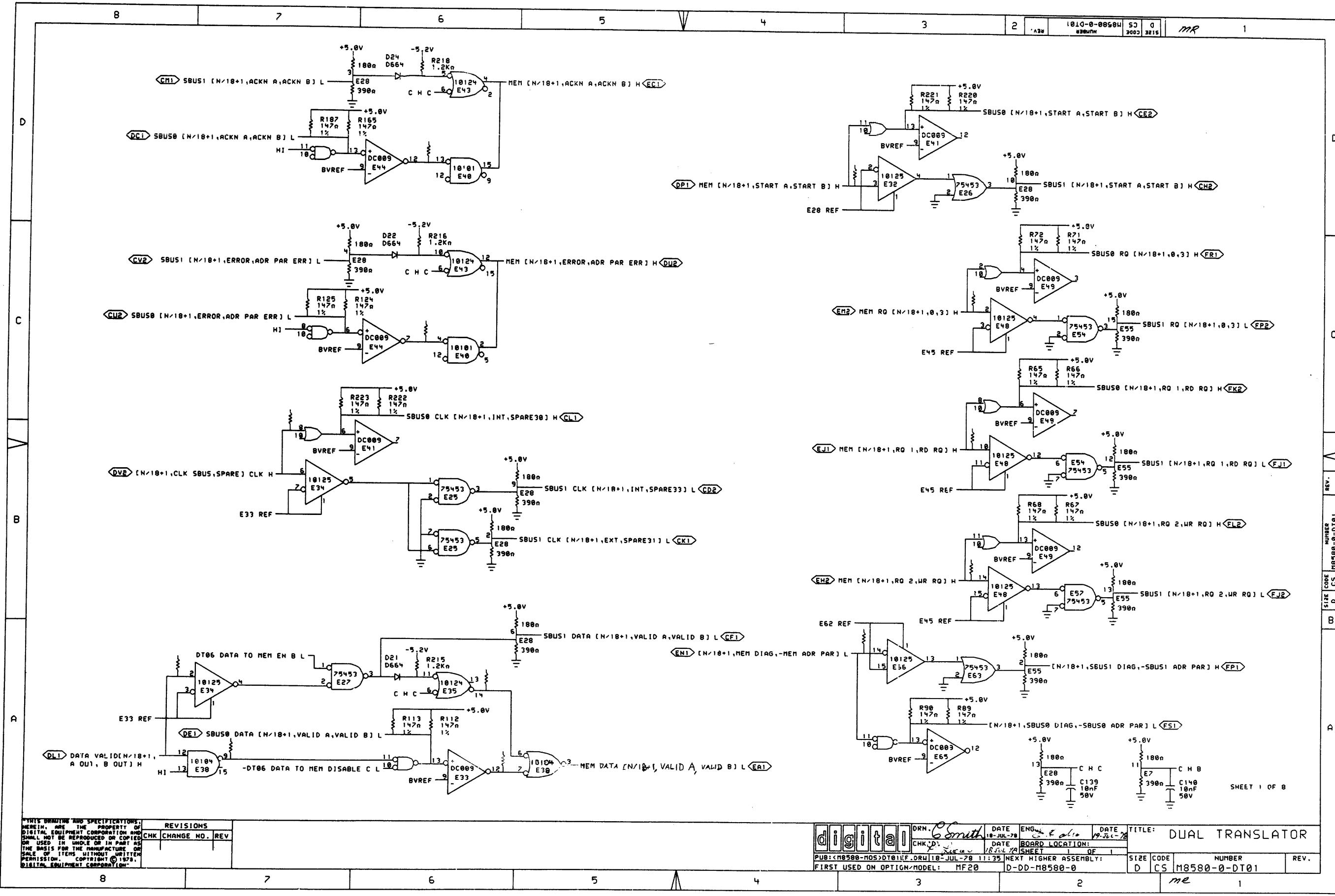
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10

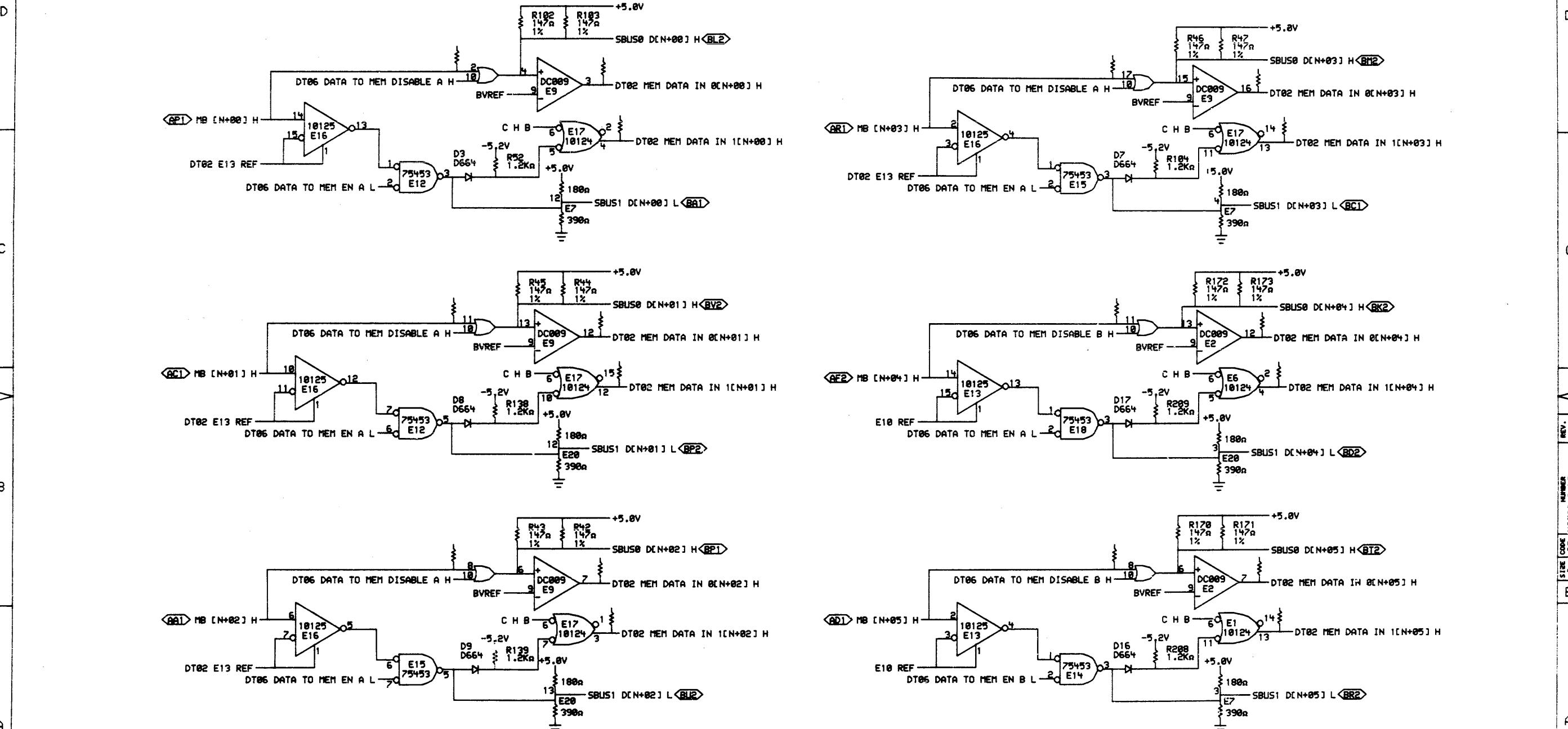
1

1

REVISIONS	DRW:	DATE:	ENG.:	DATE:	TITLE:		
CHK CHANGE NO. REV	<i>s. Thorby</i>	11-APR-77	<i>W. Snelten</i>	11-APR-77	ADAPTER INTERFACE		
- M8563-0001 C	CHD:	BOARD LOCATION:	RAF02	SHEET	POWER AND GROUND		
<i>J. A. Johnson</i>	DATE:	1	OF 1				
<i>W. S. Snelten</i>	DMC7ER.DRW 4,626	11-APR-77 10:01	NEXT HIGHER ASSEMBLY:	SIZE	CODE	NUMBER	REV.
<i>See M8563-0001 1-JUN-77</i>	FIRST USED ON OPTION/MODEL:	DMA20	B-DD-M8563-0	D	CS	M8563-0-DMC7	C



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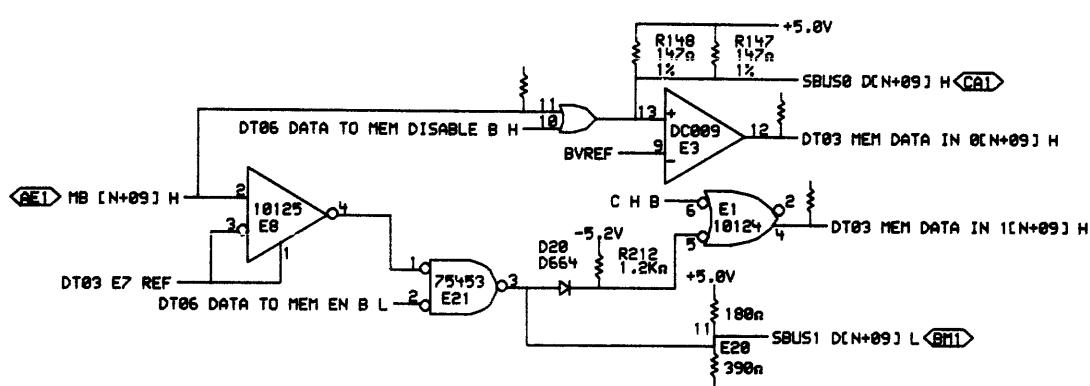
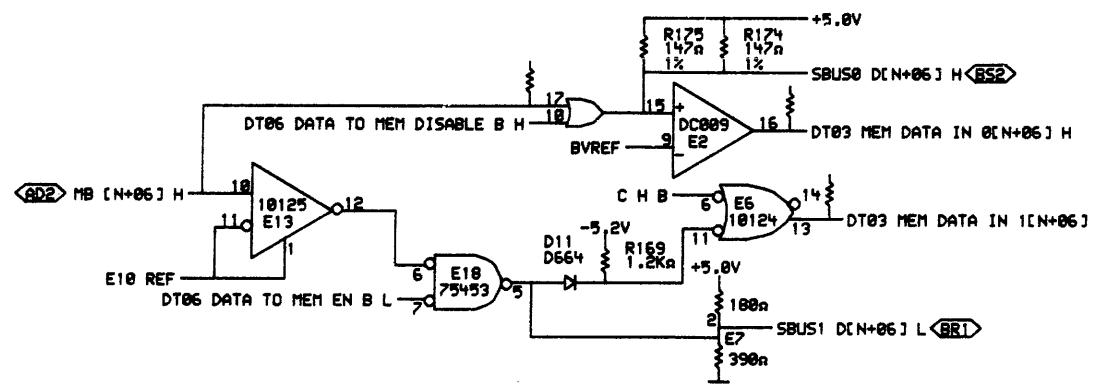
SHEET 2 OF 8

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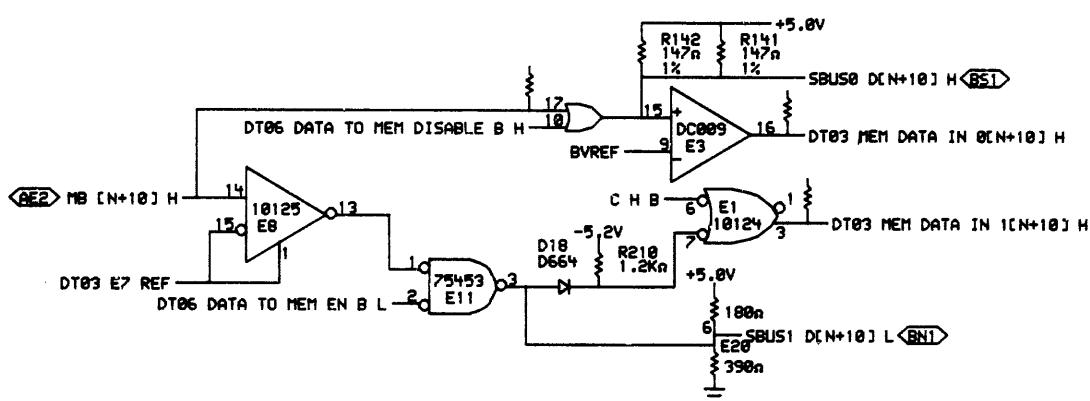
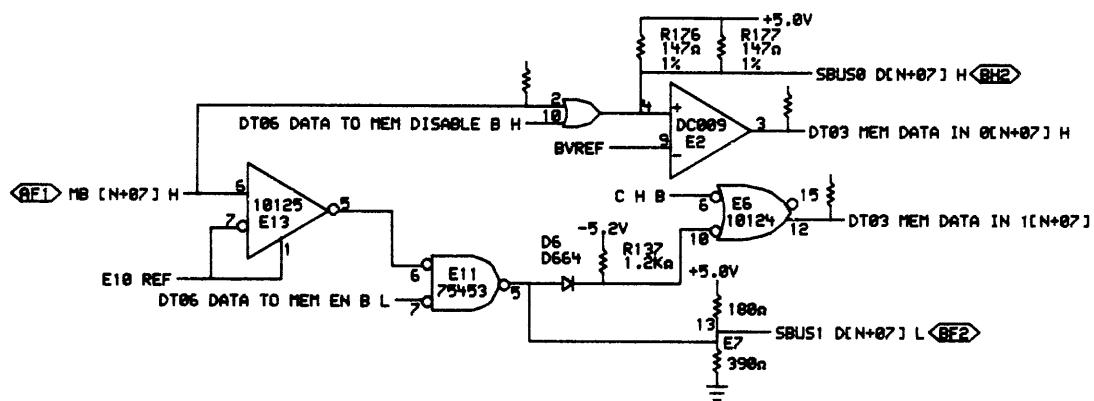
REVISIONS		
CHK	CHANGE NO.	REV

DIGITAL	DRN. 10	DATE 14-JUL-78	ENG. JJ Chin	DATE 7-18-78	TITLE: DUAL TRANSLATOR
CHCK	10	DATE 14-JUL-78	BOARD LOCATION:	1 OF 1	DATA TRNCVR 0-5
PUBL. M8580-0-DT02REF.DRN. 10- JUN-78 10133					
FIRST USED ON OPTION/MODEL: MF20	D-DD-M8580-0	SIZE CODE D CS	NUMBER M8580-0-DT02	REV. 1	

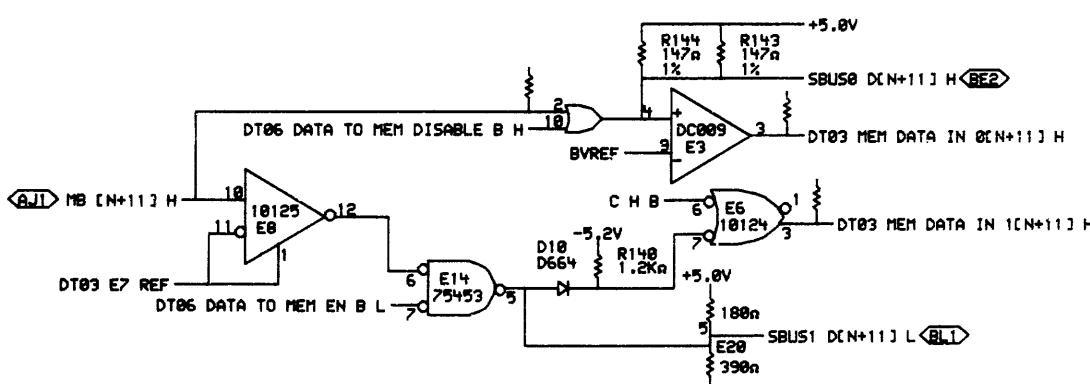
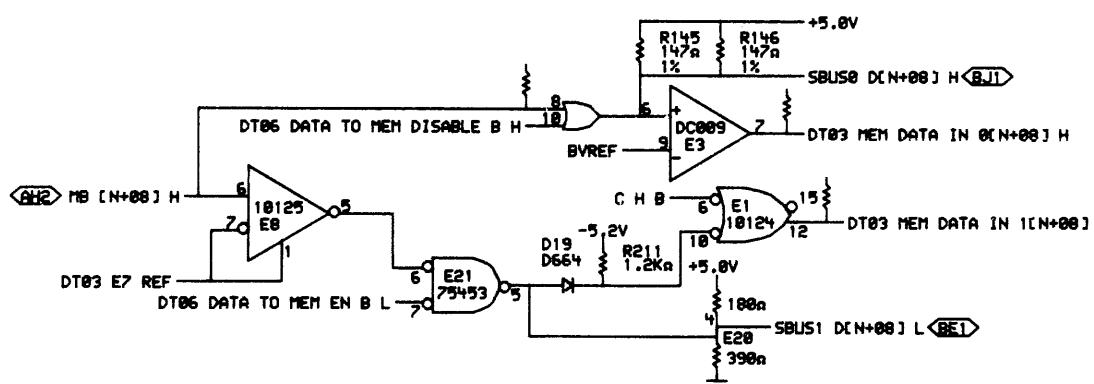
D



C



B



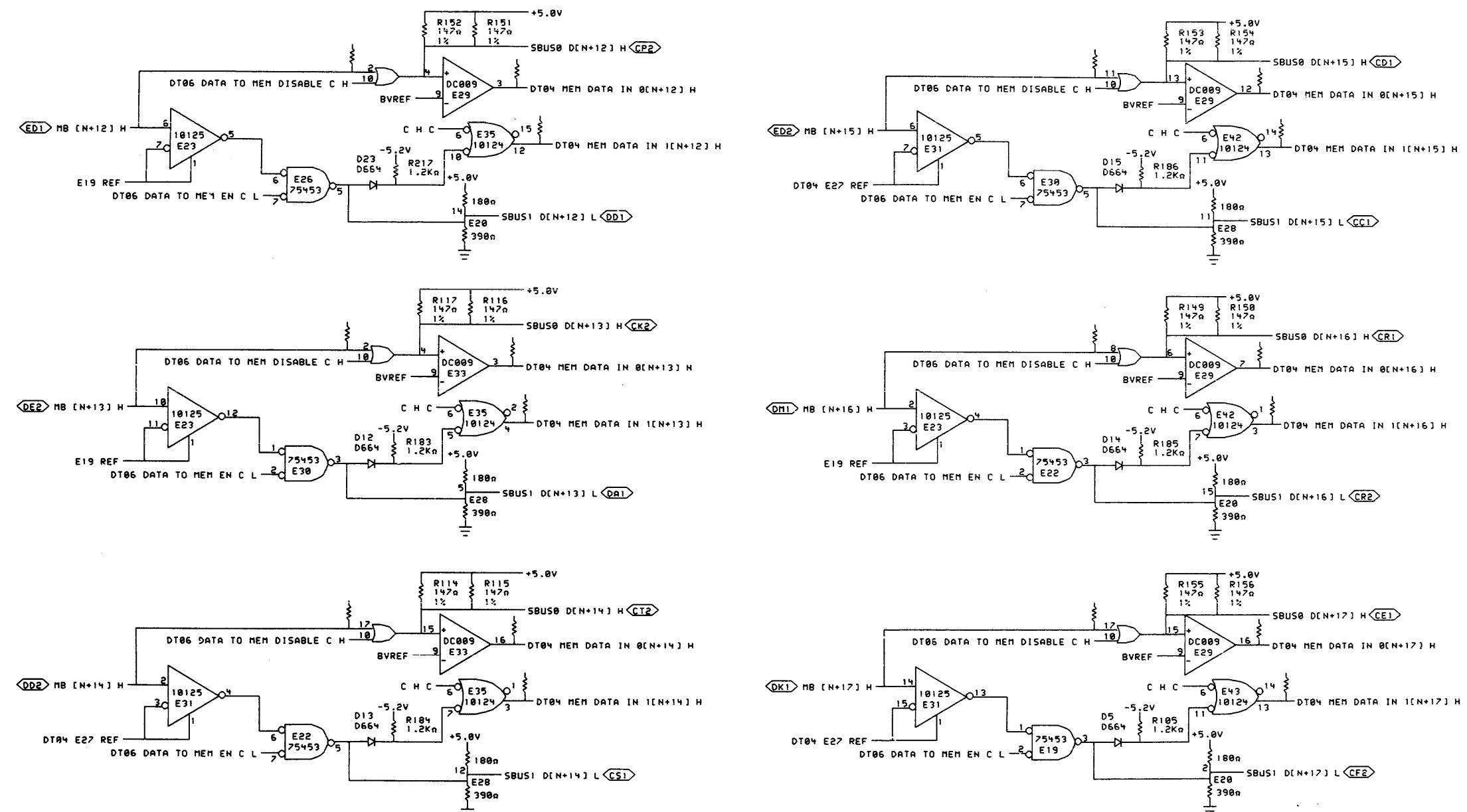
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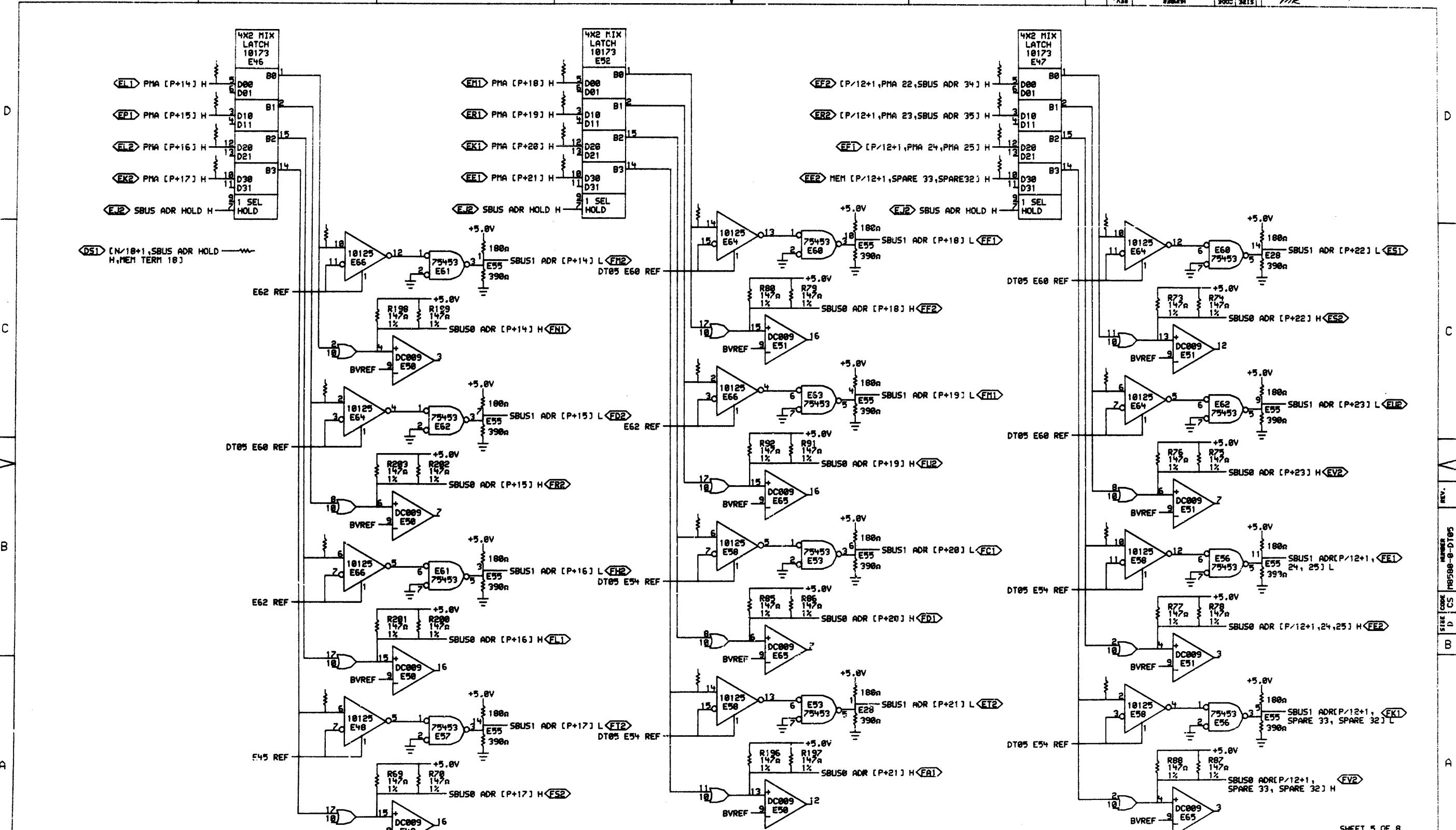
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101100

REVISIONS
CHG CHANGE NO. REV

DRN. C Smith DATE 14-JUN-78 ENG. DJ Chin DATE 7-18-78 TITLE: DUAL TRANSLATOR
CHK'D. Lucien DATE 18-JUL-78 BOARD LOCATION: 1 OF 1 DATA TRNCVR 6-11
PUBL. (M8580-N05>DT03EF.DRN 106-JUN-78 10134 NEXT HIGHER ASSEMBLY: SIZE CODE NUMBER REV.
FIRST USED ON OPTION/MODEL: MF20 D-DD-M8580-0 D CS M8580-0-DT03

SHEET 3 OF 8



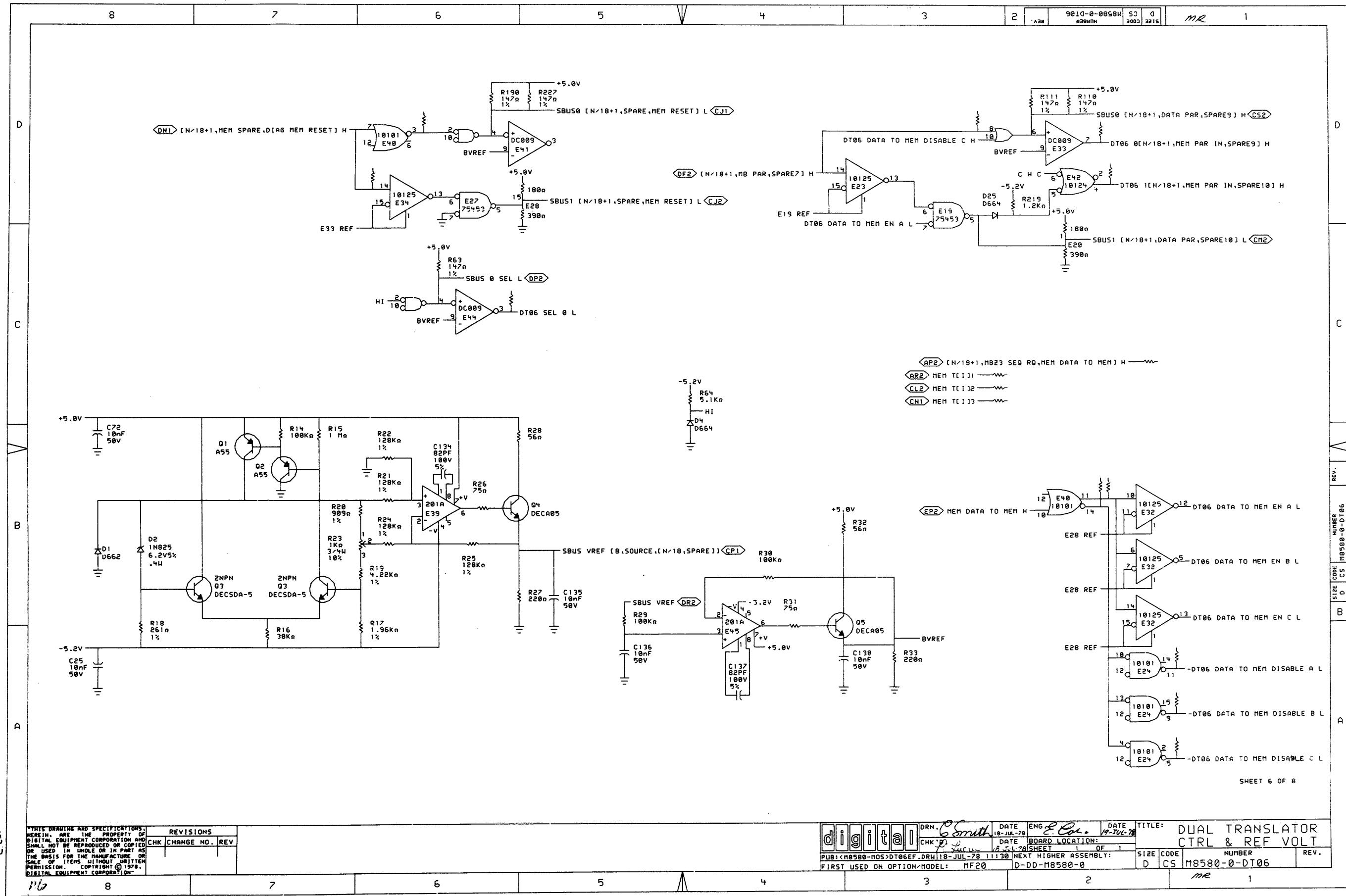


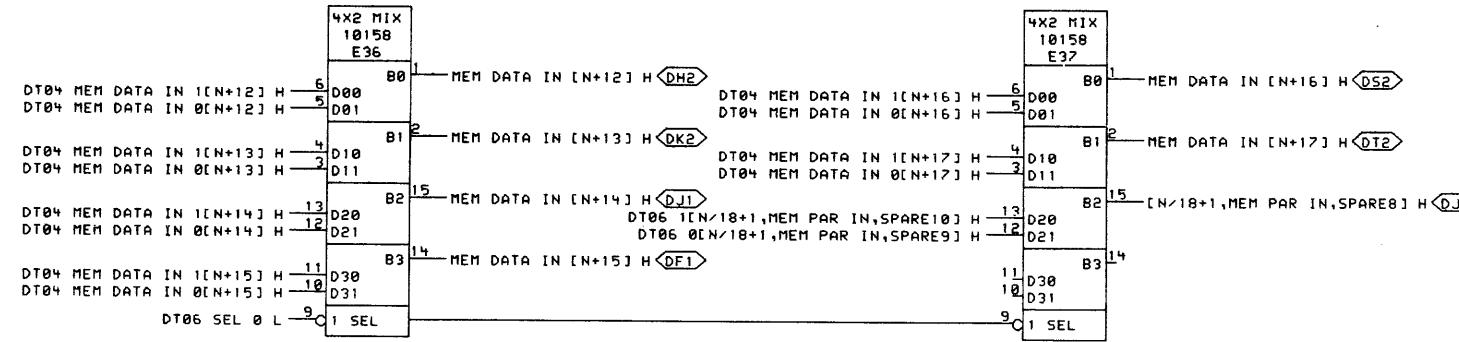
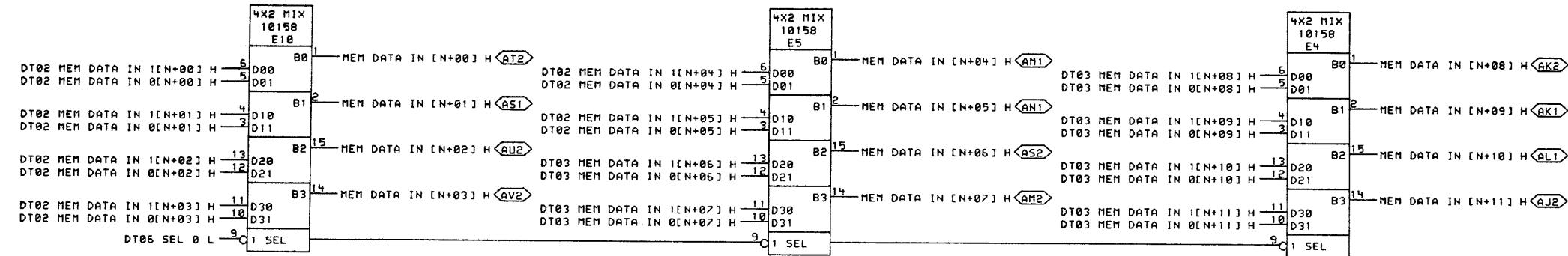
SHEET 5 OF 8

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REVISIONS		
CHG	CHANGE NO.	REV

DRN. P. Smith DATE 14-JUL-78 ENG. J. J. Chin DATE 7-19-78 TITLE: DUAL TRANSLATOR
CNC'D BY: DATE BOARD LOCATION:
PLB1-M8580-0-DT05 SHEET 1 OF 1 ADDRESS DRIVERS
FIRST USED ON OPTION/MODEL: MF20 D-DD-M8580-0 REV. D CS M8580-0-DT05



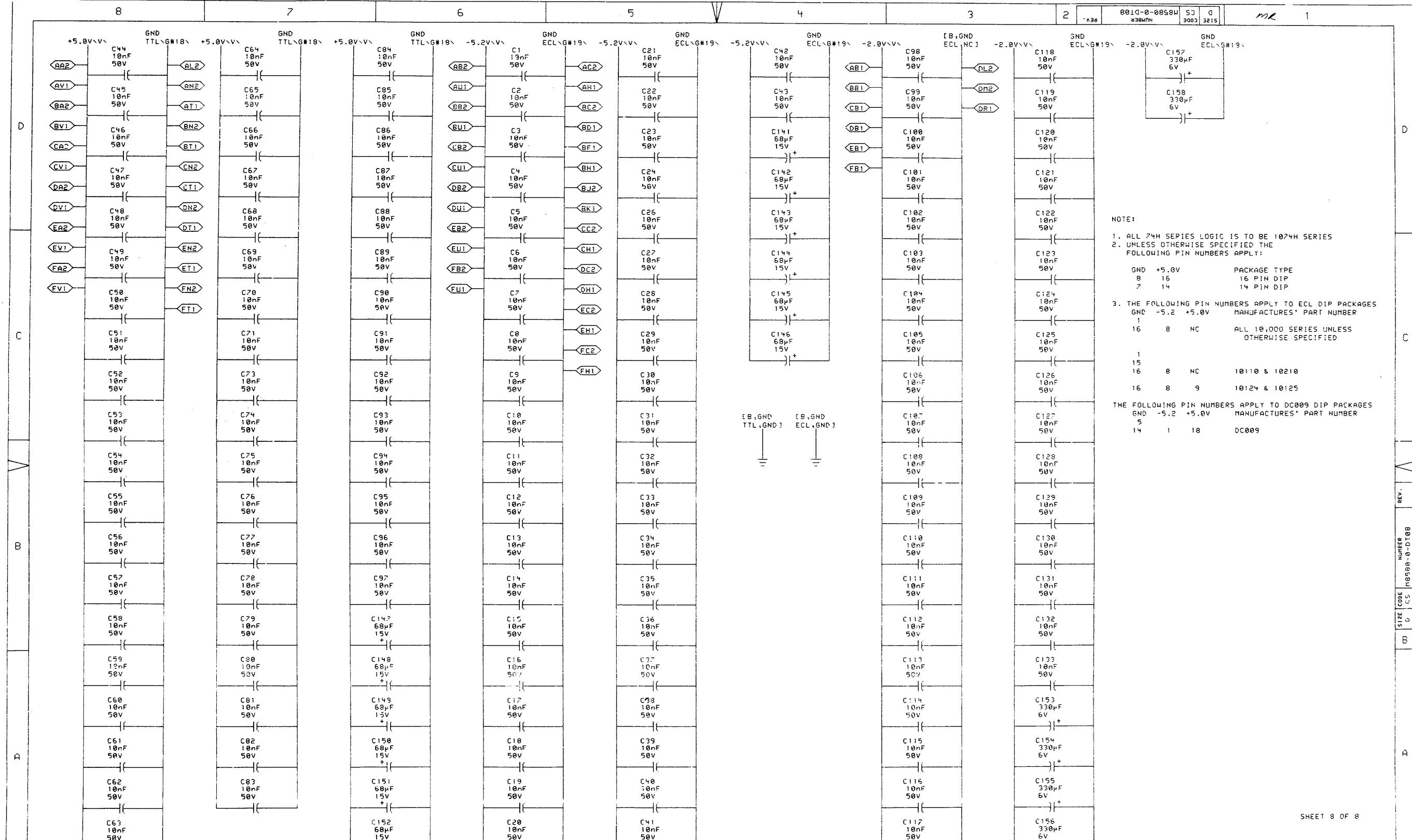


SHEET 7 OF 8

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REVISIONS	CHK	CHANGE NO.	REV
-----------	-----	------------	-----

digital DRN: C. Smith DATE: 16-JUN-78 TITLE: BUS SELECT
CHK: D. Lucius DATE: 18-JUN-78 MEM DATA DRVRS
BOARD LOCATION: SHEET 1 OF 1
DT07EF.DRMC4,6723 105-JUN-78 16:53 NEXT HIGHER ASSEMBLY:
FIRST USED ON OPTION/MODEL: MF20 ID-DD-M8580-0
SIZE CODE D CS M8580-0-DT07 REV. 1



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REVISIONS
CHK CHANGE NO. REV

DRN: *digitat* DATE: JUN-78 ENG. : TITLE: DUAL TRANSLATOR
DATE: 05-JUN-78 BOARD LOCATION: POWER, GND, CAPS.
CHK: D SLOC: 1 OF 1 SHEET: 1 OF 1
D108EFC.DRWE4,572 FIRST USED ON OPTION MODEL: MF20 D-DO-M8580
SIZE CODE: D CS M8580-0-DT08

- NOTE:
 1. ALL 74H SERIES LOGIC IS TO BE 1074H SERIES
 2. UNLESS OTHERWISE SPECIFIED THE
 FOLLOWING PIN NUMBERS APPLY:

GND +5.0V PACKAGE TYPE
 8 16 16 PIN DIP
 7 14 14 PIN DIP

3. THE FOLLOWING PIN NUMBERS APPLY TO ECL DIP PACKAGES
 GND -5.2 +5.0V MANUFACTURES' PART NUMBER

1	16	8 NC	ALL 10,000 SERIES UNLESS OTHERWISE SPECIFIED
15	16	8 NC	10110 & 10210
16	8 9	10124 & 10125	

- THE FOLLOWING PIN NUMBERS APPLY TO DC009 DIP PACKAGES
 GND -5.2 +5.0V MANUFACTURES' PART NUMBER

5	14	1 18 DC009
---	----	------------

SHEET 8 OF 8

SIZE CODE NUMBER REV.
D CS M8580-0-DT08

8 7 6 5 V 4 3 2 1

D	RESISTOR SHOWN ON DRW# REF VALUE TERMINATES SIGNAL	RESISTOR SHOWN ON DRW# REF VALUE TERMINATES SIGNAL	RESISTOR SHOWN ON DRW# REF VALUE TERMINATES SIGNAL
R61(1)	DT01 A6 68n %E33(12)	R2(1) DT03 B5 68n DT03 MEM DATA IN I[N+07] H	R108(1) DT04 B3 68n MB [N+17] H
R62(1)	DT01 A6 68n %E35(14)	R99(1) DT03 A5 68n DT03 MEM DATA IN I[N+08] H	R129(1) DT01 C3 68n MEM RQ [N/18+1,0,3] H
R109(1)	DT01 A7 68n %E38(9)	R101(1) DT03 D2 68n DT03 MEM DATA IN I[N+09] H	R206(1) DT06 C3 68a MEM T[1]I
R58C(1)	DT06 B2 68n %E40(11)	R3(1) DT03 B2 68n DT03 MEM DATA IN I[N+10] H	R214(1) DT06 C3 68n MEM T[1]I2
R13(1)	DT06 B2 68n %E40(14)	R36(1) DT03 A2 68n DT03 MEM DATA IN I[N+11] H	R213(1) DT06 C3 68n MEM T[1]I3
R108C(1)	DT06 D6 68n %E40(3)	R162(1) DT04 D5 68n DT04 MEM DATA IN 0[N+12] H	R126(1) DT01 B3 68n MEM [N/18+1,RQ 1,RD RQ] H
R228C(1)	DT01 D6 68n %E44(12)	R164(1) DT04 C5 68n DT04 MEM DATA IN 0[N+13] H	R127(1) DT01 B3 68n MEM [N/18+1,RQ 2,WR RQ] H
R224(1)	DT01 C6 68n %E44(7)	R158(1) DT04 B5 68n DT04 MEM DATA IN 0[N+14] H	R56(1) DT01 D3 68n MEM [N/18+1,START A,START B] H
R92(1)	DT05 C7 68n %E46(1)	R159(1) DT04 D2 68n DT04 MEM DATA IN 0[N+15] H	R195(1) DT05 D3 68n MEM [P/12+1,SPARE 33,SPARE32] H
R128C(1)	DT05 A2 68n %E46(14)	R119(1) DT04 C2 68n DT04 MEM DATA IN 0[N+16] H	R230(1) DT05 D7 68n PMA [P+14] H
R94(1)	DT05 B7 68n %E46(15)	R121(1) DT04 B2 68n DT04 MEM DATA IN 0[N+17] H	R231(1) DT05 D7 68n PMA [P+15] H
R205C(1)	DT05 C7 68n %E46(2)	R161(1) DT04 D5 68n DT04 MEM DATA IN I[N+12] H	R191(1) DT05 D7 68n PMA [P+16] H
R167(1)	DT05 C2 68n %E47(1)	R163(1) DT04 B5 68n DT04 MEM DATA IN I[N+13] H	R229(1) DT05 D7 68n PMA [P+17] H
R131(1)	DT05 A2 68n %E47(14)	R157(1) DT04 A5 68n DT04 MEM DATA IN I[N+14] H	R82(1) DT05 D5 68n PMA [P+18] H
R130C(1)	DT05 B2 68n %E47(15)	R160(1) DT04 D2 68n DT04 MEM DATA IN I[N+15] H	R81(1) DT05 D5 68n PMA [P+19] H
R204(1)	DT05 C2 68n %E47(2)	R118(1) DT04 B2 68n DT04 MEM DATA IN I[N+16] H	R83(1) DT05 D5 68n PMA [P+20] H
R166C(1)	DT05 C5 68n %E52(1)	R120(1) DT04 A2 68n DT04 MEM DATA IN I[N+17] H	R84(1) DT05 D5 68n PMA [P+21] H
R232(1)	DT05 A5 68n %E52(14)	R122(1) DT06 D2 68n DT06 0[N/18+1,MEM PAR IN,SPARE9] H	R189(1) DT01 B7 68n [N/18+1,CLK SBUS,SPARE] CLK H
R93(1)	DT05 B5 68n %E52(15)	R123(1) DT06 D2 68n DT06 I[N/18+1,MEM PAR IN,SPARE10] H	R59(1) DT06 D3 68n [N/18+1,MB PAR,SPARE7] H
R95(1)	DT05 C5 68n %E52(2)	R98(1) DT06 A1 68n DT06 DATA TO MEM DISABLE A H	R207(1) DT06 C2 68a [N/18+1,MB23 SEQ RQ,MEM DATA TO MEM] H
R68C(1)	DT01 A8 68n DATA VALID[N/18+1, A OUT, B OUT] H	R168(1) DT06 A1 68n DT06 DATA TO MEM DISABLE B H	R56(1) DT01 A3 68a -[N/18+1,MEM DIAG,-MEM ADR PAR] H
R49(1)	DT02 D5 68n DT02 MEM DATA IN 0[N+01] H	R182(1) DT06 A1 68n DT06 DATA TO MEM DISABLE C H	R225(1) DT06 D6 68n [N/18+1,MEM SPARE,DIAG MEM RESET] H
R51(1)	DT02 C5 68n DT02 MEM DATA IN 0[N+01] H	R4(1) DT06 C6 68n -DT06 SEL 0 H	R226(1) DT05 C7 68n [N/18+1,SBUS ADR HOLD H,MEM TERM 18]
R8(1)	DT02 B5 68n DT02 MEM DATA IN 0[N+02] H	R12(1) DT02 D6 68n MB [N+00] H	R192(1) DT05 D3 68n [P/12+1,PMA 22,SBUS ADR 34] H
R6(1)	DT02 D2 68n DT02 MEM DATA IN 0[N+03] H	R9(1) DT02 C6 68n MB [N+01] H	R193(1) DT05 D3 68n [P/12+1,PMA 23,SBUS ADR 35] H
R39(1)	DT02 C2 68n DT02 MEM DATA IN 0[N+04] H	R54(1) DT02 B6 68n MB [N+02] H	R194(1) DT05 D3 68n [P/12+1,PMA 24,PMA 25] H
R41(1)	DT02 B2 68n DT02 MEM DATA IN 0[N+05] H	R55(1) DT02 D3 68n MB [N+03] H	
R48(1)	DT02 C5 68n DT02 MEM DATA IN I[N+00] H	R180(1) DT02 C3 68n MB [N+04] H	
R50(1)	DT02 B5 68n DT02 MEM DATA IN I[N+01] H	R179(1) DT02 B3 58n MB [N+05] H	
R10(1)	DT02 A5 68n DT02 MEM DATA IN I[N+02] H	R181(1) DT03 D6 68n MB [N+06] H	
R7(1)	DT02 C2 68n DT02 MEM DATA IN I[N+03] H	R178(1) DT03 C6 68n MB [N+07] H	
R38(1)	DT02 B2 68n DT02 MEM DATA IN I[N+04] H	R134(1) DT03 B6 68n MB [N+08] H	
R40(1)	DT02 A2 68n DT02 MEM DATA IN I[N+05] H	R133(1) DT03 D3 68n MB [N+09] H	
R1(1)	DT03 D5 68n DT03 MEM DATA IN 0[N+06] H	R135(1) DT03 C3 68n MB [N+10] H	
R3(1)	DT03 C5 68n DT03 MEM DATA IN 0[N+07] H	R136(1) DT03 B3 68n MB [N+11] H	
R100C(1)	DT03 B5 68n DT03 MEM DATA IN 0[N+08] H	R57(1) DT04 D6 68n MB [N+12] H	
R132(1)	DT03 D2 68n DT03 MEM DATA IN 0[N+09] H	R11(1) DT04 C6 68n MB [N+13] H	
R35(1)	DT03 C2 68n DT03 MEM DATA IN 0[N+10] H	R107(1) DT04 B6 68n MB [N+14] H	
R37(1)	DT03 B2 68n DT03 MEM DATA IN 0[N+11] H	R106(1) DT04 D3 68n MB [N+15] H	
R51(1)	DT03 D5 68n DT03 MEM DATA IN I[N+06] H	R53(1) DT04 C3 68n MB [N+16] H	

NOTE:

- ALL TERMINATORS HAVE PIN TWO CONNECTED TO -2.0V AND ARE 5% 1/4WATT UNLESS OTHERWISE SPECIFIED
- ENTRIES ARE SORTED BY SIGNAL NAME
- % INDICATES OUTPUT OF DIP LOC AND <> INDICATES PIN NUMBER

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REVISIONS		
CHK	CHANGE NO.	REV

digital DRN. *Smith* DATE 09-JUN-78 ENG. *J. Clin* DATE 09-JUN-78 TITLE: DUAL TRANSLATOR TERMINATORS

CHK	10.	Surch	DATE	BOARD LOCATION:
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FIRST USED ON OPTION/MODEL: MF20		D-00-M8580-0	SIZE	CODE
			D	NUMBER
			CS	REV.
			C	M8580-0-RES

8 7 6 5 V 4 3 2 1

SIZE CODE NUMBER
D CS M8580-0-RES

REV.

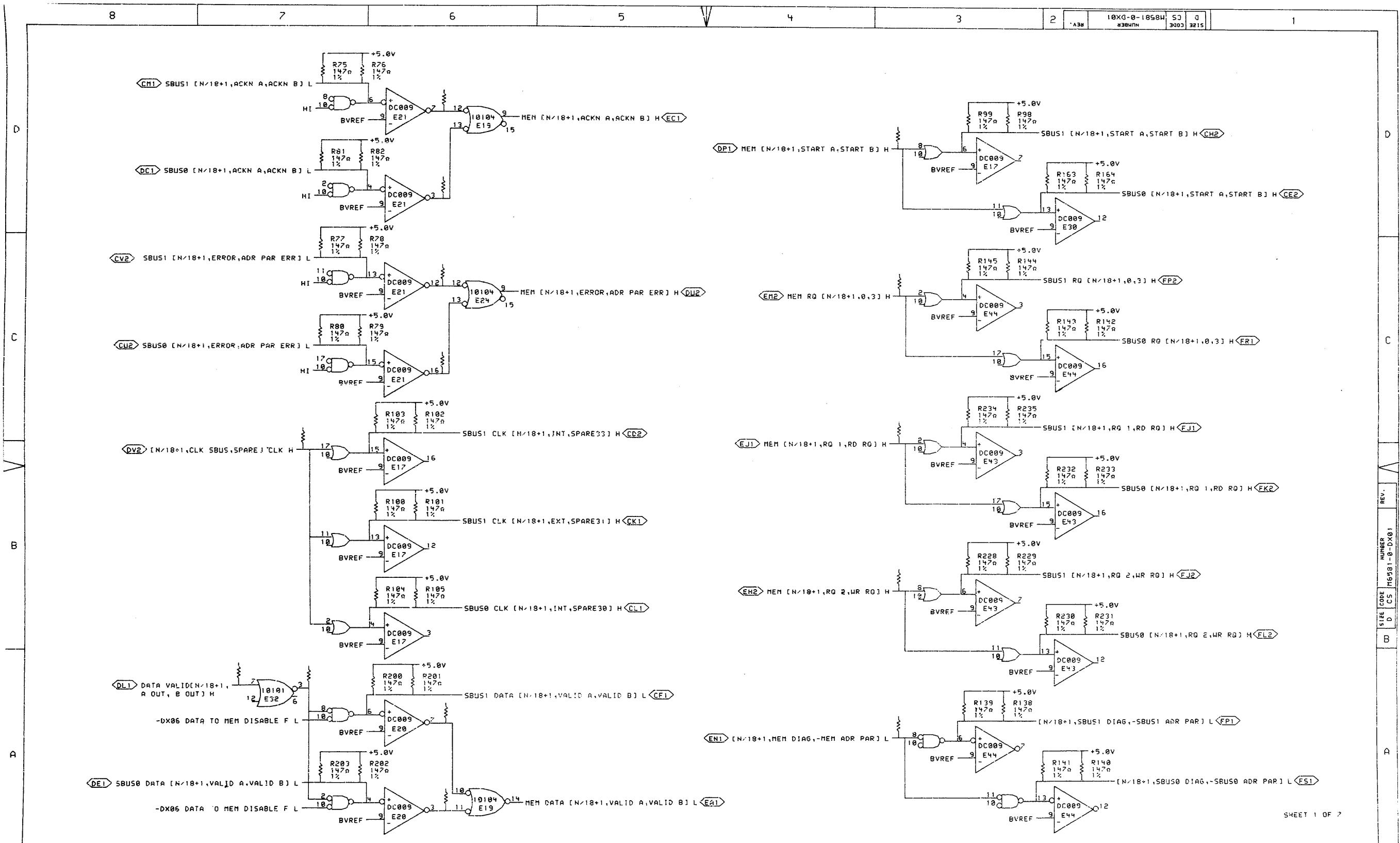
B

A

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8	7	6	5	V	4	3	2	D D M8581-0 REV. 2	1																																																							
<p style="text-align: center;">DRAWING NUMBER PAGE PART NO. DESCRIPTION REVISIONS</p> <p style="text-align: center;">FILE: ORIGINAL LAYOUT</p> <p style="text-align: center;">ECO NUMBER</p> <p style="text-align: center;">MODULE REVISION</p> <p style="text-align: center;">A</p> <table border="1"> <tbody> <tr> <td>D-UA-M8581-0-0</td><td>5</td><td>XBUS TRANSLATOR</td><td>A</td> </tr> <tr> <td>K-PL-M8581-0-DBP</td><td>2</td><td>XBUS TRANSLATOR</td><td>A</td> </tr> <tr> <td>D-CS-M8581-0-DX01</td><td>1</td><td>XBUS TRANSLATOR</td><td>-</td> </tr> <tr> <td>D-CS-M8581-0-DX02</td><td>1</td><td>DATA TRNCVR 0-5</td><td>-</td> </tr> <tr> <td>D-CS-M8581-0-DX03</td><td>1</td><td>DATA TRNCVR 6-11</td><td>-</td> </tr> <tr> <td>D-CS-M8581-0-DX04</td><td>1</td><td>DATA TRNCVR 12-17</td><td>-</td> </tr> <tr> <td>D-CS-M8581-0-DX05</td><td>1</td><td>ADDRESS DRIVERS</td><td>-</td> </tr> <tr> <td>D-CS-M8581-0-DX06</td><td>1</td><td>CTRL & REF VOLT</td><td>-</td> </tr> <tr> <td>D-CS-M8581-0-DX07</td><td>1</td><td>POWER. GND. CAPS.</td><td>-</td> </tr> <tr> <td>D-CS-M8581-0-RES</td><td>1</td><td>TERMINATORS</td><td>-</td> </tr> <tr> <td>D-MD-5013219-0-0</td><td>5</td><td>DRILL & ETCH DRAWING</td><td>A</td> </tr> <tr> <td></td><td>5013219</td><td>ETCH CIRCUIT BOARD</td><td>B</td> </tr> <tr> <td>K-PC-M8581-0-DBC</td><td>-</td><td>P.C. DESIGN DATA BASE</td><td>A</td> </tr> <tr> <td>P00-M8581-00</td><td>-</td><td>PROCESS SHEET (REF ONLY)</td><td>-</td> </tr> </tbody> </table> <p>NOTES:</p>									D-UA-M8581-0-0	5	XBUS TRANSLATOR	A	K-PL-M8581-0-DBP	2	XBUS TRANSLATOR	A	D-CS-M8581-0-DX01	1	XBUS TRANSLATOR	-	D-CS-M8581-0-DX02	1	DATA TRNCVR 0-5	-	D-CS-M8581-0-DX03	1	DATA TRNCVR 6-11	-	D-CS-M8581-0-DX04	1	DATA TRNCVR 12-17	-	D-CS-M8581-0-DX05	1	ADDRESS DRIVERS	-	D-CS-M8581-0-DX06	1	CTRL & REF VOLT	-	D-CS-M8581-0-DX07	1	POWER. GND. CAPS.	-	D-CS-M8581-0-RES	1	TERMINATORS	-	D-MD-5013219-0-0	5	DRILL & ETCH DRAWING	A		5013219	ETCH CIRCUIT BOARD	B	K-PC-M8581-0-DBC	-	P.C. DESIGN DATA BASE	A	P00-M8581-00	-	PROCESS SHEET (REF ONLY)	-
D-UA-M8581-0-0	5	XBUS TRANSLATOR	A																																																													
K-PL-M8581-0-DBP	2	XBUS TRANSLATOR	A																																																													
D-CS-M8581-0-DX01	1	XBUS TRANSLATOR	-																																																													
D-CS-M8581-0-DX02	1	DATA TRNCVR 0-5	-																																																													
D-CS-M8581-0-DX03	1	DATA TRNCVR 6-11	-																																																													
D-CS-M8581-0-DX04	1	DATA TRNCVR 12-17	-																																																													
D-CS-M8581-0-DX05	1	ADDRESS DRIVERS	-																																																													
D-CS-M8581-0-DX06	1	CTRL & REF VOLT	-																																																													
D-CS-M8581-0-DX07	1	POWER. GND. CAPS.	-																																																													
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REVISIONS		CHK	CHANGE NO.	REV.																																																												
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REVISIONS	CHK	CHANGE NO.	REV	DRN	DATE	ENG	DATE	TITLE:
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				C				

PUB: M8581-MOS>DX01B.DRW | 26-JUL-78 08:01 | NEXT HIGHER ASSEMBLY:
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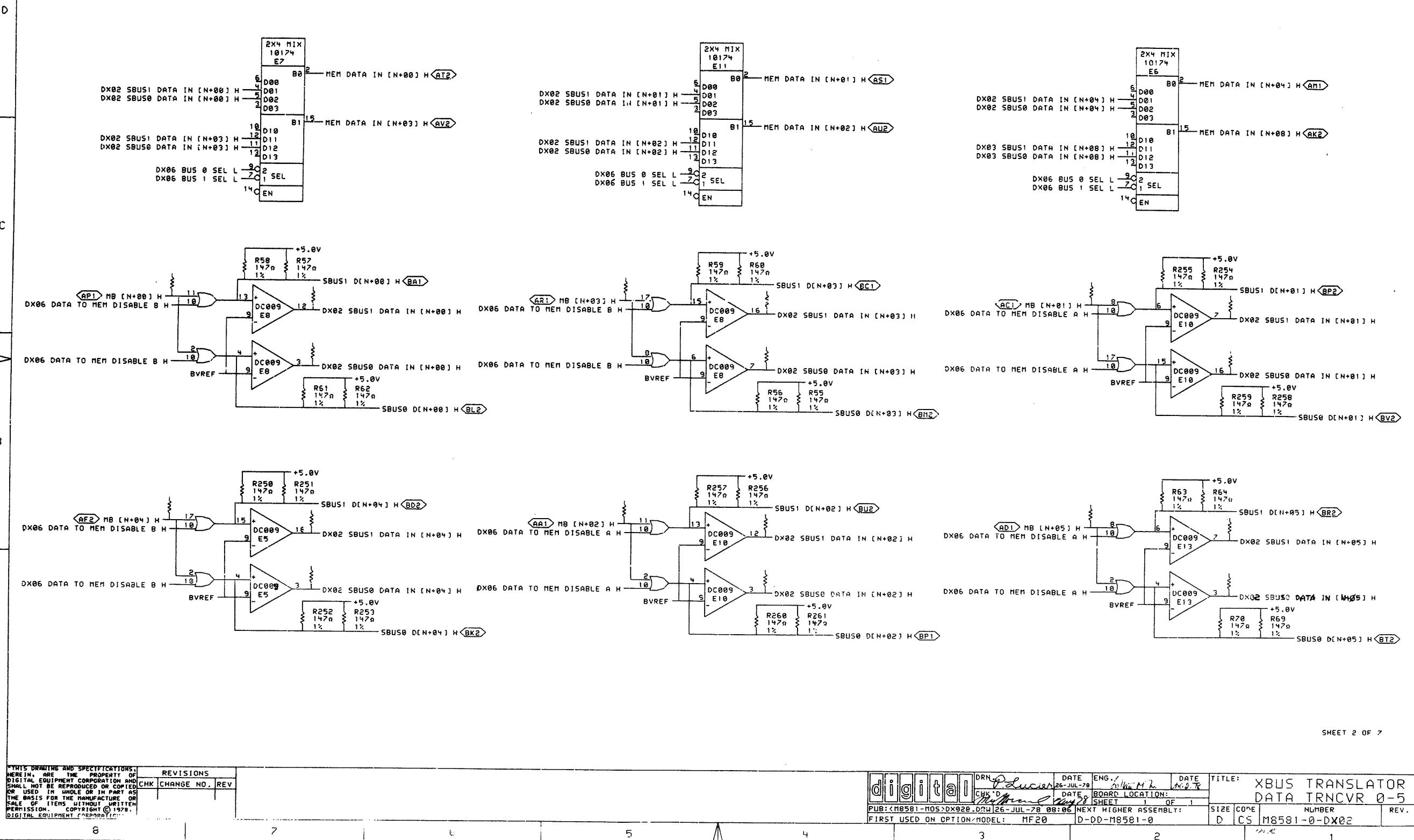
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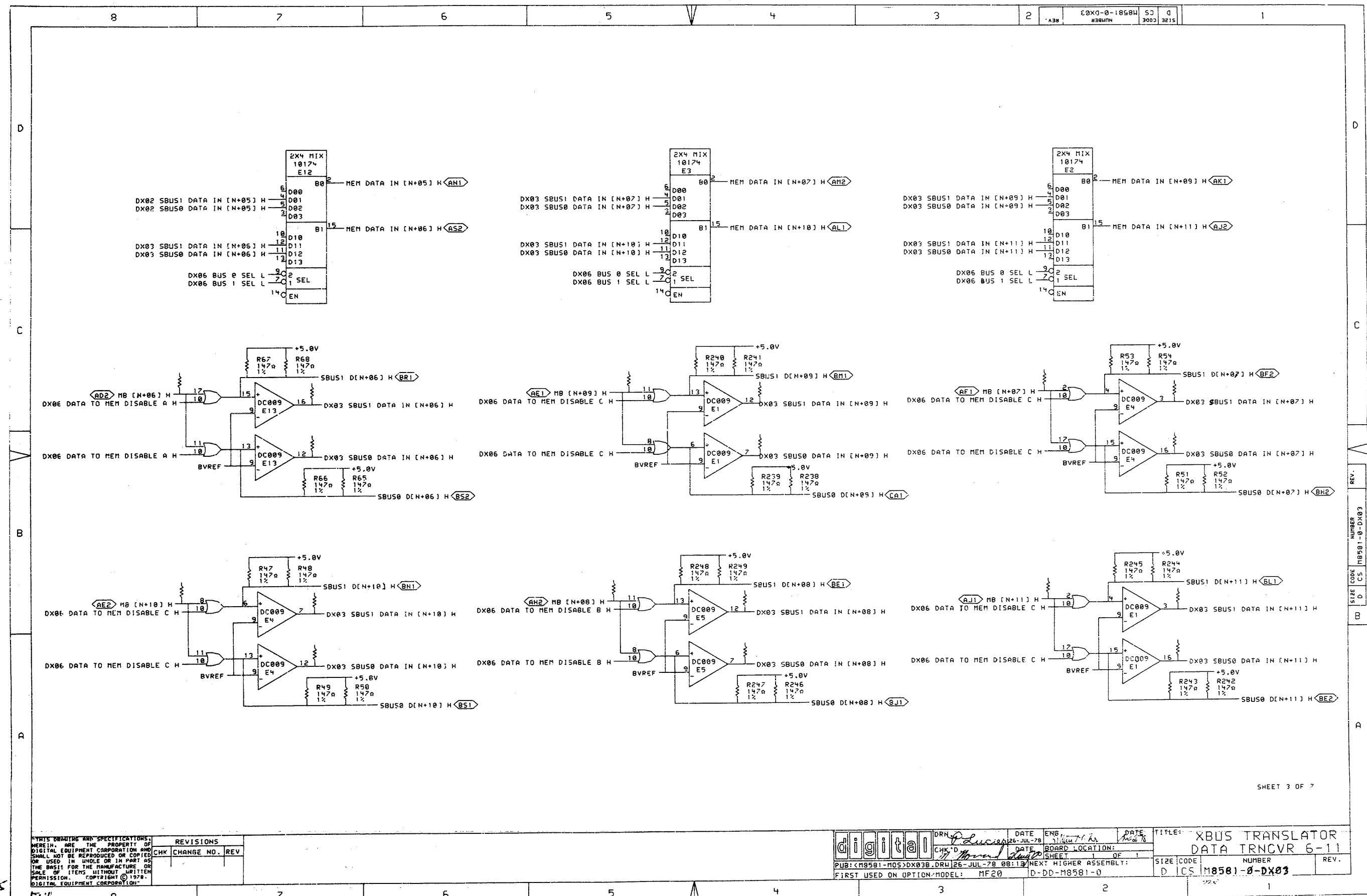
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CS

M8581-0-DX01

SHEET 1 OF 2





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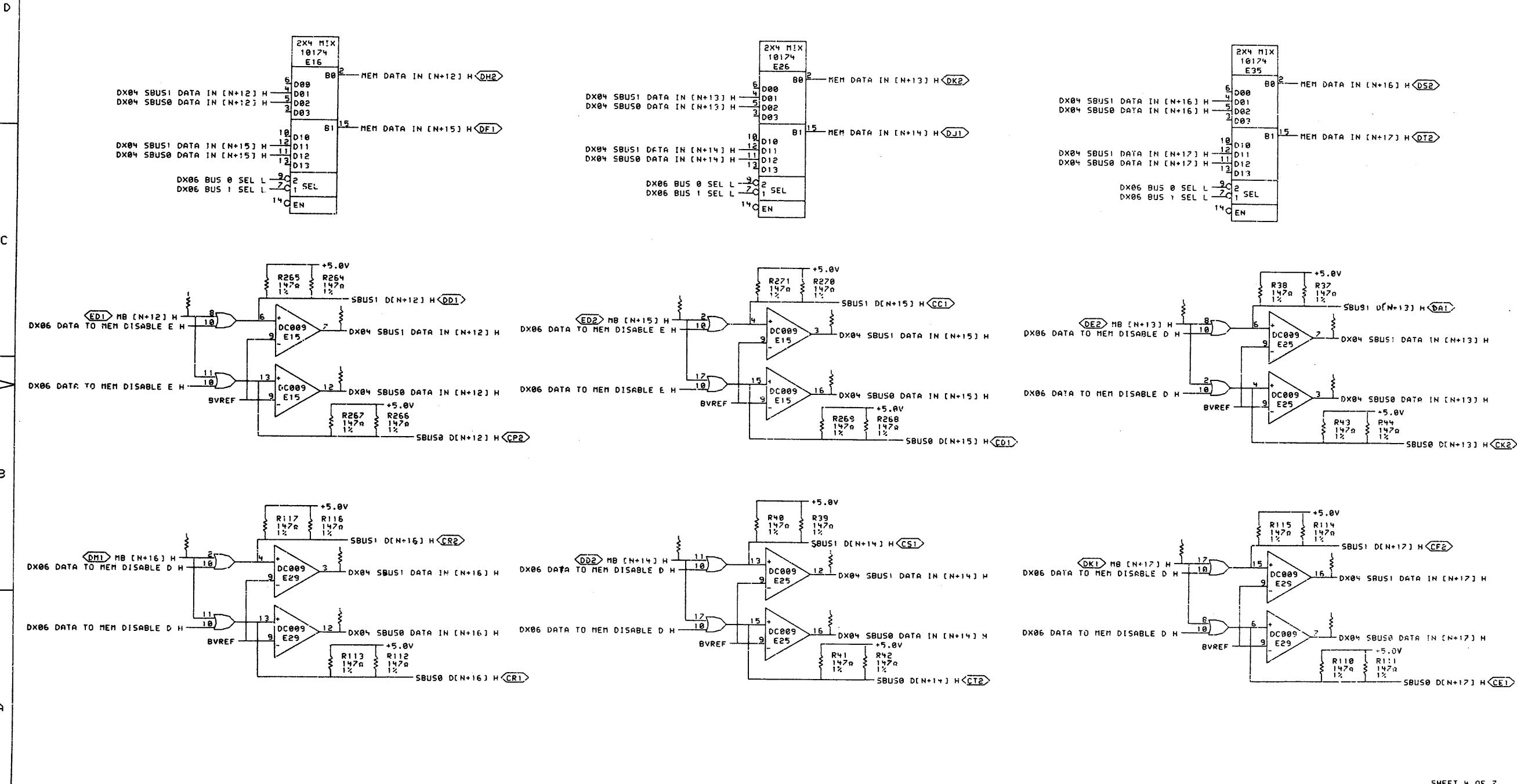
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		digital	DATE 26-JUL-78	BOARD LOCATION: Sheet 1 of 1		DATA TRNCVR 6-11
		CHK'D by <i>[Signature]</i>	DATE 26-JUL-78			

PUB: CM8581-MOS>DX03B.DRU 26-JUL-78 08:13/NEXT HIGHER ASSEMBLY:
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SIZE CODE M8581-0-DX03 REV. 2

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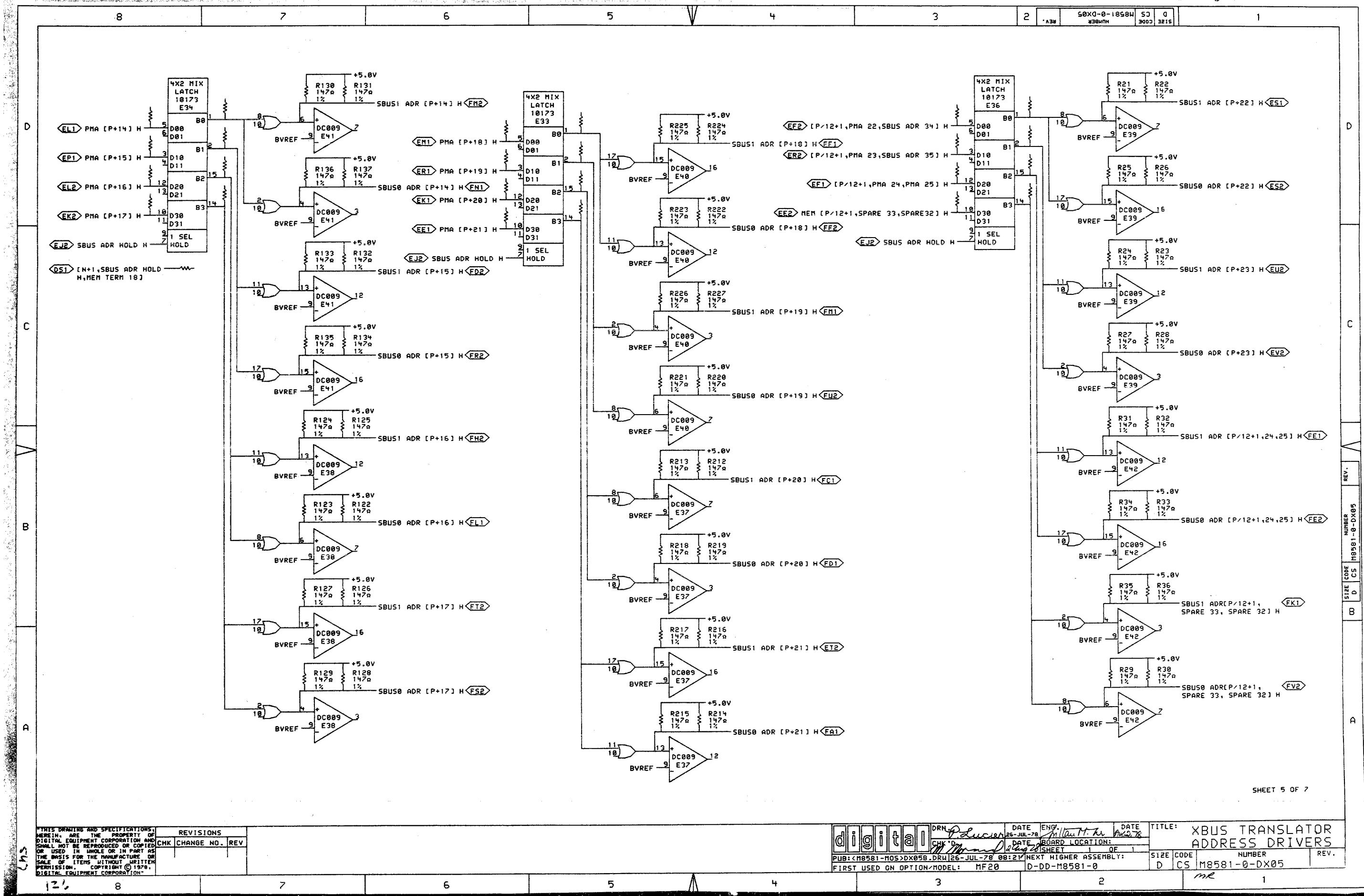


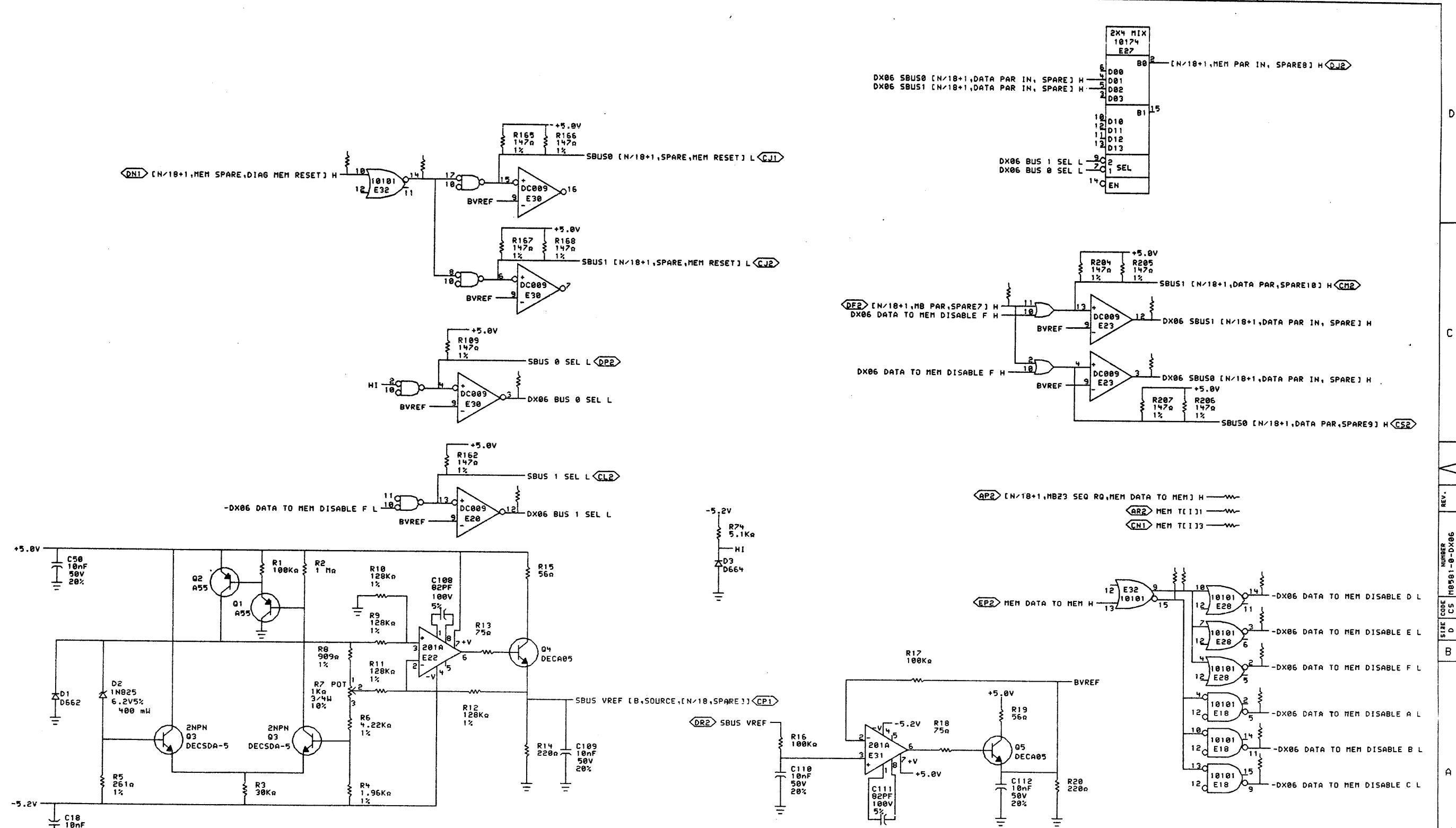
SHEET 4 OF 7

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REVISIONS	
CHK	CHANGE NO. REV

digital DRN Polycell DATE 26-JUL-78 ENG. M. L. R. DATE 26-JUL-78
CHK Signature DATE BOARD LOCATION:
PUB: M8581-MOS>DX04B.DRW SHEET 1 OF 1
FIRST USED ON OPTION/MODEL: MF20 D-DD-M3581-0
SIZE CODE D CS M8581-0-DX04 NUMBER REV.



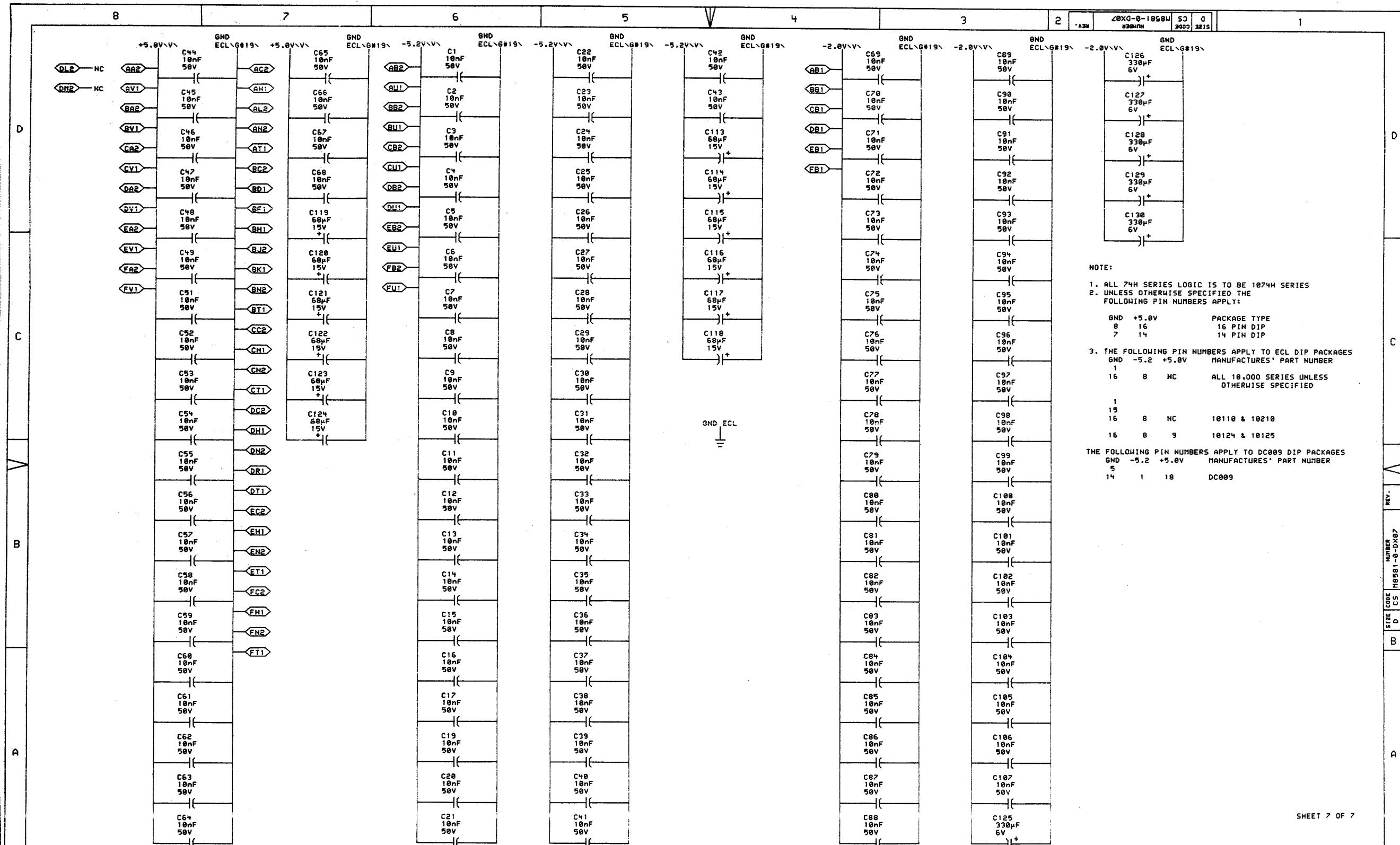


SHEET 6 OF 7

THIS DRAWING AND SPECIFICATIONS			REVISIONS		
CHK	CHANGE NO.	REV	DRN	DATE	ENG
				117-AUG-78	P. Lawrence Sept. 78

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digital	DRN	DATE	ENG	TITLE
M. Morel	117-AUG-78	P. Lawrence Sept. 78		XBUS TRANSLATOR CTRL & REF VOLT
CHK'D	DATE	BOARD LOCATION		
PUB: M8581-M05>DX06B.DR1	117-AUG-78 09:16	NEXT HIGHER ASSEMBLY:	SIZE	CODE
FIRST USED ON OPTION/MODEL:	MF20	10-DD-M8581-0	NUMBER	REV.
			D	ICS M8581-0-DX06



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digital	DRN: <i>D-1250</i>	DATE: <i>17-AUG-78</i>	ENG: <i>Plenum</i>	DATE: <i>Sept. 19</i>	TITLE: XBUS TRANSLATOR POWER. GND. CAPS.		
	CHK'D: <i>M. M. [Signature]</i>	DATE: <i>17-AUG-78</i>	BOARD LOCATIONS:				
			SHEET: <i>1</i>	OF: <i>1</i>			
PUB1M8581-1-MOS>DX07B.DRW 17-AUG-78 08:14:3 NEXT HIGHER ASSEMBLY:				SIZE: <i>D</i>	CODE: <i>CS</i>	NUMBER: <i>M8581-0-DX07</i>	REV.:
FIRST USED ON OPTION/MODEL: MF20				D-DD-M8581-0			

8 7 6 5 4 3 2 1

RESISTOR	SHOWN ON	VALUE	TERMINATES	RESISTOR	SHOWN ON	VALUE	TERMINATES	RESISTOR	SHOWN ON	VALUE	TERMINATES			
LOC(PIN)	DRW#	REF	SIGNAL	LOC(PIN)	DRW#	REF	SIGNAL	LOC(PIN)	DRW#	REF	SIGNAL			
R223(1)	DX01	A6	68Ω	%E20(3)	R187(1)	DX03	A2	68Ω	DX03 SBUS0 DATA IN [N+11] H	R278(1)	DX03	B3	68Ω	MB [N+11] H
R224(1)	DX01	A6	68Ω	%E20(7)	R159(1)	DX03	C7	68Ω	DX03 SBUS1 DATA IN [N+06] H	R283(1)	DX04	C7	68Ω	MB [N+12] H
R178(1)	DX01	C6	68Ω	%E21(12)	R147(1)	DX03	C2	68Ω	DX03 SBUS1 DATA IN [N+07] H	R168(1)	DX04	C3	68Ω	MB [N+13] H
R169(1)	DX01	C6	68Ω	%E21(16)	R188(1)	DX03	B4	68Ω	DX03 SBUS1 DATA IN [N+08] H	R167(1)	DX04	B5	68Ω	MB [N+14] H
R198(1)	DX01	D6	68Ω	%E21(3)	R186(1)	DX03	C4	68Ω	DX03 SBUS1 DATA IN [N+09] H	R275(1)	DX04	C5	68Ω	MB [N+15] H
R272(1)	DX01	D6	68Ω	%E21(7)	R145(1)	DX03	B7	68Ω	DX03 SBUS1 DATA IN [N+10] H	R172(1)	DX04	B7	68Ω	MB [N+16] H
R87(1)	DX06	D6	68Ω	%E32(14)	R184(1)	DX03	B2	68Ω	DX03 SBUS1 DATA IN [N+11] H	R173(1)	DX04	B3	68Ω	MB [N+17] H
R71(1)	DX06	B2	68Ω	%E32(15)	R286(1)	DX04	B7	68Ω	DX04 SBUS0 DATA IN [N+12] H	R183(1)	DX01	C3	68Ω	MEM RQ [N+18+1,0,3] H
R197(1)	DX01	A7	68Ω	%E32(3)	R84(1)	DX04	B2	68Ω	DX04 SBUS0 DATA IN [N+13] H	R276(1)	DX06	B2	68Ω	MEM T[1]
R171(1)	DX06	B2	68Ω	%E32(9)	R85(1)	DX04	A4	68Ω	DX04 SBUS0 DATA IN [N+14] H	R289(1)	DX06	B2	68Ω	MEM T[1]J3
R297(1)	DX05	D5	68Ω	%E33(1)	R287(1)	DX04	B4	68Ω	DX04 SBUS0 DATA IN [N+15] H	R300(1)	DX01	C3	68Ω	MEM [N+18+1,RQ 1,RD RQJ] H
R295(1)	DX05	D5	68Ω	%E33(14)	R119(1)	DX04	A7	68Ω	DX04 SBUS0 DATA IN [N+16] H	R299(1)	DX01	B3	68Ω	MEM [N+18+1,RQ 2,WR RQJ] H
R296(1)	DX05	D5	68Ω	%E33(15)	R121(1)	DX04	A2	68Ω	DX04 SBUS0 DATA IN [N+17] H	R161(1)	DX01	D3	68Ω	MEM [N+18+1,START A,START BJ] H
R298(1)	DX05	D5	68Ω	%E33(2)	R285(1)	DX04	C7	68Ω	DX04 SBUS1 DATA IN [N+12] H	R91(1)	DX05	D3	68Ω	MEM [P+12+1,SPARE 53,SPARE32] H
R178(1)	DX05	D7	68Ω	%E34(1)	R83(1)	DX04	C2	68Ω	DX04 SBUS1 DATA IN [N+13] H	R176(1)	DX05	D8	68Ω	PMA [P+14] H
R180(1)	DX05	D7	68Ω	%E34(14)	R06(1)	DX04	B4	68Ω	DX04 SBUS1 DATA IN [N+14] H	R177(1)	DX05	D8	68Ω	PMA [P+15] H
R179(1)	DX05	D7	68Ω	%E34(15)	R288(1)	DX04	C4	68Ω	DX04 SBUS1 DATA IN [N+15] H	R175(1)	DX05	D8	68Ω	PMA [P+16] H
R181(1)	DX05	D7	68Ω	%E34(2)	R118(1)	DX04	B7	68Ω	DX04 SBUS1 DATA IN [N+16] H	R174(1)	DX05	D8	68Ω	PMA [P+17] H
R92(1)	DX05	D2	68Ω	%E36(1)	R120(1)	DX04	B2	68Ω	DX04 SBUS1 DATA IN [N+17] H	R209(1)	DX05	D6	68Ω	PMA [P+18] H
R95(1)	DX05	D2	68Ω	%E36(14)	R150(1)	DX06	C6	68Ω	-DX06 BUS 0 SEL H	R208(1)	DX05	D6	68Ω	PMA [P+19] H
R94(1)	DX05	D2	68Ω	%E36(15)	R151(1)	DX06	B6	68Ω	-DX06 BUS 1 SEL H	R211(1)	DX05	D6	68Ω	PMA [P+20] H
R93(1)	DX05	D2	68Ω	%E36(2)	R282(1)	DX06	A1	68Ω	DX06 DATA TO MEM DISABLE A H	R210(1)	DX05	C6	68Ω	PMA [P+21] H
R292(1)	DX01	A7	68Ω	DATA VALID[N+18+1, A OUT, B OUT] H	R279(1)	DX06	A1	68Ω	DX06 DATA TO MEM DISABLE B H	R293(1)	DX05	C8	68Ω	[N+1,SBUS ADR HOLD H, MEM TERM 18]
R152(1)	DX02	B7	68Ω	DX02 SBUS0 DATA IN [N+00] H	R237(1)	DX06	A1	68Ω	DX06 DATA TO MEM DISABLE C H	R160(1)	DX01	B7	68Ω	[N+18+1,CLK SBUS, SPAREJ CLK H
R194(1)	DX02	B2	68Ω	DX02 SBUS0 DATA IN [N+01] H	R106(1)	DX06	B1	68Ω	DX06 DATA TO MEM DISABLE D H	R199(1)	DX06	C3	68Ω	[N+18+1,HB PAR, SPAREJ H]
R192(1)	DX02	A4	68Ω	DX02 SBUS0 DATA IN [N+02] H	R252(1)	DX06	B1	68Ω	DX06 DATA TO MEM DISABLE E H	R277(1)	DX06	B2	68Ω	[N+18+1,M823 SEQ RQ, MEM DATA TO MEM] H
R154(1)	DX02	B4	68Ω	DX02 SBUS0 DATA IN [N+03] H	R196(1)	DX06	A1	68Ω	DX06 DATA TO MEM DISABLE F H	R182(1)	DX01	A3	68Ω	-[N+18+1, MEM DIAG, -MEM ADR PAR1] H
R190(1)	DX02	A7	68Ω	DX02 SBUS0 DATA IN [N+04] H	R290(1)	DX06	C2	68Ω	DX06 SBUS [N+18+1, DATA PAR IN, SPAREJ] H	R294(1)	DX06	D7	68Ω	[N+18+1, MEM SPARE, DIAG MEM RESET] H
R156(1)	DX02	A2	68Ω	DX02 SBUS0 DATA IN [N+05] H	R291(1)	DX06	C2	68Ω	DX06 SBUS1 [N+18+1, DATA PAR IN, SPAREJ] H	R88(1)	DX05	D3	68Ω	[P+12+1,PMA 22,SBUS ADR 34] H
R153(1)	DX02	C7	68Ω	DX02 SBUS1 DATA IN [N+00] H	R97(1)	DX02	C7	68Ω	HB [N+00] H	R89(1)	DX05	D3	68Ω	[P+12+1,PMA 23,SBUS ADR 35] H
R195(1)	DX02	C2	68Ω	DX02 SBUS1 DATA IN [N+01] H	R263(1)	DX02	C3	68Ω	MB [N+01] H	R90(1)	DX05	D3	68Ω	[P+12+1,PMA 24,PMA 25] H
R193(1)	DX02	B4	68Ω	DX02 SBUS1 DATA IN [N+02] H	R284(1)	DX02	B5	68Ω	MB [N+02] H					
R155(1)	DX02	C4	68Ω	DX02 SBUS1 DATA IN [N+03] H	R95(1)	DX02	C5	68Ω	MB [N+03] H					
R191(1)	DX02	B7	68Ω	DX02 SBUS1 DATA IN [N+04] H	R281(1)	DX02	B7	68Ω	MB [N+04] H					
R157(1)	DX02	B2	68Ω	DX02 SBUS1 DATA IN [N+05] H	R72(1)	DX02	B3	68Ω	MB [N+05] H					
R156(1)	DX03	B7	68Ω	DX03 SBUS0 DATA IN [N+06] H	R73(1)	DX03	C7	68Ω	MB [N+06] H					
R146(1)	DX03	B2	68Ω	DX03 SBUS0 DATA IN [N+07] H	R45(1)	DX03	C3	68Ω	MB [N+07] H					
R189(1)	DX03	A4	68Ω	DX03 SBUS0 DATA IN [N+08] H	R280(1)	DX03	B5	68Ω	MB [N+08] H					
R185(1)	DX03	B4	68Ω	DX03 SBUS0 DATA IN [N+09] H	R236(1)	DX03	C5	68Ω	MB [N+09] H					
R148(1)	DX03	A7	63Ω	DX03 SBUS0 DATA IN [N+10] H	R46(1)	DX03	B7	68Ω	MB [N+10] H					

NOTE:

- ALL TERMINATORS HAVE PIN TWO CONNECTED TO -2.8V AND ARE 5% 1/4WATT UNLESS OTHERWISE SPECIFIED
- ENTRIES ARE SORTED BY SIGNAL NAME
- % INDICATES OUTPUT OF DIP LOC AND <> INDICATES PIN NUMBER

THIS DRAWING AND SPECIFICATIONS

REVISIONS		
CHK	CHANGE NO.	REV.

DATE

BOARD LOCATION:

DATE

NUMBER OF SHEET

NEXT HIGHER ASSEMBLY:

SIZE CODE

NUMBER

REV.

DRN. 10
Smith 16-AUG-78

ENG P. Karamay 17-78

DATE

1 OF 1

FIRST USED ON OPTION/MODEL: MF20

D- DD-M8581-0

SIZE CODE

M8581-0-RES

NUMBER

REV.

D CS

1

129

THIS DRAWING AND SPECIFICATIONS

REVISIONS

CHK

CHANGE NO.

REV.

DATE

BOARD LOCATION:

DATE

NUMBER OF SHEET

NEXT HIGHER ASSEMBLY: