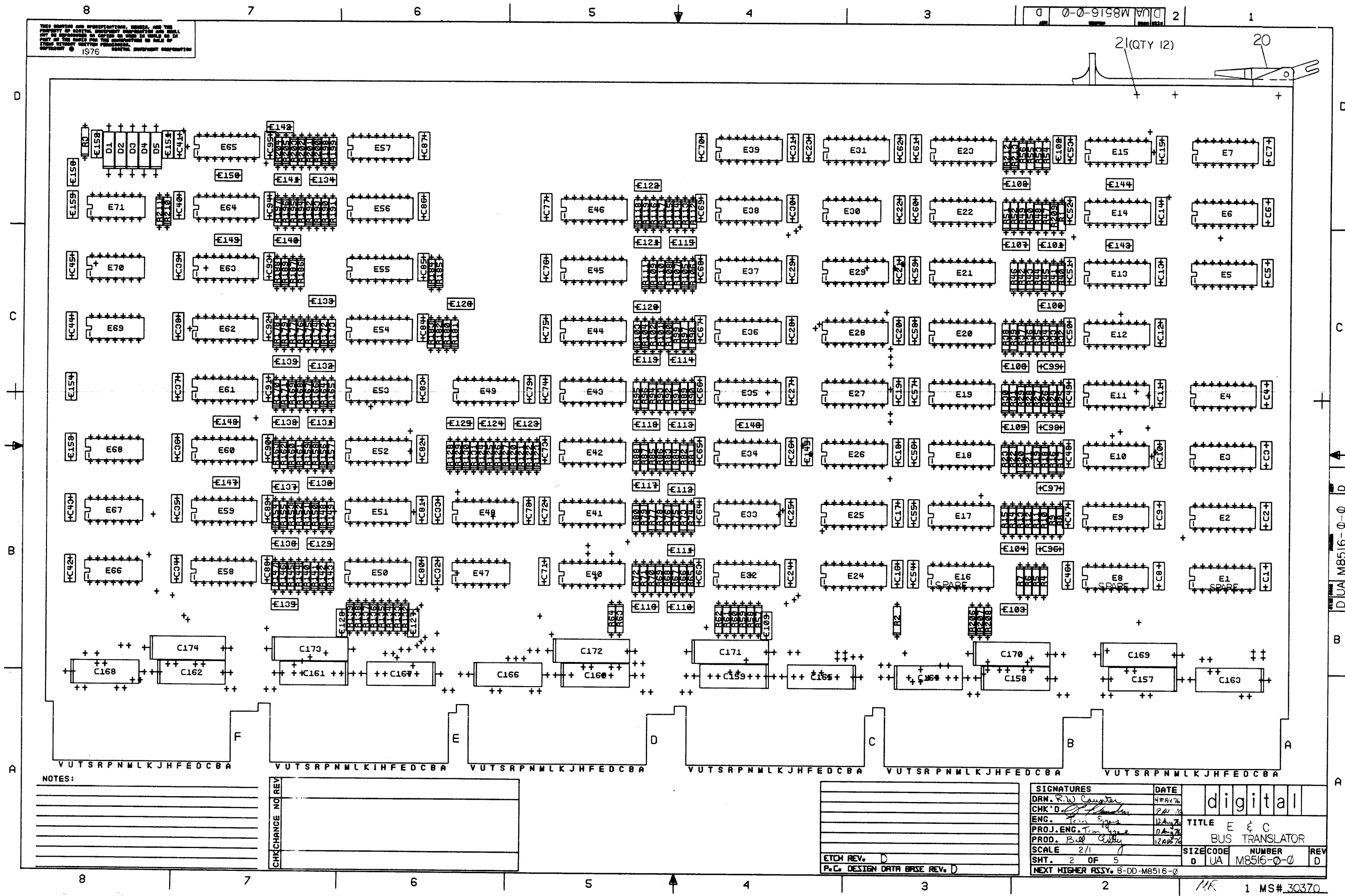


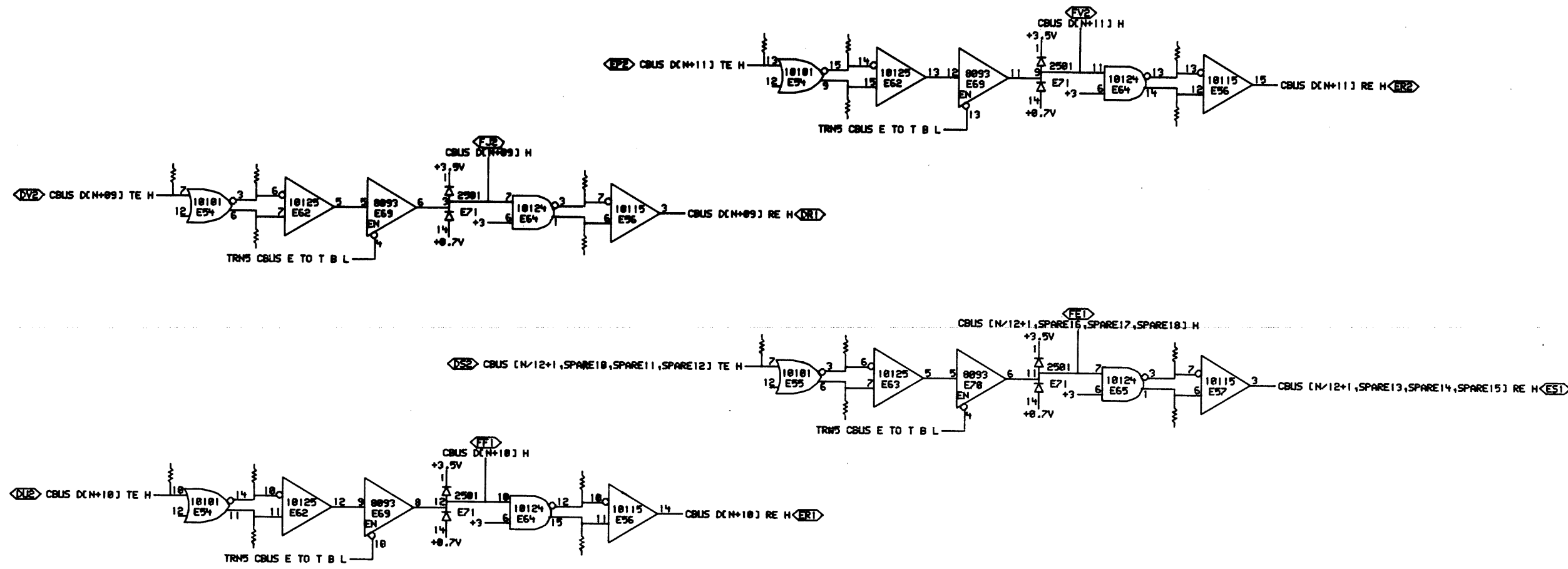
[illegible]

MR

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SIGNATURES		DATE	digital	
DRN. R.W. Carpenter		4/10/76	TITLE E & C BUS TRANSLATOR	
CHK'D. J. J. [Signature]		7/21/76		
ENG. [Signature]		12/2/76		
PROJ. ENG. [Signature]		12/2/76		
PROD. Bill [Signature]		12/2/76	SIZE CODE NUMBER REV 0 UA M8516-0-0 D	
SCALE 2/1				
SHT. 2 OF 5				
NEXT HIGHER ASSY. B-DD-M8516-0			1 MS# 30370	



BOARD LOCATION	VARIABLE
4AF04	N=24
4AF05	N=12
4AF06	N=00

2 OF 6

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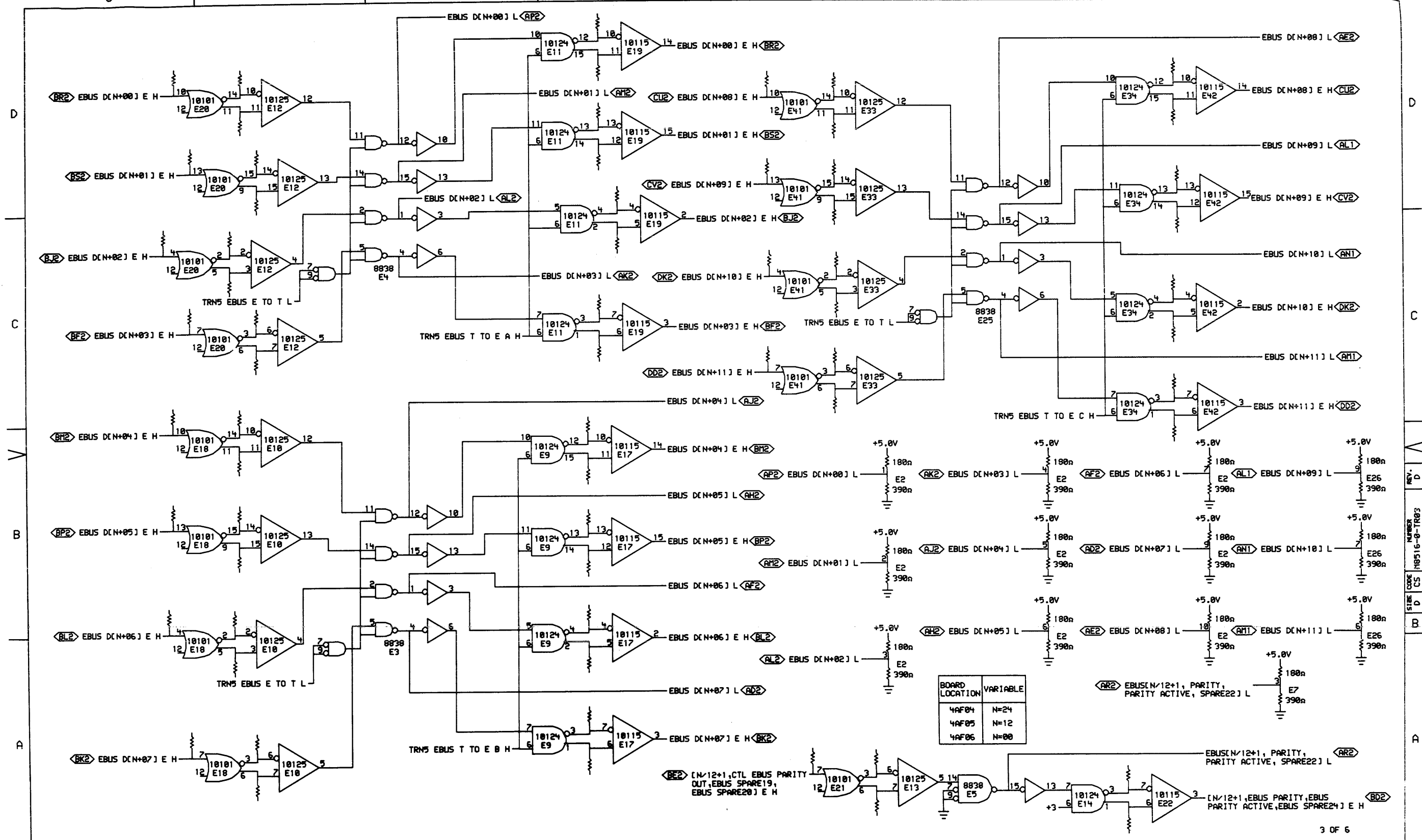
REV	CHANGE NO.	REV
1	1	1
2	2	2
3	3	3
4	4	4
5	5	5
6	6	6
7	7	7
8	8	8
9	9	9
10	10	10

digital	DATE	22-SEP-75	DATE	22-SEP-75	TITLE	E & C BUS TRANSLATOR
	TIME	14:12:13	TIME	14:12:13	NUMBER	1
FIRST USED ON OPTION MODEL		KL10	NEXT HIGHER ASSEMBLY		B-DD-M8516-0	REV.
SIZE		D	CODE		CS	NUMBER
M8516-0-TR02		REV.		C		

M8517 { SLOT 14 - 11=12 & N=3φ
SLOT 15 - N=φ6 & N=24
SLOT 16 - N=φφ & N=18

M8516 { SLOT 4 - 24 → 35
SLOT 5 - 12 → 23
SLOT 6 - φφ → 11

REV. D 1
E8011-0-11R03
M8516-0-TR03
SIZE CODE CS 300 3215



REV. D
NUMBER
M8516-0-TR03
SIZE CODE CS
D 300 3215

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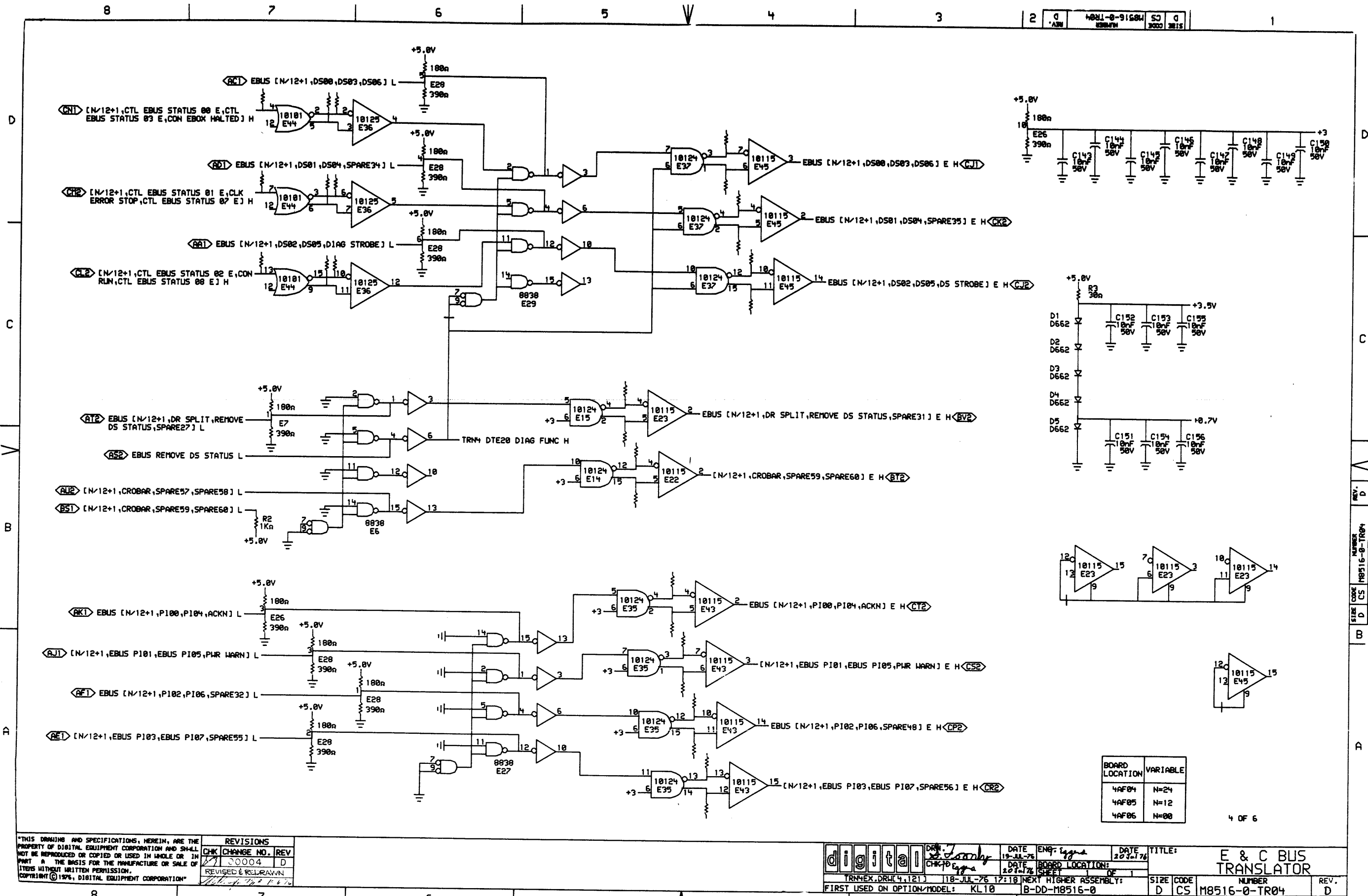
REV	CHANGE NO.	REV
00004	D	

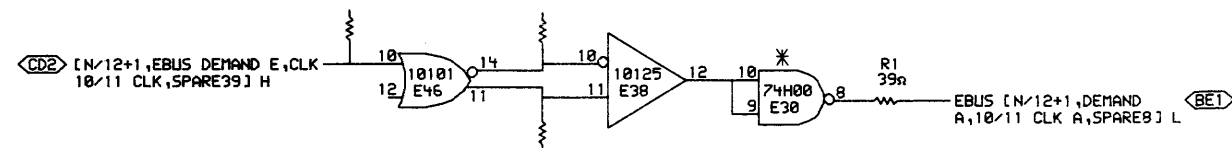
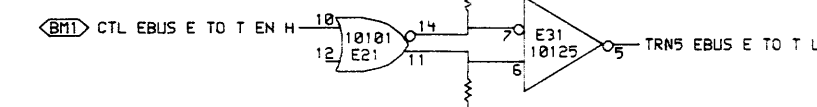
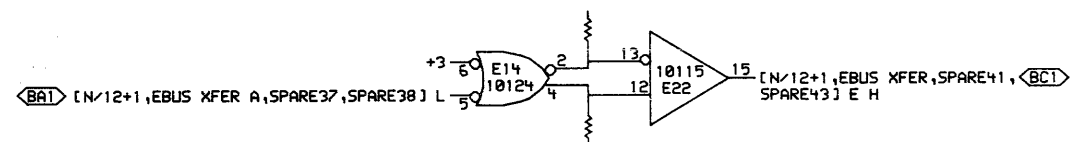
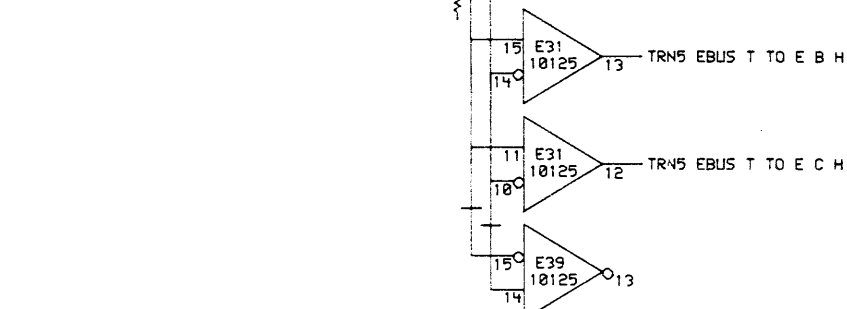
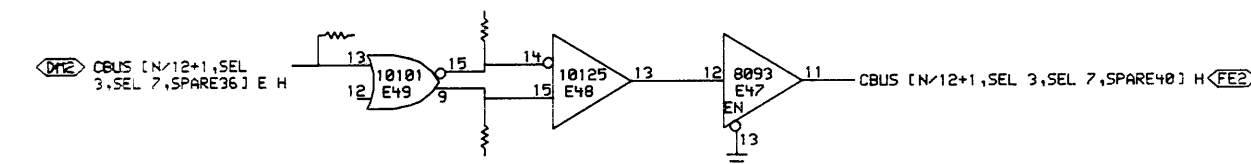
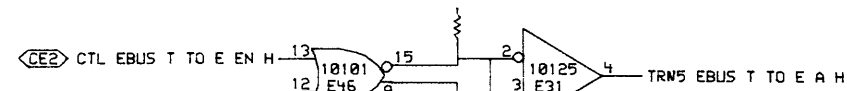
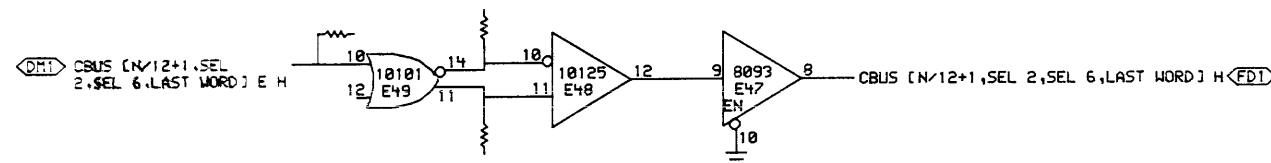
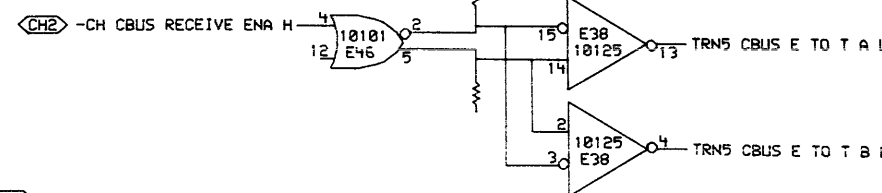
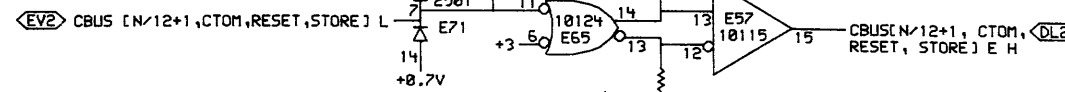
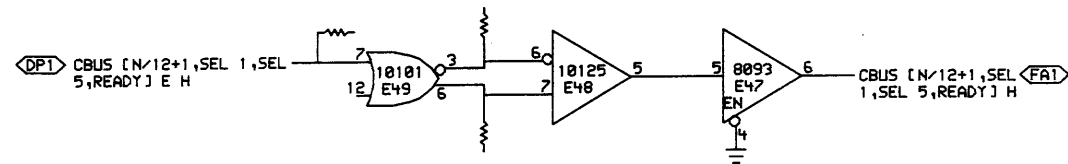
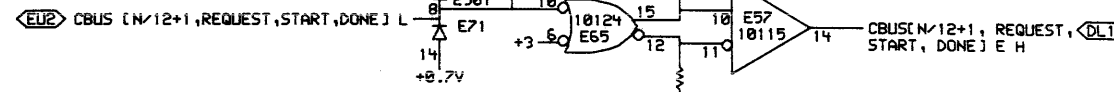
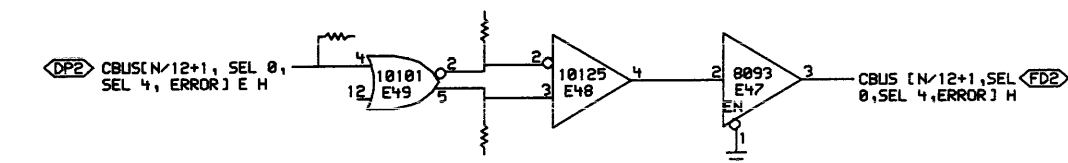
REVISED & REDRAWN
M8516-0-TR03

DATE	ENG	DATE	TITLE
19-11-76	gma	20-11-76	E & C BUS TRANSLATOR

TRNEX, DRW 4, 121
18-JUL-76 12:14 NEXT HIGHER ASSEMBLY:
FIRST USED ON OPTION/MODEL: KL10 B-DD-M8516-0

SIZE	CODE	NUM ER	REV.
D	CS	M8516-0-TR03	D





BOARD LOCATION	VARIABLE
4AF04	N=24
4AF05	N=12
4AF06	N=00

5 OF 6

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REVISIONS		
CHK	CHANGE NO.	REV
<i>[Signature]</i>	00004	D
REVISED & REDRAWN		

d	i	g	i	t	a	l
TRN5EX.DRW4,121						
FIRST USED ON OPTION						

DRN. <i>7</i>	DATE 19-JUL-76	ENG. <i>Eggs</i>	DATE 20JUL76
CHKD <i>Eggs</i>	DATE 20JUL76	BOARD LOCATION: SHEET <i>1</i> OF <i>1</i>	
18-JUL-76 17:20		NEXT HIGHER ASSEMBLY: MODEL: KL10	
		B-DD-M8516-0	

76	TITLE
	SIZE
	D

E & C BUS TRANSLATOR		
CODE	NUMBER	REV
CS	M8516-0-TR05	D

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538-0-9160H
S300 3000 3015

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RESISTOR LOC(PIN)	SHOWN DRW#	ON REF	VALUE	TERMINATES SIGNAL
R10K(1)	TR03	B5	60n	XE9K15)
R5K(1)	TR03	A5	60n	XE9K2)
R7K(1)	TR03	A5	60n	XE9K3)
R4K(1)	TR03	B5	60n	XE9K4)
R138K(1)	TR01	D7	60n	CBUS D(N+00) TE H
R137K(1)	TR01	D7	60n	CBUS D(N+01) TE H
R135K(1)	TR01	C7	60n	CBUS D(N+02) TE H
R134K(1)	TR01	C7	60n	CBUS D(N+03) TE H
R136K(1)	TR01	B7	60n	CBUS D(N+04) TE H
R133K(1)	TR01	D4	60n	CBUS D(N+05) TE H
R132K(1)	TR01	C4	60n	CBUS D(N+06) TE H
R139K(1)	TR01	C4	60n	CBUS D(N+07) TE H
R101K(1)	TR01	B4	60n	CBUS D(N+08) TE H
R100K(1)	TR02	C7	60n	CBUS D(N+09) TE H
R103K(1)	TR02	B7	60n	CBUS D(N+10) TE H
R102K(1)	TR02	D5	60n	CBUS D(N+11) TE H
R125K(1)	TR05	D7	60n	CBUS [N/12+1,SEL 1,SEL 5,READY] E H
R127K(1)	TR05	C7	60n	CBUS [N/12+1,SEL 2,SEL 6,LAST WORD] E H
R126K(1)	TR05	B7	60n	CBUS [N/12+1,SEL 3,SEL 7,SPARE36] E H
R104K(1)	TR02	C5	60n	CBUS [N/12+1,SPARE10,SPARE11,SPARE12] TE H
R105K(1)	TR01	A7	60n	CBUS[N/12+1, PAR LEFT, PAR RIGHT, SPARE1] TE H
R124K(1)	TR05	D7	60n	CBUS[N/12+1, SEL 0, SEL 4, ERROR] E H
R207K(1)	TR05	A2	60n	CTL EBUS [N/12+1,SPARE44,E TO T EN,T TO E EN] H
R200K(1)	TR05	A2	60n	CTL EBUS [N/12+1,SPARE46,SPARE47,SPARE40] H
R42K(1)	TR03	D0	60n	EBUS D(N+00) E H
R46K(1)	TR03	D0	60n	EBUS D(N+01) E H
R43K(1)	TR03	C0	60n	EBUS D(N+02) E H
R44K(1)	TR03	C0	60n	EBUS D(N+03) E H
R15K(1)	TR03	B0	60n	EBUS D(N+04) E H
R14K(1)	TR03	B0	60n	EBUS D(N+05) E H
R13K(1)	TR03	B0	60n	EBUS D(N+06) E H
R12K(1)	TR03	A0	60n	EBUS D(N+07) E H
R71K(1)	TR03	D4	60n	EBUS D(N+08) E H
R72K(1)	TR03	D4	60n	EBUS D(N+09) E H
R60K(1)	TR03	C4	60n	EBUS D(N+10) E H
R67K(1)	TR03	C4	60n	EBUS D(N+11) E H
R63K(1)	TR01	B4	60n	EBUS[N/12+1, F00, CS00, CS04] E H
R69K(1)	TR01	A4	60n	EBUS[N/12+1, F02, CS02, CS06] E H
R70K(1)	TR01	B4	60n	[N/12+1,APR EBUS F01,EBUS CS01,EBUS CS05] E H
R110K(1)	TR01	A7	60n	[N/12+1,CLK EBUS CLK, SPARE3,SPARE4] H

RESISTOR LOC(PIN)	SHOWN DRW#	ON REF	VALUE	TERMINATES SIGNAL
R64K(1)	TR01	A4	60n	[N/12+1,CTL CONSOLE CONTROL,EBUS CS03 E,CLK EBUS RESET E] H
R45K(1)	TR03	A4	60n	[N/12+1,CTL EBUS PARITY OUT,EBUS SPARE19, EBUS SPARE20] E H
R103K(1)	TR04	D7	60n	[N/12+1,CTL EBUS STATUS 00 E,CTL EBUS STATUS 03 E,CON EBOX HALTED] H
R104K(1)	TR04	D7	60n	[N/12+1,CTL EBUS STATUS 01 E,CLK ERROR STOP,CTL EBUS STATUS 07 E] H
R110K(1)	TR04	C7	60n	[N/12+1,CTL EBUS STATUS 02 E,CON RUN,CTL EBUS STATUS 00 E] H
R119K(1)	TR05	A7	60n	[N/12+1,EBUS DEMAND E,CLK 10/11 CLK,SPARE39] H
R206K(1)	TR05	A2	60n	[N/12+1,SPARE40,SPARE49,-CH CBUS RECEIVE ENA] H

NOTE:

1. ALL TERMINATORS HAVE PIN TWO CONNECTED TO -2.0V AND ARE 5% 1/4WATT UNLESS OTHERWISE SPECIFIED
2. ENTRIES ARE SORTED BY SIGNAL NAME
3. X INDICATES OUTPUT OF DIP LOC AND () INDICATES PIN NUMBER

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REVISIONS		
CHK	CHANGE NO.	REV

digital
M85162.RVD(4,427)

DRN. *C. Smith*
DATE 10-JUL-76
ENG. *gpa*
DATE 20-JUL-76
BOARD LOCATION: 2 OF 2
118-JUL-76 17:50 INERT HIGHER ASSEMBLY:
FIRST USED ON OPTION/MODEL: KL10 B-DD-M8516-0

TITLE: E & C BUS TRANS TERMINATORS
SIZE CODE D CS
NUMBER M8516-0-RES
REV. D

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