REVISION CONTROL SHEET

CUSTOMER PRINT SET | MFG SET

DRAWING NO	NO OF SHT	DESCRIPTION	OPTION NO/FILE DATE	REVISIONS
		MODULE REVISION		A B B
D-UA-M8512-Ø-Ø	5	DATA PATH		- A A1
D-CS-M8512-Ø-DPØ1	1	DATA PATH AR REGISTER		- A A
D-CS-M8512-Ø-DPØ2	1	DATA PATH ARX & MQ REGISTERS		- A A
D-CS-M8512-Ø-DPØ3	1	DATA PATH AD & ADX ADDERS		- A A
D-CS-M8512-Ø-DPØ4	1	DATA PATH EBUS, FM, BR, BRX		- A A
D-CS-M8512-Ø-DPØ5	1	DATA PATH POWER, GND, CAPS		- A B
D-CS-M8512-Ø-RES	2	DATA PATH TERMINATORS		- A A
K-CO-M8512-0-4	1	DATA PATH BOARD		C C C
D-AH-M8512-Ø-5	4	DATA PATH BOARD		B B B
B-MH-M8512-Ø-6	1	MODULE ECO HISTORY		- A B
5010376		ETCH CIRCUIT BOARD		C C C
P00-M8512-ØØ		PROCESS SHEET (REF ONLY)		- - -

SEE SHEET 3

REWORK VERSION

RELAYOUT VERSION SEE SHEET 3

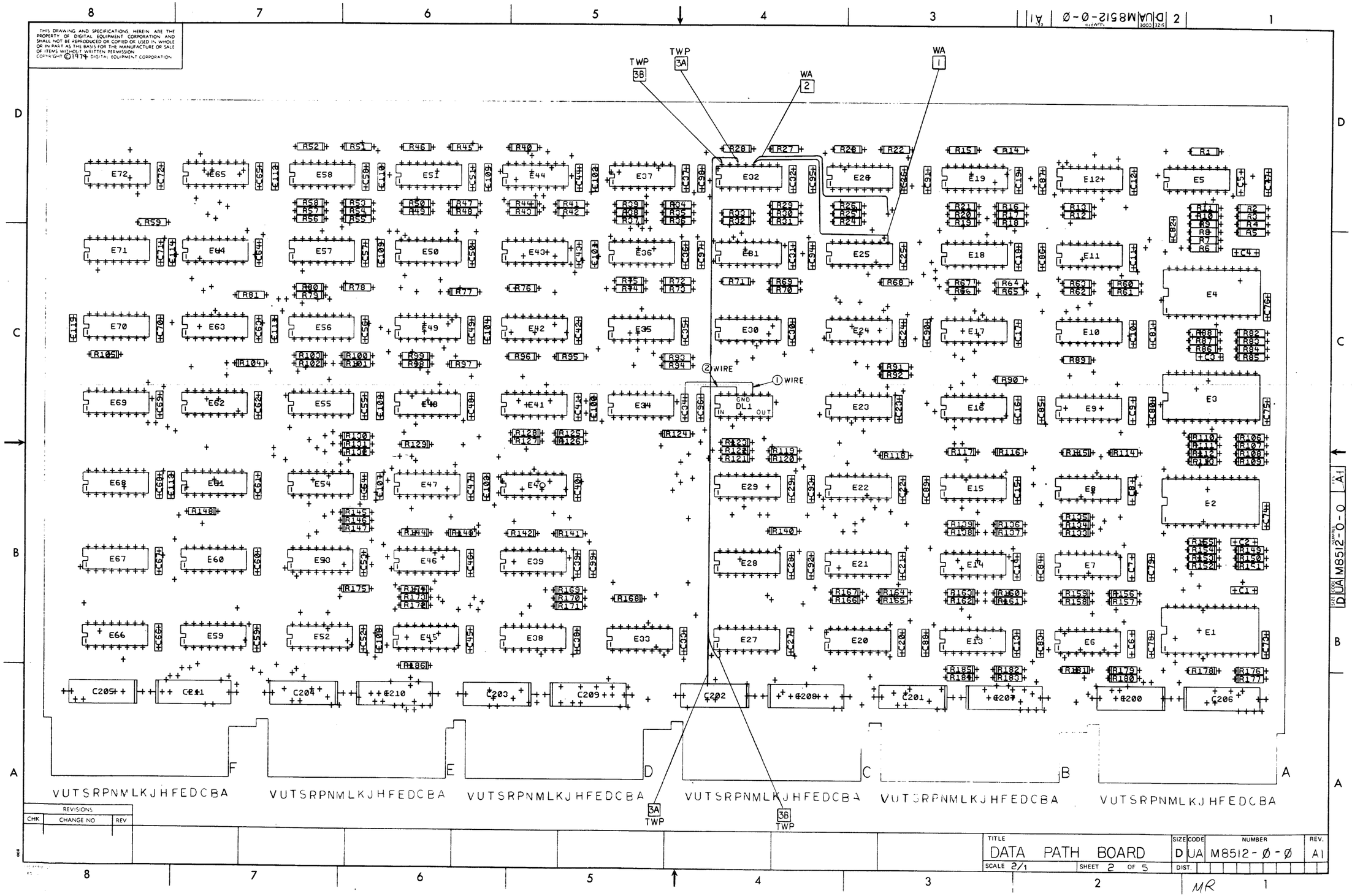
ECO NO	ORIG.	00001	00002

CUSTOMER PRINT SET CODES

X = PRINT OF DOCUMENT INCLUDED IN PRINT SET
C = INCLUDES ALL PRINTS INDICATED ON DOCUMENT
S = CONFIDENTIAL AUTHORIZED SIGNATURE REQUIRED

TITLE		SIZE	CODE	NUMBER	REV
DATA PATH BOARD	SHEET 2 OF 3	B	DD	M8512-Ø	B

MR



REVISIONS		
CHK	CHANGE NO	REV

TITLE	SIZE	CODE	NUMBER	REV.
DATA PATH BOARD	D	UA	M8512-Ø-Ø	A1
SCALE 2/1 SHEET 2 OF 5	DIST.			

MR

DUA M8512-0-0 A1

SIZE	CODE	NUMBER	REV.
B	DD	M8512-0	B

DRAWING NO.	NO. OF SHTS.	PART NO.	DESCRIPTION	(NEW LAYOUT VERSION) REVISIONS
			MODULE REVISION	C
D-UA-M8512-0-0	5		DATA PATH	B
D-CS-M8512-0-DP01	1		DATA PATH AR REGISTER	A
D-CS-M8512-0-DP02	1		DATA PATH ARX & MQ REGISTERS	A
D-CS-M8512-0-DP03	1		DATA PATH AD & ADX ADDERS	A
D-CS-M8512-0-DP04	1		DATA PATH EBUS FM, BR, BRX	A
D-CS-M8512-0-DP05	1		DATA PATH POWER, GND, CAPS	B
D-CS-M8512-0-RES	2		DATA PATH TERMINATORS	A
K-CO-M8512-4	1		DATA PATH BOARD	D
D-AH-M8512-5	4		DATA PATH BOARD	C
B-MH-M8512-6	1		MODULE ECO HISTORY	B
5010376			ETCH CIRCUIT BOARD	D
P00-M8512-00			PROCESS SHEET (REF ONLY)	-

NOTES:

REVISIONS	REV.	CHG NO.	DATE
	B	00002	

digital

USED ON OPTION/MODEL		DRN		
		H Demmore	9-1-77	
		CHK'D		
		ENG.		
		PROD.		

TITLE: DATA PATH BOARD

SIZE	CODE	NUMBER	REV.
B	DD	M8512-0	B

SHEET 3 OF 3

13

DRB 126

MR

23 (QTY 12) 22

SPARE

NOTES:

CHK	CHANGE NO	REV

ETCH REV.
P.C. DESIGN DATA BASE REV.

SIGNATURES	DATE
DRN. K. Yamashita	2/1977
CHK'D. R.W. Conklin	2/1977
ENG. E. Cole	2/4/77
PROJ.ENG. E. Cole	2/4/77
PROD. W. Bailey	2/4/77

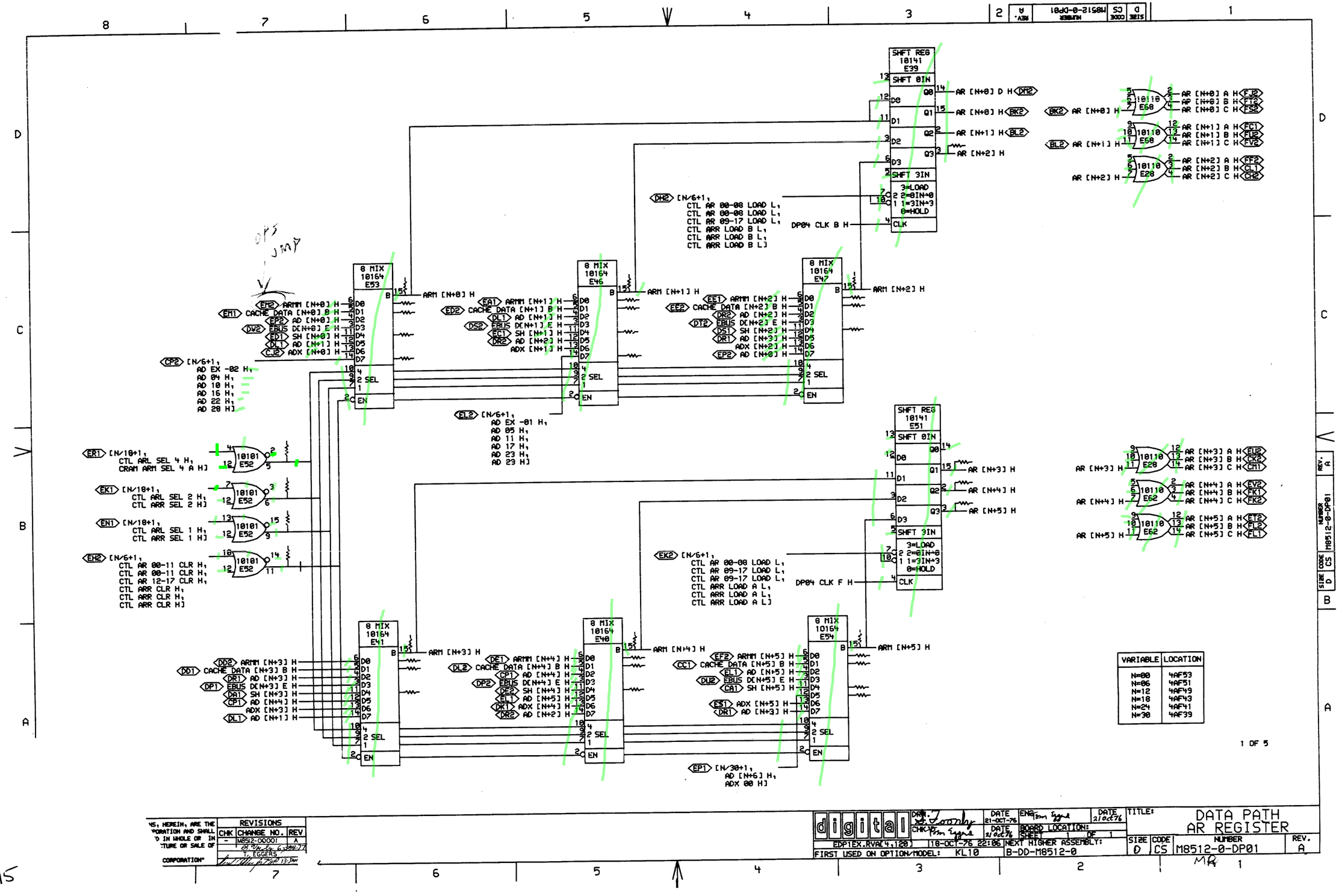
digital

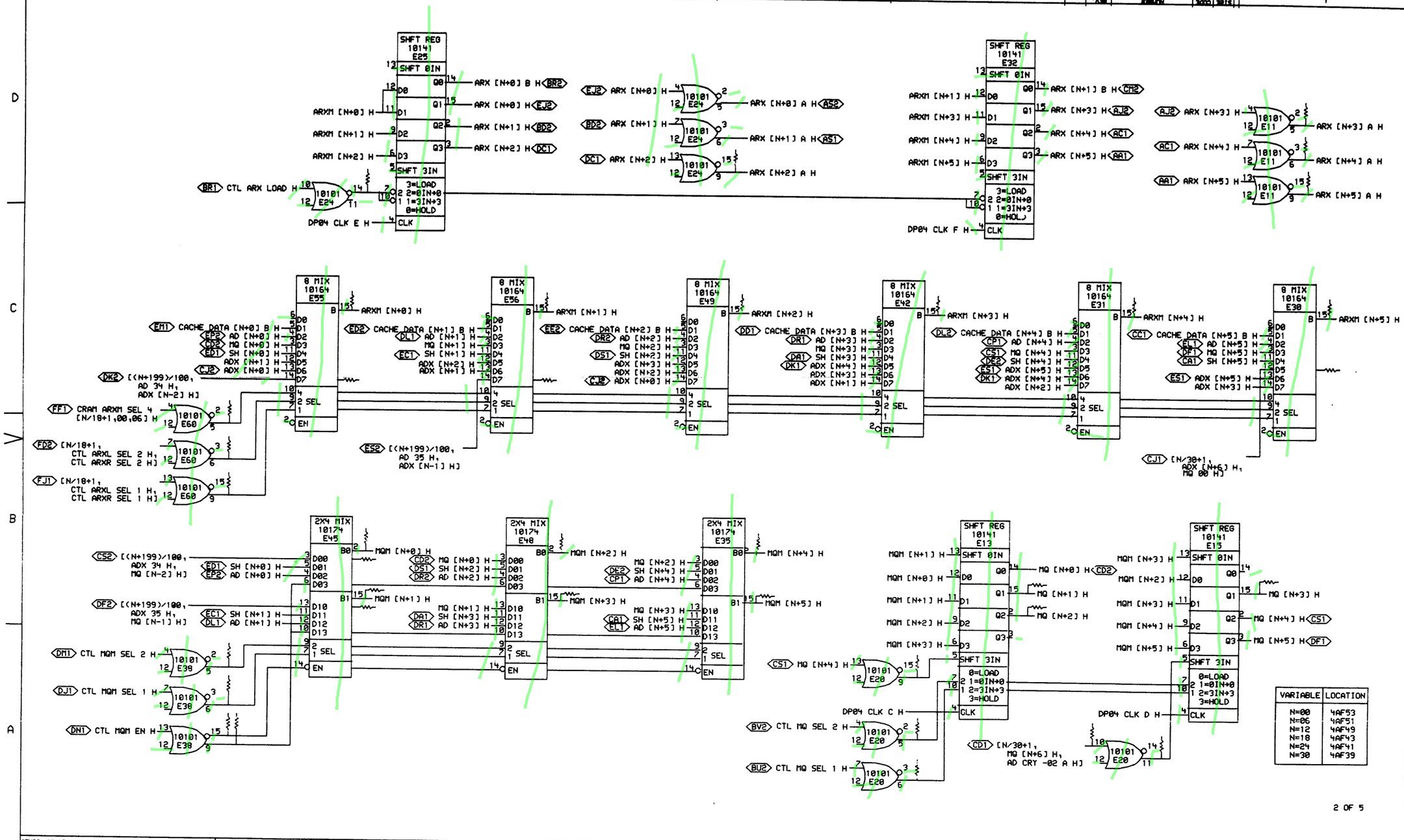
TITLE DATA PATH BOARD

SCALE 2/1
SHT. 2 OF 5
NEXT HIGHER ASSY. B-DD-M8512-Ø

SIZE	CODE	NUMBER	REV
D	UA	M8512-Ø-Ø	B

MR 1 MS# 104469

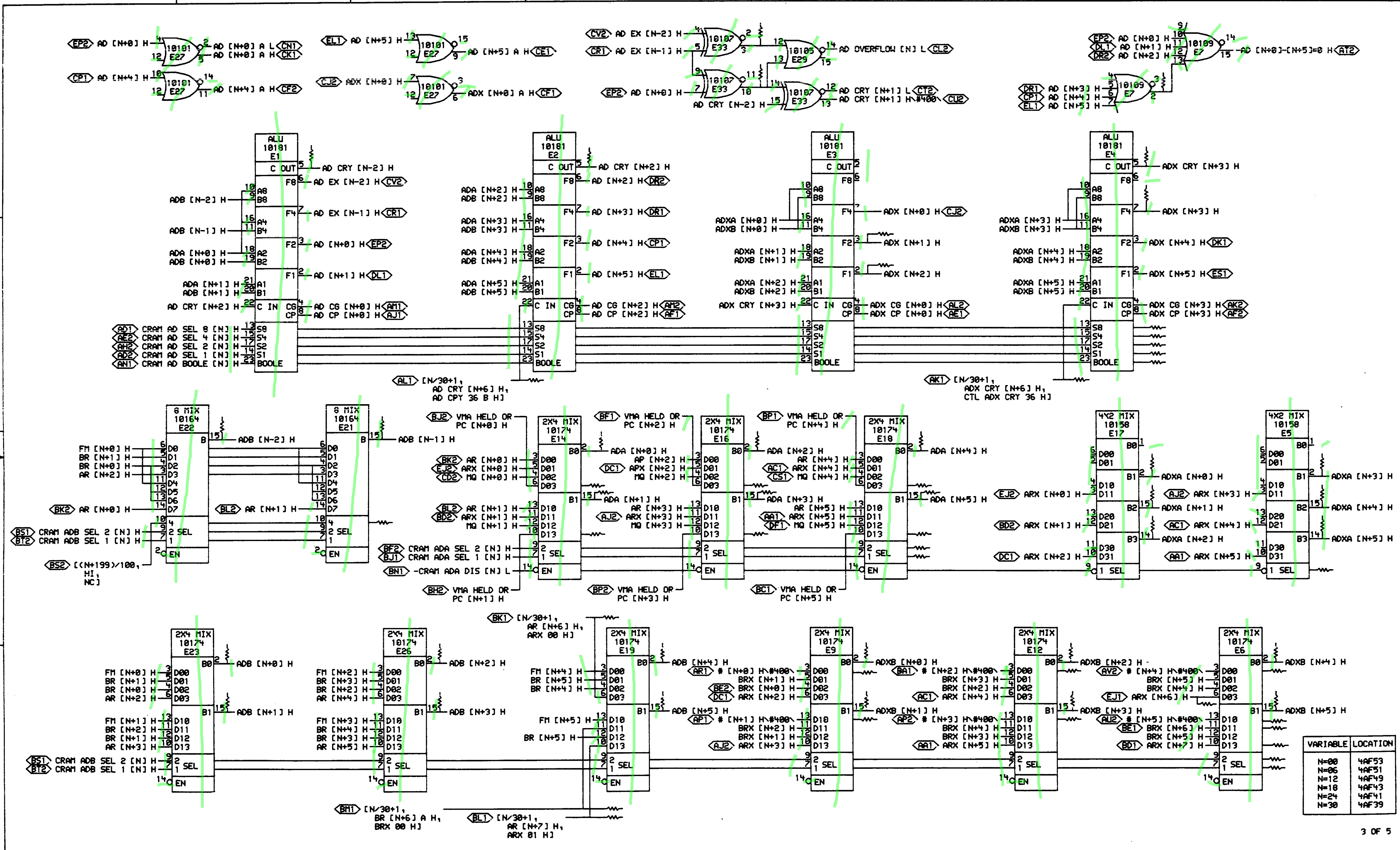

SHFT REG 10141 E39
SHFT 0IN
SHFT 3IN
3=LOAD 2=0IN→0 1 1=3IN→3 0=HOLD
CLK
8 MIX 10164 E53
8 MIX 10164 E46
8 MIX 10164 E47
8 MIX 10164 E41
8 MIX 10164 E40
8 MIX 10164 E54
2 SEL
EN
SHFT REG 10141 E51
10101 E52
10110 E28
10110 E58
10110 E62
DP04 CLK B H
DP04 CLK F H
AR [N+0] H
AR [N+1] H
AR [N+2] H
AR [N+3] H
AR [N+4] H
AR [N+5] H
ARM [N+0] H
ARM [N+1] H
ARM [N+2] H
ARM [N+3] H
ARM [N+4] H
ARM [N+5] H
CTL AR 00-08 LOAD L,
CTL AR 09-17 LOAD L,
CTL ARR LOAD B L,
CTL ARR LOAD A L,
CTL ARL SEL 4 H,
CRAM ARM SEL 4 A H]
CTL ARL SEL 2 H,
CTL ARR SEL 2 H]
CTL ARL SEL 1 H,
CTL ARR SEL 1 H]
CTL AR 00-11 CLR H,
CTL AR 12-17 CLR H,
CTL ARR CLR H,
AD EX -02 H,
AD EX -01 H,
AD 04 H,
AD 05 H,
AD 10 H,
AD 11 H,
AD 16 H,
AD 17 H,
AD 22 H,
AD 23 H,
AD 28 H]
AD 29 H]
ADX 00 H]
CACHE DATA [N+0] B H
CACHE DATA [N+1] B H
CACHE DATA [N+2] B H
CACHE DATA [N+3] B H
CACHE DATA [N+4] B H
CACHE DATA [N+5] B H
AD [N+0] H
AD [N+1] H
AD [N+2] H
AD [N+3] H
AD [N+4] H
AD [N+5] H
ADX [N+1] H
ADX [N+2] H
ADX [N+3] H
DCN+0] E H
DCN+1] E H
DCN+2] E H
DCN+3] E H
DCN+4] E H
DCN+5] E H
SH [N+0] H
SH [N+1] H
SH [N+2] H
SH [N+3] H
SH [N+4] H
SH [N+5] H
ARMM [N+0] H
ARMM [N+1] H
ARMM [N+2] H
ARMM [N+3] H
ARMM [N+4] H
ARMM [N+5] H
VARIABLE
LOCATION
N=00
4AF53
N=06
4AF51
N=12
4AF49
N=18
4AF43
N=24
4AF41
N=30
4AF39
1 OF 5
REVISIONS
CHK
CHANGE NO.
REV
M8512-00001
T. EGGERS
digital
EDPTEX.RVAL4.1201
FIRST USED ON OPTION/MODEL: KL10
18-OCT-76 22:06
B-DD-M8512-0
DATE 21-OCT-76
DATE 21Oct76
TITLE:
DATA PATH
AR REGISTER
SIZE D
CODE CS
NUMBER M8512-0-DP01
REV. A



REVISIONS		
CHK	CHANGE NO.	REV
–	M8512-00001	A
J.EGGERS		

digital

DRN: J. Sooby	DATE 21-OCT-76	ENG: Tom Eggers	DATE 21-OCT-76	TITLE: DATA PATH ARX & MQ REGISTERS	
CHK: Tom Eggers	DATE 21-OCT-76	BOARD LOCATION:	SHEET 1 OF		
EDP2EX.RVAC4 1201	19-OCT-76 12:43	NEXT HIGHER ASSEMBLY:		SIZE D	CODE CS
FIRST USED ON OPTION/MODEL: KL10		B-DD-M8512-0		NUMBER M8512-0-DP02	REV. A



VARIABLE	LOCATION
N=00	4AF53
N=06	4AF51
N=12	4AF49
N=18	4AF43
N=24	4AF41
N=30	4AF39

REVISIONS		
CHK	CHANGE NO.	REV
-	M8512-00001	A

digital

DRN. | DATE 21-OCT-76 | ENG. | DATE 21 Oct 76
CHKD. | DATE 21 Oct 76 | BOARD LOCATION:
SHEET 1 OF 1

EDP3EX.RVA[4,120] 18-OCT-76 22:31 NEXT HIGHER ASSEMBLY: B-DD-M8512-0
FIRST USED ON OPTION/MODEL: KL10

TITLE: DATA PATH AD & ADX ADDERS

SIZE D | CODE CS | NUMBER M8512-0-DP03 | REV. A

8 MIX 10164 E57 — R88 100Ω — EBUS D[N+0] E H
8 MIX 10164 E58 — R50 100Ω — EBUS D[N+1] E H
8 MIX 10164 E43 — R76 100Ω — EBUS D[N+2] E H
8 MIX 10164 E44 — R40 100Ω — EBUS D[N+3] E H
8 MIX 10164 E36 — R37 100Ω — EBUS D[N+4] E H
8 MIX 10164 E37 — R38 100Ω — EBUS D[N+5] E H

BK2 AR [N+0] H
CD2 BR [N+0] H
FM [N+0] H
BE2 BRX [N+0] H
EJ2 ARX [N+0] H
EH2 ADX [N+0] H
EF2 AD [N+0] H

BL2 AR [N+1] H
BD2 BR [N+1] H
FM [N+1] H
DL1 BRX [N+1] H
ARX [N+1] H
ADX [N+1] H
AD [N+1] H

DC1 AR [N+2] H
DR2 BR [N+2] H
FM [N+2] H
BRX [N+2] H
ARX [N+2] H
ADX [N+2] H
AD [N+2] H

AJ2 AR [N+3] H
DR1 BR [N+3] H
FM [N+3] H
BRX [N+3] H
ARX [N+3] H
ADX [N+3] H
AD [N+3] H

CS1 AR [N+4] H
BC1 BR [N+4] H
DK1 FM [N+4] H
CP1 BRX [N+4] H
ARX [N+4] H
ADX [N+4] H
AD [N+4] H

DF1 AR [N+5] H
AA1 BR [N+5] H
ES1 FM [N+5] H
EL1 BRX [N+5] H
ARX [N+5] H
ADX [N+5] H
AD [N+5] H

FD1 DIAG 04 A H
FA1 DIAG 05 A H
FE1 DIAG 06 A H
EF1 DIAG READ FUNC 12X H
FE2 [N/10+1, CTL AD TO EBUS L H; CTL AD TO EBUS R H]

10101 E59

FP1 APR FM BLOCK 4 H — R58 56Ω
FM1 APR FM BLOCK 2 H — R57 56Ω
FR1 APR FM BLOCK 1 H — R56 56Ω
FR2 APR FM ADR 10 H — R55 56Ω
FN1 APR FM ADR 4 H — R54 56Ω
FS1 APR FM ADR 2 H — R53 56Ω
FP2 APR FM ADR 1 H — R51 56Ω

10101 E66
10101 E67

FM [N+0] H … FM [N+5] H

128X1RAM 10147 E69 (FM [N+0] H)
128X1RAM 10147 E70 (FM [N+1] H)
128X1RAM 10147 E71 (FM [N+2] H)
128X1RAM 10147 E72 (FM [N+3] H)
128X1RAM 10147 E65 (FM [N+4] H)
128X1RAM 10147 E58 (FM [N+5] H)

Q; 0 1 2 3 4 5 6 ADR; AND EN; WRITE; D

AR [N+0] H (BK2) … AR [N+5] H — R105 56Ω

PARITY 10160 E64
FM [N+0] H … FM [N+5] H
ODD — EDP FM PARITY [N+0]-[N+5] H (FM2)

R123 100Ω — DL1 13nS — R119 100Ω
DP04 CLK C H — 10105 E29
P52 56Ω — DP04 FM WRITE L
CE2 [N/10+1, CON FM WRITE 00-17 L; CON FM WRITE 18-35 L]

CLK EDP [N] DELAYED H
CR2 CLK EDP [N] H
10210 E34
DP04 CLK A H
DP04 CLK B H
DP04 CLK C H
SEE NOTE 1
DP04 CLK D H
DP04 CLK E H
DP04 CLK F H
FACTORY ADJUSTMENT ONLY

NOTE 1: REPLACEMENT OF E34 REQUIRES FACTORY DESKEW ADJUSTMENT

SHFT REG 10141 E61
BR [N+0] A H … BR [N+2] H
FJ2 AR [N+0] A H
FC1 AR [N+1] A H
FF2 AR [N+2] A H
DJ2 CRAM BR LOAD A H
DP04 CLK A H

SHFT REG 10141 E63
BM2 AR [N+3] A H
EU2 AR [N+4] A H
EV2 AR [N+5] A H
ET2 BR [N+3] H … BR [N+5] H

SHFT REG 10141 E8
AS2 ARX [N+0] A H
AS1 ARX [N+1] A H
APX [N+2] A H
BPX [N+0] H … BPX [N+2] H
AR2 CRAM BRX LOAD A H
DP04 CLK D H

SHFT REG 10141 E10
BE2 ARX [N+3] A H
ARX [N+4] A H
ARX [N+5] A H
BRX [N+3] H … BRX [N+5] H
DP04 CLK E H

SHFT 0IN; SHFT 3IN; 3=LOAD 2 2=0IN+0 1 1=3IN+3 0=HOLD; CLK

VARIABLE	LOCATION
N=00	4AF53
N=06	4AF51
N=12	4AF49
N=18	4AF43
N=24	4AF41
N=30	4AF39

REVISIONS

CHK	CHANGE NO.	REV
–	M8512-00001	A

T. EGGERS 13-JAN-77

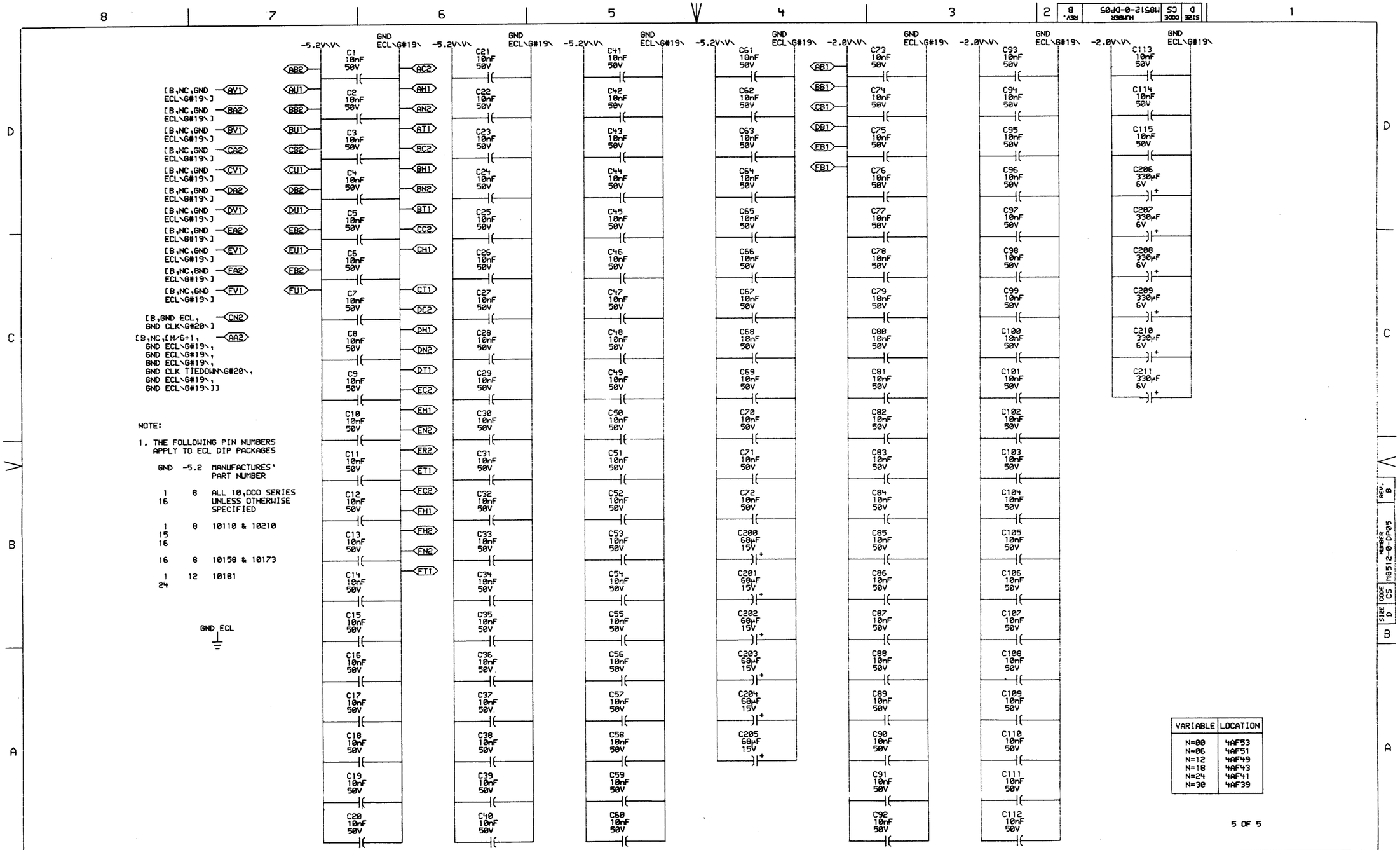
digital

DRN. D. Toomey 21-OCT-76 | ENG Tom Eggers 21 Oct 76 | DATE 21 Oct 76
CHK'D Tom Eggers | BOARD LOCATION: | SHEET 1 OF 1
EDP4EX.RVA[4,120] 18-OCT-76 22:41 | NEXT HIGHER ASSEMBLY:
FIRST USED ON OPTION/MODEL: KL10 | B-DD-M8512-0

TITLE: DATA PATH EBUS, FM, BR, BR.

SIZE	CODE	NUMBER	REV.
D	CS	M8512-0-DP04	A

MR 1



NOTE:

1. THE FOLLOWING PIN NUMBERS APPLY TO ECL DIP PACKAGES

GND	-5.2	MANUFACTURES' PART NUMBER
1 / 16	8	ALL 10,000 SERIES UNLESS OTHERWISE SPECIFIED
1 / 15 / 16	8	10110 & 10210
16	8	10158 & 10173
1 / 24	12	10181

GND ECL

VARIABLE	LOCATION
N=00	4AF53
N=06	4AF51
N=12	4AF49
N=18	4AF43
N=24	4AF41
N=30	4AF39

5 OF 5

SIZE	CODE	NUMBER	REV.
D	CS	M8512-0-DP05	B

REV. B

REVISIONS		
CHK	CHANGE NO.	REV
TE	M8512-00002	B
HJ 9-7-77		
T. EGGERS		
TE 9-27-77		

digital

DRN: T. Eggers	DATE: 16-JUN-77	ENG: Tom Bruso	DATE: 22 June 77
CHK: M. Mayhew	DATE:	BOARD LOCATION:	
EDP5EX.DRW[4,536] 15-JUN-77 10:18		SHEET 1 OF 1	
FIRST USED ON OPTION/MODEL: KL10		NEXT HIGHER ASSEMBLY: B-DD-M8512-0	

TITLE: DATA PATH POWER, GND, CAPS

19

RESISTOR LOC(PIN)	SHOWN DRW#	ON REF	VALUE	TERMINATES SIGNAL
R119(1)	DP04	B7	100Ω	%DL1(7)
R135(1)	DP04	A4	68Ω	%E11(14)
R139(1)	DP02	A2	68Ω	%E20(11)
R136(1)	DP02	A3	68Ω	%E20(5)
R137(1)	DP02	A3	68Ω	%E20(6)
R105(1)	DP02	A3	68Ω	%E20(9)
R27(1)	DP02	D6	68Ω	%E24(14)
R120(1)	DP04	B7	68Ω	%E29(2)
R123(1)	DP04	B7	100Ω	%E29(3)
R122(1)	DP03	D4	68Ω	%E33(10)
R121(1)	DP03	D4	68Ω	%E33(3)
R81(1)	DP04	A7	68Ω	%E38(14)
R74(1)	DP02	A7	68Ω	%E38(15)
R73(1)	DP02	A7	68Ω	%E38(5)
R93(1)	DP02	A7	68Ω	%E38(6)
R94(1)	DP02	A7	68Ω	%E38(9)
R127(1)	DP01	B7	68Ω	%E52(11)
R95(1)	DP01	B7	68Ω	%E52(5)
R126(1)	DP01	B7	68Ω	%E52(6)
R125(1)	DP01	B7	68Ω	%E52(9)
R105(1)	DP04	C2	56Ω	%E58(13)
R75(1)	DP04	C7	68Ω	%E59(14)
R35(1)	DP04	D7	68Ω	%E59(5)
R34(1)	DP04	D7	68Ω	%E59(6)
R72(1)	DP04	C7	68Ω	%E59(9)
R31(1)	DP02	B7	68Ω	%E60(5)
R30(1)	DP02	B7	68Ω	%E60(6)
R69(1)	DP02	B7	68Ω	%E60(9)
R58(1)	DP04	C5	56Ω	%E66(5)
R57(1)	DP04	C5	56Ω	%E66(6)
R56(1)	DP04	C5	56Ω	%E66(9)
R51(1)	DP04	B5	56Ω	%E67(11)
R55(1)	DP04	C5	56Ω	%E67(5)
R54(1)	DP04	C5	56Ω	%E67(6)
R53(1)	DP04	B5	56Ω	%E67(9)
R133(1)	DP03	D2	68Ω	%E7(2)
R115(1)	DP03	A4	68Ω	# [N+0] H\#400\
R89(1)	DP03	A4	68Ω	# [N+1] H\#400\
R12(1)	DP03	A2	68Ω	# [N+2] H\#400\
R13(1)	DP03	A2	68Ω	# [N+3] H\#400\

RESISTOR LOC(PIN)	SHOWN DRW#	ON PEF	VALUE	TERMINATES SIGNAL
R181(1)	DP03	A1	68Ω	# [N+4] H\#400\
R159(1)	DP03	A1	68Ω	# [N+5] H\#400\
R155(1)	DP03	D5	68Ω	AD CRY [N+2] H
R160(1)	DP03	D7	68Ω	AD CRY [N-2] H
R176(1)	DP03	B5	68Ω	ADA [N+0] H
R152(1)	DP03	B5	68Ω	ADA [N+1] H
R150(1)	DP03	B4	68Ω	ADA [N+2] H
R108(1)	DP03	B4	68Ω	ADA [N+3] H
R109(1)	DP03	B3	68Ω	ADA [N+4] H
R112(1)	DP03	B3	68Ω	ADA [N+5] H
R154(1)	DP03	A7	68Ω	ADB [N+0] H
R153(1)	DP03	A7	68Ω	ADB [N+1] H
R149(1)	DP03	A6	68Ω	ADB [N+2] H
R151(1)	DP03	A6	68Ω	ADB [N+3] H
R110(1)	DP03	A5	68Ω	ADB [N+4] H
R111(1)	DP03	A5	68Ω	ADB [N+5] H
R177(1)	DP03	B6	68Ω	ADB [N-1] H
R178(1)	DP03	B7	68Ω	ADB [N-2] H
R88(1)	DP03	D2	68Ω	ADX CRY [N+3] H
R174(1)	DP03	C3	68Ω	ADX [N+1] H
R129(1)	DP03	C3	68Ω	ADX [N+2] H
R43(1)	DP03	D2	68Ω	ADX [N+3] H
R106(1)	DP03	B2	68Ω	ADXA [N+0] H
R85(1)	DP03	B2	68Ω	ADXA [N+1] H
R87(1)	DP03	B2	68Ω	ADXA [N+2] H
R82(1)	DP03	B1	68Ω	ADXA [N+3] H
R11(1)	DP03	B1	68Ω	ADXA [N+4] H
R10(1)	DP03	B1	68Ω	ADXA [N+5] H
R107(1)	DP03	A4	68Ω	ADXB [N+0] H
R84(1)	DP03	A4	68Ω	ADXB [N+1] H
R86(1)	DP03	A2	68Ω	ADXB [N+2] H
R83(1)	DP03	A2	68Ω	ADXB [N+3] H
R8(1)	DP03	A1	68Ω	ADXB [N+4] H
R9(1)	DP03	A1	68Ω	ADXB [N+5] H
R59(1)	DP01	D3	68Ω	AR [N+2] H
R148(1)	DP01	B3	68Ω	AR [N+3] H
R66(1)	DP01	B3	68Ω	AR [N+4] H
R104(1)	DP01	B3	68Ω	AR [N+5] H
R142(1)	DP01	C6	68Ω	ARM [N+0] H
R169(1)	DP01	C5	68Ω	ARM [N+1] H

RESISTOR LOC(PIN)	SHOWN DRW#	ON REF	VALUE	TERMINATES SIGNAL
R170(1)	DP01	C3	68Ω	ARM [N+2] H
R46(1)	DP01	A6	68Ω	ARM [N+3] H
P43(1)	DP01	A5	68Ω	ARM [N+4] H
R47(1)	DP01	A3	68Ω	ARM [N+5] H
R175(1)	DP01	C6	68Ω	ARMM [N+0] H
R173(1)	DP01	C5	68Ω	ARMM [N+1] H
R143(1)	DP01	C3	68Ω	ARMM [N+2] H
R128(1)	DP01	A6	68Ω	ARMM [N+3] H
R141(1)	DP01	A5	68Ω	ARMM [N+4] H
R145(1)	DP01	A3	68Ω	ARMM [N+5] H
R114(1)	DP02	D4	68Ω	ARX [N+2] A H
R62(1)	DP02	D1	68Ω	ARX [N+3] A H
R61(1)	DP02	D1	68Ω	ARX [N+4] A H
R60(1)	DP02	D1	68Ω	ARX [N+5] A H
R160(1)	DP03	A2	68Ω	ARX [N+6] H
R157(1)	DP03	A1	68Ω	ARX [N+7] H
R24(1)	DP02	C7	68Ω	ARXM [N+0] H
R25(1)	DP02	C5	68Ω	ARXM [N+1] H
R68(1)	DP02	C4	68Ω	ARXM [N+2] H
R26(1)	DP02	C3	68Ω	ARXM [N+3] H
R29(1)	DP02	C2	68Ω	ARXM [N+4] H
R33(1)	DP02	C1	68Ω	ARXM [N+5] H
R166(1)	DP04	A7	68Ω	BR [N+0] H
R167(1)	DP04	A7	68Ω	BR [N+1] H
R92(1)	DP04	A7	68Ω	BR [N+2] H
R22(1)	DP04	A5	68Ω	BR [N+3] H
R20(1)	DP04	A5	68Ω	BR [N+4] H
R18(1)	DP04	A5	68Ω	BR [N+5] H
R48(1)	DP04	A3	68Ω	BRX [N+1] H
R42(1)	DP04	A3	68Ω	BRX [N+2] H
R41(1)	DP04	A2	68Ω	BRX [N+3] H
R36(1)	DP04	A2	68Ω	BRX [N+4] H
R38(1)	DP04	A2	68Ω	BRX [N+5] H
R158(1)	DP03	A1	68Ω	BRX [N+6] H
R130(1)	DP01	C6	68Ω	CACHE DATA [N+0] B H
R100(1)	DP01	C5	68Ω	CACHE DATA [N+1] B H
R99(1)	DP01	C3	68Ω	CACHE DATA [N+2] B H
R96(1)	DP01	A6	68Ω	CACHE DATA [N+3] B H
R71(1)	DP01	A5	68Ω	CACHE DATA [N+4] B H
R146(1)	DP01	A3	68Ω	CACHE DATA [N+5] B H

RESISTOR LOC(PIN)	SHOWN DRW#	ON REF	VALUE	TERMINATES SIGNAL
R124(1)	DP04	B2	68Ω	CLK EDP [N] H
R6(1)	DP03	C2	68Ω	CRAM AD BOOLE [N] H
R3(1)	DP03	C2	68Ω	CRAM AD SEL 1 [N] H
R5(1)	DP03	C2	68Ω	CRAM AD SEL 2 [N] H
R4(1)	DP03	C2	68Ω	CRAM AD SEL 4 [N] H
R2(1)	DP03	C2	68Ω	CRAM AD SEL 8 [N] H
R1(1)	DP03	B1	68Ω	CRAM ADA DIS [N] H
R64(1)	DP03	B3	68Ω	CRAM ADA SEL 1 [N] H
P17(1)	DP03	B3	68Ω	CRAM ADA SEL 2 [N] H
P179(1)	DP03	A1	68Ω	CRAM ADB SEL 1 [N] H
R156(1)	DP03	A1	68Ω	CRAM ADB SEL 2 [N] H
R148(1)	DP04	B1	68Ω	DP04 CLK A H
R171(1)	DP04	B1	68Ω	DP04 CLK B H
R164(1)	DP04	B1	68Ω	DP04 CLK C H
R134(1)	DP04	B1	68Ω	DP04 CLK D H
R63(1)	DP04	B1	68Ω	DP04 CLK E H
R49(1)	DP04	B1	68Ω	DP04 CLK F H
R52(1)	DP04	B6	56Ω	-DP04 FM WRITE H
R80(1)	DP04	D6	100Ω	EBUS D[N+0] E H
R50(1)	DP04	D5	100Ω	EBUS D[N+1] E H
R76(1)	DP04	D4	100Ω	EBUS D[N+2] E H
R40(1)	DP04	D3	100Ω	EBUS D[N+3] E H
R37(1)	DP04	D2	100Ω	EBUS D[N+4] E H
R39(1)	DP04	D1	100Ω	EBUS D[N+5] E H
R164(1)	DP04	C5	68Ω	FM [N+0] H
R91(1)	DP04	C4	68Ω	FM [N+1] H
R26(1)	DP04	C4	68Ω	FM [N+2] H
R23(1)	DP04	C3	68Ω	FM [N+3] H
R19(1)	DP04	C3	68Ω	FM [N+4] H
R21(1)	DP04	C2	68Ω	FM [N+5] H
R103(1)	DP02	B3	68Ω	MQ [N+1] H
R90(1)	DP02	B3	68Ω	MQ [N+2] H
R44(1)	DP02	B1	68Ω	MQ [N+3] H
R103(1)	DP02	B6	68Ω	MQM [N+0] H
R162(1)	DP02	B6	68Ω	MQM [N+1] H
R161(1)	DP02	B5	68Ω	MQM [N+2] H
R182(1)	DP02	B5	68Ω	MQM [N+3] H
R116(1)	DP02	B4	68Ω	MQM [N+4] H
R138(1)	DP02	B4	68Ω	MQM [N+5] H
R101(1)	DP01	C6	68Ω	SH [N+0] H

NOTE:

1. ALL TERMINATORS HAVE PIN TWO CONNECTED TO -2.0V AND ARE 5% 1/4WATT UNLESS OTHERWISE SPECIFIED
2. ENTRIES ARE SORTED BY SIGNAL NAME
3. % INDICATES OUTPUT OF DIP LOC AND () INDICATES PIN NUMBER

REVISIONS

CHK	CHANGE NO.	REV
-	M8512-00001	A

DPN. C Smith 20-OCT-76
CHK. Tom Eggers 21 Oct 76
ENG. Tom Eggers 21 Oct 76
BOARD LOCATION:
SHEET 1 OF 2
M8512I.RVAL4,120] 20-OCT-76 20:08
NEXT HIGHER ASSEMBLY: B-DD-M8512-3
FIRST USED ON OPTION/MODEL: KL10

TITLE: DATA PATH TERMINATORS

SIZE	CODE	NUMBER	REV.
D	CS	M8512-0-RES	A

RESISTOR LOC(PIN)	SHOWN ON DRW#	REF	VALUE	TERMINATES SIGNAL
R78(1)	DP01	C5	68Ω	SH [N+1] H
R77(1)	DP01	C3	68Ω	SH [N+2] H
R97(1)	DP01	A6	68Ω	SH [N+3] H
R32(1)	DP01	A5	68Ω	SH [N+4] H
R131(1)	DP01	A3	68Ω	SH [N+5] H
R163(1)	DP03	B5	68Ω	VMA HELD OR PC [N+0] H
R160(1)	DP03	B5	68Ω	VMA HELD OR PC [N+1] H
R117(1)	DP03	B4	68Ω	VMA HELD OR PC [N+2] H
R90(1)	DP03	B4	68Ω	VMA HELD OR PC [N+3] H
R67(1)	DP03	B3	68Ω	VMA HELD OR PC [N+4] H
R65(1)	DP03	B3	68Ω	VMA HELD OR PC [N+5] H
R102(1)	DP02	C7	68Ω	[(N+199)/100, AD 34 H, ADX [N-2] H]
R79(1)	DP02	C5	68Ω	[(N+199)/100, AD 35 H, ADX [N-1] H]
R186(1)	DP02	B7	68Ω	[(N+199)/100, ADX 34 H, MQ [N-2] H]
R172(1)	DP02	B7	68Ω	[(N+199)/100, ADX 35 H, MQ [N-1] H]
R118(1)	DP03	B6	68Ω	[(N+199)/100, HI, NC]
R113(1)	DP03	C5	68Ω	[N/30+1, AD CRY [N+6] H, AD CRY 36 B H]
R132(1)	DP01	A3	68Ω	[N/30+1, AD [N+6] H, ADX 00 H]
R7(1)	DP03	C2	68Ω	[N/30+1, ADX CRY [N+6] H, CTL ADX CRY 36 H]
R70(1)	DP02	C1	68Ω	[N/30+1, ADX [N+6] H, MQ 00 H]
R16(1)	DP03	B5	68Ω	[N/30+1, AR [N+6] H, ARX 00 H]
R14(1)	DP03	A5	68Ω	[N/30+1, AR [N+7] H, ARX 01 H]
R15(1)	DP03	A5	68Ω	[N/30+1, BR [N+6] A H, BRX 00 H]
R165(1)	DP02	A2	68Ω	[N/30+1, MQ [N+6] H, AD CRY -02 A H]
R144(1)	DP01	C5	68Ω	[N/6+1, AD EX -01 H, AD 05 H, AD 11 H, AD 17 H, AD 23 H, AD 29 H]
R147(1)	DP01	C6	68Ω	[N/6+1, AD EX -02 H, AD 04 H, AD 10 H, AD 16 H, AD 22 H, AD 28 H]

NOTE:

1. ALL TERMINATORS HAVE PIN TWO CONNECTED TO -2.0V AND ARE 5% 1/4WATT UNLESS OTHERWISE SPECIFIED
2. ENTRIES ARE SORTED BY SIGNAL NAME
3. % INDICATES OUTPUT OF DIP LOC AND () INDICATES PIN NUMBER

REVISIONS		
CHK	CHANGE NO.	REV.

M85122.RVA[4,120] 20-OCT-76 20:08

DRN. E Smith	DATE 20-OCT-76	ENG. Tom Sgra	DATE 21 Oct 76	TITLE: DATA PATH TERMINATORS			
CHK Tom Sgra	DATE 21 Oct 76	BOARD LOCATION: SHEET 2 OF 2					
FIRST USED ON OPTION/MODEL: KL10		NEXT HIGHER ASSEMBLY: B-DD-M8512-0		SIZE D	CODE CS	NUMBER M8512-0-RES	REV. A

MR