

CUSTOMER PRINT SET	MFG SET	DRAWING NO	NO OF SHT	DESCRIPTION	OPTION NO/FILE DATE
		-		MODULE REVISION	
		D-UA-M8537-Ø-Ø	5	MBOX CONTROL 4	
		D-CS-M8537-Ø-MBZ1	1	CORE BUSY, MB IN SEL N, CHAN CTL	
		D-CS-M8537-Ø-MBZ2	1	EBUS REG, MBZ DIAG MIX	
		D-CS-M8537-Ø-MBZ3	1	NXM CONTROL & TIMEOUT, HOLD ERA	
		D-CS-M8537-Ø-MBZ4	1	NXM T(N), MBOX ERR FLAGS	
		D-CS-M8537-Ø-MBZ5	1	MBOX & CHAN PAR BIT DATA PATHS	
		D-CS-M8537-Ø-MBZ6	1	EBUS REG IN MIX, CSH PAR BIT	
		D-CS-M8537-Ø-MBZ7	1	MBOX CONTROL #4 POWER, GND, CAP	
		D-CS-M8537-Ø-RES	2	MBOX CONTROL #4 TERMINATORS	
		K-CO-M8537-Ø-4	1	MBOX CONTROL 4 (CALDEC DATA BASE)	
		D-AH-M8537-Ø-5	4	MBOX CONTROL 4	
		B-MH-M8537-Ø-6	1	MODULE ECO HISTORY	
		5010927	-	ETCH CIRCUIT BOARD	
		P00-M8537-ØØ		PROCESS SHEET (REF ONLY)	
CUSTOMER PRINT SET CODES	X = PRINT OF DOCUMENT INCLUDED IN PRINT SET C = INCLUDES ALL PRINTS INDICATED ON DOCUMENT S = CONFIDENTIAL AUTHORIZED SIGNATURE REQUIRED				

REVISION CONTROL SHEET

NEW LAYOUT VERSION

REV ISIONS

TITLE

SHEET 3 OF 3

SIZE B DD

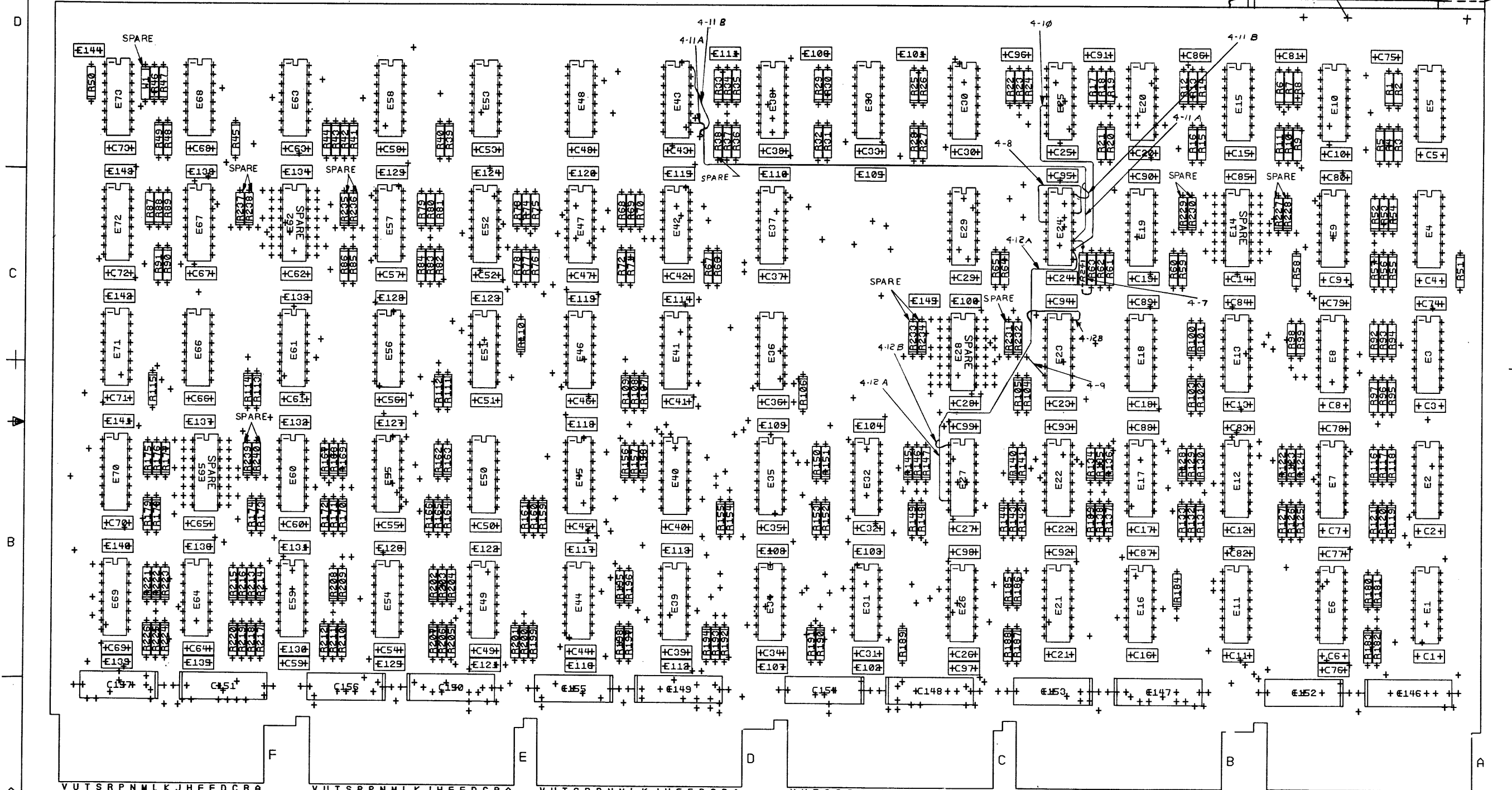
CODE NUMBER

DD M8537-Ø

REV D

MR

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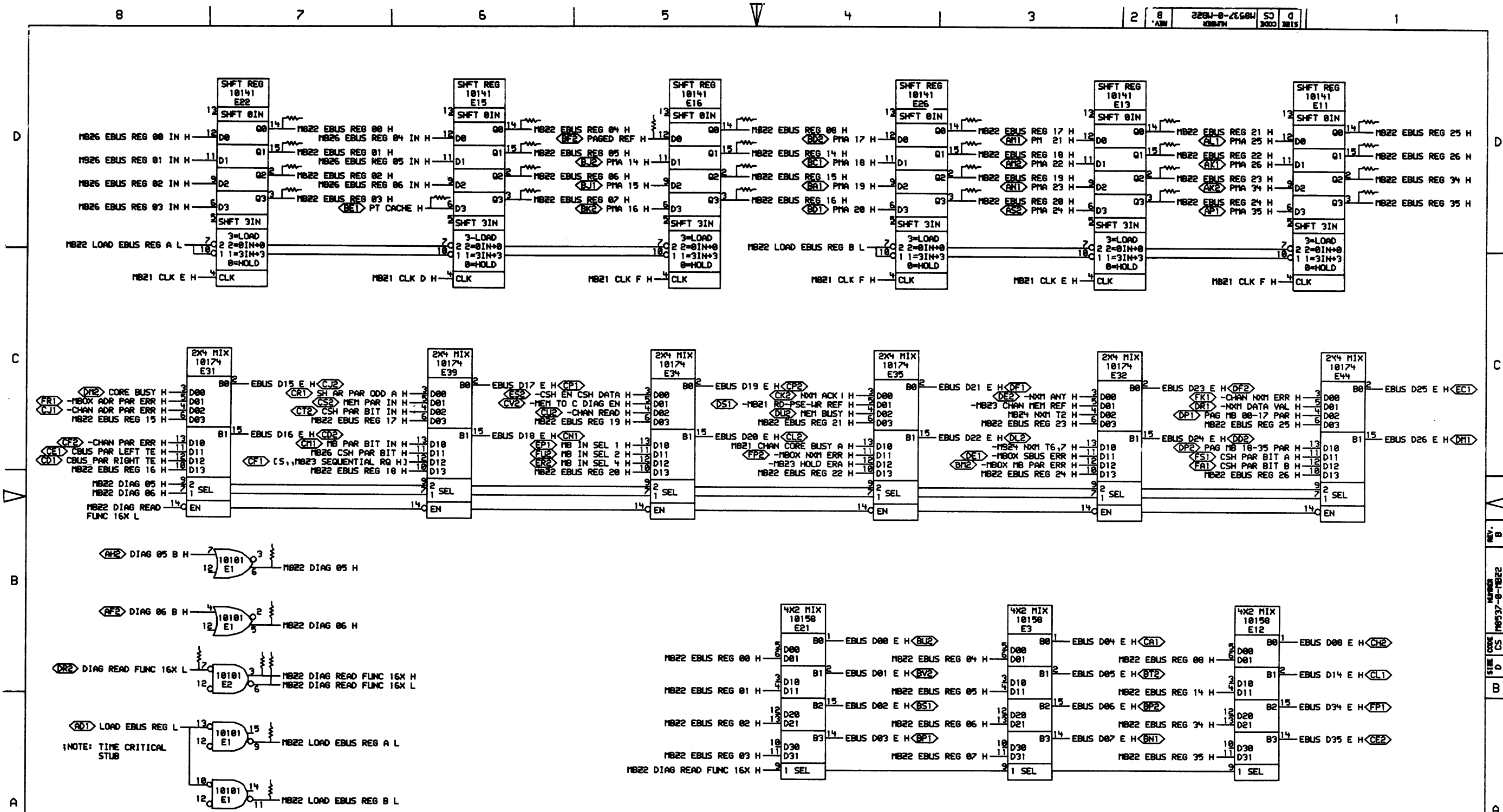
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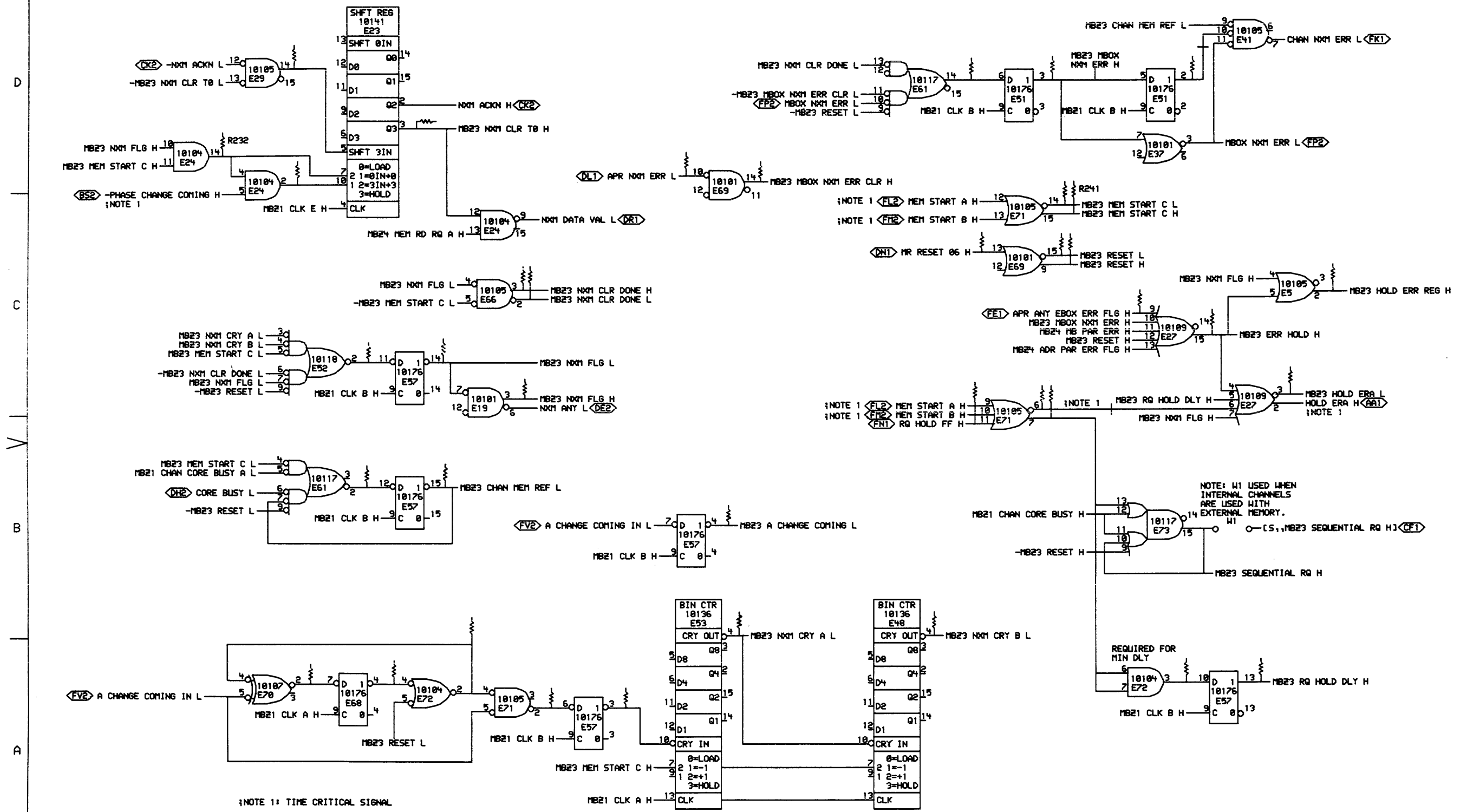
CHK	CHANGE	NO	REV

ETCH REV.	C
P.C. DESIGN DATA BASE REV.	C

SIGNATURES		DATE	digital	
DRN.	<i>Edg. Craig</i>	5/18/76		
CHK'D.	<i>Bill Emley</i>	2/24/77		
ENG.	<i>D. J. Allen</i>	4/21/76	TITLE MBOX	
PROD. ENG.	<i>D. J. Allen</i>	4/21/76	CONTROL #4	
PROD.	<i>Bill Emley</i>	4/21/77		
SCALE	2/1		SIZE	CODE
SHT.	2 OF 5		NUMBER	RE
NEXT HIGH RES. B-D-D-M8537-0			0 UA	M8537-0-0
				D

MR, MS#





D
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B
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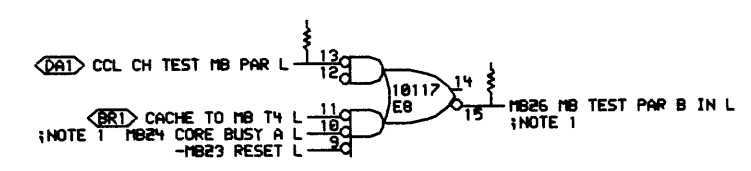
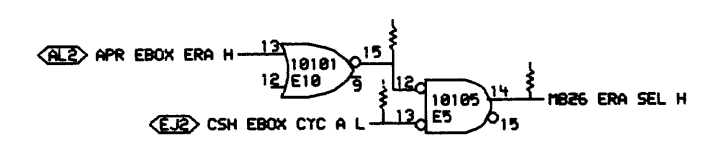
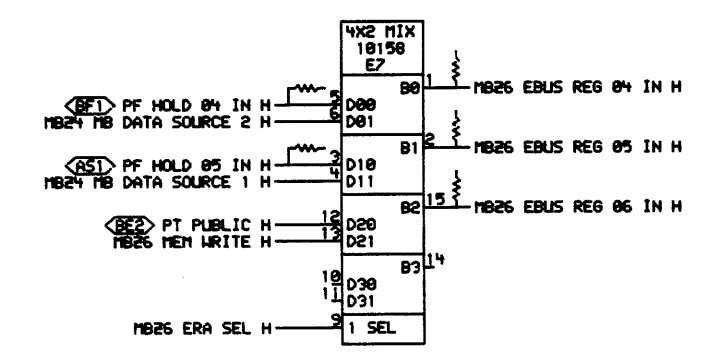
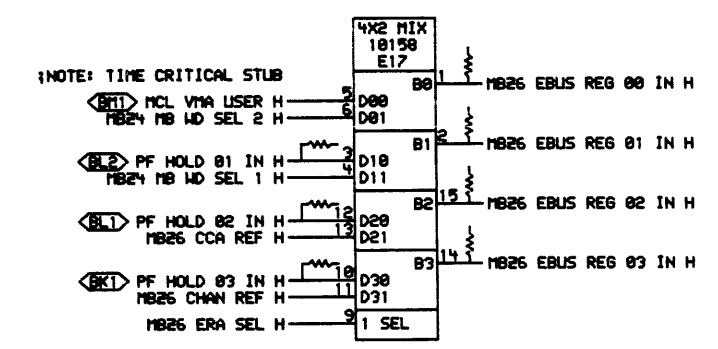
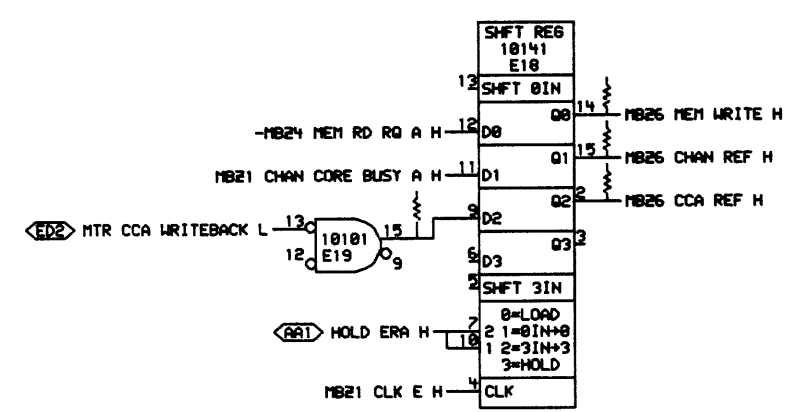
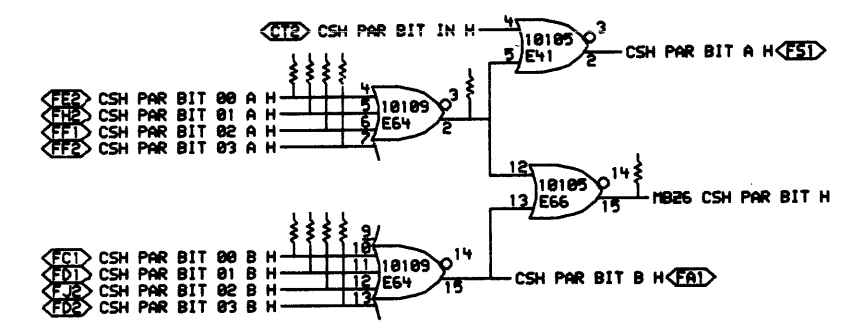
REV. C
NUMBER M8537-0-MB23
SIZE D

MR

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REVISIONS	
CHK	CHANGE NO. REV

digital	DATE 14-SEP-76	ENG. M. D. Oll	DATE 11-SEP-76	TITLE: NXM CONTROL & TIMEOUT, HOLD ERA
	124-AUG-76 11:18	11:18	11:18	11:18
FIRST USED ON OPTION MODEL: KL10		B-DD-M8537-0		SIZE CODE D CS
NUMBER M8537-0-MB23		REV. C		300



NOTE 1: TIME CRITICAL SIGNAL

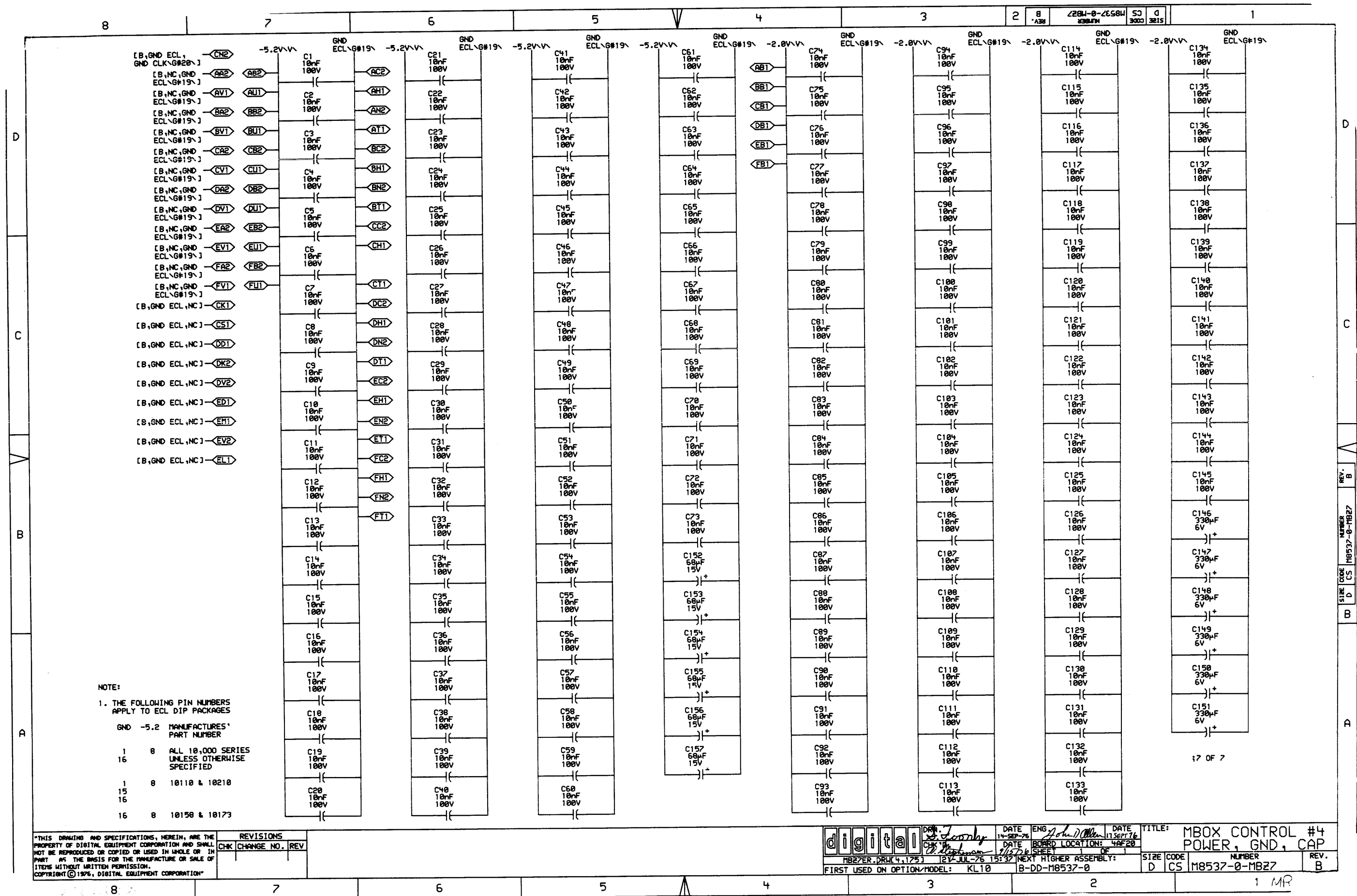
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REVISIONS		
CHK	CHANGE NO.	REV.

digital

DATE 14 SEP 76 ENG. 17 SEP 76
DATE 17 SEP 76 BOARD LOCATION: 4A228
DATE 17 SEP 76 SHEET 1 OF 1
NEXT HIGHER ASSEMBLY: B-DD-M8537-0

TITLE: EBUS REG IN MIX
CSH PAR BIT
SIZE CODE D CS
NUMBER M8537-0-MB26
REV. C



8

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REV
538-0-2558H
CS
3005
3815

RESISTOR LOC(PIN)	SHOWN DRW#	ON REF	VALUE	TERMINATES SIGNAL
R13(1)	M824	D3	68n	%E10(14)
R1(1)	M826	B3	68n	%E10(15)
R52(1)	M824	C3	68n	%E10(3)
R19(1)	M824	C3	68n	%E19(14)
R103(1)	M826	B7	68n	%E19(15)
R16(1)	M824	D3	68n	%E2(14)
R20(1)	M824	B3	68n	%E2(15)
R10(1)	M824	D2	68n	%E20(14)
R21(1)	M824	C2	68n	%E20(2)
R24(1)	M824	C2	68n	%E20(3)
R232(1)	M823	D7	68n	%E24(14)
R104(1)	M823	D7	68n	%E24(2)
R17(1)	M824	B5	68n	%E24(3)
R3(1)	M824	B4	68n	%E25(15)
R89(1)	M824	D7	68n	%E25(2)
R66(1)	M824	B2	68n	%E25(3)
R105(1)	M823	D7	68n	%E29(14)
R64(1)	M824	B2	68n	%E30(13)
R26(1)	M821	B2	68n	%E33(2)
R90(1)	M821	C4	68n	%E41(14)
R25(1)	M821	D4	68n	%E43(14)
R23(1)	M821	D4	68n	%E43(15)
R40(1)	M821	B3	68n	%E46(2)
R75(1)	M821	A3	68n	%E51(14)
R100(1)	M823	D2	68n	%E51(2)
R110(1)	M821	B7	68n	%E52(15)
R80(1)	M823	C7	68n	%E52(2)
R88(1)	M824	C7	68n	%E57(2)
R39(1)	M823	A5	68n	%E57(3)
R112(1)	M823	D3	68n	%E61(14)
R79(1)	M823	B7	68n	%E61(2)
R109(1)	M826	D6	68n	%E64(2)
R86(1)	M824	C7	68n	%E67(15)
R49(1)	M824	D6	68n	%E67(3)
R87(1)	M823	A6	68n	%E68(4)
R40(1)	M823	A7	68n	%E70(2)
R85(1)	M823	A6	68n	%E71(2)
R147(1)	M823	C3	68n	%E71(6)
R46(1)	M823	C3	68n	%E71(7)
R175(1)	M823	B6	68n	%E72(2)

RESISTOR LOC(PIN)	SHOWN DRW#	ON REF	VALUE	TERMINATES SIGNAL
R84(1)	M823	A2	68n	%E72(3)
R90(1)	M824	D6	68n	%E72(9)
R32(1)	M821	C2	68n	%E8(2)
R22(1)	M824	C2	68n	%E9(3)
R54(1)	M824	D6	68n	-ACKN PULSE H
R143(1)	M823	C2	68n	APR ANY EBOX ERR FLG H
R9(1)	M824	C4	68n	-APR MB PAR ERR H
R226(1)	M823	D5	68n	-APR NM1 ERR H
R110(1)	M824	B3	68n	-APR S ADR P ERR H
R119(1)	M824	D4	68n	-APR SBOX ERR H
R204(1)	M825	B7	68n	CBUS PAR LEFT RE H
R203(1)	M825	B7	68n	CBUS PAR RIGHT RE H
R92(1)	M826	B3	68n	-CCL CH TEST MB PAR H
R201(1)	M825	A7	68n	CCL DATA REVERSE H
R36(1)	M821	D8	68n	CCL HOLD MEM H
R170(1)	M825	A5	68n	CCL ODD MC PAR H
R179(1)	M825	A5	68n	CCW ODD ADR PAR H
R170(1)	M825	C3	68n	-CH BUF WP 06 H
R106(1)	M821	B7	68n	CLK MB2 H
R41(1)	M824	A5	68n	CORE BUSY A H
R91(1)	M821	A5	68n	CORE RD IN PROG H
R159(1)	M825	D7	68n	CRC CBUS OUT HOLD H
R209(1)	M825	C5	68n	CPC CH BUF ADP 0 H
R200(1)	M825	C5	68n	CRC CH BUF ADR 1 H
R211(1)	M825	C5	68n	CRC CH BUF ADR 2 H
R210(1)	M825	C5	68n	CRC CH BUF ADR 3 H
R206(1)	M825	C5	68n	CRC CH BUF ADR 4 H
R202(1)	M825	C5	68n	CRC CH BUF ADP 5 H
R111(1)	M825	B5	68n	CRC CH BUF ADP 6 H
R2(1)	M826	B3	68n	-CSH EBOX CYC A H
R222(1)	M826	D7	68n	CSH PAR BIT 00 A H
R220(1)	M826	C7	68n	CSH PAR BIT 00 B H
R223(1)	M826	D7	68n	CSH PAR BIT 01 A H
R219(1)	M826	C7	68n	CSH PAR BIT 01 B H
R225(1)	M826	D7	68n	CSH PAR BIT 02 A H
R210(1)	M826	C7	68n	CSH PAR BIT 02 B H
R224(1)	M826	D7	68n	CSH PAR BIT 03 A H
R215(1)	M826	C7	68n	CSH PAR BIT 03 B H
R99(1)	M821	D3	68n	-DIAG LOAD FUNC 071 H
R120(1)	M822	B8	68n	-DIAG READ FUNC 16X H

RESISTOR LOC(PIN)	SHOWN DRW#	ON REF	VALUE	TERMINATES SIGNAL
R102(1)	M824	B7	68n	MB DATA CODE 1 H
R100(1)	M824	B7	68n	MB DATA CODE 2 H
R50(1)	M824	D3	68n	MB PAR ODD H
P35(1)	M821	D5	68n	MB REQ HOLD H
P57(1)	M824	D3	68n	-MB TEST PAR A IN H
P60(1)	M821	B2	68n	MB21 AR TO MB SEL H
R72(1)	M821	B5	68n	MB21 CHAN BUF TO MB H
P50(1)	M821	D8	68n	MB21 CHAN CORE BUSY H
R100(1)	M821	D6	68n	MB21 CHAN CORE BUSY A H
R65(1)	M821	D6	68n	-MB21 CHAN CORE BUSY A H
R27(1)	M821	D7	68n	MB21 CHAN CORE BUSY IN H
R70(1)	M821	B5	68n	MB21 CHAN EPT H
R67(1)	M821	B5	68n	-MB21 CHAN EPT H
R71(1)	M821	C5	68n	MB21 CHAN STATUS TO MB H
P29(1)	M821	B2	68n	-MB21 CHAN TO MEM H
R69(1)	M821	C5	68n	-MB21 CHAN WR MEM H
R45(1)	M821	C7	68n	MB21 CLK A H
P03(1)	M821	C7	68n	MB21 CLK B H
P207(1)	M821	C7	68n	MB21 CLK C H
P14(1)	M821	B7	68n	MB21 CLK D H
P101(1)	M821	B7	68n	MB21 CLK E H
R101(1)	M821	B7	68n	MB21 CLK F H
R33(1)	M821	A5	68n	MB21 CORE RD IN PROG H
P37(1)	M821	C5	68n	MB21 CSH CHAN CYC A H
R30(1)	M821	C5	68n	-MB21 CSH CHAN CYC A H
R31(1)	M821	B2	68n	-MB21 EBOX DIAG CYC A H
P197(1)	M822	B7	68n	MB22 DIAG 05 H
R199(1)	M822	B7	68n	MB22 DIAG 06 H
R131(1)	M822	B7	68n	MB22 DIAG READ FUNC 16X H
R195(1)	M822	B7	68n	-MB22 DIAG READ FUNC 16X H
R100(1)	M822	D7	68n	MB22 EBUS REG 00 H
R105(1)	M822	D7	68n	MB22 EBUS REG 01 H
R106(1)	M822	D7	68n	MB22 EBUS REG 02 H
R107(1)	M822	D7	68n	MB22 EBUS REG 03 H
P96(1)	M822	D6	68n	MB22 EBUS REG 04 H
R93(1)	M822	D6	68n	MB22 EBUS REG 05 H
P94(1)	M822	D6	68n	MB22 EBUS REG 06 H
P95(1)	M822	D6	68n	MB22 EBUS REG 07 H
R132(1)	M822	D5	68n	MB22 EBUS REG 08 H
R130(1)	M822	D5	68n	MB22 EBUS REG 14 H

RESISTOR LOC(PIN)	SHOWN DRW#	ON REF	VALUE	TERMINATES SIGNAL
R190(1)	M822	D5	68n	MB22 EBUS REG 15 H
R109(1)	M822	D5	68n	MB22 EBUS REG 16 H
R196(1)	M822	D3	68n	MB22 EBUS REG 17 H
R194(1)	M822	D3	68n	MB22 EBUS REG 18 H
R192(1)	M822	D3	68n	MB22 EBUS REG 19 H
R191(1)	M822	D3	68n	MB22 EBUS REG 20 H
R154(1)	M822	D2	68n	MB22 EBUS REG 21 H
R153(1)	M822	D2	68n	MB22 EBUS REG 22 H
R152(1)	M822	D2	68n	MB22 EBUS REG 23 H
R149(1)	M822	D2	68n	MB22 EBUS REG 24 H
R200(1)	M822	D1	68n	MB22 EBUS REG 25 H
R190(1)	M822	D1	68n	MB22 EBUS REG 26 H
R122(1)	M822	D1	68n	MB22 EBUS REG 34 H
R127(1)	M822	D1	68n	MB22 EBUS REG 35 H
R11(1)	M822	A7	68n	-MB22 LOAD EBUS REG A H
R102(1)	M822	A7	68n	-MB22 LOAD EBUS REG B H
R12(1)	M823	B5	68n	-MB23 A CHANGE COMING H
R107(1)	M823	B6	68n	-MB23 CHAN MEM REF H
R53(1)	M823	C2	68n	MB23 ERR HOLD H
R150(1)	M823	C2	68n	-MB23 HOLD ERA H
R51(1)	M823	C1	68n	MB23 HOLD ERR REG H
R144(1)	M823	D3	68n	MB23 MBOX NM1 ERR H
R113(1)	M823	D4	68n	MB23 MBOX NM1 ERR CLR H
R241(1)	M823	C3	68n	MB23 MEM START C H
R81(1)	M823	C3	68n	-MB23 MEM START C H
R82(1)	M823	C6	68n	MB23 NM1 CLR DONE H
R114(1)	M823	C6	68n	-MB23 NM1 CLR DONE H
R63(1)	M823	D6	68n	MB23 NM1 CLR T0 H
R73(1)	M823	B5	68n	-MB23 NM1 CRY A H
R74(1)	M823	B3	68n	-MB23 NM1 CRY B H
R140(1)	M823	C6	68n	MB23 NM1 FLG H
R55(1)	M823	C6	68n	-MB23 NM1 FLG H
R20(1)	M823	C3	68n	MB23 RESET H
R47(1)	M823	C3	68n	-MB23 RESET H
R146(1)	M823	A2	68n	MB23 R0 HOLD DLY H
R140(1)	M824	C2	68n	MB24 ADR PAR ERR FLG H
R76(1)	M824	A5	68n	MB24 CORE BUSY A H
R97(1)	M824	A5	68n	-MB24 CORE BUSY A H
R34(1)	M824	A5	68n	MB24 CORE BUSY IN A H
R103(1)	M824	C6	68n	-MB24 LOAD MB # H

NOTE:

1. ALL TERMINATORS HAVE PIN TWO CONNECTED TO -2.0V AND ARE 5% 1/4WATT UNLESS OTHERWISE SPECIFIED
2. ENTRIES ARE SORTED BY SIGNAL NAME
3. % INDICATES OUTPUT OF DIP LOC AND () INDICATES PIN NUMBER

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REVISIONS	
CHK	CHANGE NO. REV

digital

DRN. <i>G. Smith</i>	DATE <i>03-AUG-76</i>	ENG. <i>John Dallen</i>	DATE <i>17 Sept 76</i>
CHK. <i>B. Thompson</i>	DATE <i>17 Sept 76</i>	BOARD LOCATION: <i>1001</i>	OF <i>2</i>
R05371.DRW 4.1751		NEXT HIGHER ASSEMBLY:	
FIRST USED ON OPTION/MODEL: <i>KL10</i>		<i>B-DD-M0537-0</i>	

TITLE: MBOX CONTROL #4 TERMINATORS	
SIZE CODE	NUMBER
D CS	M0537-0-RES
REV.	C

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1 MR

8	7	6	5	4	3	2	1																																																																																																																																																																																																																																																																																																																																																																																																																													
						3 REV.	538-0-225684 NUMBER	CS CODE	D SIZE	1																																																																																																																																																																																																																																																																																																																																																																																																																										
<table><thead><tr><th>RESISTOR LOC(PIN)</th><th>SHOWN DRW#</th><th>ON REF</th><th>VALUE</th><th>TERMINATES SIGNAL</th><th>RESISTOR LOC(PIN)</th><th>SHOWN DRW#</th><th>ON REF</th><th>VALUE</th><th>TERMINATES SIGNAL</th></tr></thead><tbody><tr><td>R124(1)</td><td>MB24</td><td>B6</td><td>68n</td><td>MB24 MB DATA SOURCE 1 H</td><td>R142(1)</td><td>MB26</td><td>D3</td><td>68n</td><td>MB26 EBUS REG 02 IN H</td></tr><tr><td>R125(1)</td><td>MB24</td><td>B6</td><td>68n</td><td>MB24 MB DATA SOURCE 2 H</td><td>R141(1)</td><td>MB26</td><td>D3</td><td>68n</td><td>MB26 EBUS REG 03 IN H</td></tr><tr><td>R61(1)</td><td>MB24</td><td>D2</td><td>68n</td><td>MB24 MB PAR ERR H</td><td>R6(1)</td><td>MB26</td><td>C3</td><td>68n</td><td>MB26 EBUS REG 04 IN H</td></tr><tr><td>R136(1)</td><td>MB24</td><td>B6</td><td>68n</td><td>MB24 MB WD SEL 1 H</td><td>R7(1)</td><td>MB26</td><td>C3</td><td>68n</td><td>MB26 EBUS REG 05 IN H</td></tr><tr><td>R137(1)</td><td>MB24</td><td>B6</td><td>68n</td><td>MB24 MB WD SEL 2 H</td><td>R10(1)</td><td>MB26</td><td>C3</td><td>68n</td><td>MB26 EBUS REG 06 IN H</td></tr><tr><td>R62(1)</td><td>MB24</td><td>A2</td><td>68n</td><td>MB24 MEM RD RQ A H</td><td>R121(1)</td><td>MB26</td><td>B2</td><td>68n</td><td>MB26 ERA SEL H</td></tr><tr><td>R70(1)</td><td>MB24</td><td>A2</td><td>68n</td><td>-MB24 MEM RD PQ A H</td><td>R56(1)</td><td>MB26</td><td>A3</td><td>68n</td><td>-MB26 MB TEST PAR B IN H</td></tr><tr><td>R77(1)</td><td>MB24</td><td>A2</td><td>68n</td><td>-MB24 MEM WR PQ A H</td><td>R116(1)</td><td>MB26</td><td>C6</td><td>68n</td><td>MB26 MEM WRITE H</td></tr><tr><td>R151(1)</td><td>MB24</td><td>D5</td><td>68n</td><td>MB24 NXM T2 H</td><td>R59(1)</td><td>MB24</td><td>C3</td><td>68n</td><td>MEM ADR PAR ERR H</td></tr><tr><td>R44(1)</td><td>MB24</td><td>D5</td><td>68n</td><td>MB24 NXM T3 H</td><td>R5(1)</td><td>MB24</td><td>D3</td><td>68n</td><td>MEM ERROR H</td></tr><tr><td>R43(1)</td><td>MB24</td><td>C5</td><td>68n</td><td>MB24 NXM T4 H</td><td>R161(1)</td><td>MB25</td><td>C2</td><td>68n</td><td>MEM PAR IN H</td></tr><tr><td>R42(1)</td><td>MB24</td><td>C5</td><td>68n</td><td>MB24 NXM T5 H</td><td>R117(1)</td><td>MB24</td><td>A2</td><td>68n</td><td>MEM RD RQ B H</td></tr><tr><td>R4(1)</td><td>MB24</td><td>B5</td><td>68n</td><td>MB24 NXM T6 H</td><td>R163(1)</td><td>MB25</td><td>B2</td><td>68n</td><td>-MEM TO C EN H</td></tr><tr><td>R145(1)</td><td>MB24</td><td>B4</td><td>68n</td><td>-MB24 NXM T6,7 H</td><td>R164(1)</td><td>MB25</td><td>B2</td><td>68n</td><td>MEM TO C SEL 1 H</td></tr><tr><td>R0(1)</td><td>MB24</td><td>D2</td><td>68n</td><td>MB24 SBUS ERR FLG H</td><td>R160(1)</td><td>MB25</td><td>B2</td><td>68n</td><td>MEM TO C SEL 2 H</td></tr><tr><td>R155(1)</td><td>MB25</td><td>A5</td><td>68n</td><td>MB25 CCH PAP BIT H</td><td>R221(1)</td><td>MB23</td><td>C3</td><td>68n</td><td>MR RESET 06 H</td></tr><tr><td>R157(1)</td><td>MB25</td><td>D3</td><td>68n</td><td>MB25 CH BUF 00-17 PAR H</td><td>R60(1)</td><td>MB21</td><td>B3</td><td>68n</td><td>-MTR CCA WRITEBACK H</td></tr><tr><td>R156(1)</td><td>MB25</td><td>D2</td><td>68n</td><td>MB25 CH BUF 18-35 PAR H</td><td>R217(1)</td><td>MB25</td><td>C7</td><td>68n</td><td>PAG MB 00-17 PAR H</td></tr><tr><td>R169(1)</td><td>MB25</td><td>C5</td><td>68n</td><td>MB25 CH BUF ADR 0 H</td><td>R213(1)</td><td>MB25</td><td>C7</td><td>68n</td><td>PAG MB 18-35 PAR H</td></tr><tr><td>R160(1)</td><td>MB25</td><td>C5</td><td>68n</td><td>MB25 CH BUF ADR 1 H</td><td>R104(1)</td><td>MB22</td><td>D5</td><td>68n</td><td>PAGED REF H</td></tr><tr><td>R167(1)</td><td>MB25</td><td>C5</td><td>68n</td><td>MB25 CH BUF ADR 2 H</td><td>R135(1)</td><td>MB26</td><td>D3</td><td>68n</td><td>PF HOLD 01 IN H</td></tr><tr><td>R172(1)</td><td>MB25</td><td>C5</td><td>68n</td><td>MB25 CH BUF ADR 3 H</td><td>R130(1)</td><td>MB26</td><td>D3</td><td>68n</td><td>PF HOLD 02 IN H</td></tr><tr><td>R171(1)</td><td>MB25</td><td>C5</td><td>68n</td><td>MB25 CH BUF ADR 4 H</td><td>R133(1)</td><td>MB26</td><td>D3</td><td>68n</td><td>PF HOLD 03 IN H</td></tr><tr><td>R174(1)</td><td>MB25</td><td>C5</td><td>68n</td><td>MB25 CH BUF ADR 5 H</td><td>R126(1)</td><td>MB26</td><td>C3</td><td>68n</td><td>PF HOLD 04 IN H</td></tr><tr><td>R166(1)</td><td>MB25</td><td>B5</td><td>68n</td><td>MB25 CH BUF ADR 6 H</td><td>R123(1)</td><td>MB26</td><td>C3</td><td>68n</td><td>PF HOLD 05 IN H</td></tr><tr><td>R173(1)</td><td>MB25</td><td>C7</td><td>68n</td><td>MB25 CH BUF IN 00-17 PAR H</td><td>R15(1)</td><td>MB22</td><td>D6</td><td>68n</td><td>PT CACHE H</td></tr><tr><td>R165(1)</td><td>MB25</td><td>C7</td><td>68n</td><td>MB25 CH BUF IN 18-35 PAR H</td><td>R115(1)</td><td>MB23</td><td>C3</td><td>68n</td><td>RQ HOLD FF H</td></tr><tr><td>R212(1)</td><td>MB25</td><td>A7</td><td>68n</td><td>MB25 CH BUF MB SEL H</td><td></td><td></td><td></td><td></td><td></td></tr><tr><td>R150(1)</td><td>MB25</td><td>A5</td><td>68n</td><td>MB25 CH BUF PAP BIT H</td><td></td><td></td><td></td><td></td><td></td></tr><tr><td>R214(1)</td><td>MB25</td><td>B7</td><td>68n</td><td>MB25 CH PEG 00-17 PAR H</td><td></td><td></td><td></td><td></td><td></td></tr><tr><td>R216(1)</td><td>MB25</td><td>B7</td><td>68n</td><td>MB25 CH REG 18-35 PAR H</td><td></td><td></td><td></td><td></td><td></td></tr><tr><td>R205(1)</td><td>MB25</td><td>B5</td><td>68n</td><td>MB25 CH REG HOLD H</td><td></td><td></td><td></td><td></td><td></td></tr><tr><td>R162(1)</td><td>MB25</td><td>D2</td><td>68n</td><td>-MB25 LOW H</td><td></td><td></td><td></td><td></td><td></td></tr><tr><td>R176(1)</td><td>MB25</td><td>D5</td><td>68n</td><td>MB25 MB CH BUF 00-17 PAP H</td><td></td><td></td><td></td><td></td><td></td></tr><tr><td>R177(1)</td><td>MB25</td><td>D5</td><td>68n</td><td>MB25 MB CH BUF 18-35 PAR H</td><td></td><td></td><td></td><td></td><td></td></tr><tr><td>R120(1)</td><td>MB26</td><td>C6</td><td>68n</td><td>MB26 CCA REF H</td><td></td><td></td><td></td><td></td><td></td></tr><tr><td>R129(1)</td><td>MB26</td><td>C6</td><td>68n</td><td>MB26 CHAN REF H</td><td></td><td></td><td></td><td></td><td></td></tr><tr><td>R193(1)</td><td>MB26</td><td>D6</td><td>68n</td><td>MB26 CSH PAP BIT H</td><td></td><td></td><td></td><td></td><td></td></tr><tr><td>R134(1)</td><td>MB26</td><td>D3</td><td>68n</td><td>MB26 EBUS REG 00 IN H</td><td></td><td></td><td></td><td></td><td></td></tr><tr><td>R139(1)</td><td>MB26</td><td>D3</td><td>68n</td><td>MB26 EBUS REG 01 IN H</td><td></td><td></td><td></td><td></td><td></td></tr></tbody></table>											RESISTOR LOC(PIN)	SHOWN DRW#	ON REF	VALUE	TERMINATES SIGNAL	RESISTOR LOC(PIN)	SHOWN DRW#	ON REF	VALUE	TERMINATES SIGNAL	R124(1)	MB24	B6	68n	MB24 MB DATA SOURCE 1 H	R142(1)	MB26	D3	68n	MB26 EBUS REG 02 IN H	R125(1)	MB24	B6	68n	MB24 MB DATA SOURCE 2 H	R141(1)	MB26	D3	68n	MB26 EBUS REG 03 IN H	R61(1)	MB24	D2	68n	MB24 MB PAR ERR H	R6(1)	MB26	C3	68n	MB26 EBUS REG 04 IN H	R136(1)	MB24	B6	68n	MB24 MB WD SEL 1 H	R7(1)	MB26	C3	68n	MB26 EBUS REG 05 IN H	R137(1)	MB24	B6	68n	MB24 MB WD SEL 2 H	R10(1)	MB26	C3	68n	MB26 EBUS REG 06 IN H	R62(1)	MB24	A2	68n	MB24 MEM RD RQ A H	R121(1)	MB26	B2	68n	MB26 ERA SEL H	R70(1)	MB24	A2	68n	-MB24 MEM RD PQ A H	R56(1)	MB26	A3	68n	-MB26 MB TEST PAR B IN H	R77(1)	MB24	A2	68n	-MB24 MEM WR PQ A H	R116(1)	MB26	C6	68n	MB26 MEM WRITE H	R151(1)	MB24	D5	68n	MB24 NXM T2 H	R59(1)	MB24	C3	68n	MEM ADR PAR ERR H	R44(1)	MB24	D5	68n	MB24 NXM T3 H	R5(1)	MB24	D3	68n	MEM ERROR H	R43(1)	MB24	C5	68n	MB24 NXM T4 H	R161(1)	MB25	C2	68n	MEM PAR IN H	R42(1)	MB24	C5	68n	MB24 NXM T5 H	R117(1)	MB24	A2	68n	MEM RD RQ B H	R4(1)	MB24	B5	68n	MB24 NXM T6 H	R163(1)	MB25	B2	68n	-MEM TO C EN H	R145(1)	MB24	B4	68n	-MB24 NXM T6,7 H	R164(1)	MB25	B2	68n	MEM TO C SEL 1 H	R0(1)	MB24	D2	68n	MB24 SBUS ERR FLG H	R160(1)	MB25	B2	68n	MEM TO C SEL 2 H	R155(1)	MB25	A5	68n	MB25 CCH PAP BIT H	R221(1)	MB23	C3	68n	MR RESET 06 H	R157(1)	MB25	D3	68n	MB25 CH BUF 00-17 PAR H	R60(1)	MB21	B3	68n	-MTR CCA WRITEBACK H	R156(1)	MB25	D2	68n	MB25 CH BUF 18-35 PAR H	R217(1)	MB25	C7	68n	PAG MB 00-17 PAR H	R169(1)	MB25	C5	68n	MB25 CH BUF ADR 0 H	R213(1)	MB25	C7	68n	PAG MB 18-35 PAR H	R160(1)	MB25	C5	68n	MB25 CH BUF ADR 1 H	R104(1)	MB22	D5	68n	PAGED REF H	R167(1)	MB25	C5	68n	MB25 CH BUF ADR 2 H	R135(1)	MB26	D3	68n	PF HOLD 01 IN H	R172(1)	MB25	C5	68n	MB25 CH BUF ADR 3 H	R130(1)	MB26	D3	68n	PF HOLD 02 IN H	R171(1)	MB25	C5	68n	MB25 CH BUF ADR 4 H	R133(1)	MB26	D3	68n	PF HOLD 03 IN H	R174(1)	MB25	C5	68n	MB25 CH BUF ADR 5 H	R126(1)	MB26	C3	68n	PF HOLD 04 IN H	R166(1)	MB25	B5	68n	MB25 CH BUF ADR 6 H	R123(1)	MB26	C3	68n	PF HOLD 05 IN H	R173(1)	MB25	C7	68n	MB25 CH BUF IN 00-17 PAR H	R15(1)	MB22	D6	68n	PT CACHE H	R165(1)	MB25	C7	68n	MB25 CH BUF IN 18-35 PAR H	R115(1)	MB23	C3	68n	RQ HOLD FF H	R212(1)	MB25	A7	68n	MB25 CH BUF MB SEL H						R150(1)	MB25	A5	68n	MB25 CH BUF PAP BIT H						R214(1)	MB25	B7	68n	MB25 CH PEG 00-17 PAR H						R216(1)	MB25	B7	68n	MB25 CH REG 18-35 PAR H						R205(1)	MB25	B5	68n	MB25 CH REG HOLD H						R162(1)	MB25	D2	68n	-MB25 LOW H						R176(1)	MB25	D5	68n	MB25 MB CH BUF 00-17 PAP H						R177(1)	MB25	D5	68n	MB25 MB CH BUF 18-35 PAR H						R120(1)	MB26	C6	68n	MB26 CCA REF H						R129(1)	MB26	C6	68n	MB26 CHAN REF H						R193(1)	MB26	D6	68n	MB26 CSH PAP BIT H						R134(1)	MB26	D3	68n	MB26 EBUS REG 00 IN H						R139(1)	MB26	D3	68n	MB26 EBUS REG 01 IN H					
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NOTE: 1. ALL TERMINATORS HAVE PIN TWO CONNECTED TO -2.0V AND ARE 5% 1/4WATT UNLESS OTHERWISE SPECIFIED 2. ENTRIES ARE SORTED BY SIGNAL NAME 3. % INDICATES OUTPUT OF DIP LOC AND () INDICATES PIN NUMBER																																																																																																																																																																																																																																																																																																																																																																																																																																				
<div><div><div>THIS DRAWING AND SPECIFICATIONS, HEREIN, ARE THE PROPERTY OF DIGITAL EQUIPMENT CORPORATION AND SHALL NOT BE REPRODUCED OR COPIED OR USED IN WHOLE OR IN PART AS THE BASIS FOR THE MANUFACTURE OR SALE OF ITEMS WITHOUT WRITTEN PERMISSION. COPYRIGHT © 1976, DIGITAL EQUIPMENT CORPORATION</div><div>REVISIONS CHK CHANGE NO. REV</div></div><div><div>digital</div><div>DRN. <i>G. Smith</i> DATE <i>05-AUG-76</i> ENG. <i>John D. Allen</i> DATE <i>17-SEP-76</i> BY <i>D. J. Stephens</i> DATE <i>7/15/76</i> SHEET <i>2</i> OF <i>2</i></div><div>TITLE: MBOX CONTROL #4 TERMINATORS</div></div><div><div>85372.DRW(4,175) FIRST USED ON OPTION MODEL: KL10</div><div>DATE <i>30-AUG-76</i> 17:34 NEXT HIGHER ASSEMBLY: B-DD-M8537-0</div><div>SIZE CODE D CS NUMBER M8537-0-RES REV. C</div></div></div>																																																																																																																																																																																																																																																																																																																																																																																																																																				
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