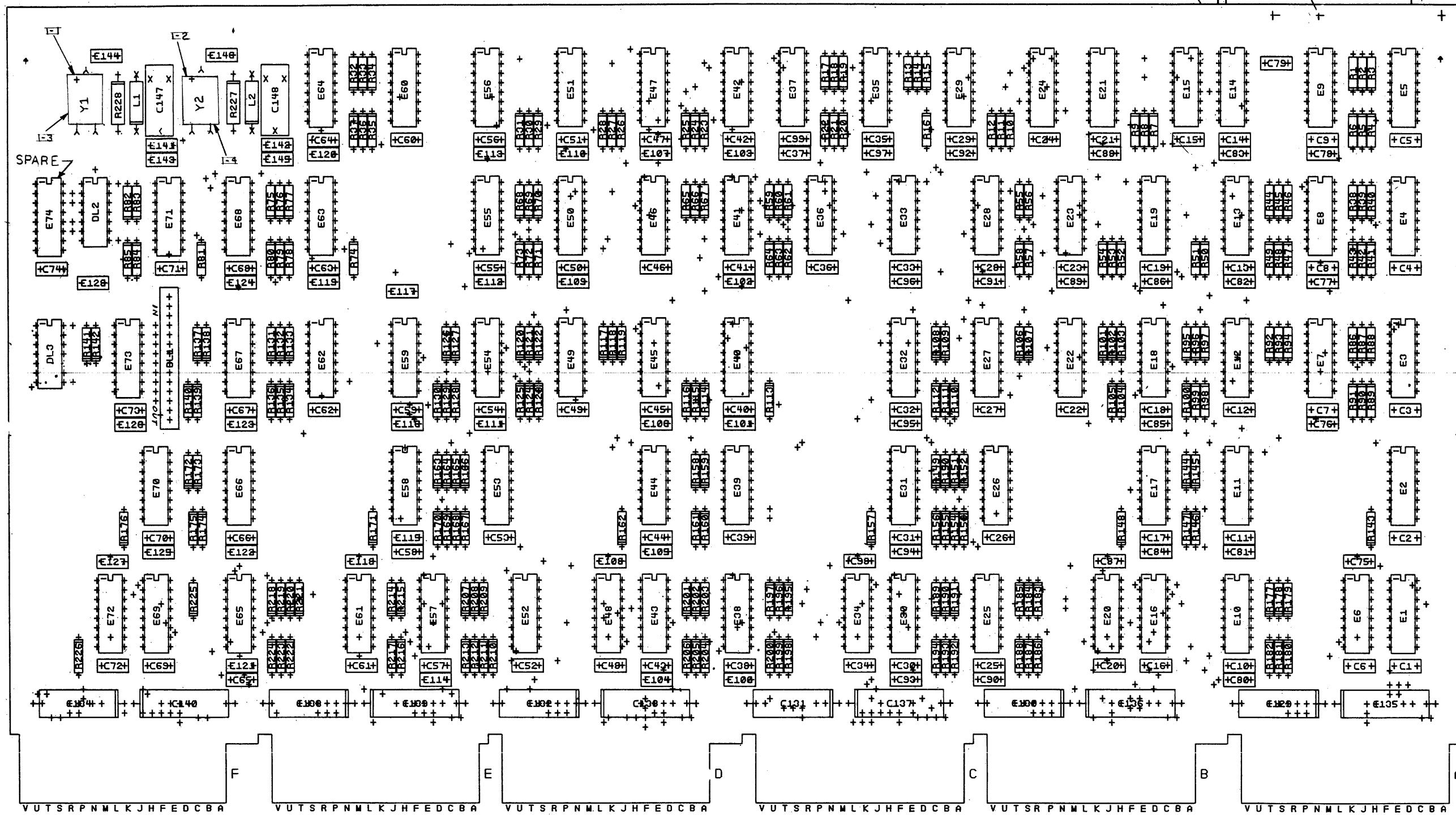




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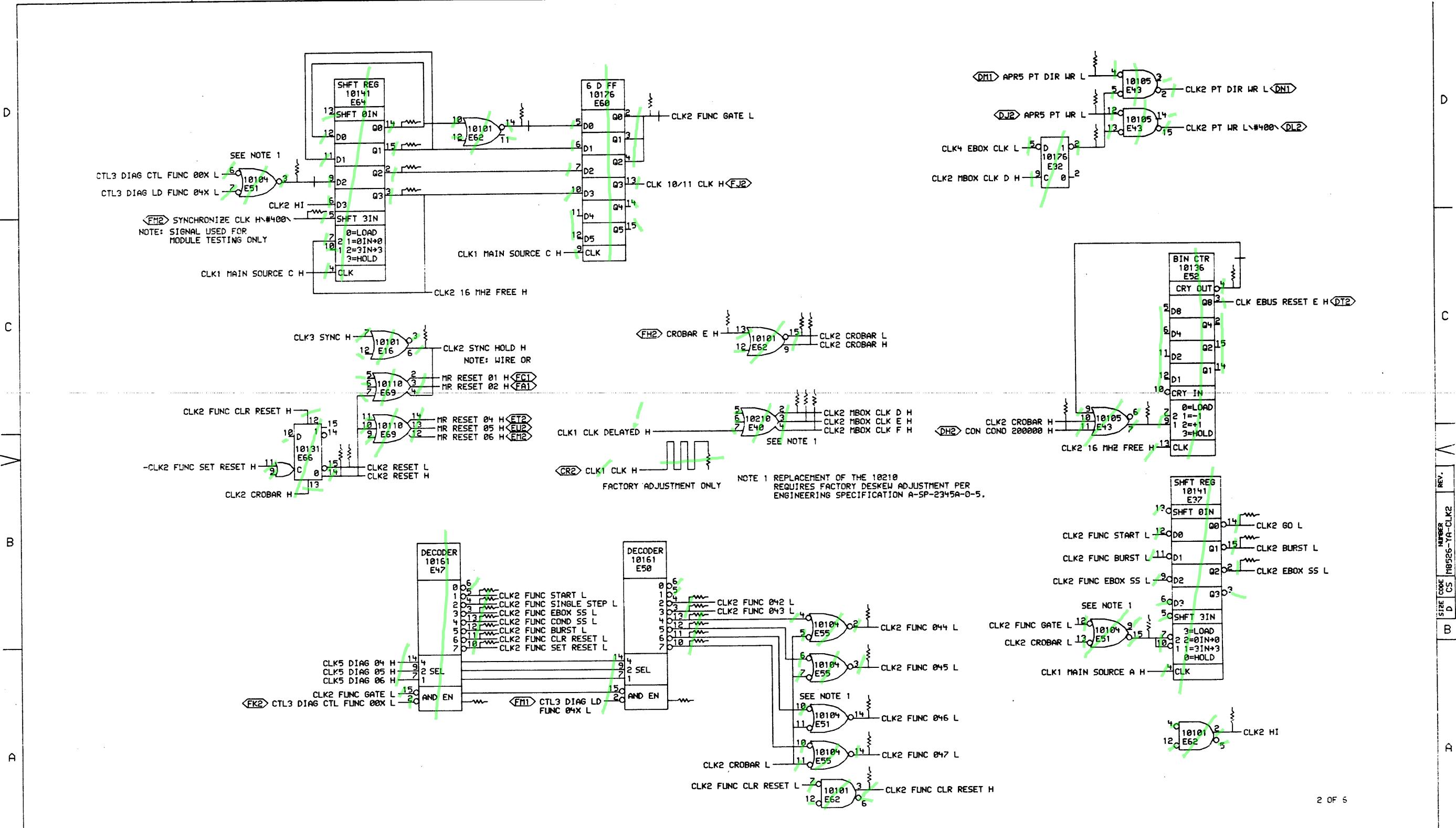


## NOTES

ETCH REV. C-P1  
P.C. DESIGN DATA BASE REV. C-P1

SIGNATURES	DATE	digital		
DRN. <i>Linda Foster</i>	9/26/77			
CHK'D. RW <i>W. M. WATTS</i>	10/6/77			
ENG. <i>M. Burkhardt</i>	10/6/77	TITLE		
PROJ. ENG. <i>M. Burkhardt</i>	10/6/77	CLK CONTROL		
PROD. <i>Bill Early</i>	11/10/77			
SCALE <i>2/1</i>	SIZE	CODE	NUMBER	REV
SHT. 2 OF 5	D	UA	M8526-YA-Ø	A
NEXT HIGHER FSSY: R-DN-MR8526-YA				





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	REVIS
CHK	CHANGE

d i g i t a l DRN: 7  
 CLK2EF.DR4[4,677] 28 FEB  
 FIRST USED ON OPTION/MODEL: K

DATE  
25-FEB-  
DATE  
2/27/08  
N  
L10

ENG.  
W. Bruce  
BOARD LOC  
2 SHEET  
NEXT HIGHER  
B-DD-M853

*short* D  
291  
LOCATION: 4  
1 OF  
ASSEMBLY:  
26-YA

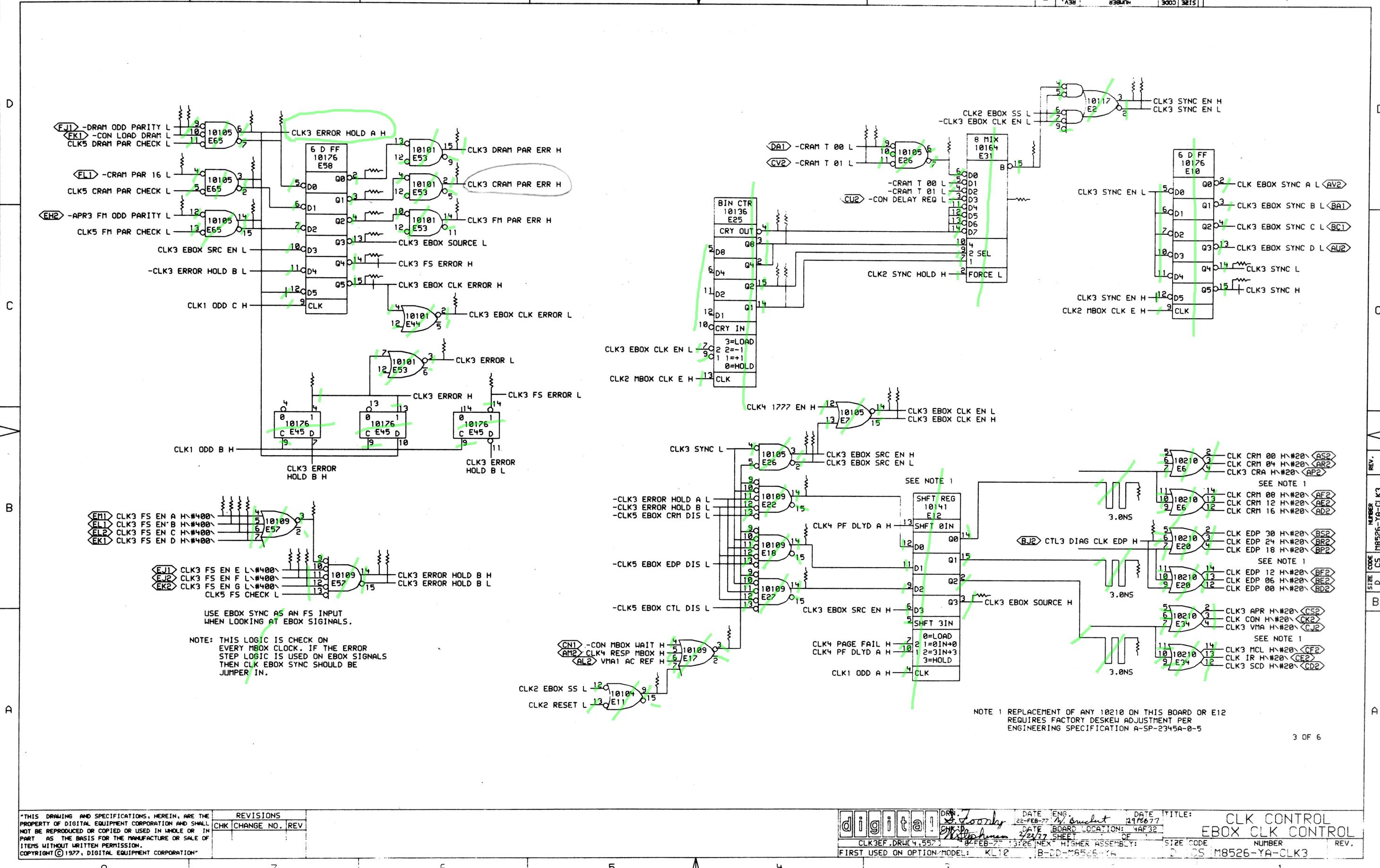
DATE FEB 17	TITLE
AF32	
1	
	SIZE D

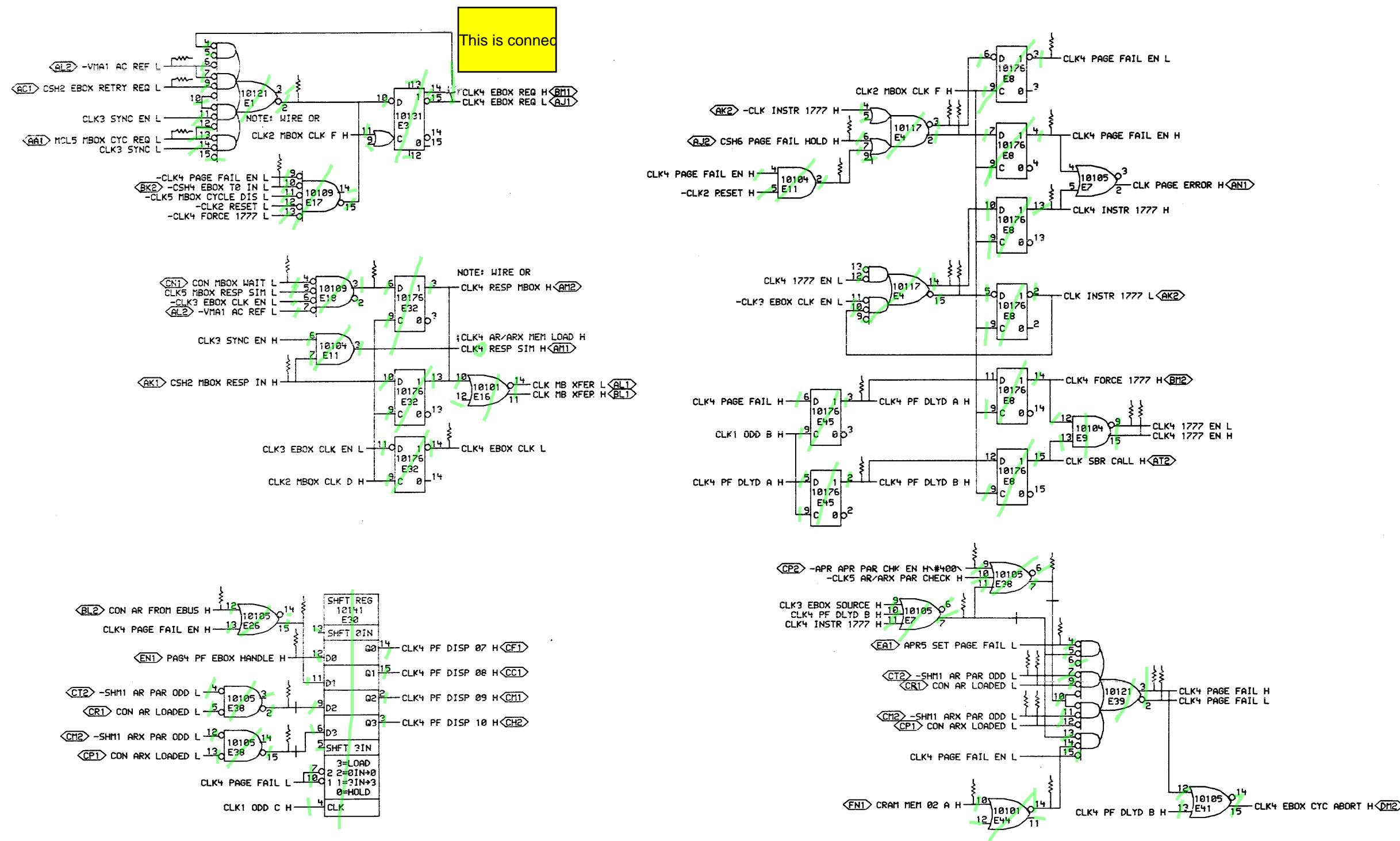
C  
DIAGN  
CODE CS M85

ALK CO  
NOSTIC  
NUMBER  
526-YA-0

NTROL  
C CONT  
CLK2

TROL  
REV.





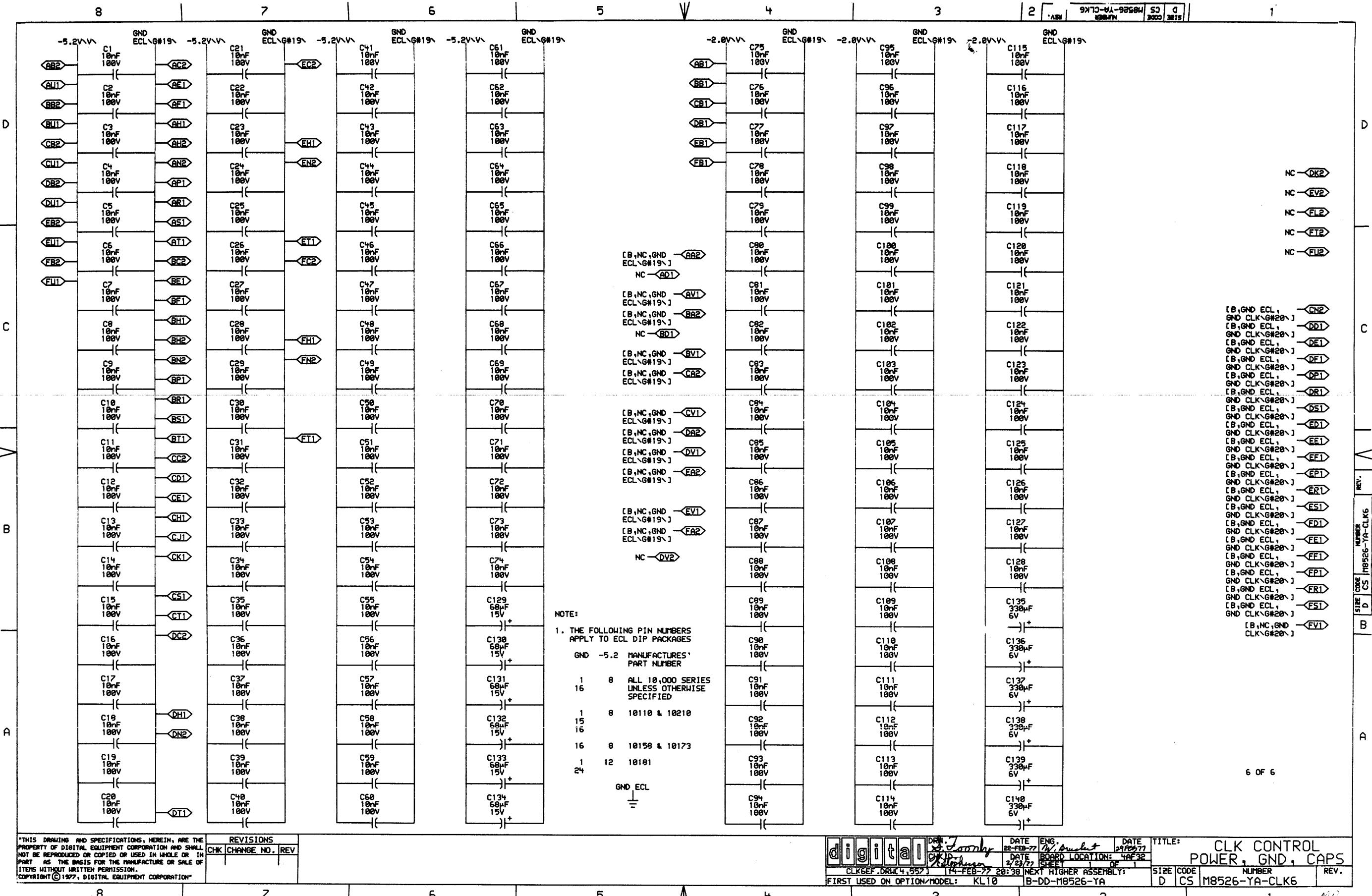
4 OF 8

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CHI

DIGITAL	DRW. <i>S. Foosby</i>	DATE 25-FEB-77	ENG. <i>M. Brundret</i>	DATE 21-FEB-77	TITLE: CLK CONTROL
CHK ID: <i>Watchesman</i>	DATE 3/2/77	BOARD LOCATION: 4AF32		PAGE FAIL LOGIC	
CLK4EF.DPLC4.6223		12-FEB-77 11:09	NEXT HIGHER ASSEMBLY: B-DD-M8526-YA		SIZE D
FIRST USED ON OPTION/MODEL: KL10		12-FEB-77 11:09			CODE CS NUMBER M8526-YA-CLK4
REV.					





D	RESISTOR LOC(PIN)	SHOWN ON DRW#	REF	VALUE	TERMINATES SIGNAL	RESISTOR LOC(PIN)	SHOWN ON DRW#	REF	VALUE	TERMINATES SIGNAL	RESISTOR LOC(PIN)	SHOWN ON DRW#	REF	VALUE	TERMINATES SIGNAL	RESISTOR LOC(PIN)	SHOWN ON DRW#	REF	VALUE	TERMINATES SIGNAL
	R85C1> CLK1 C4 100Ω %DL2(7)					R22C1> CLK2 B2 68Ω %E51(15)					R140C1> CLK1 C6 100Ω CLK1 EBUS CLK SOURCE H					R178C1> CLK2 B7 68Ω -CLK2 RESET H				
	R142C1> CLK1 C3 100Ω %DL3(7)					R36C1> CLK2 D7 68Ω %E51(3)					R77C1> CLK1 C2 68Ω -CLK1 ERROR STOP H					R225C1> CLK2 C6 68Ω CLK2 SYNC HOLD H				
	R87C1> CLK4 D7 68Ω %E11(2)					R204C1> CLK2 C2 68Ω %E52(4)					R137C1> CLK1 C7 100Ω CLK1 GATED H					R124C1> CLK3 D6 68Ω CLK3 CRAM PAR ERR H				
	R88C1> CLK4 D4 68Ω %E11(2)					R29C1> CLK1 C7 68Ω %E56(2)					R31C1> CLK1 B3 68Ω CLK1 GATED EN H					R127C1> CLK3 D6 68Ω CLK3 DRAM PAR ERR H				
	R146C1> CLK3 A5 68Ω %E11(9)					R211C1> CLK3 B7 68Ω %E57(3)					R18C1> CLK1 D3 68Ω CLK1 MAIN SOURCE A H					R105C1> CLK3 C3 68Ω CLK3 EBOX CLK EN H				
	R143C1> CLK3 B2 68Ω %E12(14)					R166C1> CLK3 D6 68Ω %E58(2)					R2C1> CLK1 D3 68Ω CLK1 MAIN SOURCE B H					R188C1> CLK3 C3 68Ω -CLK3 EBOX CLK EN H				
	R148C1> CLK3 B2 68Ω %E12(15)					R165C1> CLK3 D6 68Ω %E58(3)					R30C1> CLK1 D3 68Ω CLK1 MAIN SOURCE C H					R158C1> CLK3 C6 68Ω CLK3 EBOX CLK ERROR H				
	R157C1> CLK3 A2 68Ω %E12(2)					R168C1> CLK3 C6 68Ω %E58(4)					R162C1> CLK1 B2 68Ω CLK1 MBOX A H					R108C1> CLK3 C6 68Ω -CLK3 EBOX CLK ERROR H				
	R152C1> CLK3 A4 68Ω %E17(3)					R34C1> CLK2 D6 68Ω %E62(14)					R171C1> CLK1 A2 68Ω CLK1 MBOX B H					R53C1> CLK3 B3 68Ω CLK3 EBOX SOURCE H				
C	R93C1> CLK3 B4 68Ω %E18(14)					R21C1> CLK1 A4 68Ω %E63(2)					R176C1> CLK1 A2 68Ω CLK1 MBOX C H					R109C1> CLK3 C6 68Ω -CLK3 EBOX SOURCE H				
	R118C1> CLK4 C6 68Ω %E18(3)					R135C1> CLK2 D6 68Ω %E64(14)					R97C1> CLK1 C1 68Ω CLK1 ODD A H					R86C1> CLK3 B4 68Ω CLK3 EBOX SRC EN H				
	R8C1> CLK5 C5 68Ω %E21(4)					R33C1> CLK2 D6 68Ω %E64(15)					R115C1> CLK1 C1 68Ω CLK1 ODD B H					R170C1> CLK3 B4 68Ω -CLK3 EBOX SRC EN H				
	R92C1> CLK3 B4 68Ω %E22(14)					R35C1> CLK2 D6 68Ω %E64(2)					R191C1> CLK1 C1 68Ω CLK1 ODD C H					R167C1> CLK3 C7 68Ω CLK3 ERROR H				
	R10C1> CLK5 C6 68Ω %E24(2)					R169C1> CLK3 C7 68Ω %E65(15)					R79C1> CLK1 B7 68Ω -CLK1 RATE SELECTED H					R122C1> CLK3 C6 68Ω -CLK3 ERROR H				
	R154C1> CLK3 C4 68Ω %E25(14)					R164C1> CLK3 D7 68Ω %E65(2)					R209C1> CLK2 D6 68Ω CLK2 16 MHZ FREE H					R100C1> CLK3 D7 68Ω CLK3 ERROR HOLD A H				
	R153C1> CLK3 C4 68Ω %E25(15)					R163C1> CLK3 D7 68Ω %E65(7)					R6C1> CLK2 B2 68Ω -CLK2 BURST H					R95C1> CLK3 B6 68Ω CLK3 ERROR HOLD B H				
	R156C1> CLK3 C4 68Ω %E25(2)					R114C1> CLK1 D3 68Ω %E67(15)					R206C1> CLK2 C4 68Ω CLK2 CROBAR H					R116C1> CLK3 B6 68Ω -CLK3 ERROR HOLD B H				
	R158C1> CLK3 C4 68Ω %E25(4)					R80C1> CLK1 D5 68Ω %E68(14)					R25C1> CLK2 C4 68Ω -CLK2 CROBAR H					R120C1> CLK3 C6 68Ω CLK3 FM PAR ERR H				
	R198C1> CLK4 B7 68Ω %E26(15)					R138C1> CLK1 D5 68Ω %E68(15)					R90C1> CLK2 B2 68Ω -CLK2 EBOX SS H					R214C1> CLK3 B7 68Ω CLK3 FS EN A H\#400\				
	R155C1> CLK3 D3 68Ω %E26(7)					R132C1> CLK1 D5 68Ω %E68(2)					R1C1> CLK2 B5 68Ω -CLK2 FUNC 042 H					R215C1> CLK3 B7 68Ω CLK3 FS EN B H\#400\				
	R99C1> CLK3 B4 68Ω %E27(14)					R81C1> CLK1 D5 68Ω %E68(3)					R5C1> CLK2 B5 68Ω -CLK2 FUNC 043 H					R217C1> CLK3 B7 68Ω CLK3 FS EN C H\#400\				
	R71C1> CLK1 C2 68Ω %E27(2)					R200C1> CLK4 B3 68Ω %E7(7)					R4C1> CLK2 B4 68Ω -CLK2 FUNC 044 H					R216C1> CLK3 B7 68Ω CLK3 FS EN D H\#400\				
	R89C1> CLK3 D2 68Ω %E31(15)					R174C1> CLK1 A6 68Ω %E70(15)					R49C1> CLK2 A4 68Ω -CLK2 FUNC 045 H					R213C1> CLK3 B7 68Ω CLK3 FS EN E L\#400\				
	R202C1> CLK2 D2 68Ω %E32(2)					R175C1> CLK1 B5 68Ω %E70(3)					R67C1> CLK2 A4 68Ω -CLK2 FUNC 046 H					R212C1> CLK3 B7 68Ω CLK3 FS EN F L\#400\				
	R193C1> CLK4 A7 68Ω %E38(15)					R76C1> CLK1 C6 68Ω %E71(14)					R62C1> CLK2 A4 68Ω -CLK2 FUNC 047 H					R207C1> CLK3 B7 68Ω CLK3 FS EN G L\#400\				
	R192C1> CLK4 A7 68Ω %E38(2)					R84C1> CLK1 C7 68Ω %E71(15)					R20C1> CLK2 B6 68Ω -CLK2 FUNC BURST H					R121C1> CLK3 C6 68Ω CLK3 FS ERROR H				
	R196C1> CLK4 B3 68Ω %E38(7)					R83C1> CLK1 D7 68Ω %E71(2)					R172C1> CLK2 A4 68Ω CLK2 FUNC CLR RESET H					R64C1> CLK3 C6 68Ω -CLK3 FS ERROR H				
	R42C1> CLK4 C3 68Ω %E4(14)					R75C1> CLK1 D6 68Ω %E71(3)					R134C1> CLK2 B6 68Ω -CLK2 FUNC CLR RESET H					R59C1> CLK3 C1 68Ω CLK3 SYNC H				
B	R46C1> CLK4 C3 68Ω %E4(15)					R141C1> CLK1 C4 100Ω %E73(15)					R14C1> CLK2 B6 68Ω -CLK2 FUNC COND SS H					R151C1> CLK3 C1 68Ω -CLK3 SYNC H				
	R94C1> CLK4 D3 68Ω %E4(2)					R82C1> CLK1 C5 100Ω %E73(3)					R60C1> CLK2 B6 68Ω -CLK2 FUNC EBOX SS H					R177C1> CLK3 D2 68Ω CLK3 SYNC EN H				
	R45C1> CLK4 D3 68Ω %E4(3)					R74C1> CLK1 C3 68Ω %E73(5)					R26C1> CLK2 D5 68Ω -CLK2 FUNC GATE H					R181C1> CLK3 D2 68Ω -CLK3 SYNC EN H				
	R23C1> CLK1 A4 68Ω %E41(2)					R7C1> CLK5 B7 68Ω %E9(14)					R173C1> CLK2 B6 68Ω -CLK2 FUNC SET RESET H					R55C1> CLK4 C2 68Ω CLK4 1777 EN H				
	R68C1> CLK1 D2 68Ω %E41(7)					R9C1> CLK5 B6 68Ω %E9(2)					R24C1> CLK2 B6 68Ω -CLK2 FUNC SINGLE STEP H					R40C1> CLK4 C2 68Ω -CLK4 1777 EN H				
	R12C1> CLK1 B3 68Ω %E42(14)					R198C1> CLK4 B3 68Ω -APR APR PAR CHK EN H\#400\					R17C1> CLK2 B6 68Ω -CLK2 FUNC START H					R11C1> CLK4 C6 68Ω -CLK4 EBOX CLK H				
	R210C1> CLK2 C2 68Ω %E43(7)					R218C1> CLK3 C7 68Ω APR3 FM ODD PARITY H					R15C1> CLK2 B2 68Ω -CLK2 GO H					R51C1> CLK4 D3 68Ω CLK4 INSTR 1777 H				
	R159C1> CLK4 A3 68Ω %E44(14)					R203C1> CLK2 D2 68Ω -APR5 PT DIR WR H					R37C1> CLK2 A2 68Ω CLK2 HI					R98C1> CLK4 A2 68Ω CLK4 PAGE FAIL H				
	R73C1> CLK2 B5 68Ω %E50(10)					R201C1> CLK2 D2 68Ω -APR5 PT WR H					R111C1> CLK2 C4 68Ω CLK2 MBOX CLK D H					R194C1> CLK4 A2 68Ω -CLK4 PAGE FAIL H				
	R28C1> CLK2 B5 68Ω %E50(11)					R160C1> CLK4 B3 68Ω -APR5 SET PAGE FAIL H					R182C1> CLK2 C4 68Ω CLK2 MBOX CLK E H					R183C1> CLK4 D3 68Ω CLK4 PAGE FAIL EN H				
	R72C1> CLK2 B5 68Ω %E50(12)					R113C1> CLK2 B5 68Ω CLK1 CLK H					R43C1> CLK2 C4 68Ω CLK2 MBOX CLK F H					R119C1> CLK4 D3 68Ω -CLK4 PAGE FAIL EN H				
	R69C1> CLK2 B5 68Ω %E50(13)					R130C1> CLK1 C2 68Ω CLK1 CLK ON H					R144C1> CLK2 B7 68Ω CLK2 RESET H					R38C1> CLK4 C4 68Ω CLK4 PF DLYD A H				

D

C

2

B

八

RESISTOR LOCK(PIN)	SHOWN	ON	VALUE	TERMINATES SIGNAL	RESISTOR LOCK(PIN)	SHOWN	ON	VALUE	TERMINATES SIGNAL
R91K1>	CLK4	B4	68Ω	CLK4 PF DLVD B H	R205K1>	CLK2	C3	68Ω	CON CONO 200000 H
R199K1>	CLK5	B2	68Ω	-CLK5 AR/ARX PAR CHECK H	R149K1>	CLK3	D3	68Ω	CON DELAY REQ H
R57K1>	CLK5	C5	68Ω	CLK5 BURST 01 H	R224K1>	CLK3	D8	68Ω	CON LOAD DRAM H
R117K1>	CLK5	C5	68Ω	CLK5 BURST 02 H	R102K1>	CLK4	C7	68Ω	-CON MBOX WAIT H
R56K1>	CLK5	C5	68Ω	CLK5 BURST 04 H	R161K1>	CLK4	A3	68Ω	CRAM MEM 02 A H
R52K1>	CLK5	C5	68Ω	CLK5 BURST 08 H	R220K1>	CLK3	D7	68Ω	CRAM PAP 15 H
R118K1>	CLK5	C7	68Ω	CLK5 BURST 120 H	R185K1>	CLK3	D3	68Ω	CRAM T 00 H
R16K1>	CLK5	C7	68Ω	CLK5 BURST 16 H	R184K1>	CLK3	D3	68Ω	CRAM T 01 H
R129K1>	CLK5	C7	68Ω	CLK5 BURST 32 H	R133K1>	CLK2	C4	68Ω	CROBAR E H
R58K1>	CLK5	C7	68Ω	CLK5 BURST 64 H	R188K1>	CLK4	D7	68Ω	-CSH2 EBOX RETRY REQ H
R54K1>	CLK5	C4	68Ω	CLK5 BURST CNT=0 H	R112K1>	CLK4	C7	68Ω	CSH2 MBOX RESP IN H
R219K1>	CLK5	B3	68Ω	-CLK5 CRAM PAR CHECK H	R147K1>	CLK4	D7	68Ω	CSH4 EBOX T0 IN H
R125K1>	CLK5	B5	68Ω	CLK5 DIAG 04 H	R41K1>	CLK4	D4	68Ω	CSH6 PAGE FAIL HOLD H
R123K1>	CLK5	A5	68Ω	CLK5 DIAG 05 H	R186K1>	CLK3	B2	68Ω	CTL3 DIAG CLK EDP H
R128K1>	CLK5	A5	68Ω	CLK5 DIAG 06 H	R27K1>	CLK2	A6	68Ω	-CTL3 DIAG CTL FUNC 00x H
R126K1>	CLK5	A5	68Ω	-CLK5 DIAG READ H	R70K1>	CLK2	A5	68Ω	-CTL3 DIAG LD FUNC 04x H
R222K1>	CLK5	B3	68Ω	-CLK5 DRAM PAR CHECK H	R70K1>	CLK1	C3	68Ω	DESKEW CLK H#400~
R101K1>	CLK5	C2	68Ω	CLK5 EBOX CRM DIS H	R223K1>	CLK3	D7	68Ω	DRAM ODD PARITY H
R107K1>	CLK5	C2	68Ω	CLK5 EBOX CTL DIS H	R131K1>	CLK1	D4	68Ω	EXTERNAL CLK H#420~
R96K1>	CLK5	C2	68Ω	CLK5 EBOX EDP DIS H	R226K1>	CLK1	D4	270Ω	EXTERNAL CLK H#420~
R44K1>	CLK5	B7	68Ω	CLK5 EBUS 32 H	R179K1>	CLK4	D7	68Ω	-MGL5 MBOX CYC REQ H
R65K1>	CLK5	B7	68Ω	-CLK5 EBUS 32 H	R189K1>	CLK4	B7	68Ω	PAG4 PF EBOX HANDLE H
R48K1>	CLK5	A7	68Ω	CLK5 EBUS 33 H	R13K1>	CLK4	B3	68Ω	SHM1 AR PAR ODD H
R66K1>	CLK5	A7	68Ω	-CLK5 EBUS 33 H	R19K1>	CLK4	A3	68Ω	SHM1 ARX PAR ODD H
R47K1>	CLK5	A7	68Ω	CLK5 EBUS 34 H	R32K1>	CLK2	D7	68Ω	SYNCHRONIZE CLK H#400~
R63K1>	CLK5	A7	68Ω	-CLK5 EBUS 34 H	R104K1>	CLK4	D7	68Ω	VMA1 AC REF H
R58K1>	CLK5	A7	68Ω	CLK5 EBUS 35 H					
R61K1>	CLK5	A7	68Ω	-CLK5 EBUS 35 H					
R106K1>	CLK5	B2	68Ω	-CLK5 ERR STOP EN H					
R221K1>	CLK5	B3	68Ω	-CLK5 FM PAR CHECK H					
R208K1>	CLK5	B3	68Ω	-CLK5 FS CHECK H					
R145K1>	CLK5	B2	68Ω	CLK5 MBOX CYCLE DIS H					
R103K1>	CLK5	B2	68Ω	-CLK5 MBOX RESP SIM H					
R39K1>	CLK5	C3	68Ω	CLK5 RATE SEL 1 H					
R3K1>	CLK5	C3	68Ω	CLK5 RATE SEL 2 H					
R139K1>	CLK5	C3	68Ω	CLK5 SOURCE SEL 1 H					
R136K1>	CLK5	C3	68Ω	CLK5 SOURCE SEL 2 H					
R187K1>	CLK4	B7	68Ω	CON AR FROM EBUS H					
R197K1>	CLK4	B3	68Ω	-CON AR LOADED H					
R195K1>	CLK4	A3	68Ω	-CON ARX LOADED H					

NO

1. ALL TERMINATORS HAVE PIN TWO CONNECTED TO -2.8V AND ARE 5% 1/4WATT UNLESS OTHERWISE SPECIFIED
  2. ENTRIES ARE SORTED BY SIGNAL NAME
  3. % INDICATES OUTPUT OF DIP LOC AND  
<> INDICATES PIN NUMBER

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REVISIONS		
CHK	CHANGE NO.	R

digita	DRN. <i>C Smith</i>	DATE 23-FEB-77	ENG. <i>M. Brundt</i>	DATE 24-FEB-77	TITLE: CLK CONTROL TERMINATOR
CHK'D. <i>M. Brundt</i>	DATE 23-FEB-77	BOARD LOCATION: F10			SHEET 2 OF 2
F05262.DRMW4,551	23-FEB-77 14:19	NEXT HIGHER ASSEMBLY:			SIZE D NUMBER
FIRST USED ON OPTION MODEL: KL10		B-DD-M8526-YA			CODE CS M8526-YA-RES