

REVISION CONTROL SHEET

REVISIONS

CUSTOMER PRINT SET		REVISION CONTROL SHEET																	
MFG SET		DRAWING NO	NO OF SHT	DESCRIPTION	OPTION NO/FILE DATE	REVISIONS													
MODULE REVISION																			
		D-UA-M8525-0-0	7	EBOX CONTROL #2~	F H														
		D-CS-M8525-0-CON1	1	EBOX CONTROL #2 CRAM COND FIELD	E F														
		D-CS-M8525-0-CON2	1	EBOX CONTROL #2 CONSOLE CONTROL	D D														
		D-CS-M8525-0-CON3	1	EBOX CONTROL #2 INTERNAL I/O	C D														
		D-CS-M8525-0-CON4	1	EBOX CONTROL #2 PARITY LOGIC	E E														
		D-CS-M8525-0-CON5	1	EBOX CONTROL #2 XFER, FM WRITE	C C														
		D-CS-M8525-0-CON6	1	EBOX CONTROL #2 POWER, GND, CAPS	B C														
		D-CS-M8525-0-RES	2	EBOX CONTROL #2 TERMINATORS	D E														
		K-CO-M8525-0-4	1	EBOX CONTROL #2 (CALDEC DATA BASE)	C C														
		D-AH-M8525-0-5	4	EBOX CONTROL #2	C C														
		B-MH-M8525-0-6	1	MODULE ECO HISTORY	E F														
		5010689	-	ETCH CIRCUIT BOARD	C C														
		P00-M8525-00		PROCESS SHEET (REF ONLY)															
CUSTOMER PRINT SET CODES		X = PRINT OF DOCUMENT INCLUDED IN PRINT SET C = INCLUDES ALL PRINTS INDICATED ON DOCUMENT S = CONFIDENTIAL AUTHORIZED SIGNATURE REQUIRED				ECO NO	JUL 15	00006											
						TITLE	EBOX CONTROL #2			SIZE CODE	NUMBER		REV						
							SHEET 3 OF 3			B DD	M8525-0		F						

**CUSTOMER
PRINT
SET
CODES** X = PRINT OF DOCUMENT INCLUDED IN PRINT SET
C = INCLUDES ALL PRINTS INDICATED ON DOCUMENT
S = CONFIDENTIAL AUTHORIZED SIGNATURE REQUIRED

ECO NO
JL-5
00000

TITLE

EBOX CONTROL #2

SHEET 3 OF

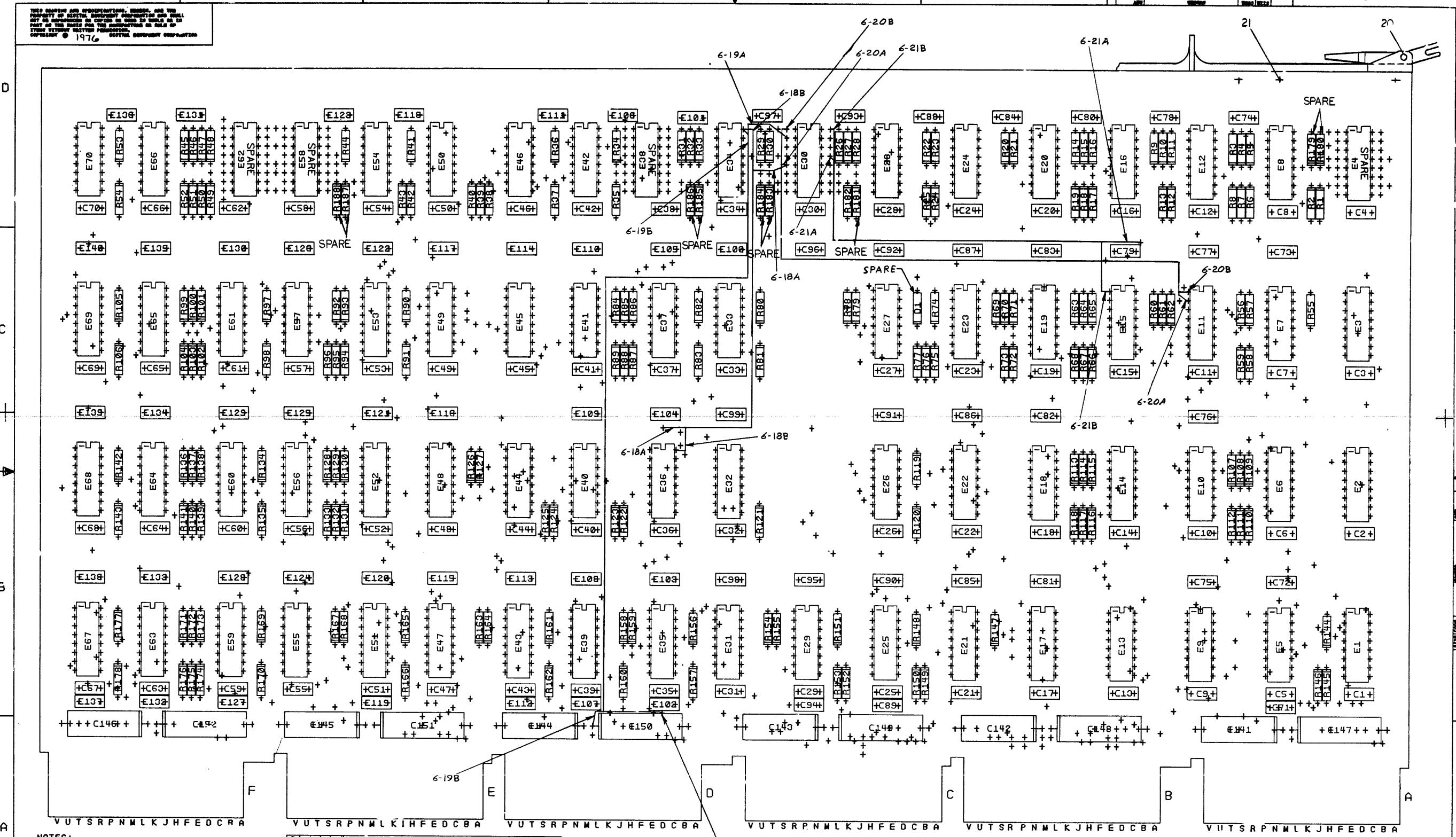
SIZE	CODE	NUMBER
B	DD	M8525-0

REV
F

MR

156

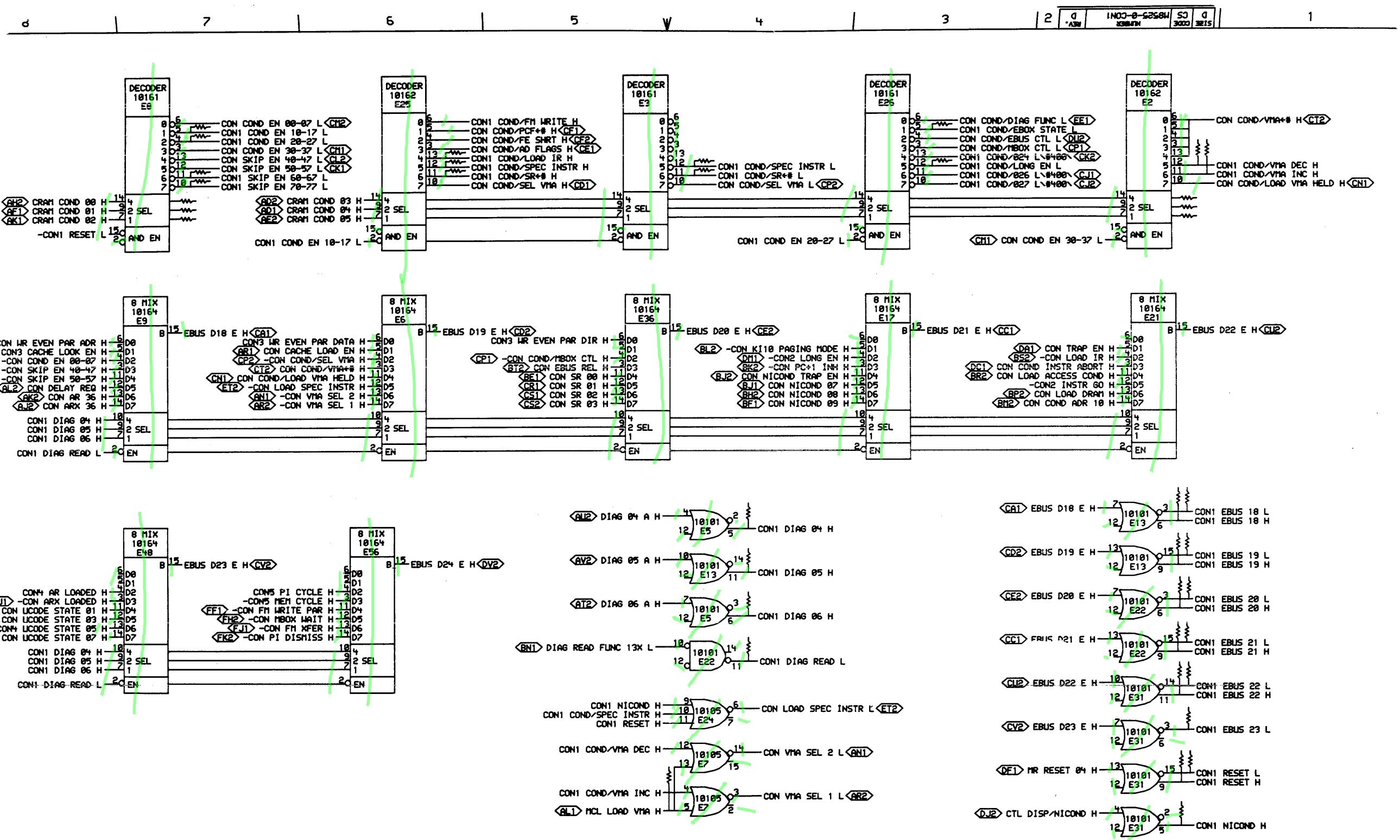
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NOTES:

ETCH REV. C
R.E. DESIGN DATA SHEET REV.

SIGNATURES MR. R. Sample	DATE 14 JUNE 76	digital			
MR. D. M. Sample	120-5%				
ENG. Tom Sample	12 Aug 76	TITLE			
ROJ. ENG. Tom Sample	13 Aug 76	DIAGNOSTIC BOARD			
ROD. Bill Sample	12 Aug 76				
CALE 2/1		SIZE	CODE	NUMBER	REV
HT. 2 OF 6		D	UA	M8525-0-0	F

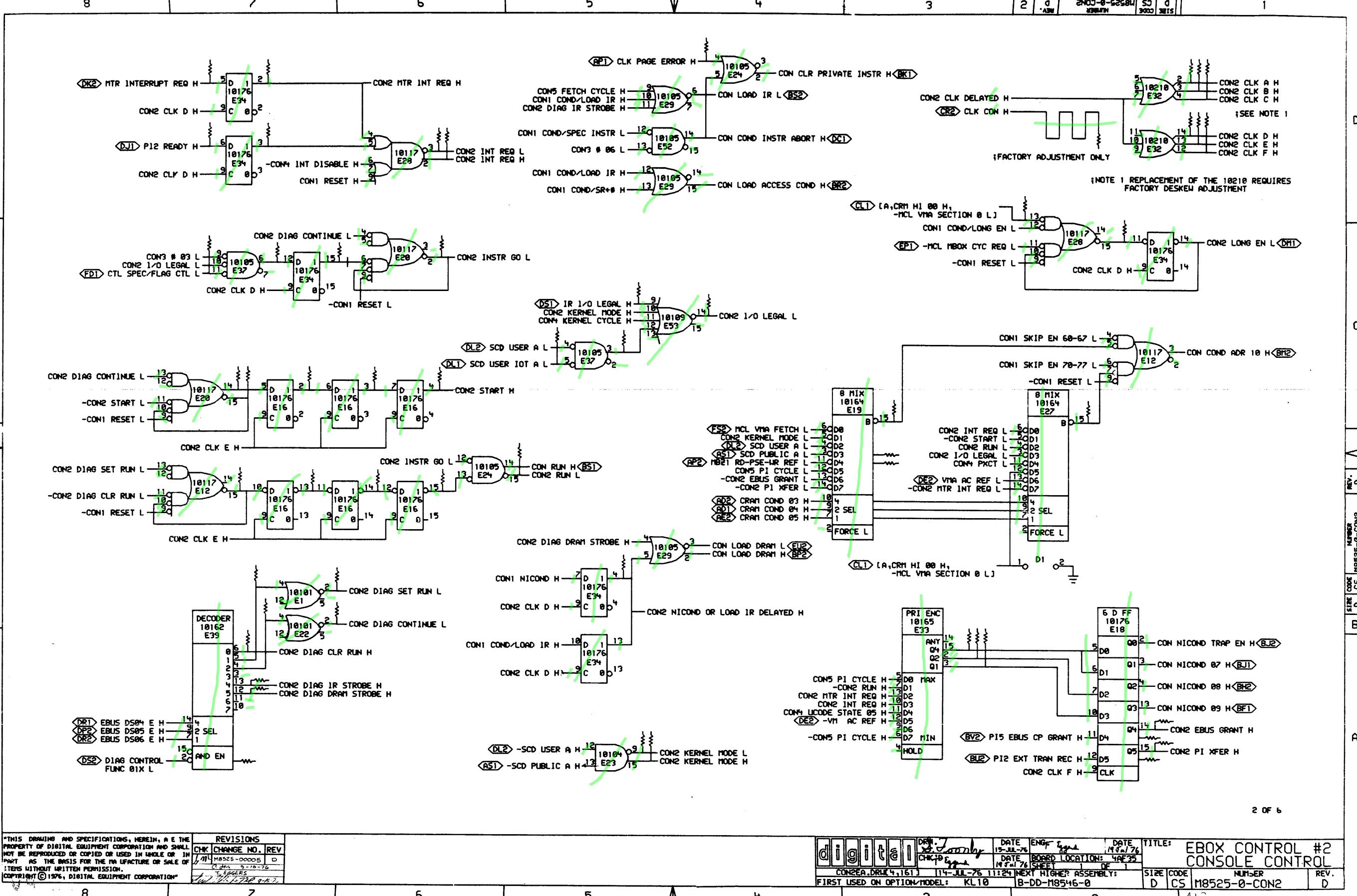


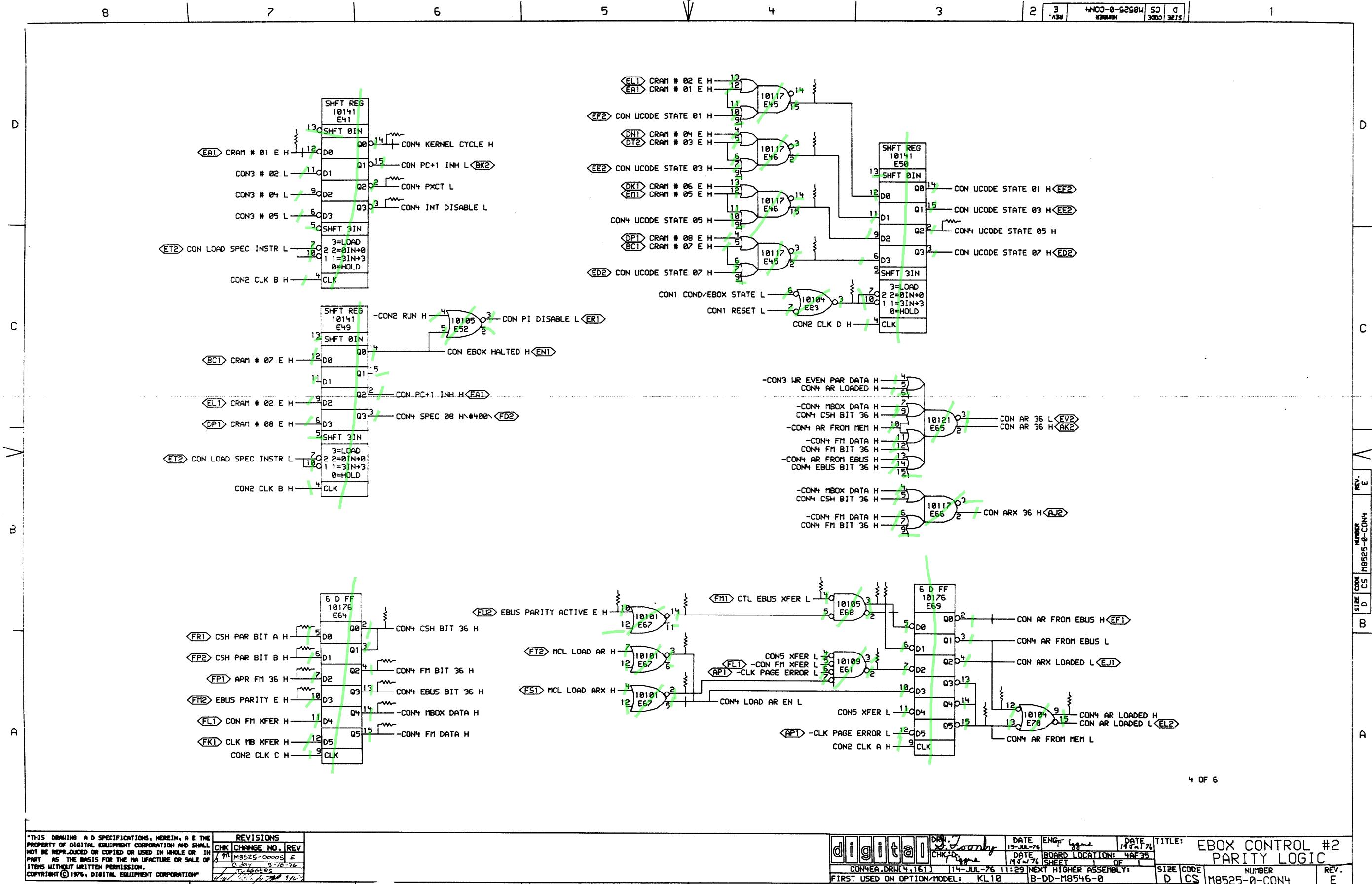
1 OF 6

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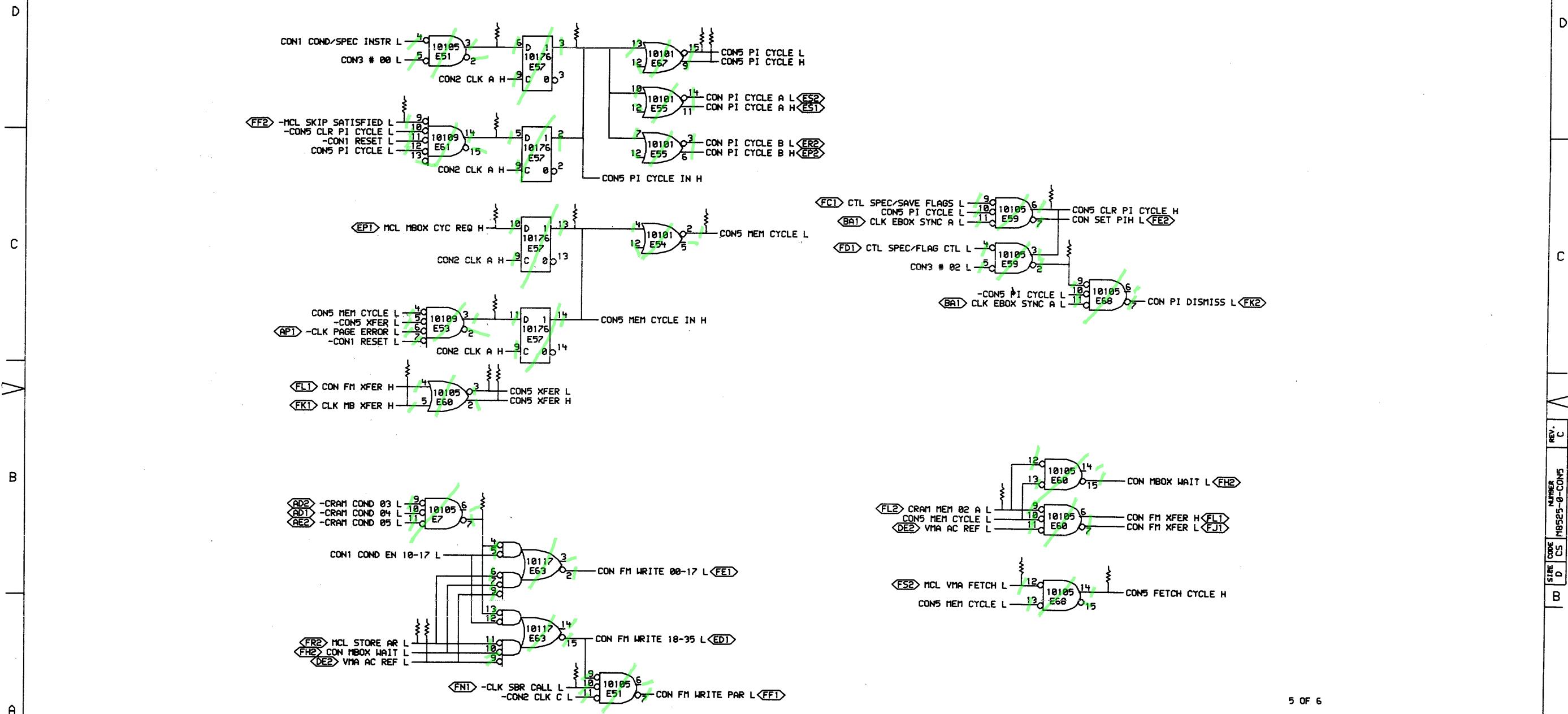
REVISIONS
CHK CHANGE NO. REV

DATE ENG DATE
05-JUL-77 5-7-77
TITLE: EBOX CONTROL #2
CRAM COND FIELD
digital 10161
DATE BOARD LOCATION: 4AE35
SHEET 1 OF 1
CONICA.DRIVE 4.5561 14-JUL-76 00:00 NEXT HIGHER ASSEMBLY:
FIRST USED ON OPTION/MODEL: KL10 B-DD-M10546-0
SIZE CODE NUMBER REV.
D CS M10525-0-CON1 D





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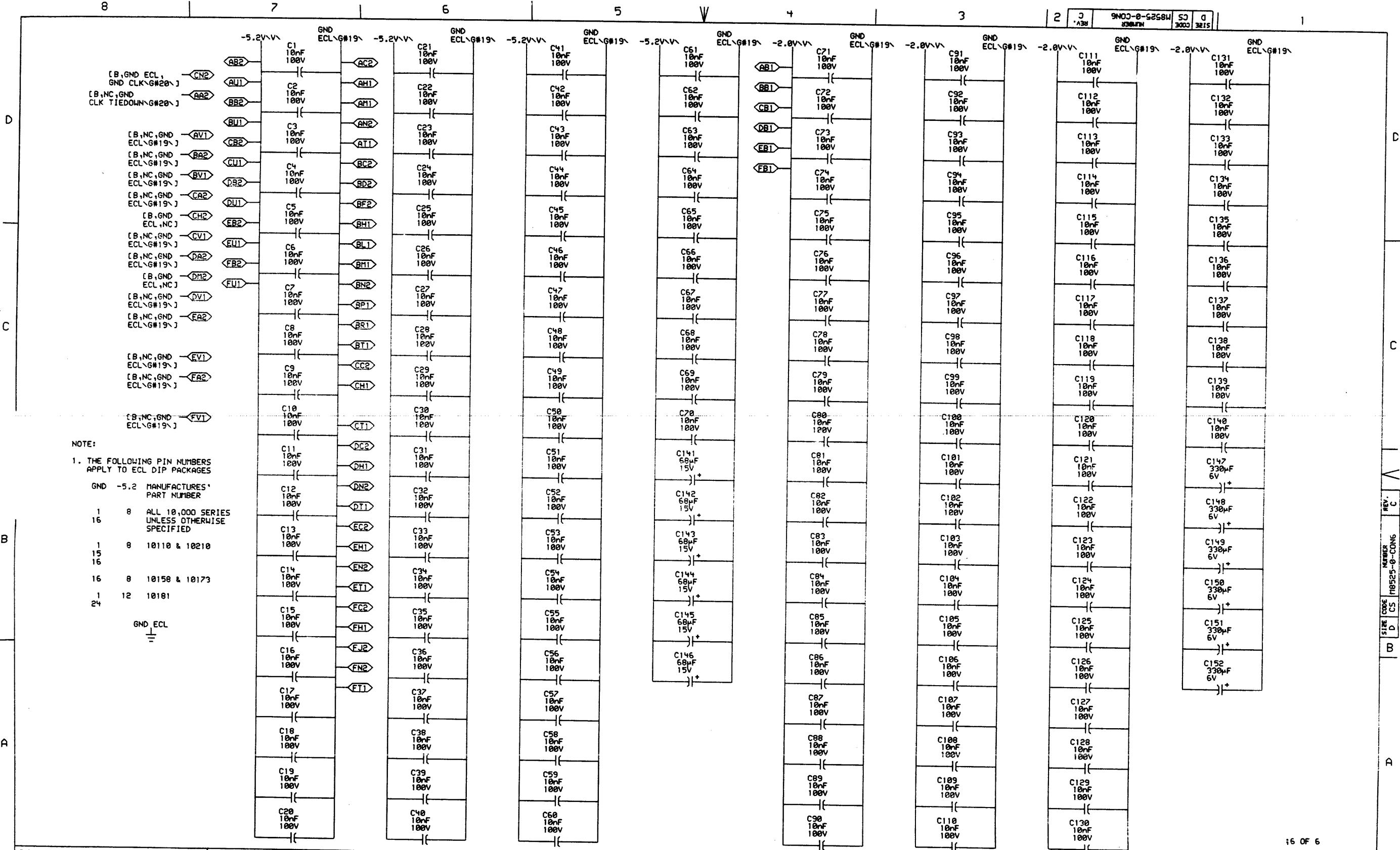


5 OF 6

SIZE CODE NUMBER REV.
D CS M8525-0-CONS C

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CHK	CHANGE NO.	REV		
10105-00006	C			

digital	DRW-00001	DATE 15-JUL-76	ENG. 113-A	DATE 14-JUL-76	TITLE: EBOX CONTROL #2
CHK-P	DATE 14-JUL-76	BOARD LOCATION: 4AF35			XFER, FM WRITE
CON5A.DRW(4,161)	14-JUL-76 11:30	SHEET 1 OF 1			
FIRST USED ON OPTION/MODEL: KL10	B-DD-M8546-0	SIZE CODE D CS NUMBER M8525-0-CONS REV. C			



16 OF 6

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CHK	CHANGE NO.	
M 8525-0000		
DATE 8-24-77		
B. BRUCKERT		

digital	DRW- 30-MAR-77 CHK'D CONSEA.DRWT(4.556)	DATE 30-MAR-77 3/27/77	ENG. on page 4AFC-35	DATE 1 APR 77 4APC-35	TITLE: EBOX CONTROL #2 POWER, GND, CAPS
FIRST USED ON OPTION/MODEL: KL10	NEXT HIGHER ASSEMBLY: 138-MAR-77 09:37	B-DD-M8525-0		D CS M8525-0-CON6	NUMBER REV. C

D	RESISTOR LOC(PIN)	SHOWN ON DRW#	REF	VALUE	TERMINATES SIGNAL	RESISTOR LOC(PIN)	SHOWN ON DRW#	REF	VALUE	TERMINATES SIGNAL	RESISTOR LOC(PIN)	SHOWN ON DRW#	REF	VALUE	TERMINATES SIGNAL	RESISTOR LOC(PIN)	SHOWN ON DRW#	REF	VALUE	TERMINATES SIGNAL
R182(1)	CON3 C3	68Ω		%E11(3)	R102(1)	CON4 A4	68Ω		%E67(2)	R7(1)	CON1 D7	68Ω		-CON1 SKIP EN 70-77 H	R51(1)	CON4 A3	68Ω		-CON4 AR FROM EBUS H	
R13(1)	CON2 B7	68Ω		%E12(15)	R53(1)	CON4 B3	68Ω		%E68(2)	R101(1)	CON2 D1	68Ω		CON2 CLK A H	R52(1)	CON4 A3	68Ω		-CON4 AR FROM MEM H	
R58(1)	CON3 B3	68Ω		%E15(18)	R5(1)	CON4 B3	68Ω		%E68(3)	R98(1)	CON2 D1	68Ω		CON2 CLK B H	R127(1)	CON4 A2	68Ω		CON4 AR LOADED H	
R59(1)	CON3 B3	68Ω		%E15(11)	R172(1)	CON5 B6	68Ω		%E7(7)	R139(1)	CON2 D1	68Ω		CON2 CLK C H	R46(1)	CON4 B6	68Ω		CON4 CSH BIT 36 H	
R62(1)	CON3 C3	68Ω		%E15(12)	R140(1)	CON4 A7	68Ω		APR FM 36 H	R41(1)	CON2 D1	68Ω		CON2 CLK D H	R100(1)	CON4 A6	68Ω		CON4 EBUS BIT 36 H	
R61(1)	CON3 C3	68Ω		%E15(13)	R121(1)	CON2 D2	68Ω		CLK CON H	R18(1)	CON2 D1	68Ω		CON2 CLK E H	R48(1)	CON4 A6	68Ω		CON4 FM BIT 36 H	
R108(1)	CON2 B7	68Ω		%E16(13)	R170(1)	CON3 C3	68Ω		-CLK EBOX SYNC A H	R108(1)	CON2 D1	68Ω		CON2 CLK F H	R7(1)	CON4 A6	68Ω		-CON4 FM DATA H	
R9(1)	CON2 B6	68Ω		%E16(14)	R134(1)	CON5 B6	68Ω		CLK MB XFER H	R8(1)	CON2 A7	68Ω		CON2 DIAG CLR RUN H	R28(1)	CON4 D6	68Ω		-CON4 INT DISABLE H	
R20(1)	CON2 B6	68Ω		%E16(15)	R50(1)	CON2 D4	68Ω		CLK PAGE ERROR H	R14(1)	CON2 B6	68Ω		-CON2 DIAG CONTINUE H	R55(1)	CON4 D6	68Ω		CON4 KERNEL CYCLE H	
R15(1)	CON2 C7	68Ω		%E16(2)	R166(1)	CON5 A6	68Ω		CLK SBR CALL H	R154(1)	CON2 A7	68Ω		CON2 DIAG DRAM STROBE H	R105(1)	CON4 A4	68Ω		-CON4 LOAD AR EN H	
R17(1)	CON2 C6	68Ω		%E16(3)	R171(1)	CON1 D7	68Ω		-CON1 COND EN 10-17 H	R153(1)	CON2 A7	68Ω		CON2 DIAG IR STROBE H	R45(1)	CON4 A6	68Ω		-CON4 MBOX DATA H	
R11(1)	CON2 C3	68Ω		%E15(15)	R119(1)	CON1 D7	68Ω		-CON1 COND EN 20-27 H	R3(1)	CON2 B6	68Ω		-CON2 DIAG SET RUN H	R77(1)	CON4 D6	68Ω		-CON4 PXCT H	
R16(1)	CON2 C7	68Ω		%E20(14)	R76(1)	CON1 D3	68Ω		-CON1 COND/EBOX STATE H	R63(1)	CON2 A2	68Ω		CON2 EBUS GRANT H	R80(1)	CON4 D3	68Ω		CON4 UCODE STATE 05 H	
R19(1)	CON2 C7	68Ω		%E20(15)	R30(1)	CON1 D6	68Ω		CON1 COND/LOAD IR H	R78(1)	CON2 C4	68Ω		-CON2 I/O LEGAL H	R103(1)	CON5 C3	68Ω		CON5 CLR PI CYCLE H	
R92(1)	CON3 D7	68Ω		%E23(14)	R22(1)	CON1 D3	68Ω		-CON1 COND/LONG EN H	R147(1)	CON2 C6	68Ω		-CON2 INSTR GO H	R152(1)	CON5 B3	68Ω		CON5 FETCH CYCLE H	
R42(1)	CON4 C3	68Ω		%E23(3)	R24(1)	CON1 D6	68Ω		CON1 COND/SPEC INSTR H	R81(1)	CON2 D6	68Ω		CON2 INT REQ H	R138(1)	CON5 C5	68Ω		-CON5 MEM CYCLE H	
R12(1)	CON2 C2	68Ω		%E27(15)	R168(1)	CON1 D4	68Ω		-CON1 COND/SPEC INSTR H	R79(1)	CON2 D6	68Ω		-CON2 INT REQ H	R44(1)	CON5 C6	68Ω		CON5 MEM CYCLE IN H	
R29(1)	CON2 C2	68Ω		%E28(15)	R151(1)	CON1 D6	68Ω		CON1 COND/SR# H	R91(1)	CON2 A5	68Ω		CON2 KERNEL MODE H	R83(1)	CON5 D5	68Ω		CON5 PI CYCLE H	
R114(1)	CON2 A3	68Ω		%E33(15)	R124(1)	CON1 D4	68Ω		-CON1 COND/SR# H	R71(1)	CON2 A5	68Ω		-CON2 KERNEL MODE H	R68(1)	CON5 D5	68Ω		-CON5 PI CYCLE H	
R117(1)	CON2 A3	68Ω		%E33(2)	R55(1)	CON1 D2	68Ω		CON1 COND/VMA DEC H	R82(1)	CON2 D7	68Ω		CON2 MTR INT REQ H	R178(1)	CON5 D6	68Ω		CON5 PI CYCLE IN H	
R118(1)	CON2 A3	68Ω		%E33(3)	R56(1)	CON1 D2	68Ω		CON1 COND/VMA INC H	R155(1)	CON2 B5	68Ω		CON2 NICOND OR LOAD IR DELAYED H	R93(1)	CON5 B6	68Ω		CON5 XFER H	
R21(1)	CON2 C6	68Ω		%E34(15)	R133(1)	CON1 B4	68Ω		CON1 DIAG 04 H	R64(1)	CON2 A2	68Ω		CON2 PI XFER H	R49(1)	CON5 B6	68Ω		-CON5 XFER H	
R26(1)	CON2 D7	68Ω		%E34(3)	R131(1)	CON1 B4	68Ω		CON1 DIAG 05 H	R129(1)	CON2 B5	68Ω		-CON2 RUN H	R167(1)	CON3 B2	68Ω		CRAM # 00 E H	
R126(1)	CON2 C5	68Ω		%E37(3)	R132(1)	CON1 B4	68Ω		CON1 DIAG 06 H	R74(1)	CON2 C6	68Ω		CON2 START H	R84(1)	CON4 D7	68Ω		CRAM # 01 E H	
R33(1)	CON2 C7	68Ω		%E37(6)	R128(1)	CON1 B4	68Ω		-CON1 DIAG READ H	R169(1)	CON3 B2	68Ω		-CON3 # 00 H	R37(1)	CON3 B6	68Ω		CRAM # 02 E H	
R120(1)	CON2 B7	68Ω		%E39(4)	R107(1)	CON1 B2	68Ω		CON1 EBUS 18 H	R173(1)	CON3 B2	68Ω		-CON3 # 02 H	R35C(1)	CON3 C6	68Ω		CRAM # 03 E H	
R144(1)	CON2 B7	68Ω		%E39(5)	R163(1)	CON1 B2	68Ω		-CON1 EBUS 18 H	R87(1)	CON3 A2	68Ω		-CON3 # 03 H	R34(1)	CON3 C6	68Ω		CRAM # 04 E H	
R125(1)	CON3 A5	68Ω		%E44(15)	R69(1)	CON1 B2	68Ω		CON1 EBUS 19 H	R88(1)	CON3 A2	68Ω		-CON3 # 04 H	R36(1)	CON3 C6	68Ω		CRAM # 05 E H	
R123(1)	CON3 A5	68Ω		%E44(2)	R161(1)	CON1 B2	68Ω		-CON1 EBUS 19 H	R89(1)	CON3 A2	68Ω		-CON3 # 05 H	R68(1)	CON3 C4	68Ω		CRAM # 06 E H	
R48(1)	CON4 D4	68Ω		%E45(15)	R115(1)	CON1 B2	68Ω		CON1 EBUS 20 H	R138(1)	CON3 A2	68Ω		-CON3 # 06 H	R164(1)	CON3 C4	68Ω		CRAM # 07 E H	
R43(1)	CON4 C4	68Ω		%E45(2)	R159(1)	CON1 B2	68Ω		-CON1 EBUS 20 H	R165(1)	CON3 C6	68Ω		-CON3 # FUNC 01X H	R66(1)	CON3 C4	68Ω		CRAM # 08 E H	
R38(1)	CON4 D4	68Ω		%E46(15)	R110(1)	CON1 B2	68Ω		CON1 EBUS 21 H	R162(1)	CON3 C6	68Ω		-CON3 # FUNC 02X H	R1(1)	CON1 D7	68Ω		CRAM COND 00 H	
R39(1)	CON4 D4	68Ω		%E46(2)	R160(1)	CON1 B2	68Ω		-CON1 EBUS 21 H	R145(1)	CON3 D2	68Ω		CON3 CACHE LOOK EN H	R2(1)	CON1 D7	68Ω		CRAM COND 01 H	
R65(1)	CON3 B4	68Ω		%E51(15)	R112(1)	CON1 B2	68Ω		CON1 EBUS 22 H	R73(1)	CON3 C3	68Ω		CON3 COND APR H	R6(1)	CON1 D7	68Ω		CRAM COND 02 H	
R98(1)	CON5 D6	68Ω		%E51(3)	R157(1)	CON1 B2	68Ω		-CON1 EBUS 22 H	R189(1)	CON3 C3	68Ω		-CON3 COND APR A H	R148(1)	CON1 D2	68Ω		CRAM COND 03 H	
R96(1)	CON5 C6	68Ω		%E53(3)	R156(1)	CON1 A2	68Ω		-CON1 EBUS 23 H	R111(1)	CON3 B3	68Ω		-CON3 COND PAG H	R149(1)	CON1 D2	68Ω		CRAM COND 04 H	
R177(1)	CON5 C3	68Ω		%E59(2)	R25(1)	CON1 A2	68Ω		CON1 NICOND H	R146(1)	CON3 D2	68Ω		CON3 KL18 PAGING EN H	R158(1)	CON1 D2	68Ω		CRAM COND 05 H	
R97(1)	CON5 C6	68Ω		%E51(14)	R5(1)	CON1 A2	68Ω		CON1 RESET H	R189(1)	CON3 D4	68Ω		CON3 WR EVEN PAR DATA H	R135(1)	CON5 B3	68Ω		-CRAM MEM 02 A H	
R186(1)	CON4 A3	68Ω		%E61(2)	R75(1)	CON1 A2	68Ω		-CON1 RESET H	R99(1)	CON3 D4	68Ω		-CON3 WR EVEN PAR DATA H	R137(1)	CON4 B7	68Ω		CSH PAR BIT A H	
R143(1)	CON4 B4	68Ω		%E67(14)	94(1)	CON1 D7	68Ω		-CON1 SKIP EN 60-67 H	R122(1)	CON3 D4	68Ω		CON3 WR EVEN PAR DIR H	R136(1)	CON4 A7	68Ω		CSH PAR BIT B H	

NOTE:

1. ALL TERMINATORS HAVE PIN TWO CONNECTED TO -2.0V AND ARE 5% 1/4WATT UNLESS OTHERWISE SPECIFIED
2. ENTRIES ARE SORTED BY SIGNAL NAME
3. % INDICATES OUTPUT OF DIP LOC AND <> INDICATES PIN NUMBER

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REVISIONS		
CHK	CHANGE NO.	REV.

RESISTOR LOC(PIN)	SHOWN ON DRW#	VALUE REF	TERMINATES SIGNAL
R142(1)	CON4	B4	68Ω -CTL EBUS XFER H
R85(1)	CON2	C7	68Ω -CTL SPEC/FLAG CTL H
R158(1)	CON2	A7	68Ω -DIAG CONTROL FUNC B1X H
R176(1)	CON4	B5	68Ω EBUS PARITY ACTIVE E H
R141(1)	CON4	A7	68Ω EBUS PARITY E H
R94(1)	CON2	C5	68Ω IR I/O LEGAL H
R67(1)	CON2	B3	68Ω -MB21 RD-PSE-WR REF H
R57(1)	CON1	A4	68Ω MCL LOAD VMA H
R27(1)	CON5	C6	68Ω MCL MBOX CYC REQ H
R104(1)	CON5	D6	68Ω MCL SKIP SATISFIED H
R174(1)	CON5	A6	68Ω -MCL STORE AR H
R72(1)	CON5	B3	68Ω -MCL VMA FETCH H
R31(1)	CON2	D7	68Ω MTR INTERRUPT REQ H
R113(1)	CON2	A2	68Ω PI2 EXT TRAN REC H
R32(1)	CON2	D7	68Ω PI2 READY H
R116(1)	CON2	A2	68Ω PI5 EBUS CP GRANT H
R70(1)	CON2	B3	68Ω -SCD PUBLIC A H
R86(1)	CON2	C5	68Ω -SCD USER IOT A H
R175(1)	CON5	A6	68Ω -VMA AC REF H
R23(1)	CON2	D2	68Ω [A,CRM HI 00 H, -MCL VMA SECTION 0 L]

NOTE:

1. ALL TERMINATORS HAVE PIN TWO CONNECTED TO -2.0V AND ARE 5% 1/4WATT UNLESS OTHERWISE SPECIFIED
2. ENTRIES ARE SORTED BY SIGNAL NAME
3. % INDICATES OUTPUT OF DIP LOC AND () INDICATES PIN NUMBER

digital	DRN. P. Smith	DATE 29-Mar-77	ENG. in charge	DATE 1 Apr-77	TITLE: EBOX CONTROL #2
3/20/77	DATE 29-Mar-77	BOARD LOCATIONS	3/20/77	SHEET 2 OF 2	TERMINATORS
R85252.DRW[4.556]	29-Mar-77 09:43	NEXT HIGHER ASSEMBLY:			SIZE D CODE CS NUMBER M8525-0-RES REV. E
FIRST USED ON OPTION/MODEL: KL10 B-DD-M8525-0					

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