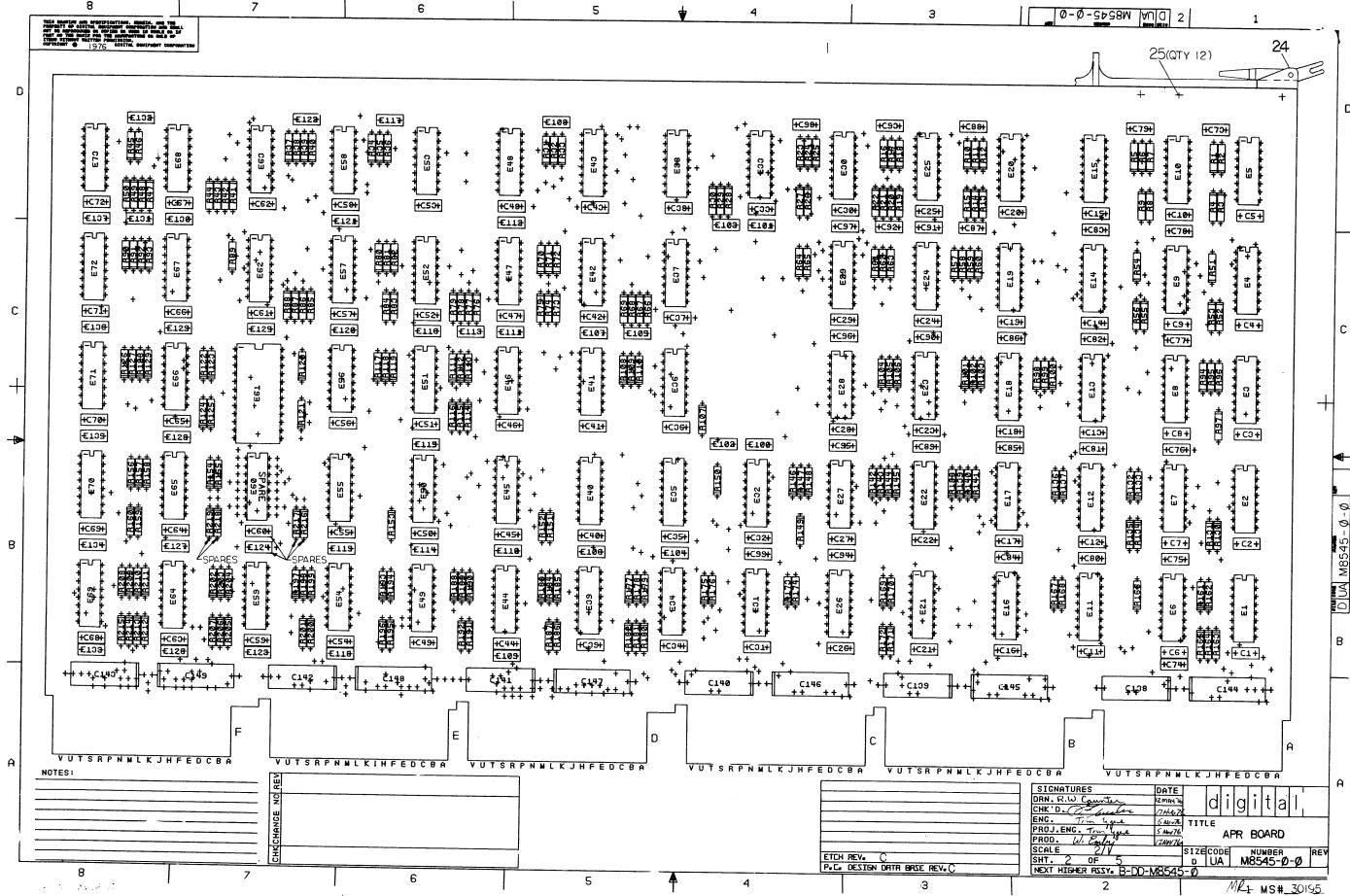
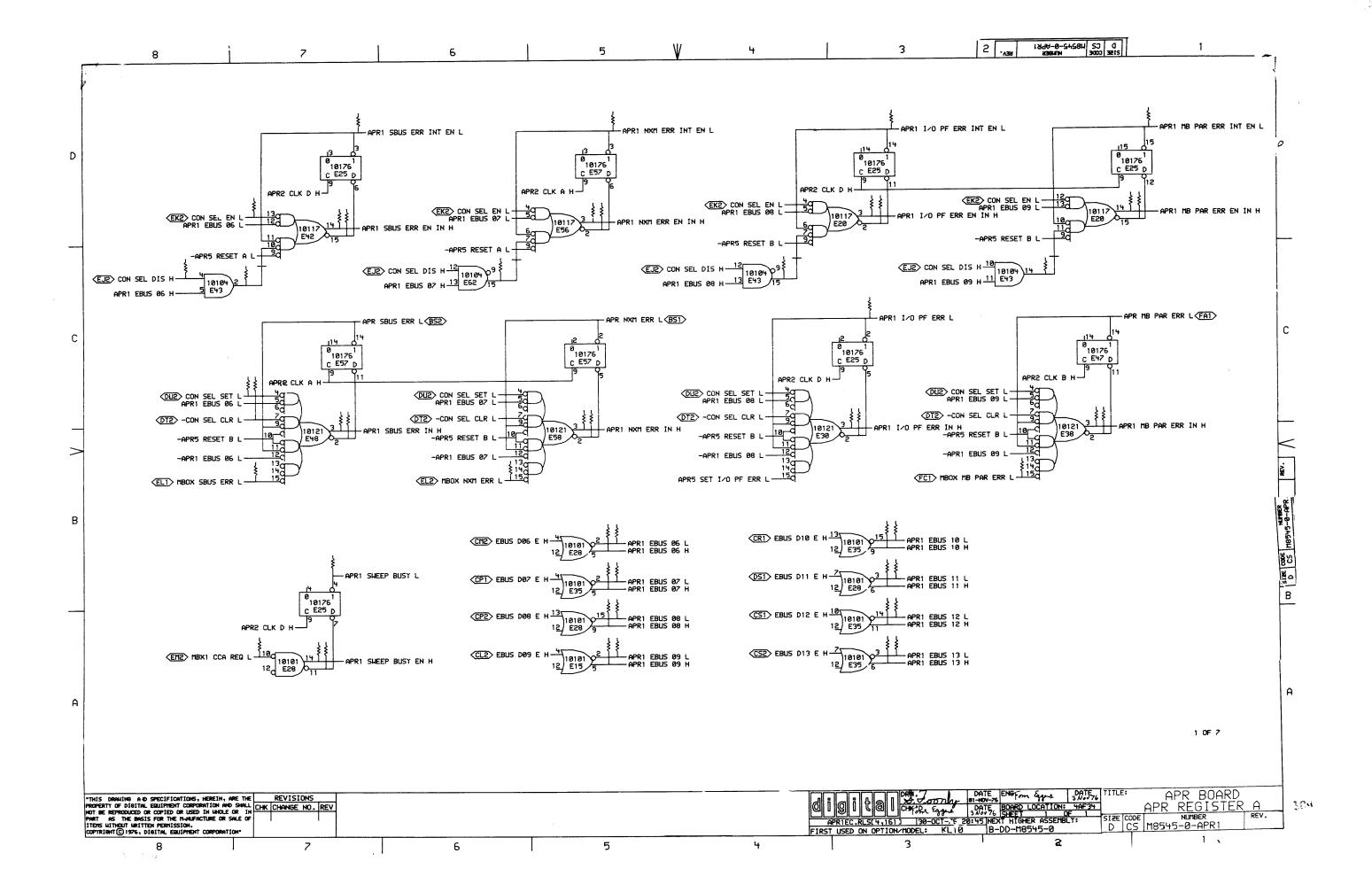
NUMBER DRAWING NO. OF PART NO. DESCRIPTION REVISIONS MODULE REVISION D-UA-M8545-Ø-Ø APR BOARD D-CS-M8545-Ø-APR1 APR BOARD APR REGISTER A D-CS-M8545-Ø-APR2 APR BOARD APR REGISTER B D-CS-M8545-Ø-APR3 APR BOARD I/O CONTROL D-CS-M8545-Ø-APR4 APR BOARD FAST MEM ADDRESS D-CS-M8545-Ø-APR5 APR BOARD FAST MEM BLOCK D-CS-M8545-Ø-APR6 APR BOARD DIAGNOSTICS D-CS-M8545-Ø-APR7 1 APR BOARD POWER, GND, CAPS D-CS-M8545-Ø-RES APR BOARD TERMINATORS D-AH-M8545-Ø-5 4 APR BOARD 5011890 ETCHED CIRCUIT BOARD M8545-Ø-L P.C. DESIGN DATA BASE REF M8545-Ø-PL INSERTION P/L DATA BASE POO-M8545-ØØ PROCESS SHEETS NOTES: 9 2 CHG DATE "THIS DRAWING AND SPECIFICATIONS, HEREIN, ARE THE PROPERTY OF DIGITAL EQUIPMENT CORPORATION AND SHALL NOT BE REPRODUCED OR COPIED OR USED IN WHOLE OR IN PART AS THE BASIS FOR THE MANUFACTURE OR SALE OF ITEMS WITHOUT WRITTEN PERMISSION.

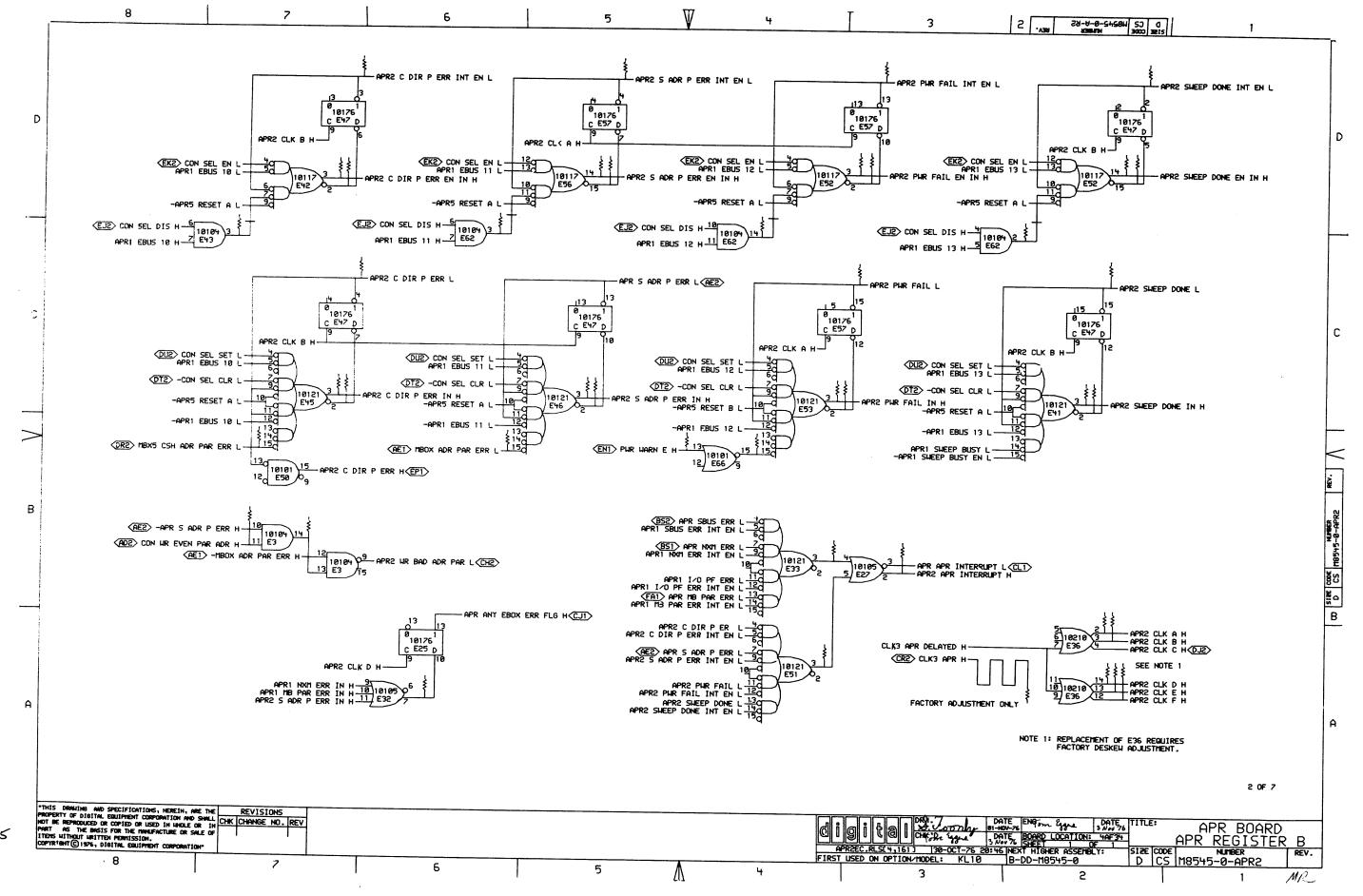
COPYRIGHT © 1976DIGITAL EQUIPMENT CORPORATION" 10/10175 TITLE USED ON OPTION/MODEL KLlØ 12 No v 76 APR MODULE

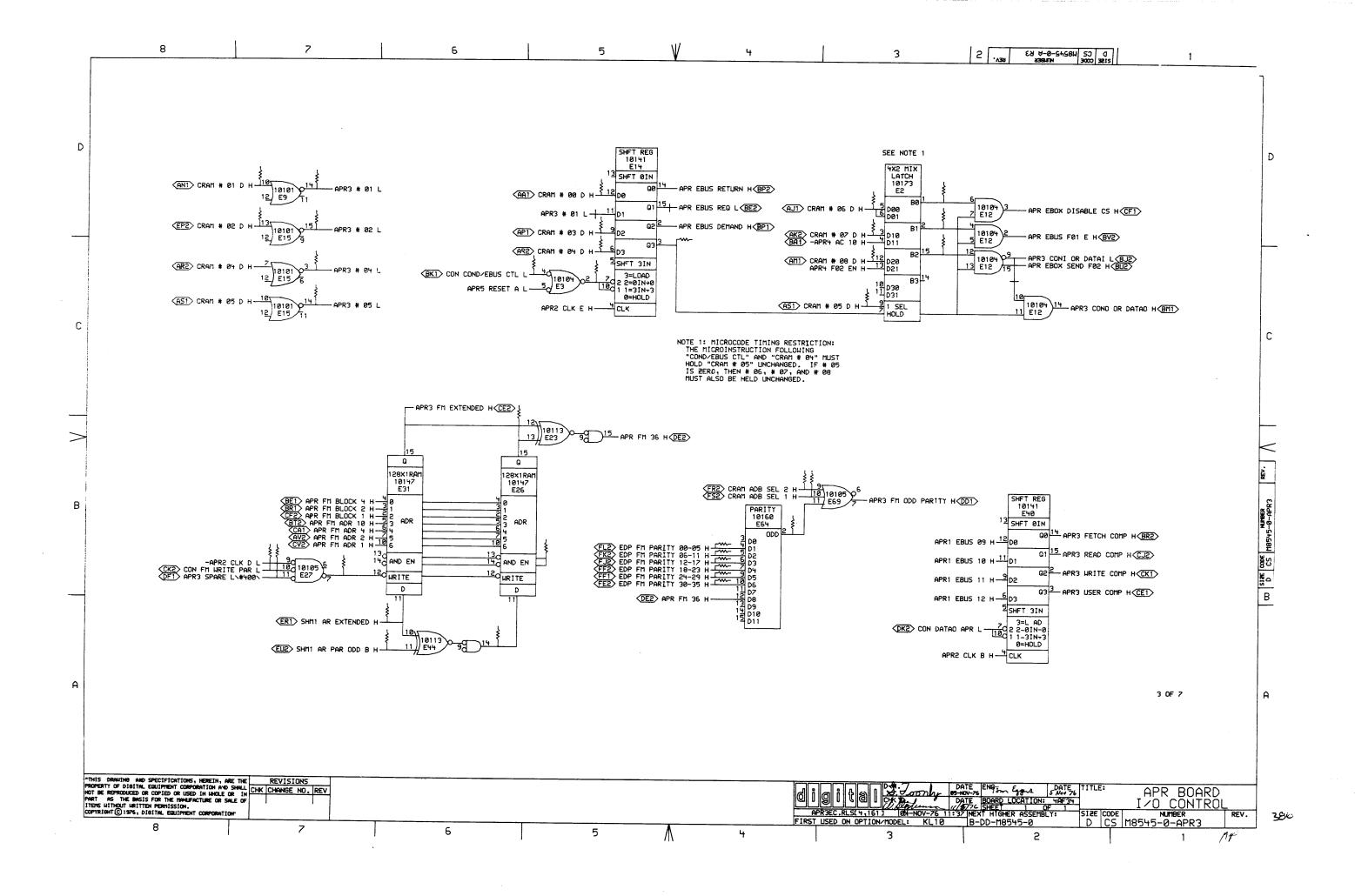
12 No v 76 B DD M8545-Ø NUMBER REV. 1240176 SHEET 1 OF 1 382 DRB 126 EN-01149-16-N176(325)

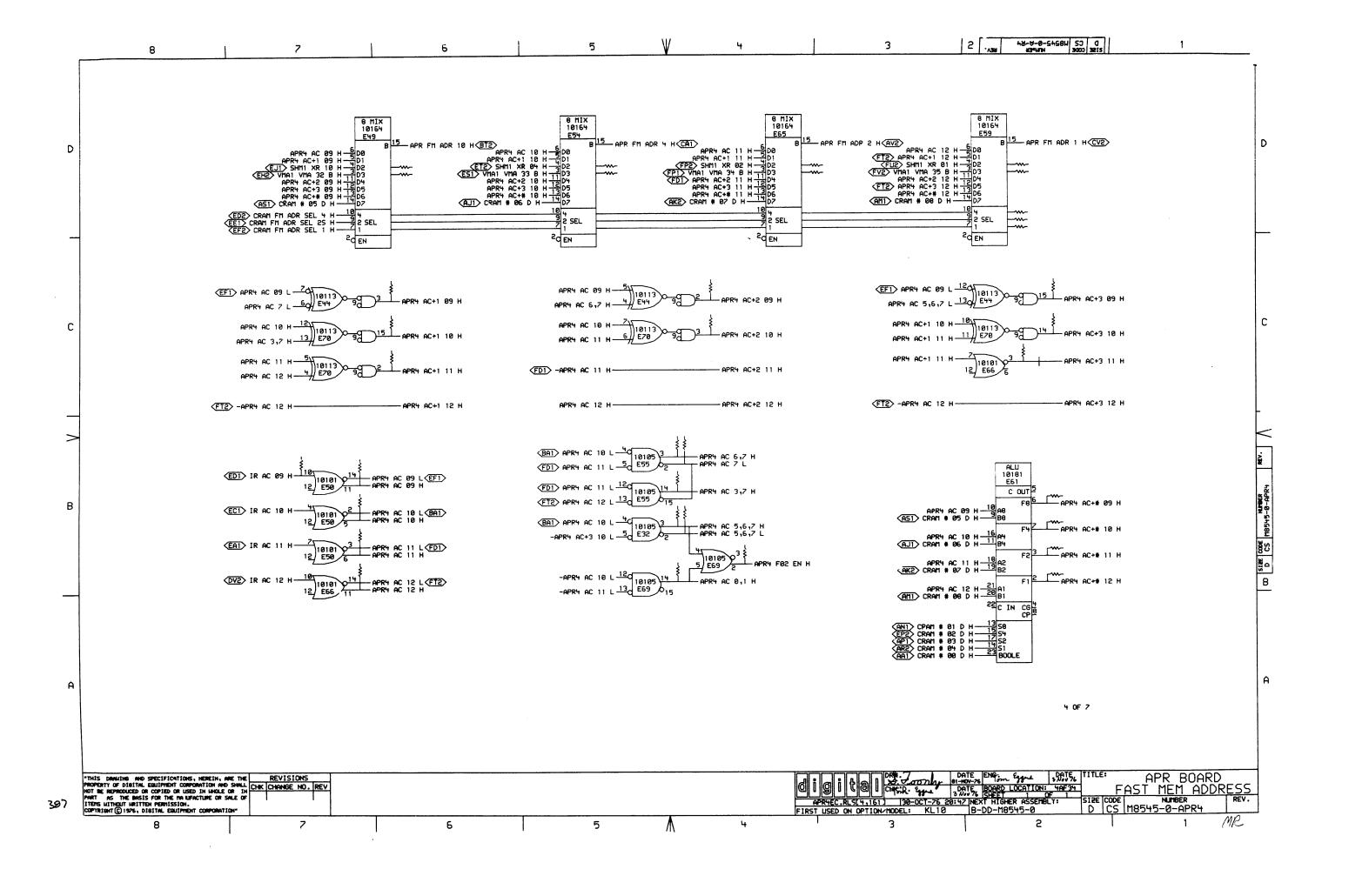
Ø-S†58W

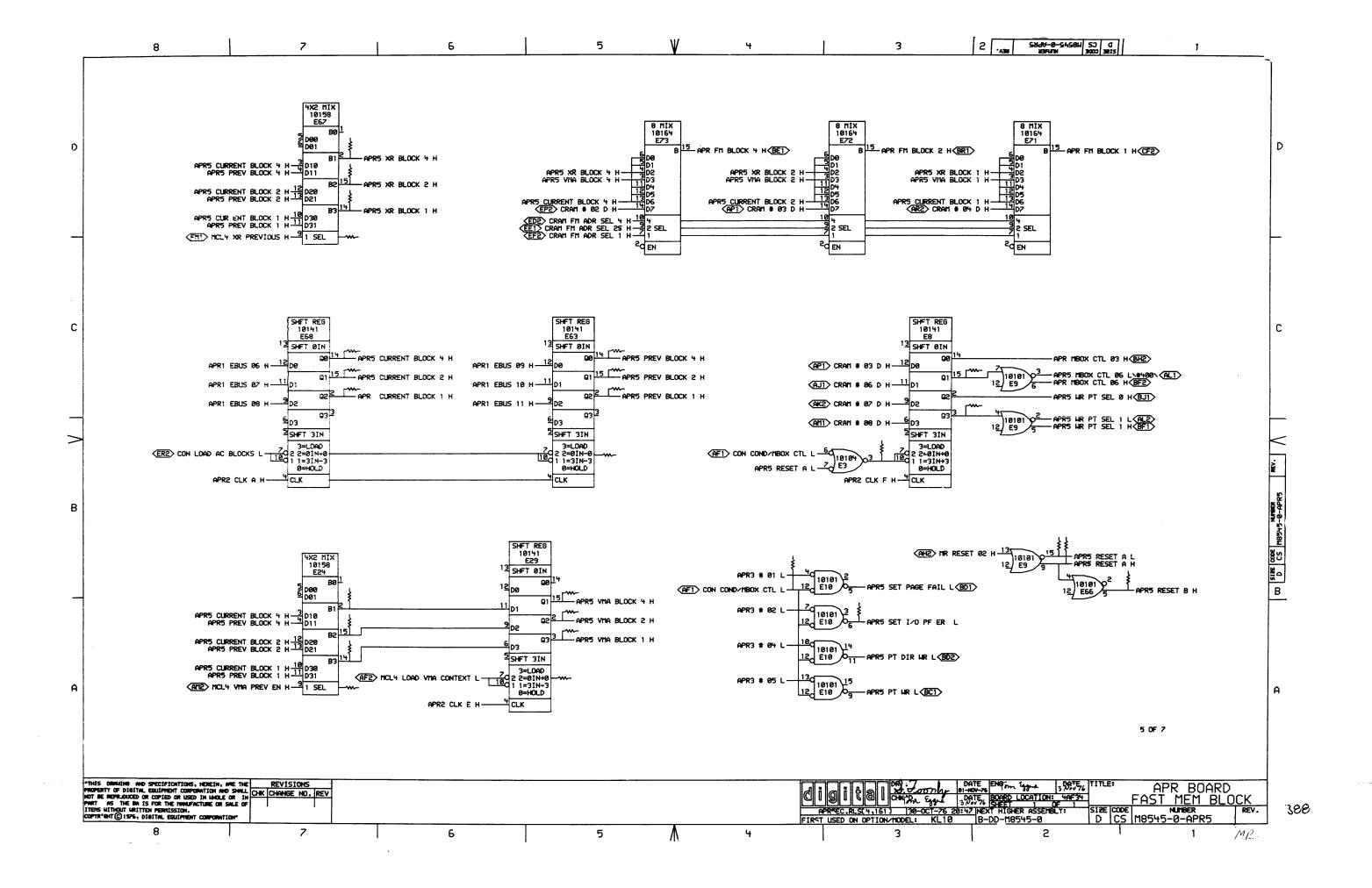


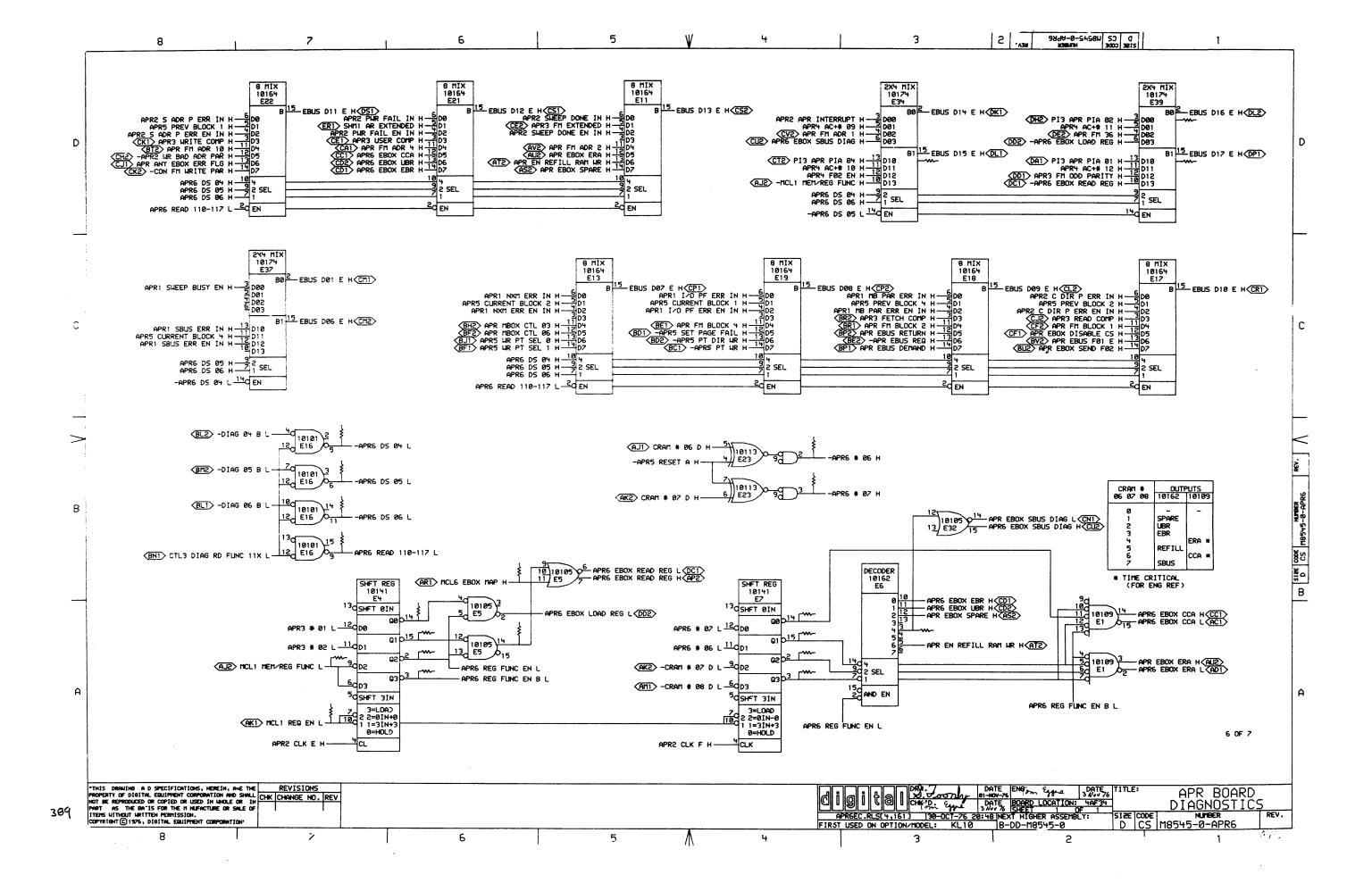


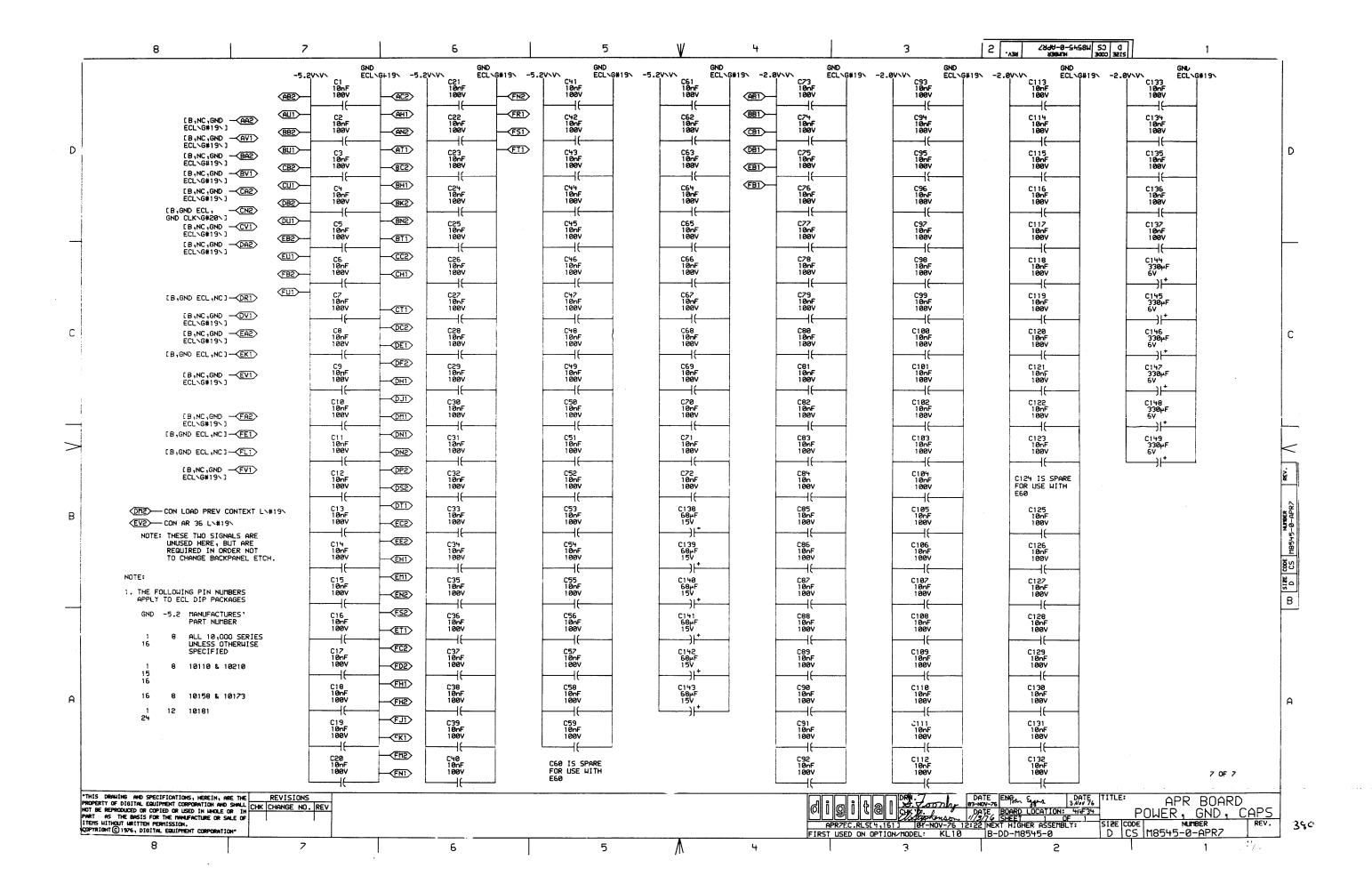












2 | . D CZ US2+2-6-KEZ SHOUN ON DRUG REF TERMINATES SIGNAL RESISTOR SHOUN ON LOC(PIN) DRH# REF PECICION SHOWN ON DRIVE REF RESISTOR TERMINATES VALUE VALUE TERMINATES RESISTOR SHOWN ON LOC(PIN) DRW# REF LOC(PIN) VALUE TERMINATES LOC(PIN) APR3 D4 R130(1) %E14(3) APR6 R165(1) %E7(2) R112(1) 9565 DS 686 -APR2 SHEEP DONE INT EN H R9(1) APR5 84 680 -CON COND/MBOX CTL H R136(1) D3 68Ω %E2(1) R163(1) APR6 A4 %E7(3) R7(1) APR3 DZ 68α -APR3 # 81 H R143(1) 87 68s -CON FM WRITE PAR H R132(1) APR3 D3 68Ω %E2(15) R55(1) APR5 C3 68 %E8(15) R53(1) APR3 D7 68₀ -APR3 # 82 H R47(1) APR5 85 680 -CON LOAD AC BLOCKS H R137(1) PDB3 D3 680 XE2(2) R54(1) APR5 C3 680 D %E8(3) R8(1) APR3 C7 -APR3 # 04 H R26(1) **600**1 C7 680 CON SEL CLR H R17(1) APR1 D1 680 2F29(15) R44(1) APR1 85 689 APR1 EBUS 06 H R6(1) APR3 C7 686 -APR3 # 05 H R33(1) CR APR 1 680 CON SEL DIS H R10(1) APR1 D3 68Ω %E20(2) APR1 R32(1) 85 680 -APR1 EBUS 06 H R149(1) APR3 B2 680 APR3 SPARE LN#4001 R11(1) APR1 D7 680 -CON SEL EN H R65(1) A7 APR5 689 XE24(14) R39(1) APR1 85 680 APR1 EBUS 07 H R214(1) APR4 680 84 APR4 AC 0.1 H R25(1) C7 68Ω -CON SEL SET H R62(1) ∆PR5 A7 680 %E24(15) R49(1) APR1 **B**5 680 -APR1 EBUS 07 H R125(1) APR4 82 APR4 AC 09 H R97(1) APR2 87 689 CON HR EVEN PAR ADR H A7 R61(1) APR5 680 2F24(2) APR1 R43(1) 85 689 APR1 EBUS 08 H R169(1) APR4 82 680 APR4 AC 18 H R129(1) PB53 D5 680 CRAM # 99 D H R174(1) 85 68Ω APR3 %E26(13) R12(1) APR1 85 680 -APR1 EBUS 08 H R208(1) APR4 87 68Ω APR4 AC 11 H R121(1) APR3 D7 680 CRAM # 81 D H R105(1) 86 680 XE26(15) R37(1) APR1 A5 68 APR1 EBUS 09 H R159(1) APR4 87 680 APR4 AC 12 H R5(1) APR3 D7 68a CROM # 82 D H R173(1) APR3 87 68Ω %E27(7) R39(1) APP1 A5 680 -APR1 EBUS 09 H R156(1) APR4 84 68Ω APR4 AC 3.7 H R93(1) D5 APR3 689 CRAM # 03 D H AZ APR1 R19(1) 680 %E29(11) R38(1) APR1 **B3** 686 APR1 EBUS 10 H R209(1) APR4 84 APR4 AC 5,6,7 H R126(1) APR3 05 68Ω CRAM # 84 D H R95(1) APR2 B7 68Ω 2E3(14) R72(1) APR1 686 **B3** -APR1 EBUS 10 H R183(1) APR4 84 -APR4 AC 5.6.7 I R124(1) APR3 C3 68a CRAM # 05 D H C5 R56(1) APR3 680 %E3(2) R41(1) APR1 **B3** 68: APR1 EBUS 11 H R190(1) APR4 84 680 APR4 AC 6.7 H R198(1) APR3 D3 680 CRAM # R6 D H R94(1) APR5 **B3** 68n %E3(3) R119(1) APR1 B3 686 -APR1 EBUS 11 H R191(1) APR4 84 680 -APR4 AC 7 H R155(1) APR3 **D3** 68Ω CRAM # 87 D H R18(1) APR1 C3 680 %E30(2) R88(1) APR1 **B3** 686 APR1 EBUS 12 H R179(1) **B**2 680 APR4 AC+# 09 H R201(1) D3 APR3 689 CRAM # 88 D H R20(1) APR2 A6 68o %E32(7) R36(1) APR1 **B**3 680 -APR1 FRUS 12 H R189(1) APR4 B2 689 APR4 AC+# 10 H R215(1) PD53 84 68a CRAM ADB SEL 1 H R147(1) 84 680 %E33(3) R89(1) APR1 A3 APR1 EBUS 13 H R185(1) **APPU** 82 APR4 AC+# 11 H 680 R213(1) APR3 B4 680 CRAM ADB SEL 2 H C R71(1) APR1 CS 68Ω %E38(2) R79(1) APR1 **A3** -APR1 EBUS 13 H R187(1) APR4 82 680 APR4 AC+# 12 H R49(1) D2 680 CRAM FM ADR SEL 1 H R1(1) APR6 96 680 %E4(14) R27(1) APR1 C3 684 -APRI I/O PF ERR H R194(1) APR4 C6 680 APR4 AC+1 89 H R48(1) D2 680 CRAM FM ADR SEL 25 H R2(1) APR6 A6 680 2E4(15) R59(1) APR1 D3 680 APRI I/O PF ERR EN IN H R199(1) APR4 C6 APR4 AC+1 10 H R58(1) DS 68Ω CRAM FM ADR SEL 4 H R79(1) APR2 cs 68Ω %E41(2) R58(1) APR1 C3 680 APRI I/O PE ERR IN H R129(1) APR4 C6 APR4 AC+1 11 H R202(1) APR3 84 680 EDP FM PARITY 00-05 H R22(1) **D**7 APR1 %E42(15) R23(1) APR 1 D3 686 -APR1 I/O PF ERR INT EN H R189(1) APR4 C4 680 APR4 AC+2 89 H R211(1) EDP FM PARITY 06-11 H APR3 84 680 R74(1) APR2 D7 680 %E42(2) R102(1) APR1 D1 689 APRI MB PAR ERR EN IN H R196(1) APR4 C4 680 APR4 AC+2 10 H R212(1) 84 68n EDP FM PARITY 12-17 H R15(1) APR 1 C2 680 2E43(14) R101(1) APR1 C2 680 APRI MB PAR ERR IN H R188(1) CS APR4 AC+3 R9 H R206(1) EDP FM PARITY 18-23 H R13(1) APR1 68s C4 Æ43(15) R24(1) APR1 D1 680 -APR1 MB PAR ERR INT EN H R150(1) APR4 C5 APR4 AC+3 18 H R205(1) 84 68a EDP FM PARITY 24-29 H R69(1) APR 1 C7 680 %E43(2) APR 1 05 68Ω APRI NXTI ERR EN IN H R154(1) APR4 C2 680 APR4 AC+3 11 H R207(1) EDP FM PARITY 30-35 H APR3 84 680 R73(1) APR2 C7 680 %E43(3) R98(1) APR1 C5 686 APRINXMERR IN H R131(1) APR4 84 680 APR4 F02 EN H R153(1) **B**7 68a IR AC 09 H R172(1) 9663 A6 680 %E44(14) R28(1) APR1 05 689 -APRI NXM ERR INT EN H R60(1) APR5 C7 680 APR5 CURRENT BLOCK 1 H R113(1) -MBOX ADR PAR ERR H R76(1) APR2 C7 680 2F45(2) R66(1) APR1 D7 680 APRI SBUS ERR EN IN H R100(1) APR5 C7 68Ω APR5 CURRENT BLOCK 2 H R29(1) APR 1 82 68Ω -MBOX MB PAR ERR H EV. R77(1) C5 68s %E46(2) R62(1) APR1 C7 68a APRI SBUS ERR IN H R63(1) APR5 C7 APR5 CURRENT BLOCK 4 H R34(1) APR 1 86 680 -MBOX NXM ERR H R81(1) APR1 **C7** 68 %E48(2) R68(1) APR1 07 680 -APR1 SBUS ERR INT EN H R145(1) APR5 C5 68₀ APR5 PREV BLOCK 1 H R31(1) 82 APR1 680 -MBOX SBUS ERR H R3(1) **APRE** A6 680 %E5(14) R189(1) APR1 87 68Ω -APR1 SHEEP BUSY H R141(1) APR5 C5 680 APR5 PREV BLOCK 2 H R104(1) APR1 A7 68a -MBX1 CCA REQ H R148(1) 680 APR2 44 %E51(3) В A7 R110(1) APR1 680 APRI SHEEP BUSY EN H R103(1) APR5 C5 680 APRS PREV BLOCK 4 H CO DE CODE MARKET R151(1) APR2 87 -M3X5 CSH ADR PAR ERR H R78(1) DS 68n %E52(15) R178(1) APR2 B3 680 APR2 APR INTERRIPT H R123(1) APR5 82 APR5 RESET A H R125(1) **APPE A**7 680 -MCL1 MEM/REG FUNC H R84(1) D3 680 %E52(2) R152(1) APR2 C6 689 -APR2 C DIR P ERR H R106(1) APR5 82 -APR5 RESET A H R52(1) APR6 97 680 -MCL1 REG EN H RRR(1) APR2 C3 689 %E53(2) R140(1) APR2 D7 APR2 C DIR P ERR EN IN H R14(1) APR5 B1 68₂ APR5 RESET B H R64(1) APR5 A5 680 -MCL4 LOOD VMG CONTEXT H R85(1) APR2 05 680 %E56(15) R138(1) APR2 CZ 680 APR2 C DIR P ERR IN H R16(1) APR5 A3 680 -APR5 SET I/O PF ERR I R57(1) APR5 A7 68Ω MCL4 VMA PREV EN H R86(1) APR 1 D5 68a %F56(2) R116(1) APR2 D6 680 -APR2 C DIR P ERR INT EN H R128(1) APR⁴5 A5 APR5 VMA BLOCK 1 H R91(1) APR5 C7 68a MCL4 XR PREVIOUS H R87(1) C5 68Ω %E58(2) R42(1) APR2 68Ω 82 APR2 CLK A H R92(1) APR5 85 APR5 VMA BLOCK 2 H R4(1) APR6 86 680 MCL6 EBOX MAP H R146(1) APR6 A3 %E6(3) R75(1) APR2 A2 680 APR2 CLK B H R45(1) APR5 85 680 APR5 VMA BLOCK 4 i R182(1) APR6 Dt 680 PI3 APR PIA 01 H R83(1) APR2 C4 689 %E62(14) R21(1) APR2 A2 68Ω APR2 CLK D H R127(1) D2 APR5 680 APR5 XR BLOCK 1 H R184(1) APR6 D1 PI3 APR PIA 82 H 68o R118(1) APR1 C6 680 %E62(15) R51(1) APR2 82 680 APR2 CLK E H APR5 D7 R99(1) 680 APR5 XR BLOCK 2 H R176(1) D3 PI3 APR PIA 84 H R82(1) CS 68Ω %E62(2) R133(1) APR2 92 680 APR2 CIKE H R46(1) APR5 D2 APR5 XR BLOCK 4 H R122(1) APR2 84 68a PUR HARN E H R117(1) C6 APR2 %E62(3) R115(1) APR2 C3 68a -APR2 PWR FAIL H R134(1) APR6 84 -APR6 # 86 H R179(1) APR3 A6 680 SHM1 AR EXTENDED H R210(1) APR3 84 680 %E64(2) R169(1) APR2 D3 APR2 PHR FAIL EN IN H R135(1) APR6 84 680 -APR6 # 07 H R192(1) APR3 86 680 SHM1 AR PAR ODD R H R35(1) APR2 84 68Ω %E66(15) R121(1) 4000 C4 APR2 PHR FAIL IN H APR6 R181(1) B7 68a APR6 DS 84 H R204(1) 02 680 SH 11 XR 01 H R164(1) APR6 A4 68Ω %E7(14) R111(1) APR2 D3 680 -APR2 PUR FAIL INT EN H R127(1) APR6 87 APR6 DS 05 H 68Ω R158(1) APR4 D4 SHM1 XR 02 H R162(1) APR6 A4 68α %E7(15) R144(1) APR2 D5 APR2 S ADR P ERR EN IN H R186(1) APR6 87 APR6 DS 86 H Α R197(1) APR4 05 680 SHM1 XR 04 H R142(1) APR2 C5 APR2 S ADR P ERR IN H R139(1) APR6 87 -APR6 READ 118-117 H R195(1) D6 APR4 680 SHM1 XR 18 H NOTE: R114(1) APR2 D5 -APR2 S ADR P ERR INT EN H R166(1) APPE -APR6 REG FUNC EN H A6 R193(1) APR4 D6 68o VMA1 VMA 32 B H 1. ALL TERMINATORS HAVE PIN THO CONNECTED 10 -2.0V AND R108(1) APR2 CS 680 -APR2 SHEEP DONE H R161(1) APR6 A6 68Ω -APR6 REG FLINC EN B H R200(1) 05 68Ω VMA1 VMA 33 B H ARE 5% 1/4HATT UNLESS OTHERHISE SPECIFIED
2. ENTRIES ARE SORTED BY SIGNAL NAME
3. % INDICATES OUTPUT OF DIP LOC AND R168(1) APR2 D2 680 APR2 SHEEP DONE EN IN R107(1) APR2 82 CLK3 APR H R157(1) APR4 D4 68Ω VM-1 VMA 34 B H R167(1) APR2 C2 68Ω APR2 SHEEP DONE IN H R96(1) APR3 C5 -CON COND/EBUS CTL H 02 () INDICATES PIN NUMBER R203(1) APR4 68a VMA1 VMA 35 B H "THIS DRAWING AND SPECIFICATIONS, MEREIN, ARE THE PROPERTY OF DIGITAL EQUIPMENT COMPORATION AND SHALL CHK CHANGE NO. REVINOT BE REPRODUCED OR COPIED OR USED IN MIGUE OR IN PART AS THE BASIS FOR THE HAMIFACTURE OR SALE OF ITEMS WITHOUT METTERN PERMISSION.

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