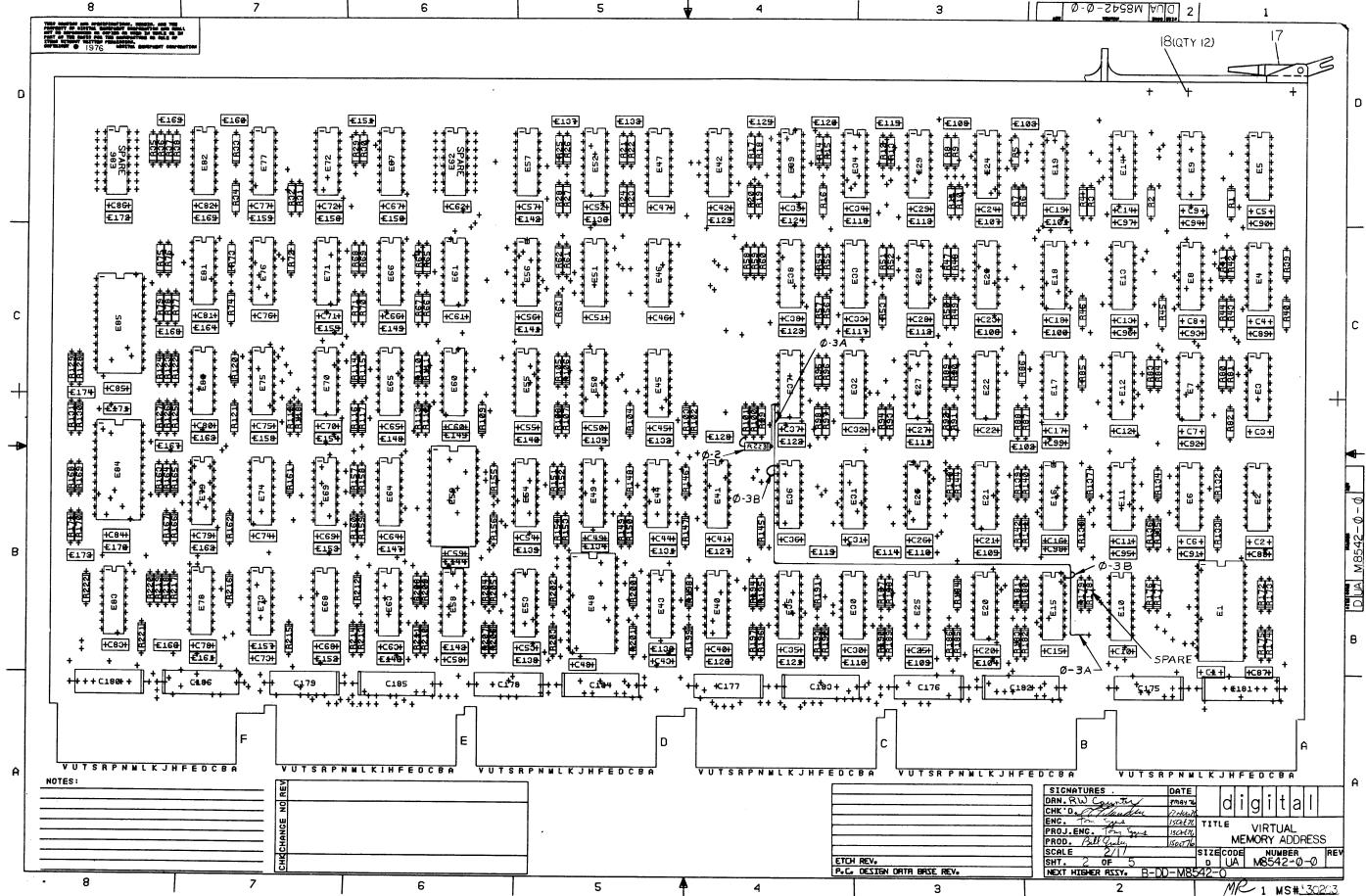
W8242-0 NOMBER DRAWING NO. SHTS PART NO. DESCRIPTION REVISIONS A MODULE REVISION 5 D-UA-M8542-Ø-Ø VIRTUAL MEMORY ADDRESS VMA BOARD ADDER & CONTROL D-CS-M8542-Ø-VMA1 1 D-CS-M8542-Ø-VMA2 VMA BOARD VMA REGISTER D-CS-M8542-Ø-VMA3 1 VMA BOARD PC & ADR BRK REG. D-CS-M8542-Ø-VMA4 VMA BOARD VMA HELD REG. D-CS-M8542-Ø-VMA5 VMA BOARD DIAGNOSTICS D-CS-M8542-Ø-VMA6 VMA BOARD POWER, GND, CAPS D-CS-M8542-Ø-RES 2 VMA BOARD TERMINATORS D-AH-M8542-Ø-5 VIRTUAL MEMORY ADDRESS 5011888 ETCHED CIRCUIT BOARD В M8542-Ø-L P/C DESIGN DATA BASE REP M8542-Ø-PL INSERTION P/L DATA BASE REF REF POO-M8542-ØØ PROCESS SHEETS NOTES: REVISIONS

DATE CHG NO. ÖN TITLE USED ON OPTION/MODEL "THIS DRAWING AND SPECIFICATIONS, HEREIN, ARE THE PROPERTY OF DIGITAL EQUIPMENT CORPORATION AND SHALL NOT BE REPRODUCED OR COPIED OR USED IN WHOLE OR IN PART AS THE BASIS FOR THE MANUFACTURE OR SALE OF ITEMS WITHOUT WRITTEN PERMISSION.

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Total FN 16x => [UB537] EBUS Reg E(4:8), E(34:35) always

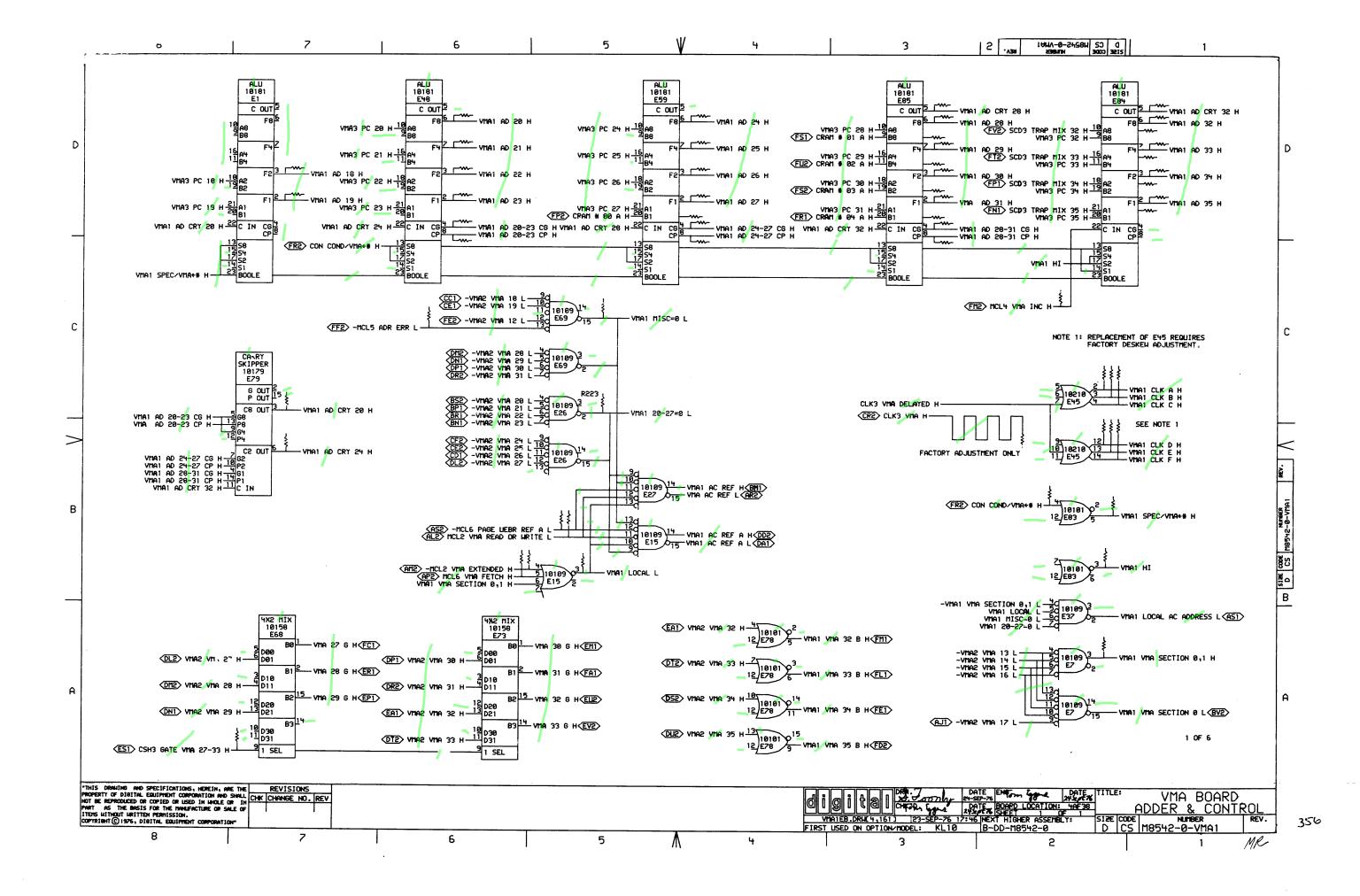
E(34:35):= PMA(34:35)

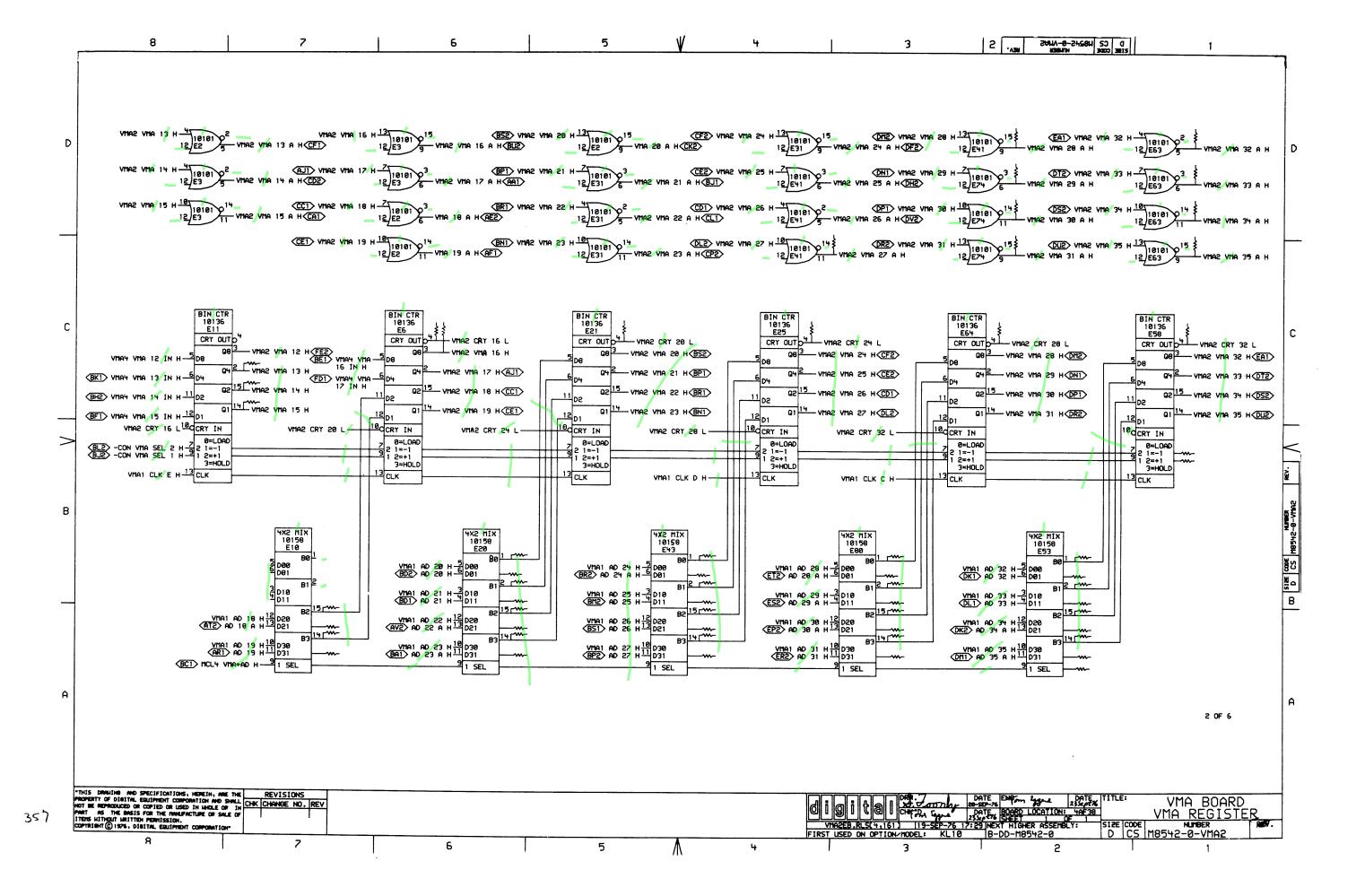
MBS78 PHA IF EBOX REQ GRANT (6) PMACES = VMAZ VMASS

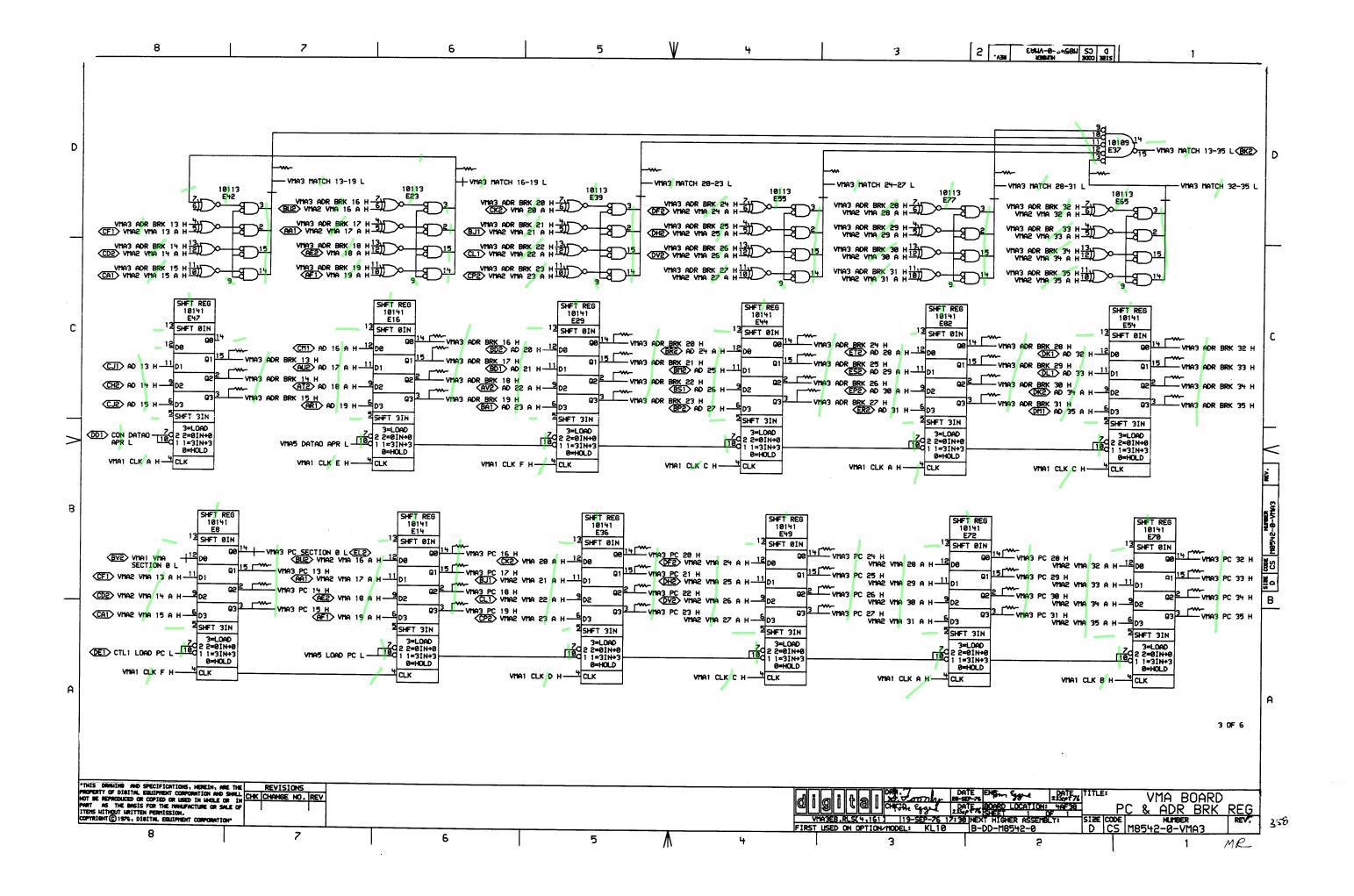
IS BBOX BRA GRANT (D) PMACES = PHAM BRASS

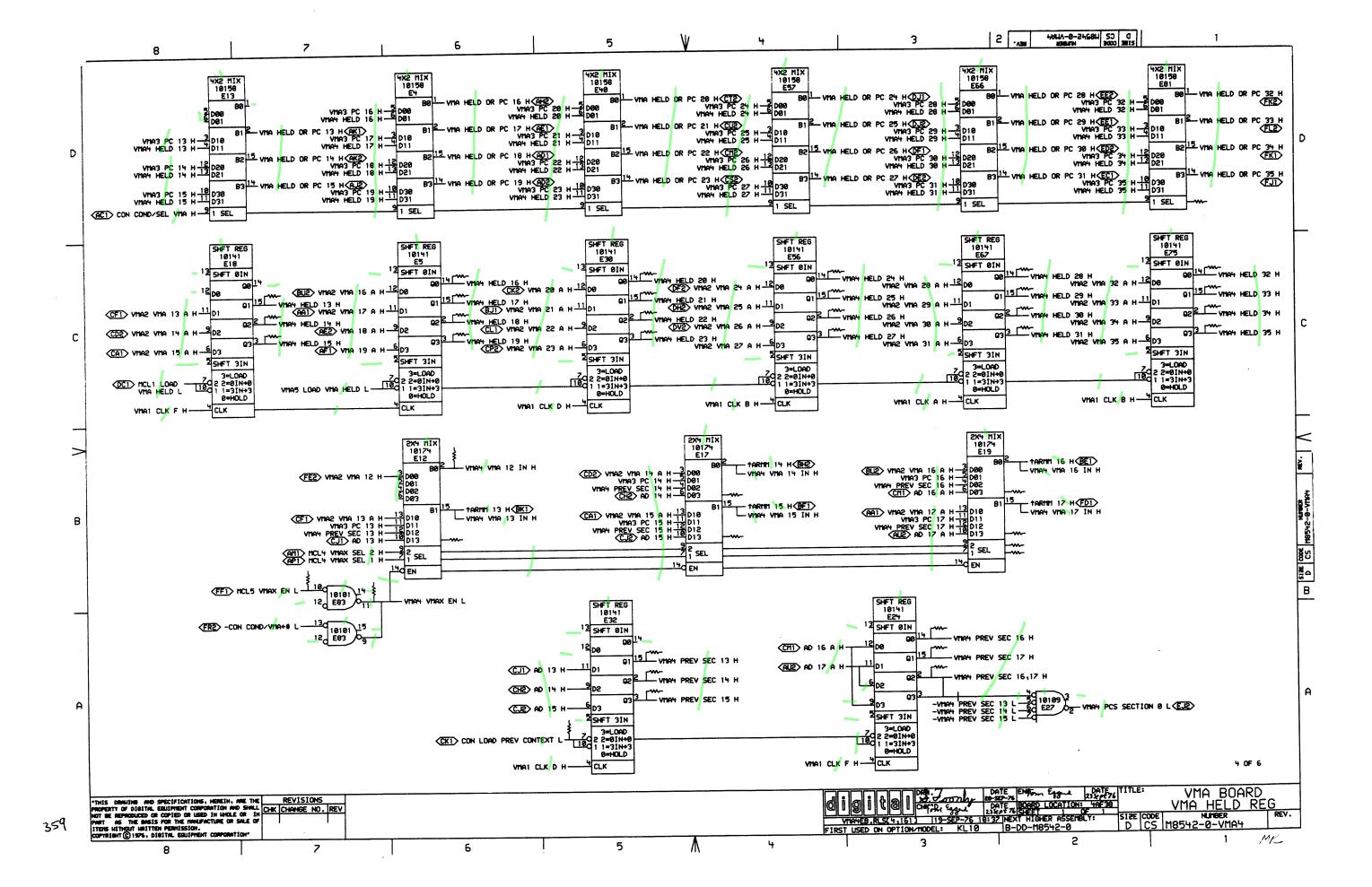
PMAM BRASS := 8 BUS ADR(85)

M 8542 / MA AD (35) output of 10181 ALL

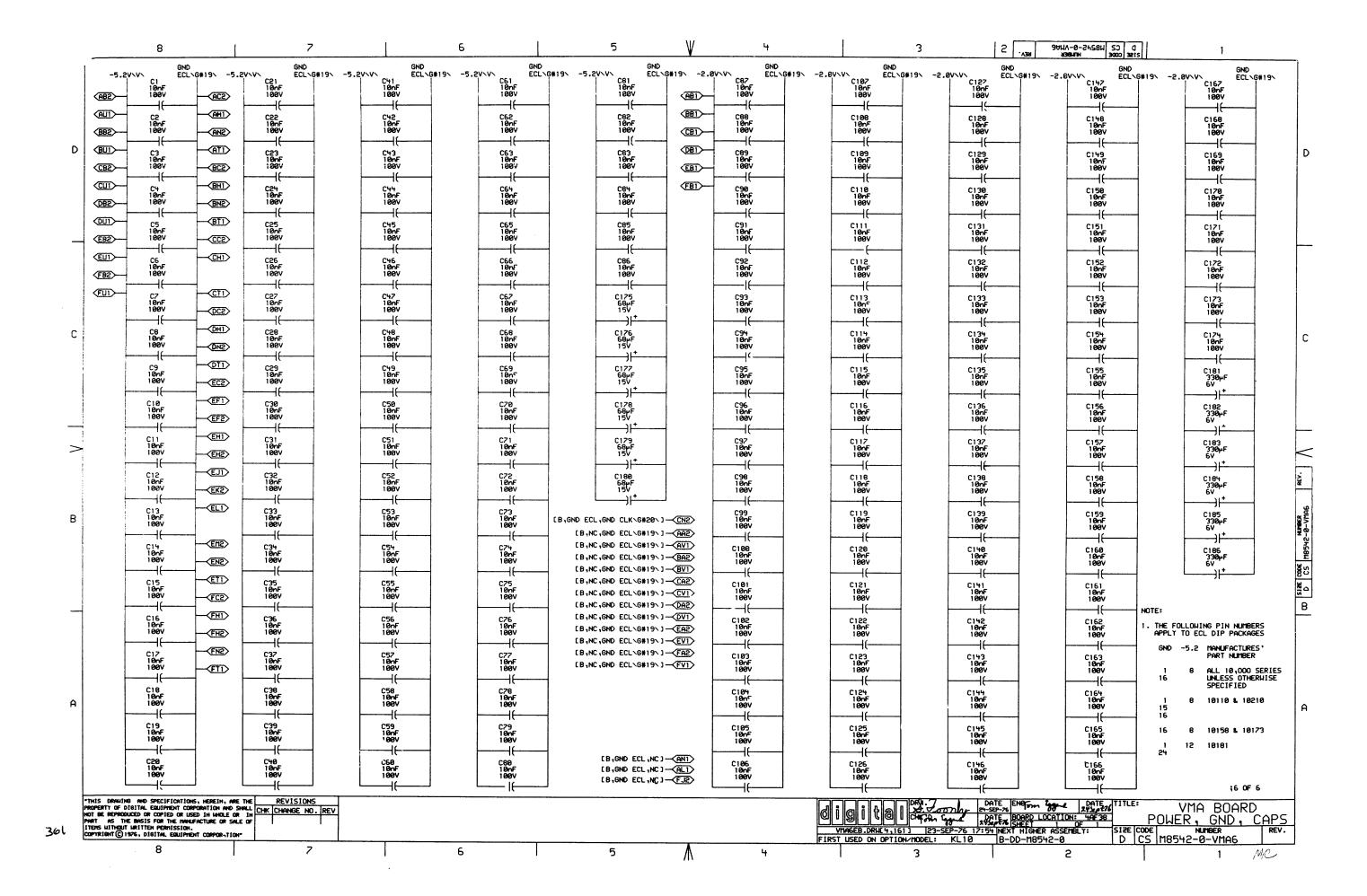












6 5 3 2 D C2 U8245-0-6E2 1 D RESISTOR SHOUN ON DRU# REF TERMINATES SHOWN ON DRW# REF RESISTOR LOC(PIN) VALUE TERMINATES SHOWN ON DRW# REF SIGNAL RESISTOR VALUE TERMINATES LOC(PIN) DRU# SIGNAL RESISTOR SHOWN ON DRW# REF R134(1) SAMV A7 LOC(PIN) VALUE %E18(14) TERMINATES R156(1) VMA2 A2 D LOC(PIN) AD 35 A H SIGNAL R166(1) R133(1) VMD2 AZ VMA1 D4 VMA1 AD 24-27 CG H %E10(15) R103(1) VMA1 82 R31(1) VMA2 D2 CLK3 VMA H VMA2 VMA 28 A H R167(1) VMA1 R144(1) VMA2 86 C4 VMA1 AD 24-27 CP H %E20(1) R197(1) R32(1) VMAY D1 CON COND/SEL VMA H VMA2 D2 680 VMA2 VMA 29 A H R139(1) R150(1) D4 A6 VMA2 685 %E20(14) VMA1 AD 25 H R201(1) R30(1) 82 D2 CON COND/VMA+# H 680 VMA2 VMA 30 A H R149(1) VMA1 A6 680 %E20(15) VMAI AD 26 H R23(1) VMA5 R29(1) cs -CON DATAD APR H VMA2 VMA 31 A H R143(1) R146(1) VMA1 86 680 VMA1 AD 27 H %E20(2) RZ(1) R112(1) VM64 A5 VMA2 D1 -CON LOAD PREV CONTEXT H VMAS VMA 32 A H R127(1) R188(1) VMOS 84 VMQ1 D3 %E43(1) VMA1 AD 28 H R211(1) VMA2 R111(1) R1 VMO2 D1 -CON VMA SEL 1 H VMA2 VMA 33 A H R165(1) R189(1) VMA2 **A4** VMA1 D3 680 %E43(14) VMA1 AD 28-31 CG H R159(1) SAMV R119(1) -CON VMA SEL 2 H VM92 D1 VMA2 VMA 34 A H R162(1) R190(1) VMA1 С3 **68**Ω VMA2 A4 680 %E43(15) VMA1 AD 28-31 CP H R155(1) R118(1) VMA1 689 SAMV C1 CRAM # 00 A H VMA2 VMA 35 A H R187(1) R122(1) VMA1 84 D3 68Ω 68Ω %E43(2) VMA1 AD 29 H R131(1) VMA1 R58(1) D3 C7 CRAM # 21 A H 68Ω VMA3 ADR BRK 13 H R289(1) VMA2 B2 68n R126(1) VMA1 D3 %E53(1) VMA! AD 30 H R130(1) VMQ1 D3 REG(1) VMAR C7 CRAM # 02 A H VMA3 ADR BRK 14 H R125(1) R213(1) VMO2 A2 VMQ1 D3 686 68Ω %E53(14) VMA1 AD 31 H R79(1) R59(1) VMA1 D3 680 VMAR C7 CRAM # 03 A H 689 VMA3 ADR BRK 15 H R210(1) R205(1) VMA1 VMA2 A2 68Ω D1 SBc %E53(15) VMA1 AD 32 H R129(1) VMA1 R48(1) D3 VMA3 680 CRAM # 84 A H C6 686 VMA3 ADR BRK 16 H R214(1) VMA2 R2 R208(1) VMA1 D1 680 %E53(2) 680 VMA1 AD 33 H R215(1) R50(1) VMAI A7 VMA3 68Ω C6 CSH3 GATE VMA 27-33 H 680 VMA3 ADR BRK 17 H R158(1) R204(1) **B**3 VMA1 D1 68Ω %F89(1) 685 VMA1 AD 34 H R45(1) VMA5 R47(1) VMA3 C6 689 -CTL1 LOAD PC H VMA3 ADR BRK 18 H R202(1) R116(1) A3 VMA1 VMA2 689 D1 %E89(14) VMA1 AD 35 H R46(1) R49(1) VMA5 B5 VMA3 -MCL1 LOAD VMA HELD H C6 68s VMA3 ADR BRK 19 H R173(1) R117(1) VMG2 A3 VMA C2 %E80(15) VMA1 AD CRY 28 H R181(1) R19(1) VMA1 85 689 VMD3 Ç5 -MCL2 VMA EXTENDED H VMA3 ADR BRK 20 H R160(1) R153(1) VMA1 VMA2 **B3** 686 %E80(2) R7 VMA1 AD CRY 24 H R89(1) VMA1 85 R15(1) VMA3 686 C5 68n -MCL2 VMA READ OR WRITE H VMA3 ADR BRK 21 H R17(1) R109(1) VMQ1 D3 680 AD 13 H 580 VMA1 AD CRY 28 H R163(1) R14(1) VMA1 CS 680 VMA3 05 MCL4 VMA INC H 686 VMA3 ADR BRK 22 H R18(1) 84 R124(1) VMAI 68s 680 VMA1 AD CRY 32 H AD 14 H R6(1) VMA4 R16(1) 82 68s VMA3 C5 680 VMA3 ADR BRK 23 H MCL4 VMAX SEL 1 H R37(1) R24(1) VMAY VMA1 cs 680 AD 15 H VMA1 CLK A H R4(1) VMQ4 B2 R108(1) VMA3 685 C4 MCL4 VMAX SEL 2 H VMA3 ADR BRK 24 H R120(1) R5(1) VM04 R2 VMAI C2 682 AD 16 A H VMA1 CLK B H R121(1) R105(1) VMA2 82 689 MCL4 VMA+AD H VMA3 С¥ VMA3 ADR BRK 25 H R9(1) R152(1) VMG1 VMQ4 B2 68ຄ CS 58a AD 17 A H VMA1 CLK C H R161(1) R106(1) VMA1 CS 680 VMA3 C4 68Ω MCL5 ADR ERR H VMA3 ADR BRK 26 H R138(1) R57(1) VMA1 VMA2 A2 B2 680 AD 18 A H 680 VMA1 CLK D H R221(1) B2 R107(1) VMA3 £4 -MCL5 VMAX EN H 680 VMA3 ADR BRK 27 H R141(1) R132(1) VMA1 680 680 AD 19 H VMA1 CLK E H R93(1) R71(1) VMA1 85 C5 680 VMA3 ADR BRK 28 H MCL6 PAGE LIEBR REF A R8(1) VMA2 R13(1) VMAI 680 82 68Ω H NS GR VMA1 CLK F H RZ9(1) R180(1) VMQ1 C5 MCL6 VMA FETCH H 68a VMA3 ADR BRK 29 H R219(1) R10(1) VMA2 86 VMA1 82 AD 21 H VMA1 HI R170(1) R68(1) VMA1 DI VMG3 CS SCD3 TRAP MIX 32 H VMA3 ADR BRK 30 H R11(1) R100(1) VMA1 VMA2 A6 85 68Ω AD 22 A H -VMA1 LOCAL H R171(1) R69(1) VMA1 D1 689 VMA3 CS SCD3 TRAP MIX 33 H VMA3 ADR BRK 31 H R179(1) R12(1) VMA2 VMA1 C5 A6 680 AD 23 A H 680 -VMA1 MISC=0 H R168(1) Di R115(1) VMA3 C1 680 SCD3 TRAP MIX 34 H VMA3 ADR BRK 32 H R102(1) R174(1) VMA1 82 В 680 680 AD 24 A H VMA1 SPEC/VMA+# H R164(1) VMAT D1 R114(1) 680 C1 VMA3 ADR BRK 33 H SCD3 TRAP MIX 35 H R148(1) VMA2 68Ω R95(1) VMAI A2 VMA1 VMA SECTION 8,1 H AD 25 H R223(1) VMQ1 C5 R110(1) VMA3 C1 -VMA1 20-27=0 H VMA3 ADR BRK 34 H R147(1) R136(1) VMA2 **A**4 VMA2 C6 AD 26 H -VMA2 CRY 16 H R176(1) R113(1) VMA1 D7 689 VMA3 C1 VMA1 AD 18 H VMA3 ADR BRK 35 H R104(1) R135(1) VMA2 A4 68Ω VMA2 C5 680 AD 27 H -VMA2 CRY 20 H R1*77*(1) R98(1) VMA1 D2 680 VMA3 D2 VMA1 AD 19 H 685 -VMA3 MATCH 13-19 H R33(1) R142(1) VMA2 VMA2 **B**3 C4 680 AD 28 A H 680 -VMA2 CRY 24 H R185(1) R20(1) D6 680 VMA3 VMA1 AD 20 H D6 68s -VMA3 MATCH 16-19 H R34(1) **B**3 RIRECT > VMA2 cs 68<sub>0</sub> AD 29 A H -VMA2 CRY 28 H R217(1) VMA1 R96(1) D6 VMA3 05 VMA1 AD 20-23 CG H 680 -VMA3 MATCH 20-23 H R35(1) SAMV R212(1) VMA2 C1 A3 AD 30 A H 300 CS -VMA2 CRY 32 H R216(1) VMQ1 C6 R101(1) VMA3 D3 VMA1 AD 20-23 CP H 680 -VMA3 MATCH 24-27 H R38(1) R84(1) VMA2 A3 68Ω VM95 C7 AD 31 H VMA2 VMA 13 H R184(1) R97(1) SIRE VMA1 D6 689 VMA1 AD 21 H VMA3 DS. ~VMA3 MATCH 28-31 H R151(1) R81(1) VMA2 VMA2 B2 c> 68Ω AD 32 H VMA2 VMA 14 H R182(1) VMA1 R99(1) D6 VMA3 680 VMA1 AD 22 H DS -VMA3 MATCH 32-35 H R152(1) R82(1) SAMV BS VMA2 C2 68o VMA2 VMA 15 H AD 33 H В R183(1) VMA1 D6 R54(1) VMA3 68Ω B2 VMA1 AD 23 H 680 R154(1) VMA2 VMA3 PC 13 H R88(1) VMA2 C6 AD 34 A F VMAZ VMA 16 H R55(1) R203(1) VMA1 D4 VMA3 VMA1 AD 24 H 87 680 VMA3 PC 14 H R63(1) VMA2 C3 VMA2 VMA 27 A H R56(1) VMA3 B2 68Ω VMA3 PC 15 H NOTE: 1. ALL TERMINATORS HAVE PIN THO CONNECTED TO -2.8V AND ARE 5% 1/4HATT UNLESS OTHERHISE SPECIFIED
2. ENTRIES ARE SORTED BY SIGNAL NAME
3. % INDICATES OUTPUT OF DIP LOC AND ( ) INDICATES PIN NUMBER THIS DRAWING AND SPECIFICATIONS, HEREIN, ARE THE REVISIONS
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D C2 W82+5-0-6E2 5 6 D RESISTOR LOC(PIN) SHOWN ON DRW# REF TERMINATES VALUE SHOWN ON DRW# REF TERMINATES RESISTOR LOC(PIN) VALUE D SIGNAL VMA4 HELD 33 H R76(1) VMA4 C1 VMA3 PC 16 H VMA3 **B**6 R42(1) R73(1) C1 VMA4 HELD 34 H VMA3 PC 17 H R41(1) R74(1) C1 VMA4 HELD 35 H VMA3 PC 18 H R175(1) VMA3 VMA4 PREV SEC 13 H VMA3 PC 19 H R83(1) VMA4 A5 R172(1) VMA3 VMA4 PREV SEC 14 H B5 VMA3 PC 20 H R86(1) VMA4 A5 VMA3 R286(1) VMA4 PREV SEC 15 H R85(1) VMA4 R198(1) VMA3 VMA3 PC 21 H R90(1) VMA4 PREV SEC 16 H VMA3 PC 22 H VMA3 85 VMA4 PREV SEC 16,17 H R94(1) VMA3 PC 23 H R202(1) VMA3 85 VMA4 PREV SEC 17 H RR7(1) VMA3 PC 24 H VMA3 83 R27(1) H MI SI AMV PAMV VMA3 PC 25 H R137(1) VMA4 VMA3 R21(1) -VMA4 VMAX EN H R3(1) VMA4 R22(1) VMA3 PC 26 H -VMA5 DATAD APR H VMA3 PC 27 H R25(1) VMA3 R193(1) VMA5 87 VMAS DIAG 84 H VMA3 PC 28 H R128(1) VMA3 -VMA5 DIAG 04 H VMA3 PC 29 H 82 PAR(1) VMAS VMA3 R123(1) VMA5 DIAG 05 H R192(1) VMA3 PC 30 H R75(1) C R91(1) -VMA5 DIAG 05 H VMA3 PC 31 H VMA5 DIAG 06 H R191(1) VMA3 PC 32 H R169(1) VMA3 B1 -VMA5 DIAG 06 H R92(1) VMA5 VMA3 PC 33 H R222(1) VMA3 VMA5 85 -VMA5 LOAD PC H R2(1) R218(1) VMA3 -VMAS LOAD VMA HELD H R1(1) VMA5 85 68ก VMA3 PC 35 H R220(1) R72(1) VMA5 A7 689 -VMA5 READ VMA H VMA4 HELD 13 H C7 R145(1) VMA5 A5 68n -VMA5 READ VMA A H VMA4 HELD 14 H C7 R51(1) VMA4 68Ω Ç7 VMA4 HELD 15 H 68n R53(1) VMA4 VMA4 HELD 16 H R43(1) VMA4 C6 VMA4 C6 VMA4 HELD 17 H R44(1) VMA4 HELD 18 H R39(1) VMA4 C6 VMA4 HELD 19 H R48(1) VMQ4 C6 680 VMA4 HELD 20 H C5 R199(1) VMA4 VMA4 HELD 21 H R194(1) VMA4 HELD 22 H В VMA4 HELD 23 H R196(1) VMQ4 C5 CD D CS M8542 VMA4 HELD 24 H R61(1) VMA4 VM-44 HELD 25 H R62(1) R28(1) VMA4 HELD 26 H VMA4 HELD 27 H R26(1) VMA4 C4 VMA4 HELD 28 H R67(1) VMA4 CS VMA4 HELD 29 H VMA4 R66(1) R65(1) VMA4 HELD 30 H VMA4 HELD 31 H VMA4 C2 680 VMA4 C1 68Ω VMA4 HELD 32 H R77(1) NOTE: 1. ALL TERMINATORS HAVE PIN THO CONNECTED TO -2.8V AND ARE 5% 1/4HATT UNLESS OTHERHISE SPECIFIED
2. ENTRIES ARE SORTED BY SIGNAL NAME
3. % INDICATES DUTPUT OF DIP LOC AND
() INDICATES PIN NUMBER Α Α DATE ENEM 2256
CHIEF DE SAME 19-55P-76 INSTANTINE ENEM 2256
M85422.RLS[4,161] 119-55P-76 18:59 NEXT HIGHER ASSENBLY:
FIRST USED ON OPTION/MODEL: KL10 B-DD-M8542-0 VMA BOARD "THIS DRAWING AND SPECIFICATIONS, HEREIN, ARE THE PROPERTY OF DIGITAL EQUIPMENT COMPONATION AND SHALL CHIK CHANGE NO. REVINOT BE REPRODUCED OR COPIED OR USED IN HABLE OR IN PART AS THE BASIS FOR THE HAMISTOLINE OR SALE OF ITEMS WITHOUT WRITTEN PERHISSION.

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