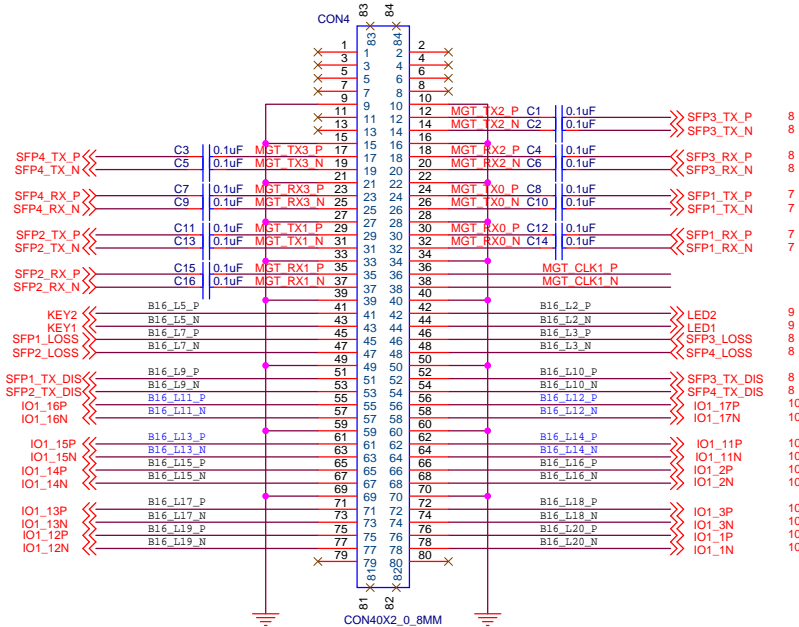
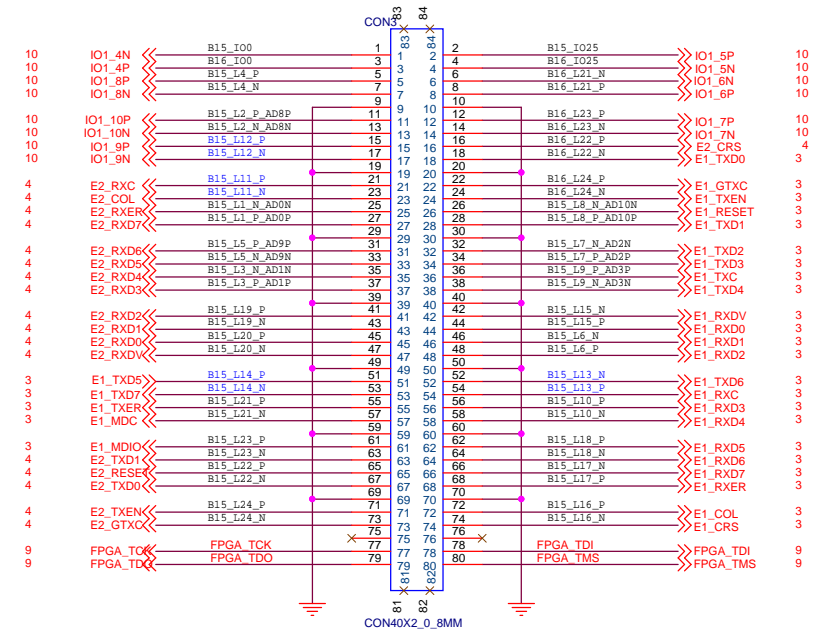
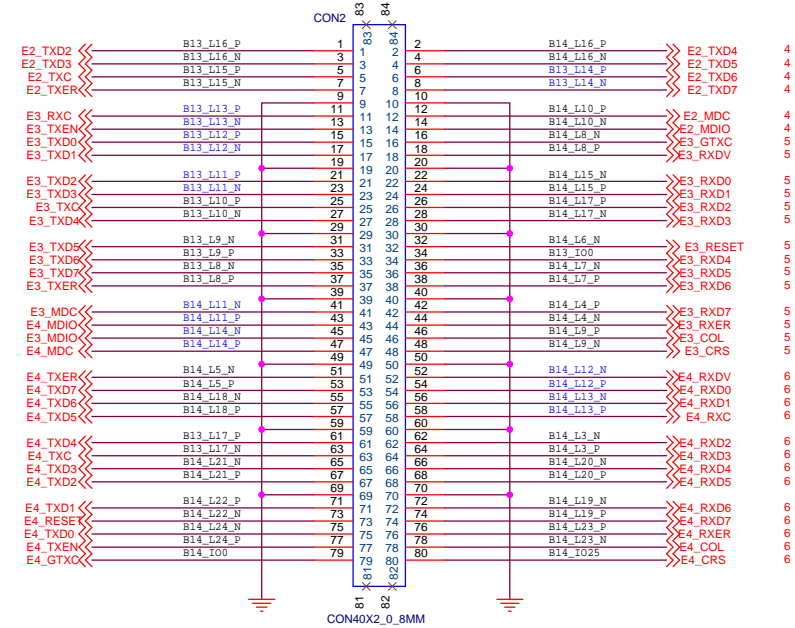

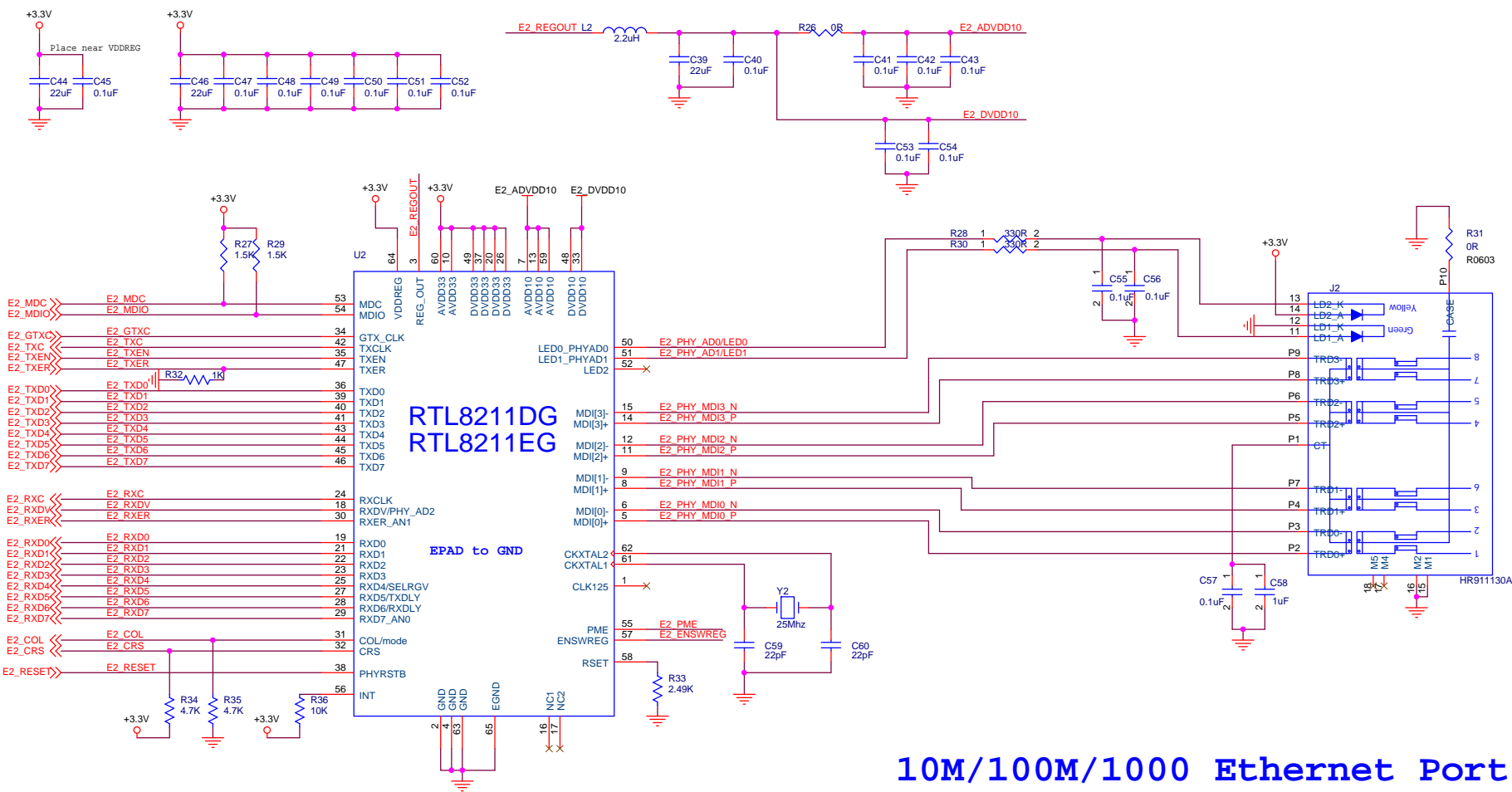


Pinout diagram for the CON40X2_0.8MM connector. The diagram shows a 2x20 pin header with pins numbered 1 to 39 on the left and 40 to 80 on the right. Power pins are marked with +5.0V at pins 1, 40, and 80. Ground pins are marked with ground symbols at pins 39, 79, and 80. The diagram lists various signals connected to the pins, including UART1_RXD, UART1_TXD, VGA_R1, VGA_R2, VGA_R0, VGA_R4, and VGA_G2. Some pins are marked with an 'X' indicating they are not connected or are reserved.

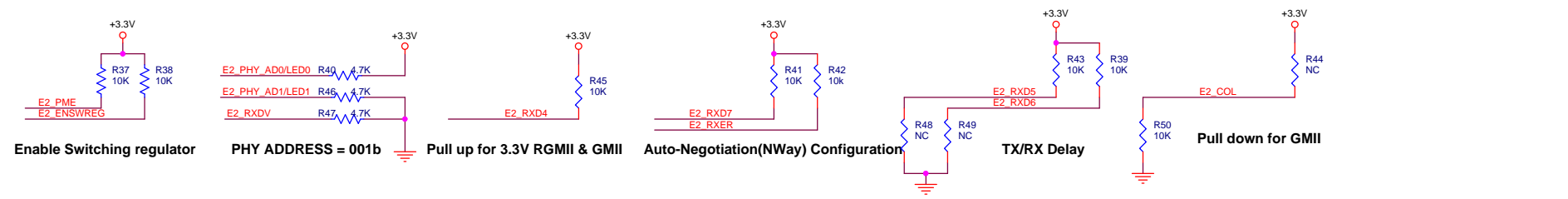
Pin	Signal
1	+5.0V
2	B13_L4_P
3	B13_L4_N
4	+5.0V
5	B13_L1_P
6	B13_L1_N
7	B13_L2_P
8	B13_L2_N
9	B13_L6_P
10	B13_L6_N
11	B34_L20_P
12	B34_L20_N
13	B34_L21_N
14	B34_L22_P
15	B34_L22_N
16	B34_L24_P
17	B34_L24_N
18	B34_L25
19	B34_L26_P
20	B34_L26_N
21	B34_L27_P
22	B34_L27_N
23	B34_L28_P
24	B34_L28_N
25	B34_L29_P
26	B34_L29_N
27	B34_L30_P
28	B34_L30_N
29	B34_L31_P
30	B34_L31_N
31	B34_L32_P
32	B34_L32_N
33	B34_L33_P
34	B34_L33_N
35	B34_L34_P
36	B34_L34_N
37	B34_L35_P
38	B34_L35_N
39	Ground
40	+5.0V
41	B13_L4_P
42	B13_L4_N
43	B13_L1_P
44	B13_L1_N
45	B13_L2_P
46	B13_L2_N
47	B13_L6_P
48	B13_L6_N
49	B34_L20_P
50	B34_L20_N
51	B34_L21_N
52	B34_L22_P
53	B34_L22_N
54	B34_L24_P
55	B34_L24_N
56	B34_L25
57	B34_L26_P
58	B34_L26_N
59	B34_L27_P
60	B34_L27_N
61	B34_L28_P
62	B34_L28_N
63	B34_L29_P
64	B34_L29_N
65	B34_L30_P
66	B34_L30_N
67	B34_L31_P
68	B34_L31_N
69	B34_L32_P
70	B34_L32_N
71	B34_L33_P
72	B34_L33_N
73	B34_L34_P
74	B34_L34_N
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77	B34_L36_P
78	B34_L36_N
79	Ground
80	+5.0V

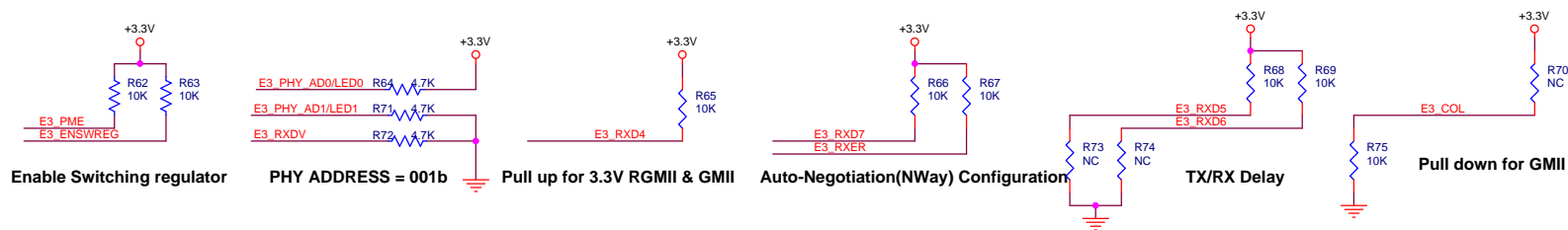
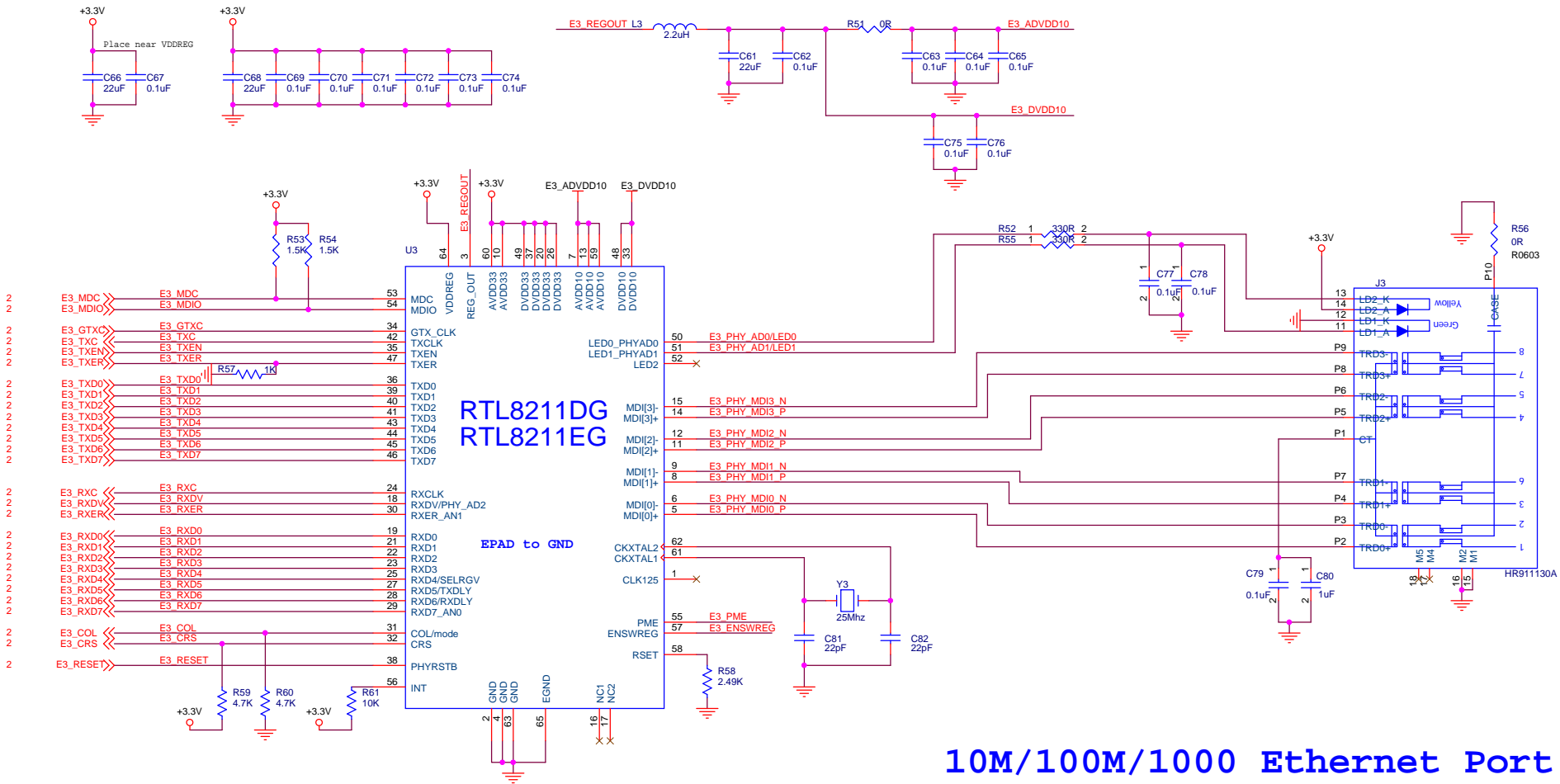


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Size	Document Number	Rev	
Custom	AX7101扩展板 Schematics	1.0	
Date:	Thursday, December 22, 2016	Sheet	2 of 11

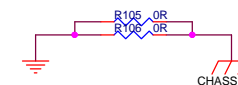
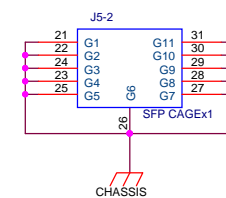
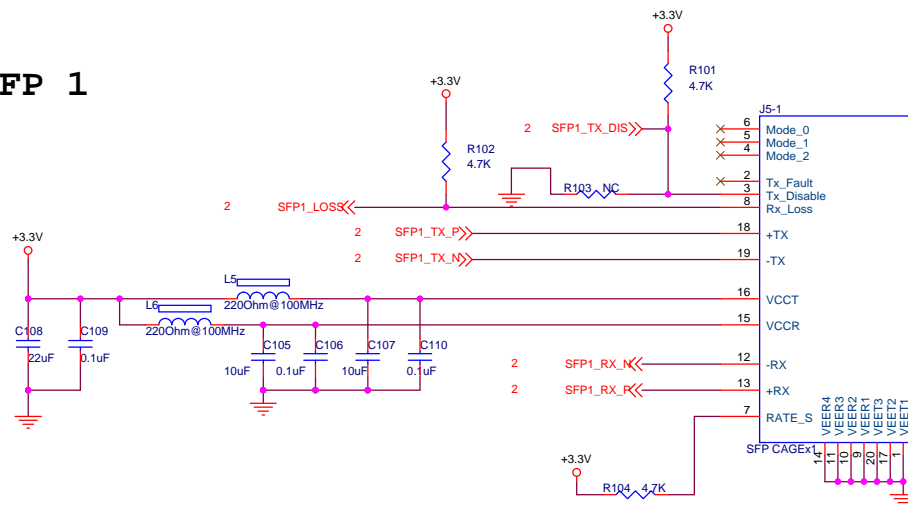


10M/100M/1000 Ethernet Port

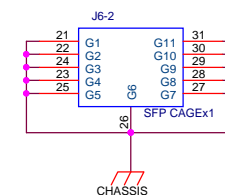
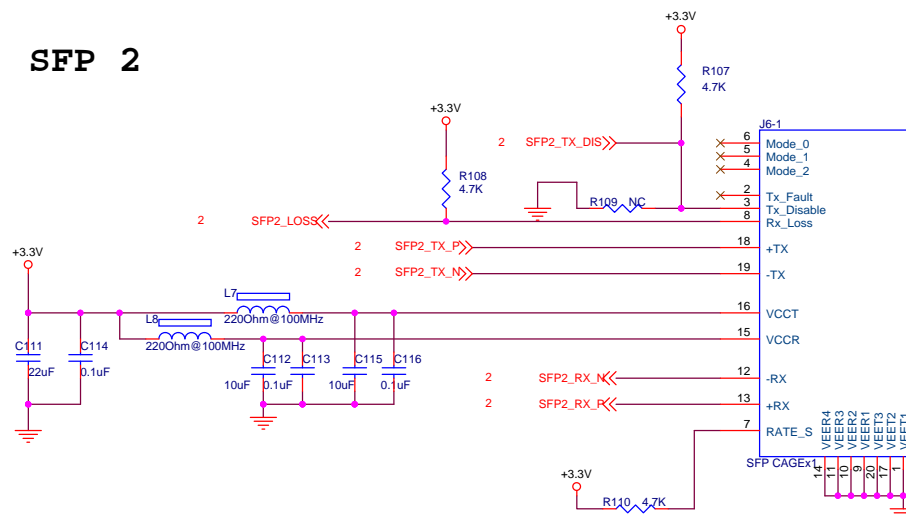




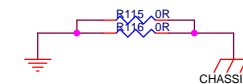
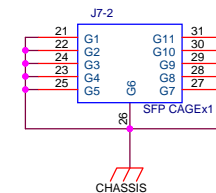
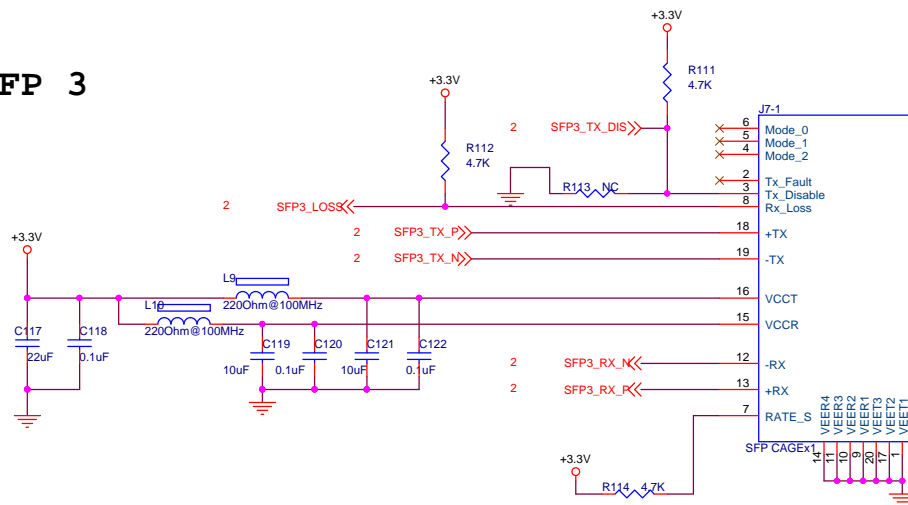
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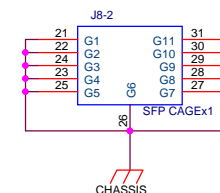
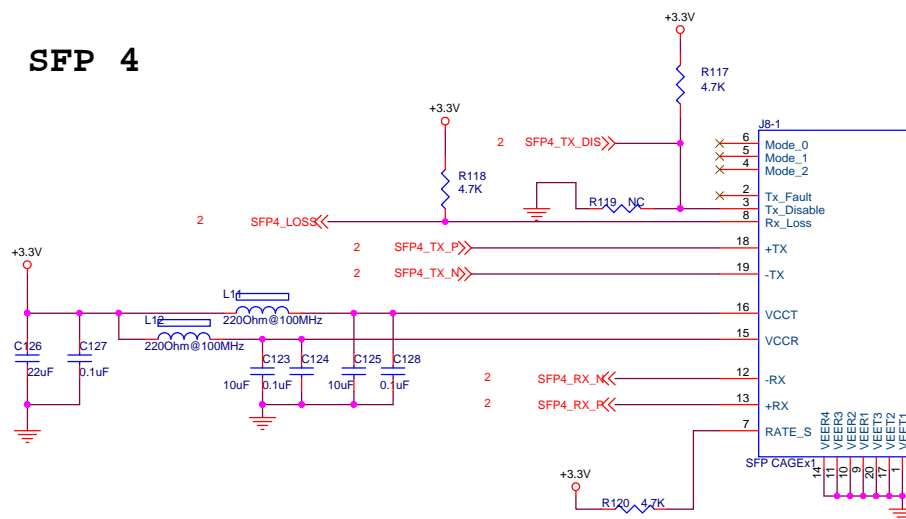
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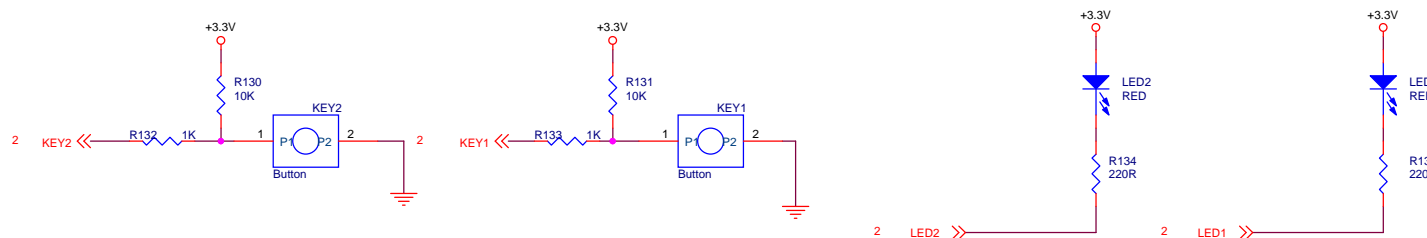
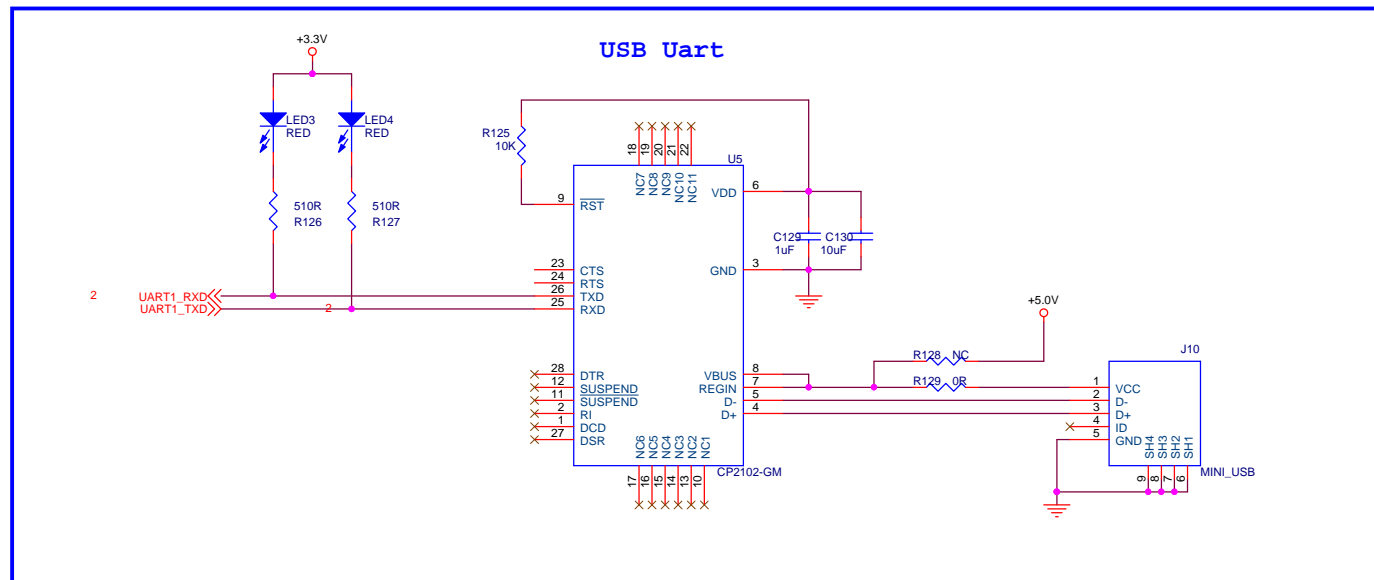
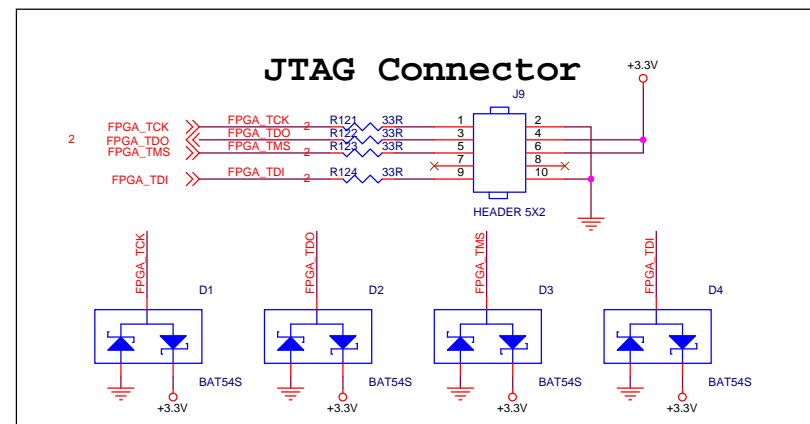


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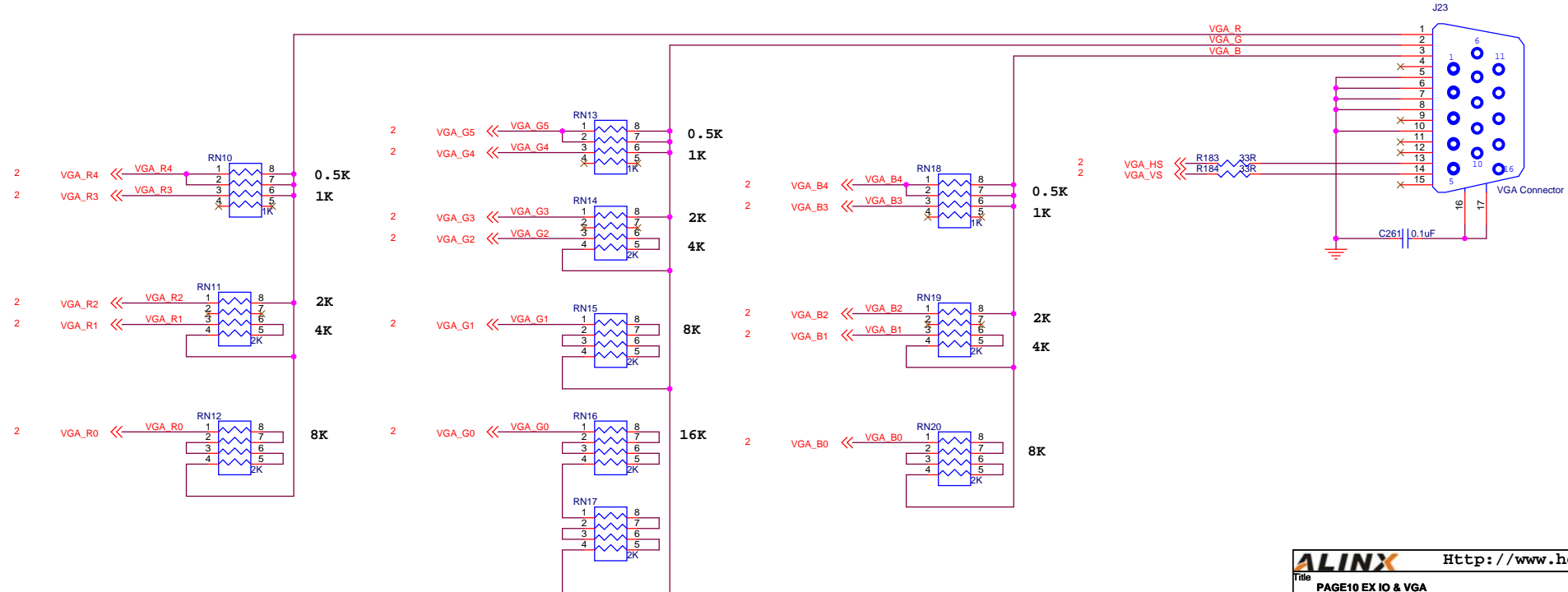
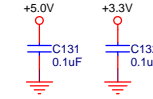
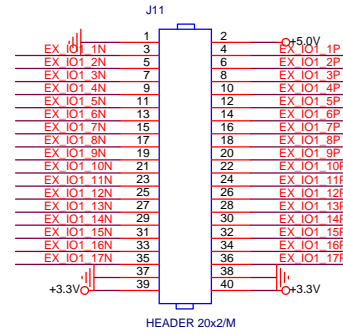
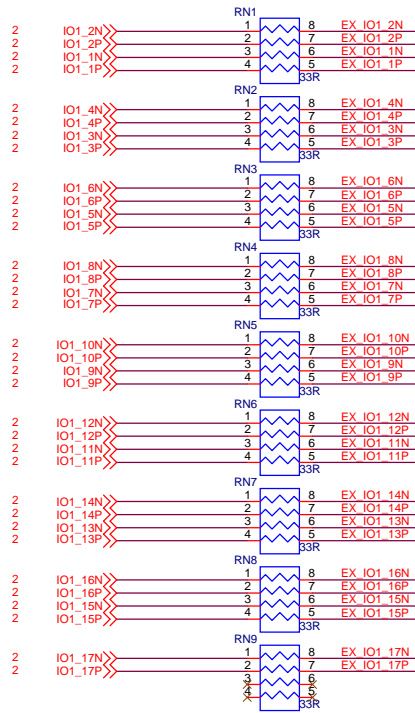


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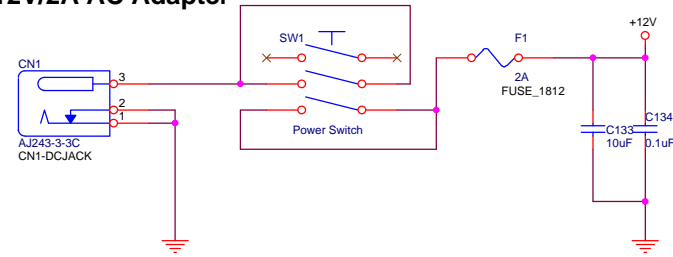


FPGA 40 PIN External IO

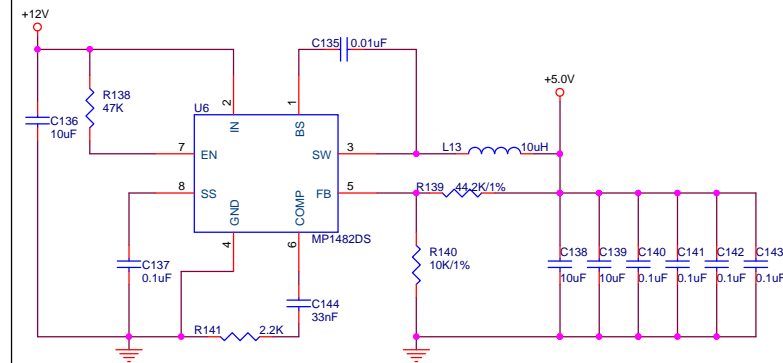


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12V/2A AC Adapter



+5.0V POWER



+3.3V POWER

