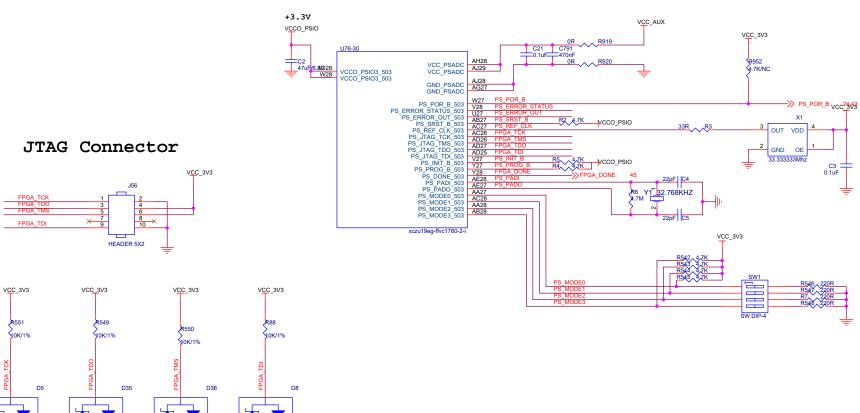


VCC_3V3

VCC_3V3

MODE[3:0] BOOT MODE Descritpion

0000	PS JTAG	PS JTAG Interface
0001	Quad_SPI(24b)	24-Bit addresssing(QSPI24)
0010	Quad_SPI(32b)	32-Bit addresssing(QSPI32)
0011	SD0(2.0)	SD2.0
0101	SD1(2.0)	SD2.0
0110	eMMC(1.8V)	eMMC version 4.5 at 1.8V
0111	USB0(2.0)	USB 2.0 only
1110	SD1 LS(3.0)	SD 3.0

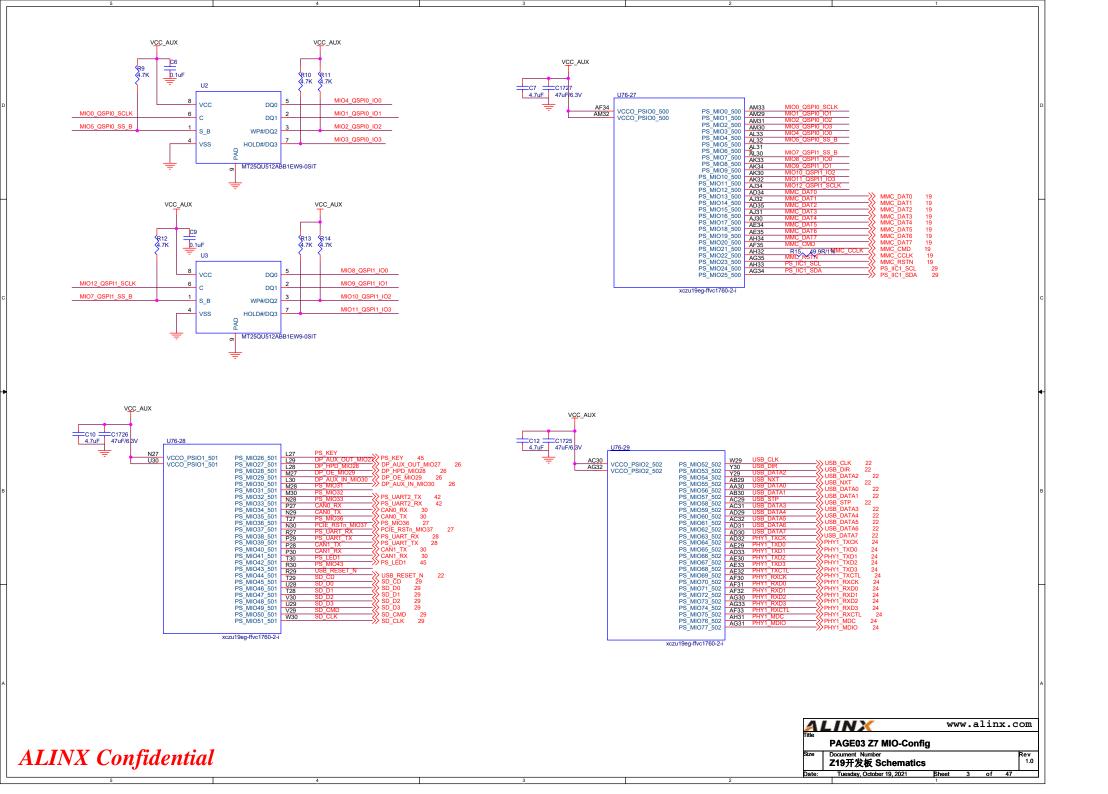


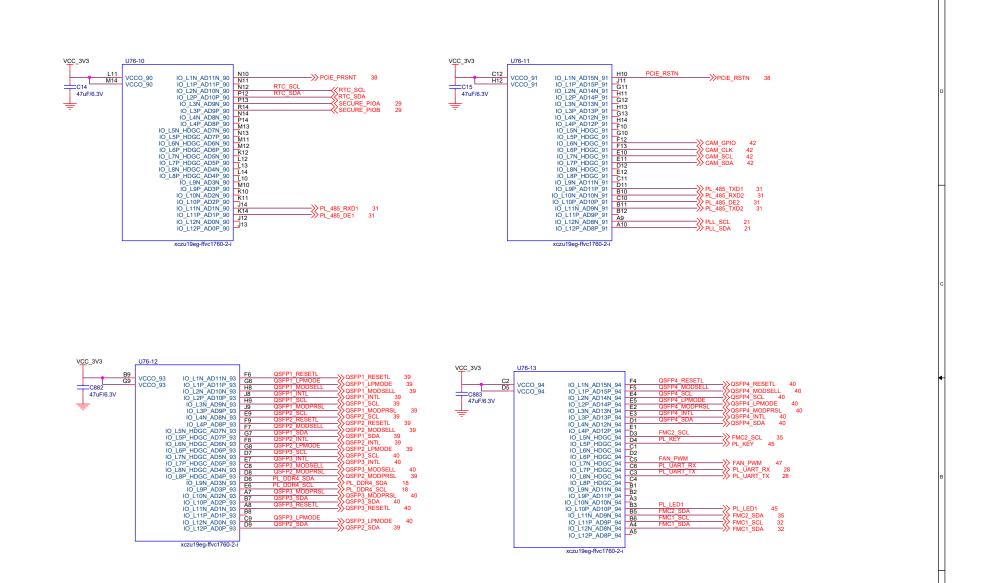
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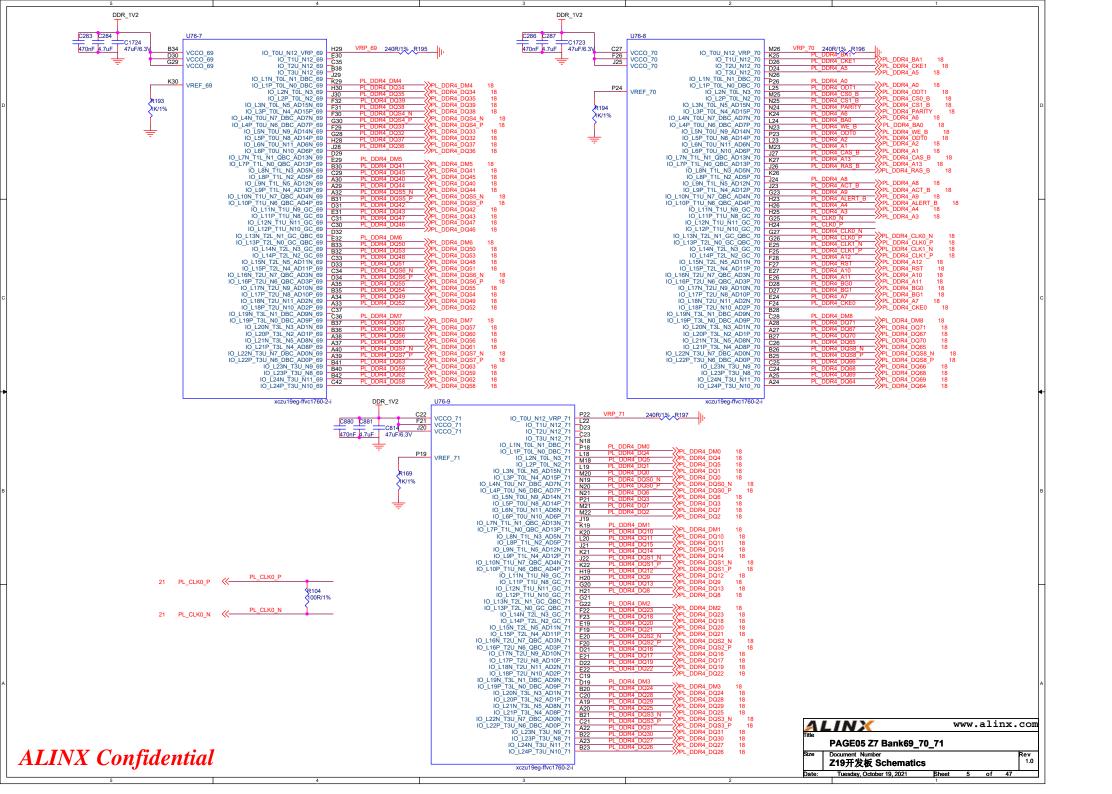
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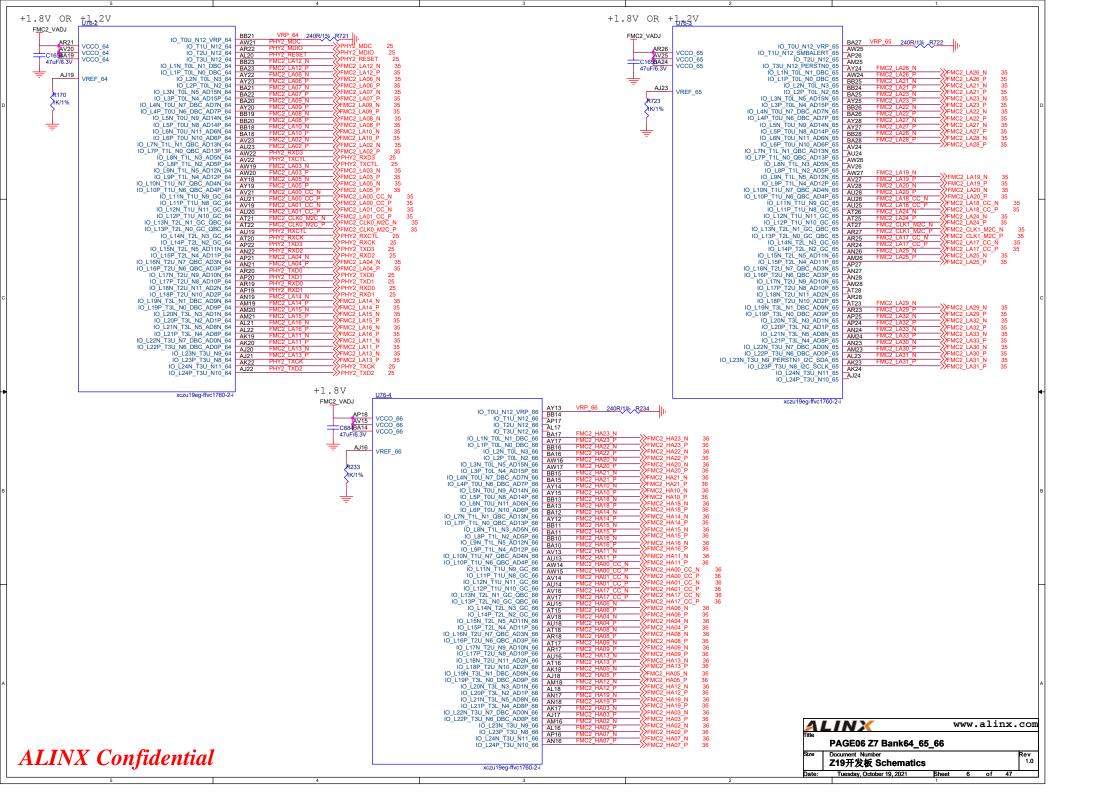
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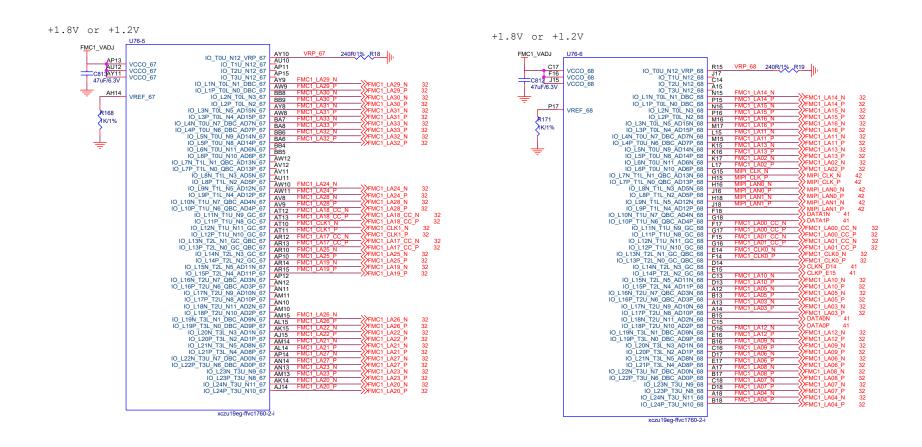
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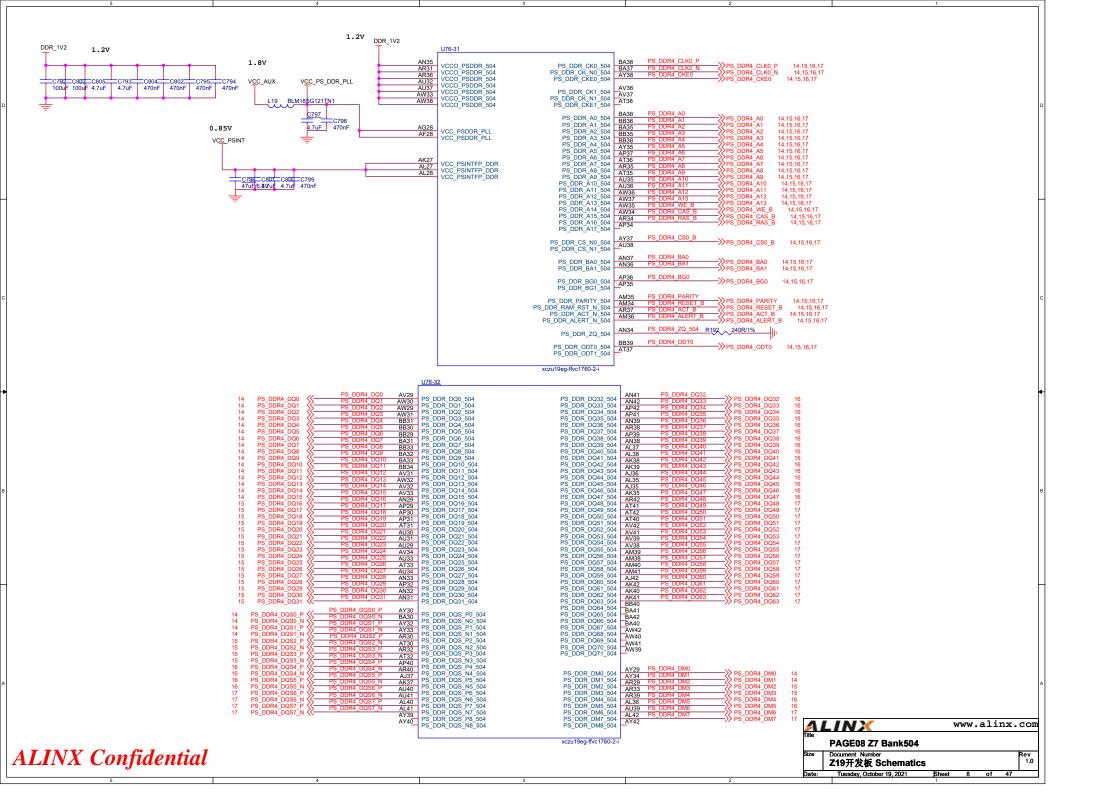


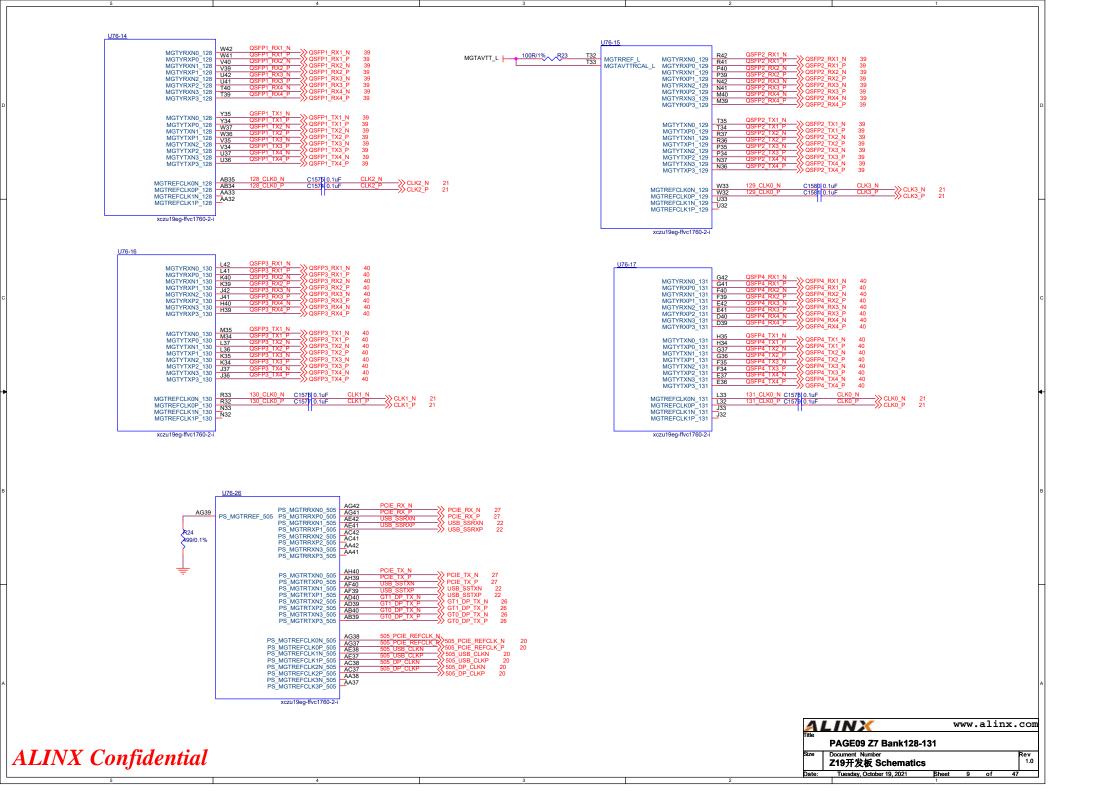


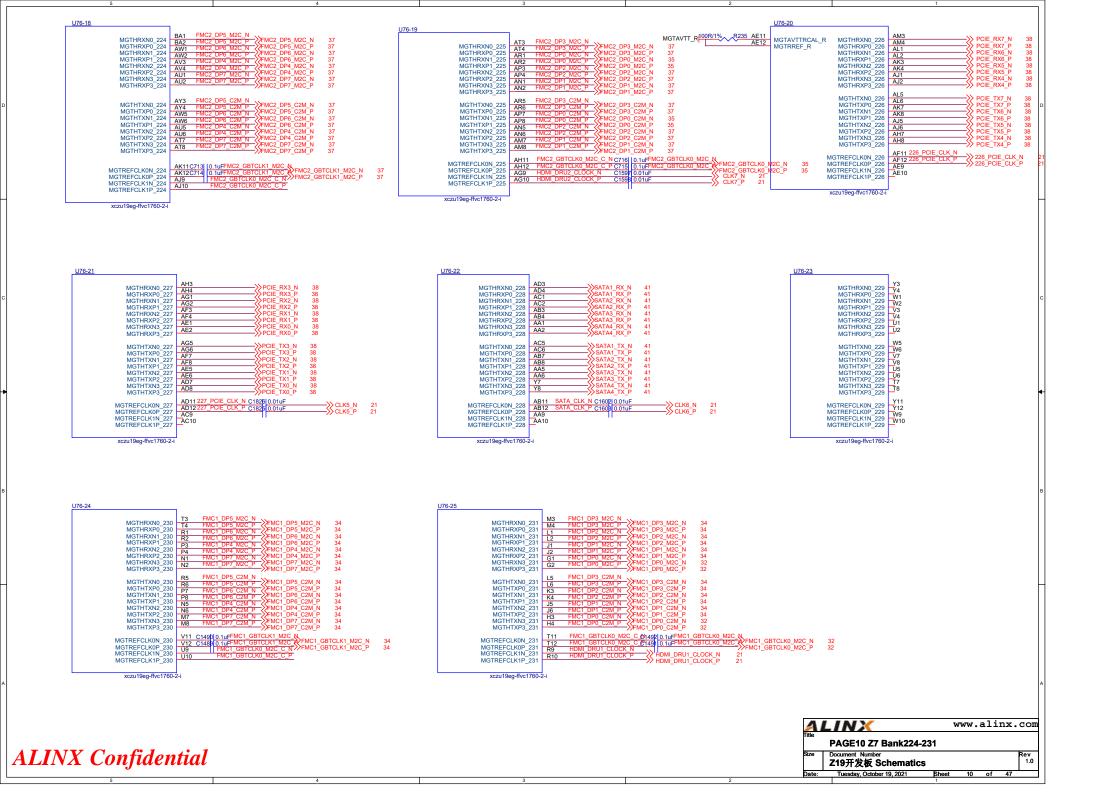
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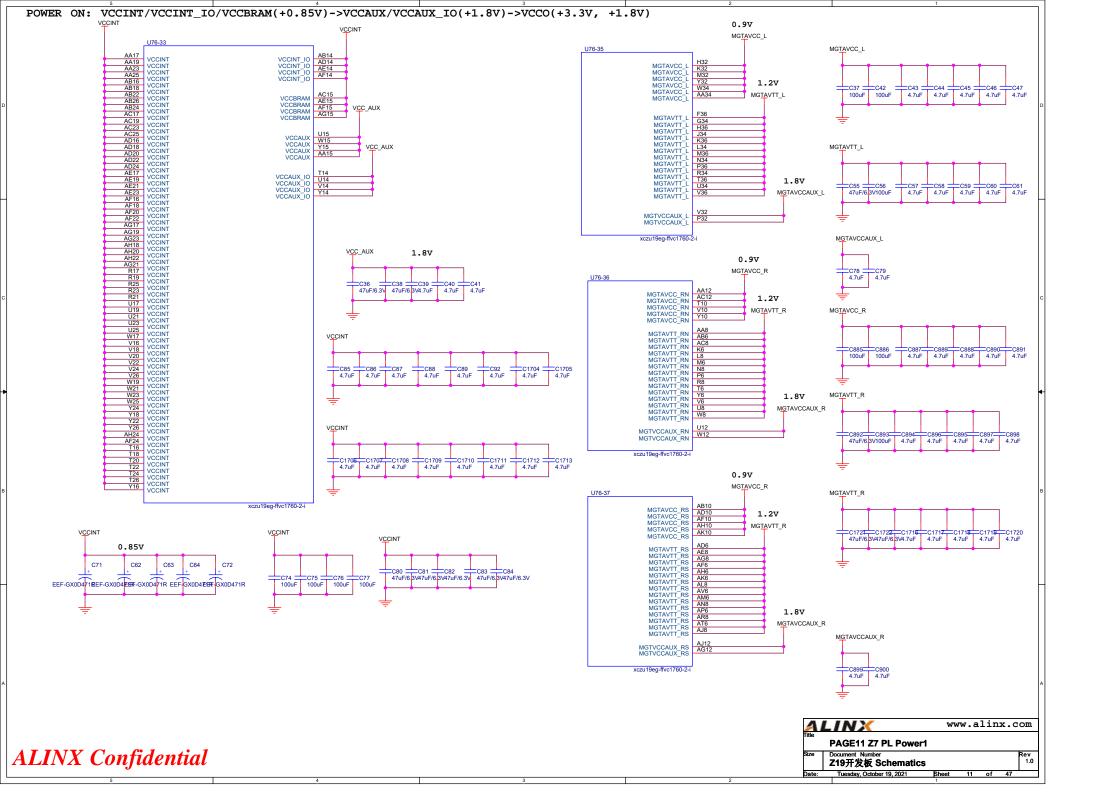
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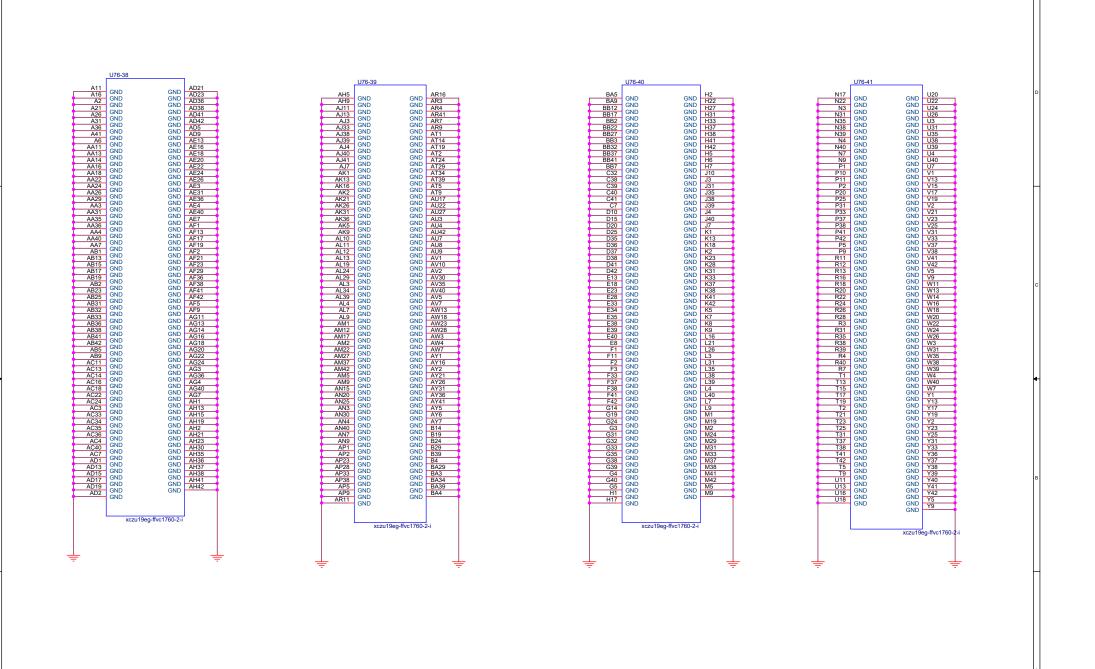




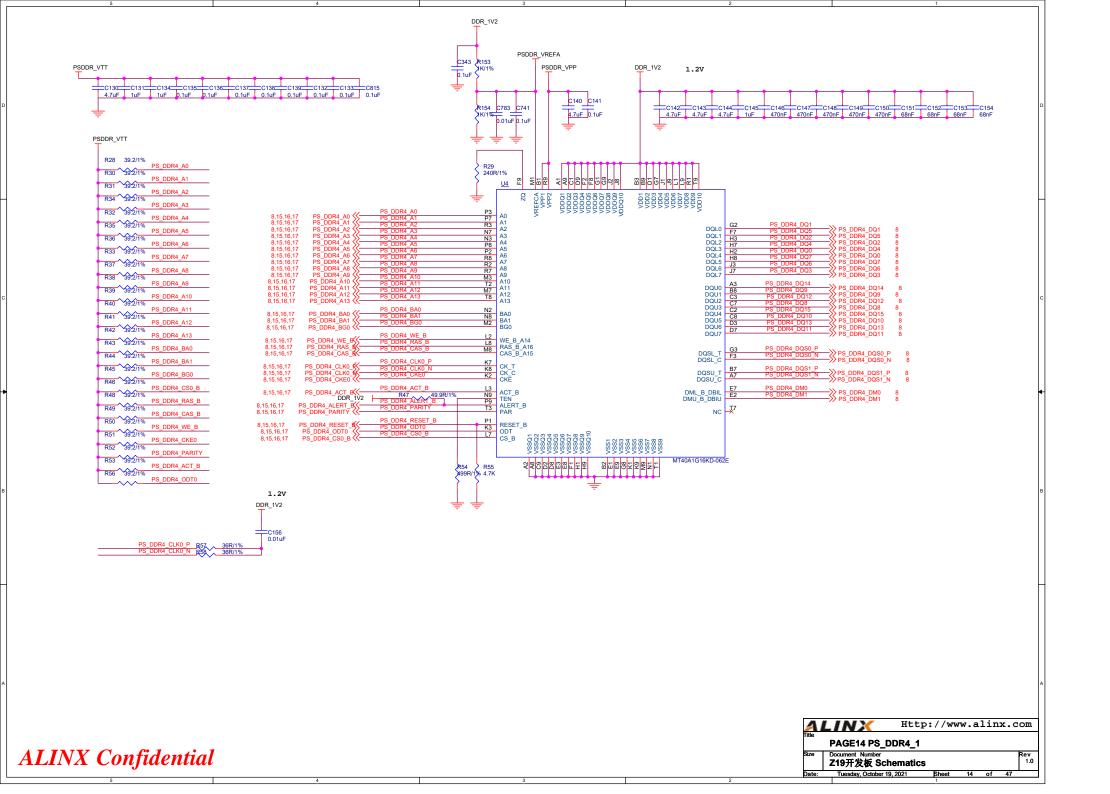


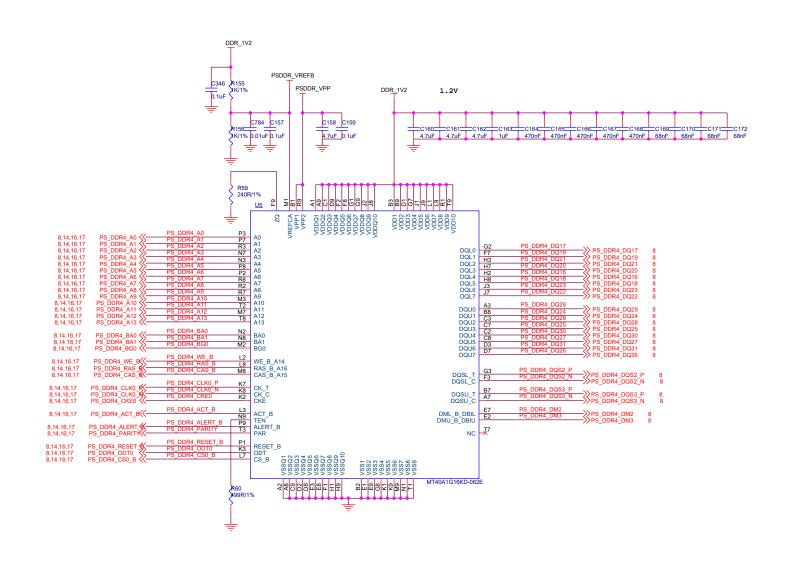


POWER ON: VCC_PSINTFP_VCC_PSINTFP_DDR(+0.85V)->VPS_MGTRAVCC(+0.9V), VCC_PSDDR_PLL(+1.8V)->VPS_MGTRAVTT(+1.8V), VCCO_PSDDR() POWER ON: VCC_PSINTLP(+0.85V)->VCC_PSAUX(+1.8V), VCC_PSADC(+1.8V), VCC_PSPLL(+1.2V)->VCCO_PSIO(+1.8V) 0.85V 1.8V VCC_PSINT VCC_PSINT U76-34 VCC_AUX VCC_AUX PS_PLL for VCC_PSINTLP for VCC_PSAUX 1.2V VCC_PSAUX VCC_PSAUX VCC_PSAUX VCC_PSAUX C104 100uF C105 C107 100uF 4.7uF PS_PLL C121 100uF 4.7uF 4.7uF 100uF 4.7uF PS_AVCC VCC_PSPLL VCC_PSPLL VCC_PSPLL PS_MGTRAVCC PS_MGTRAVCC PS_MGTRAVCC PS_AVTT PS_AVTT PS_AVCC 1.8V 0.85V 0.85V PS_MGTRAVTT PS_MGTRAVTT PS_MGTRAVTT VCC_PSINT for VCC_PSINTFP C94 C95 C96 4.7uF 470nF 470nF J22 C93 4.7uF VBAT_IN VCC_PSBATT C115 4.7uF C112 4.7uF C113 C114 4.7uF 4.7uF 100uF 100uF 100uF xczu19eg-ffvc1760-2-i C1609= 100uF C1610 4.7uF ALINX www.alinx.com PAGE12 Z7 PS Power2 ALINX Confidential Rev 1.0 Z19开发板 Schematics Tuesday, October 19, 2021



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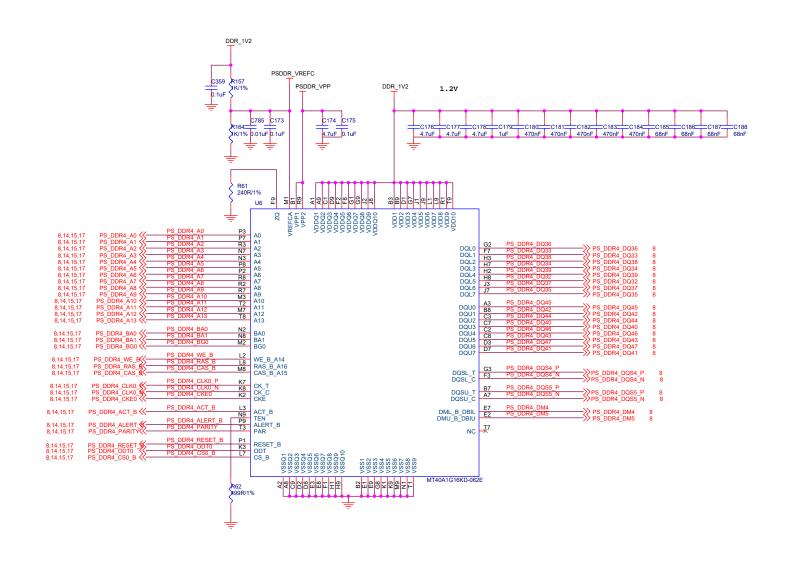




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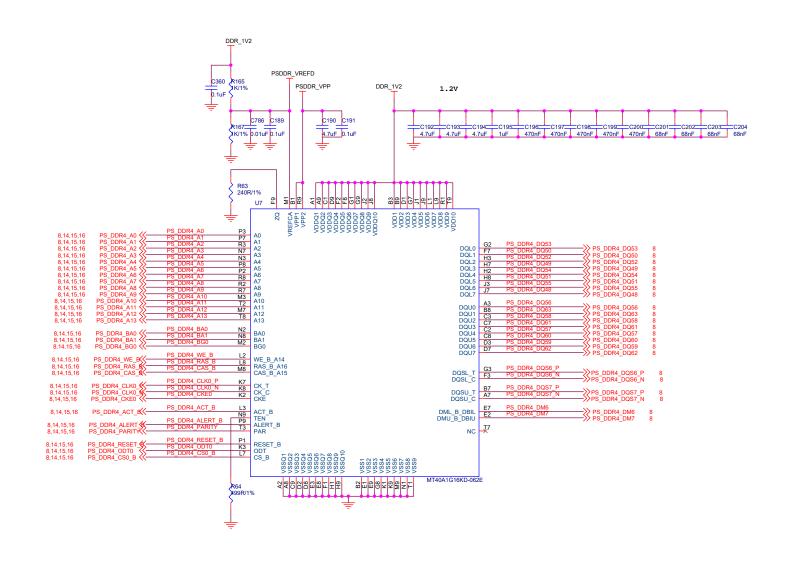


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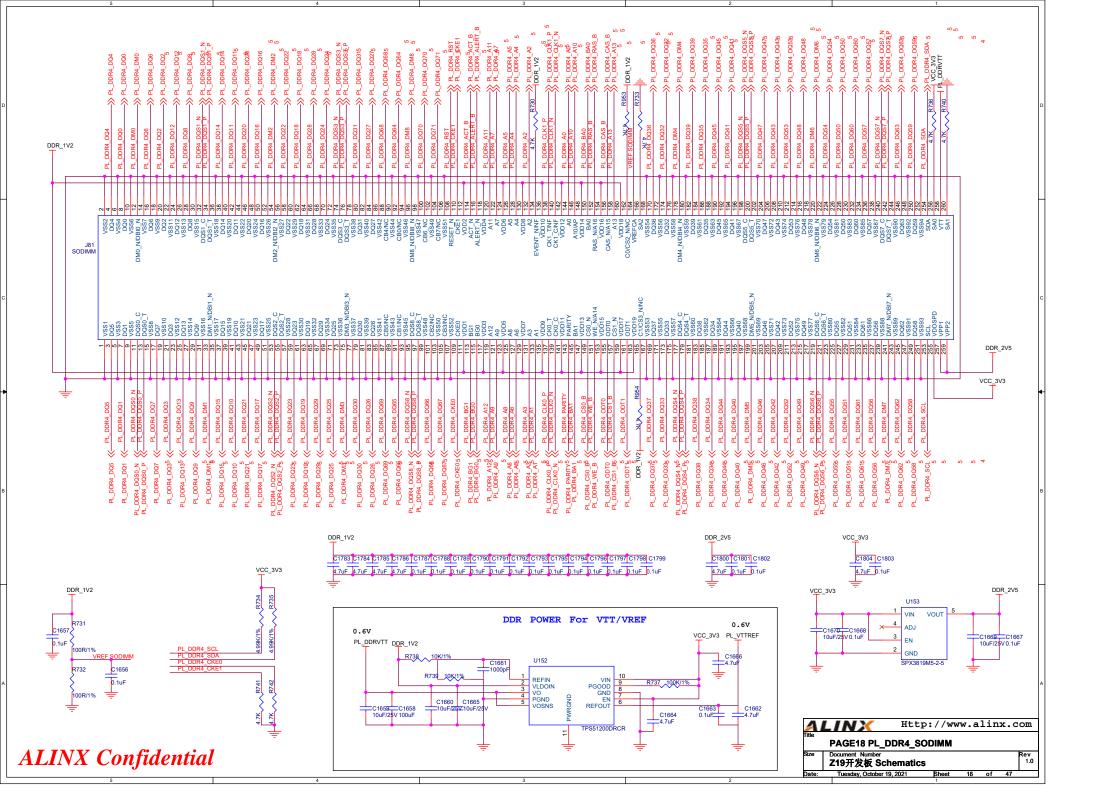
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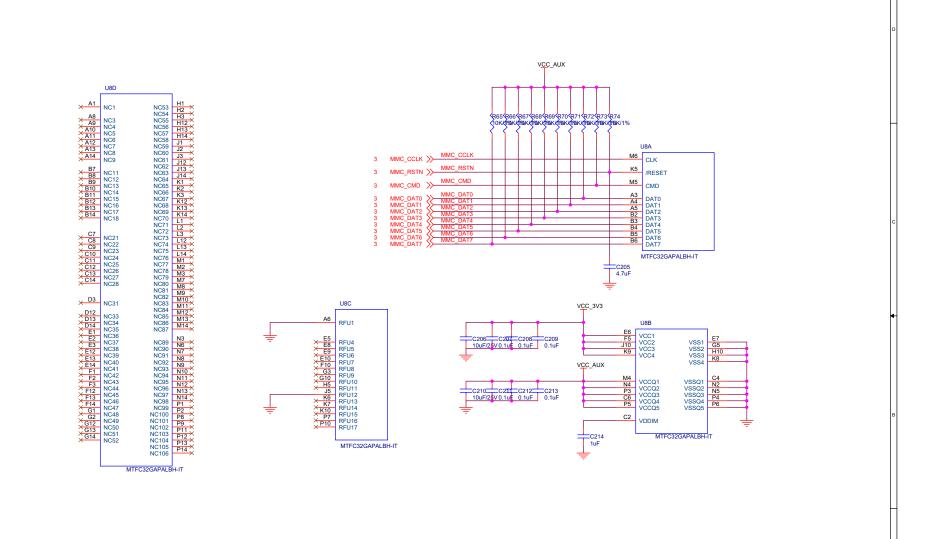
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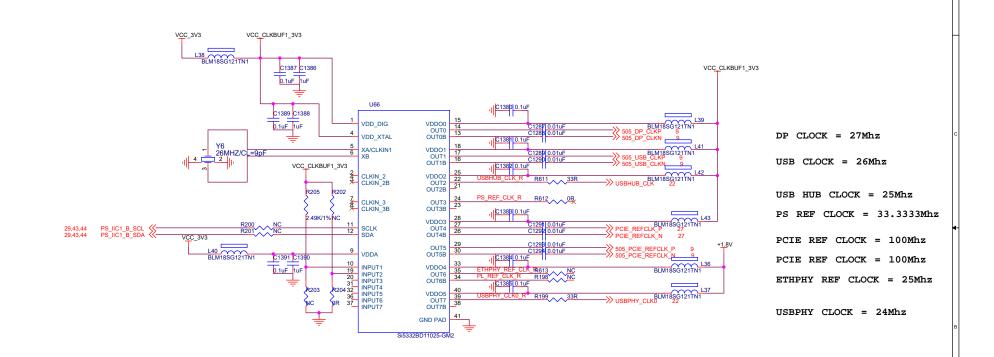


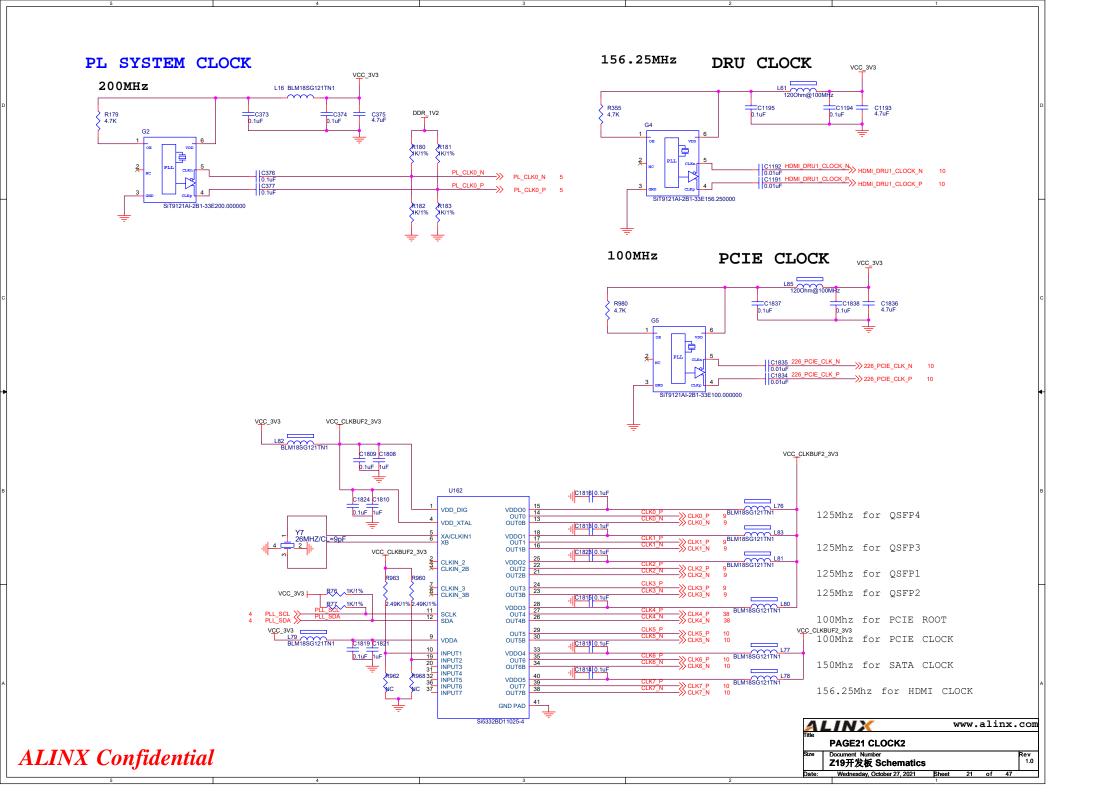


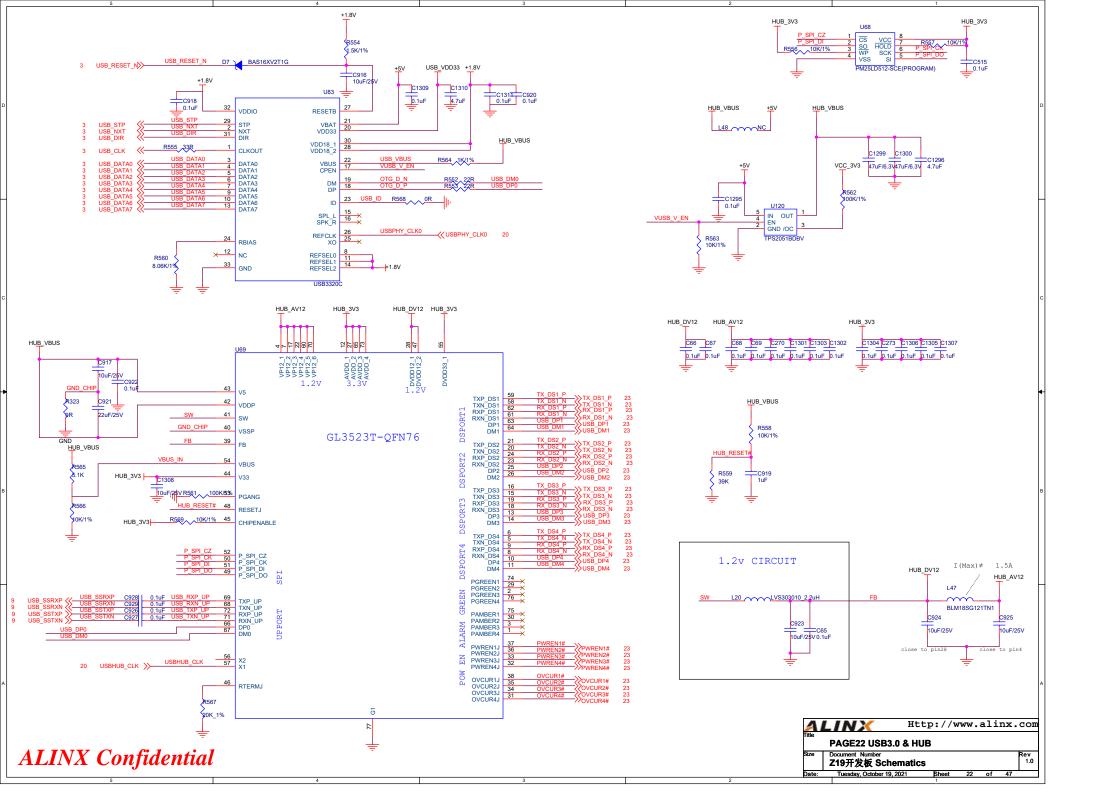
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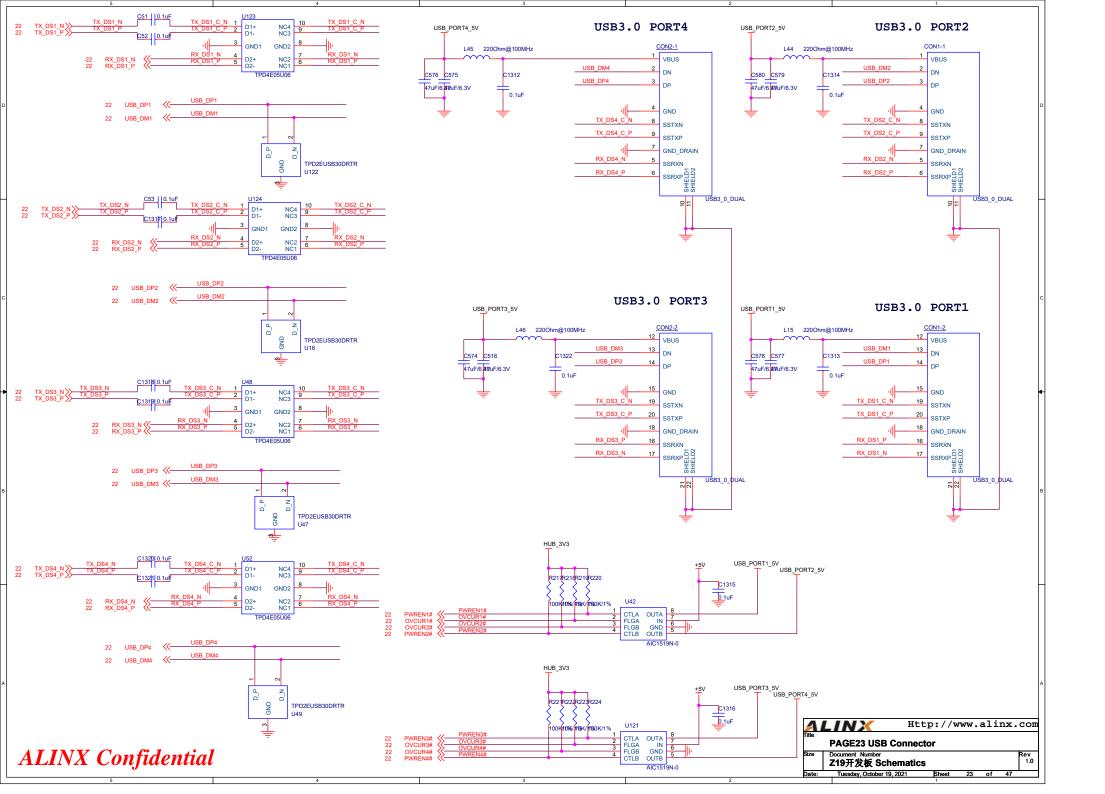
PAGE19 eMMC

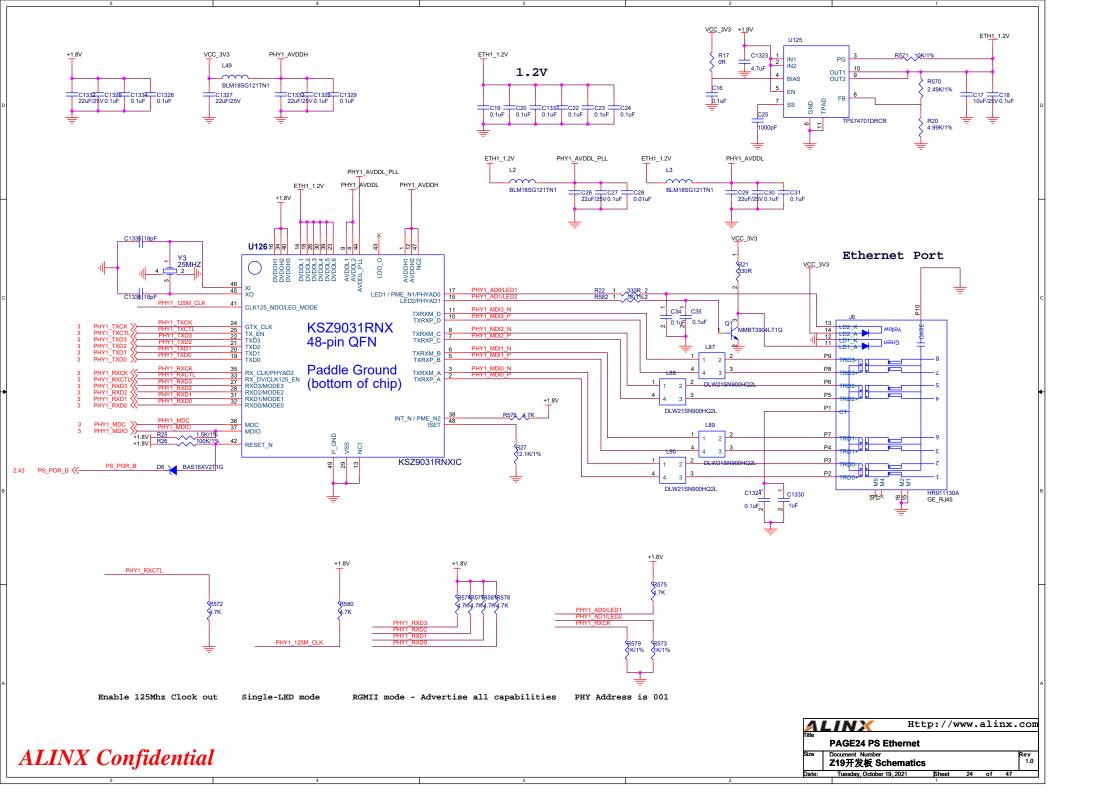
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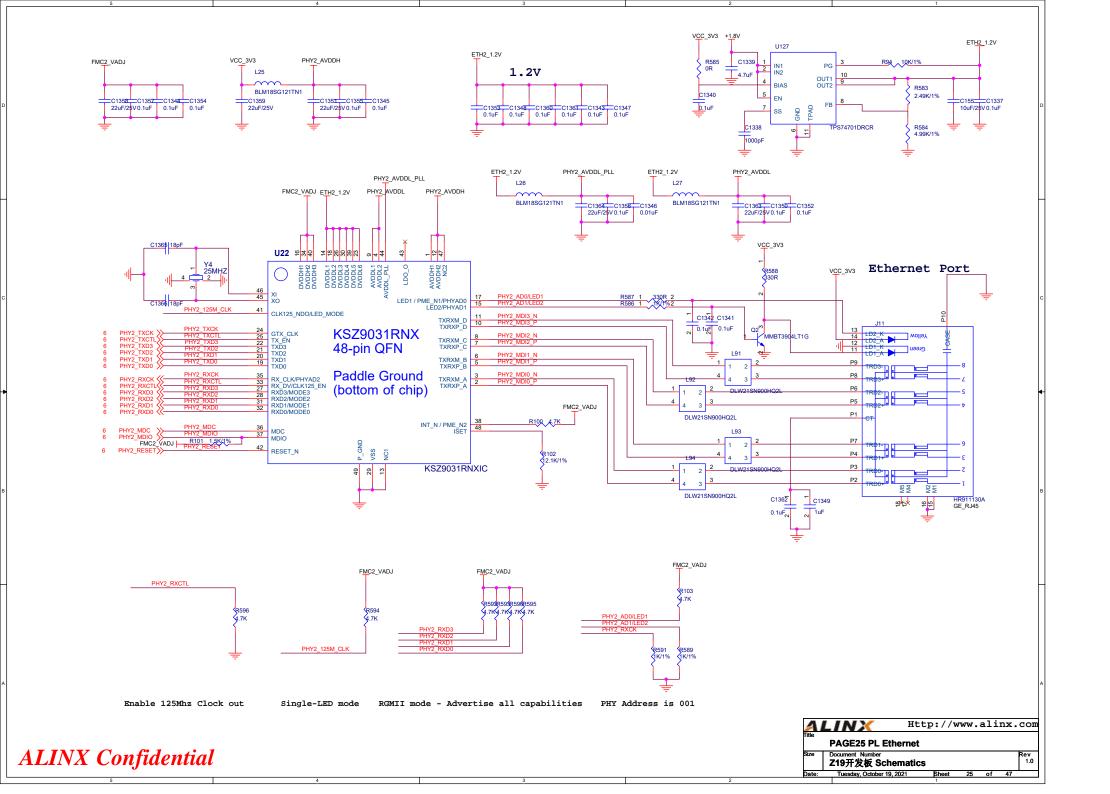


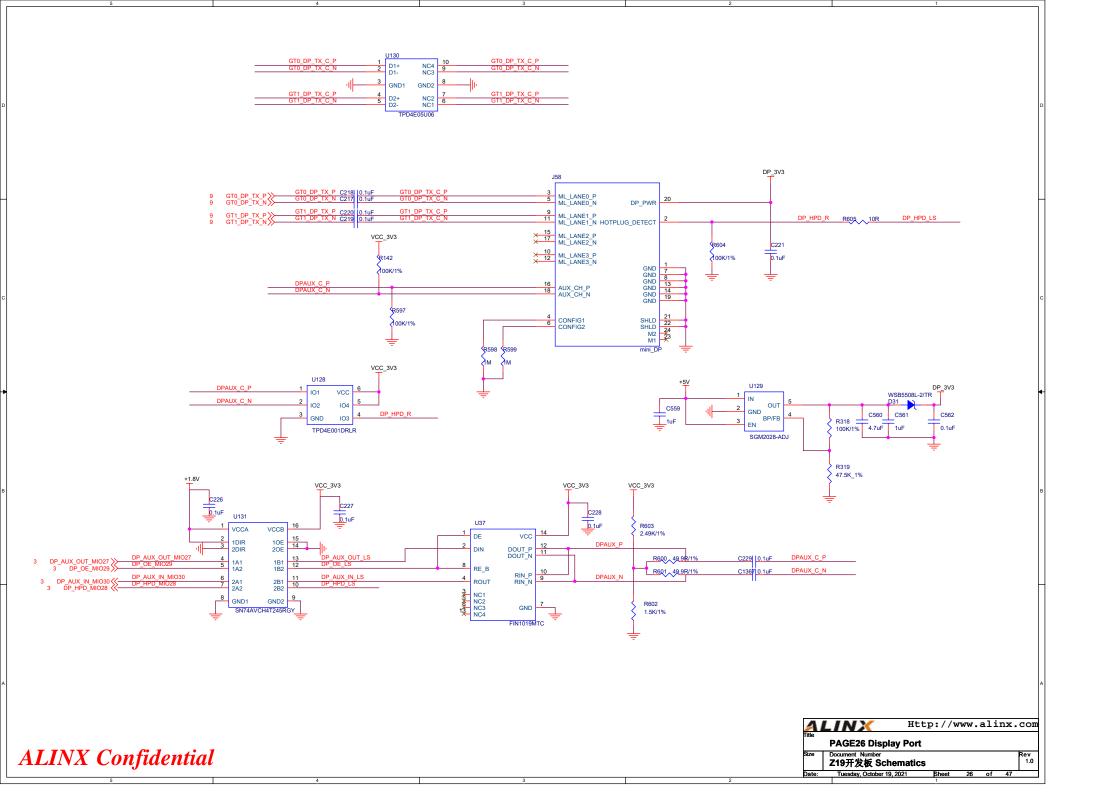


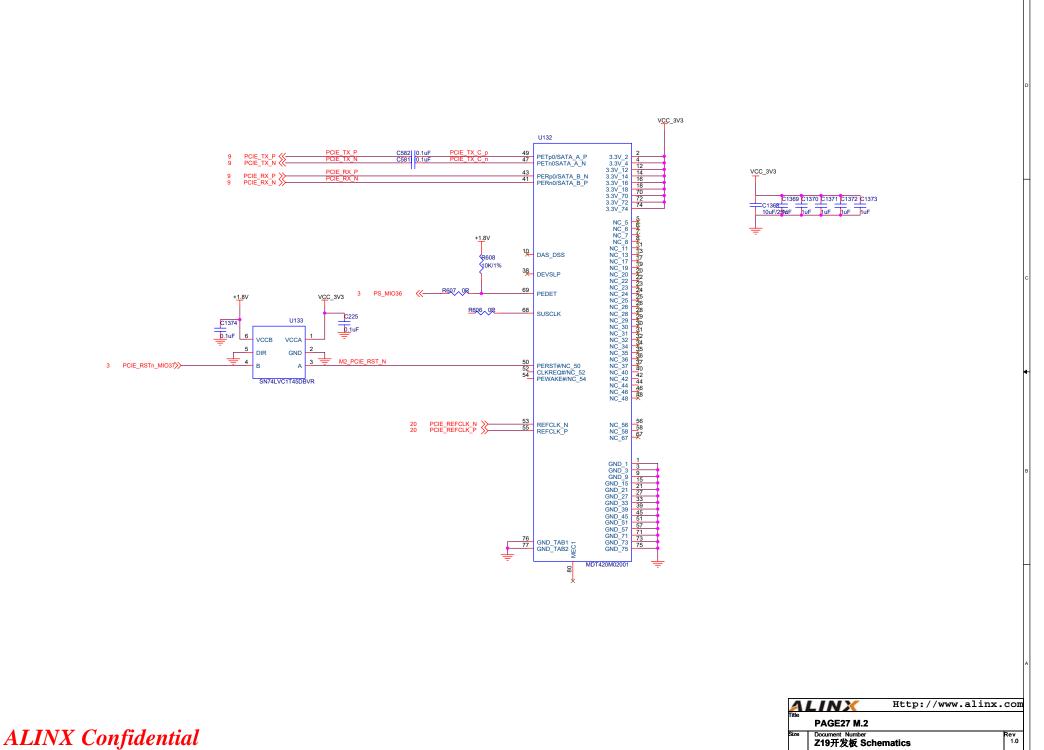






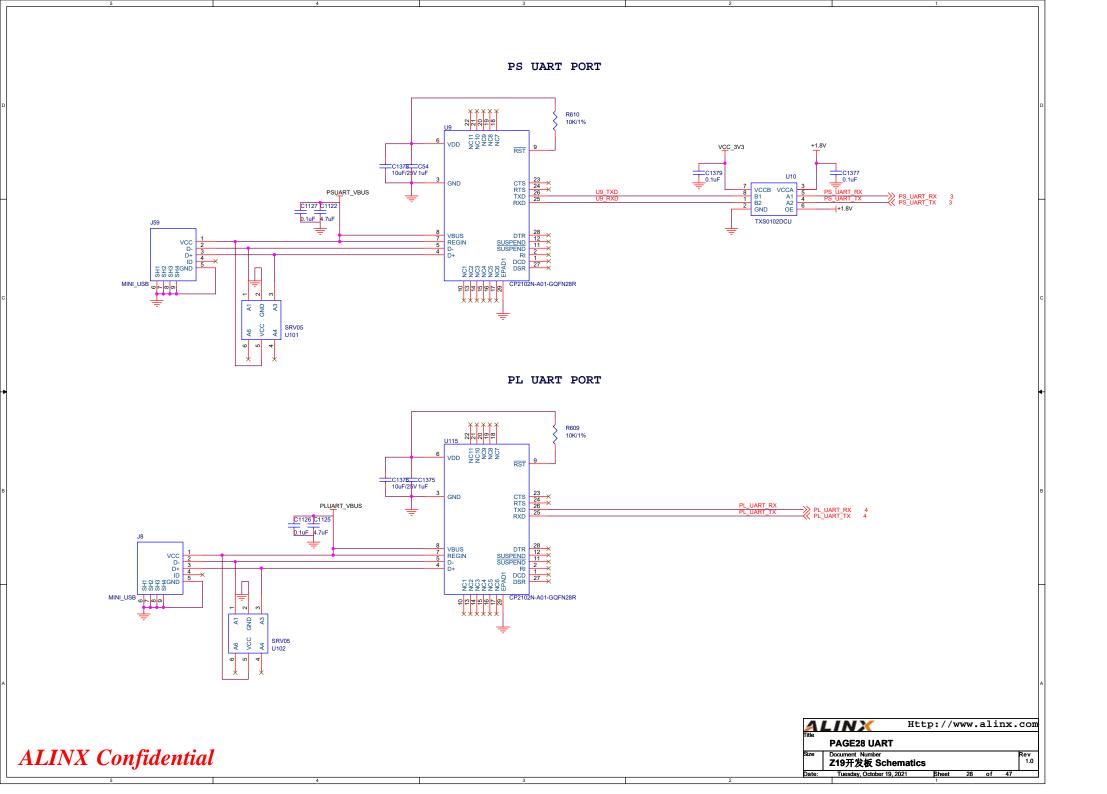


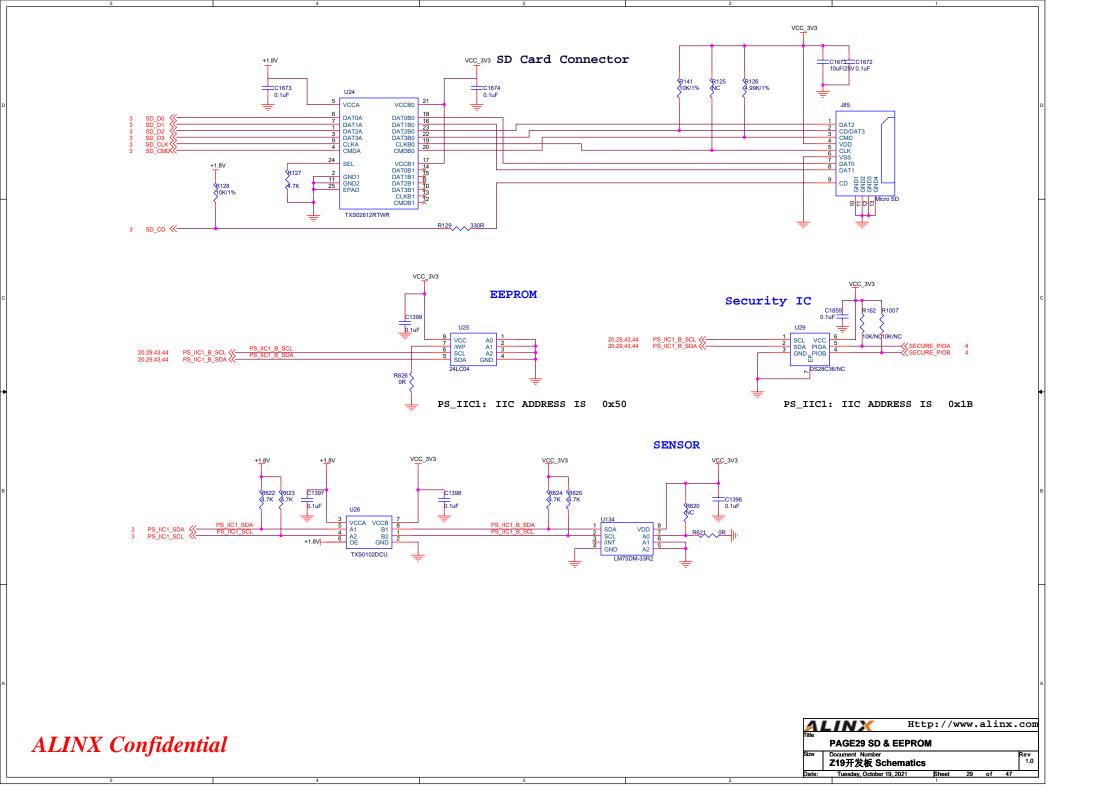


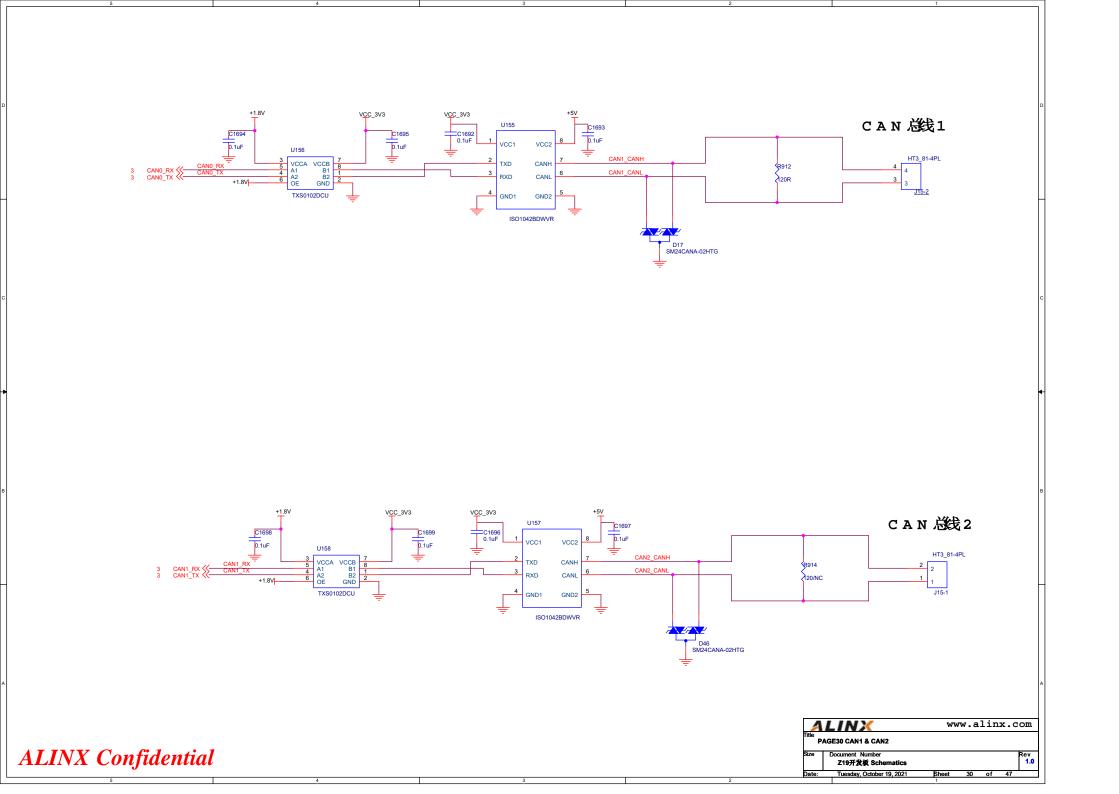


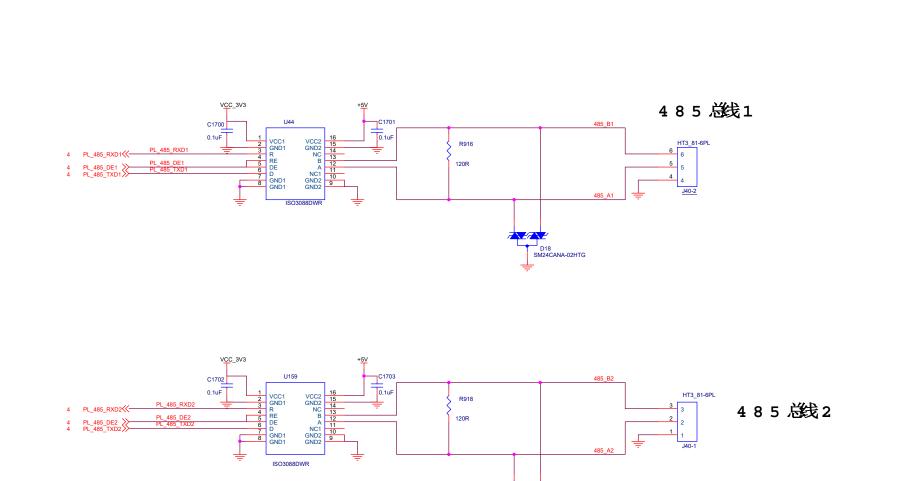
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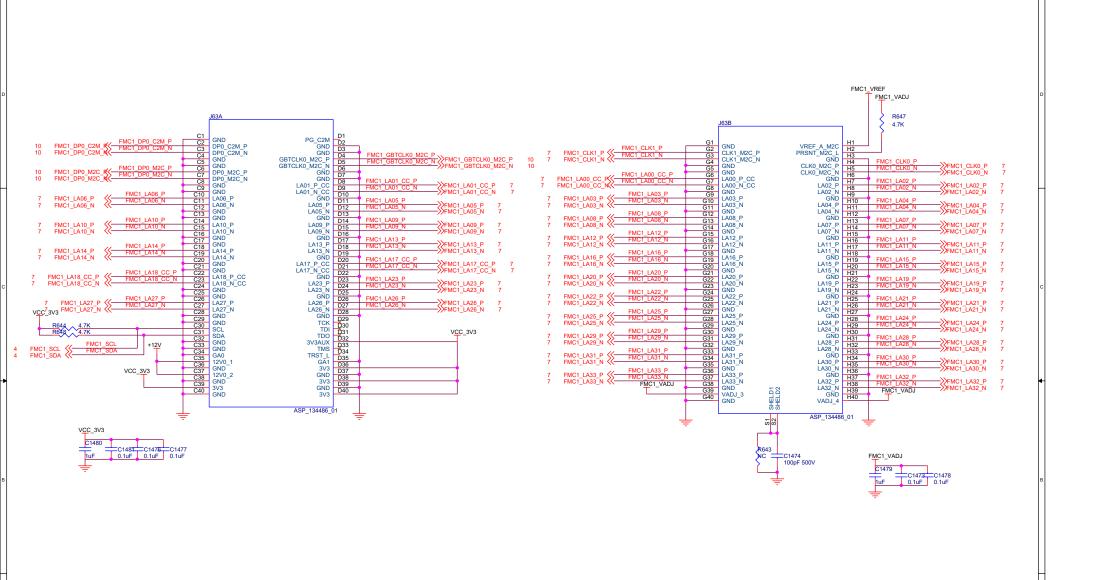




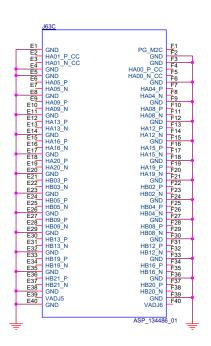


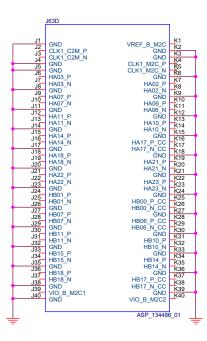
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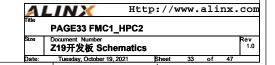
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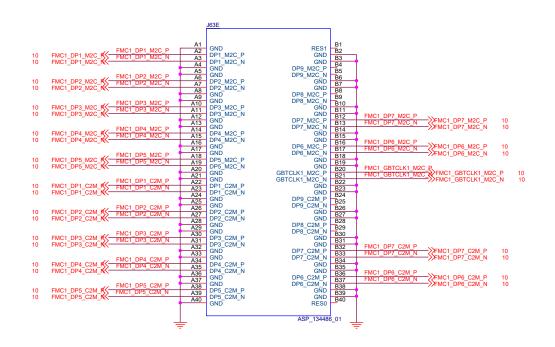


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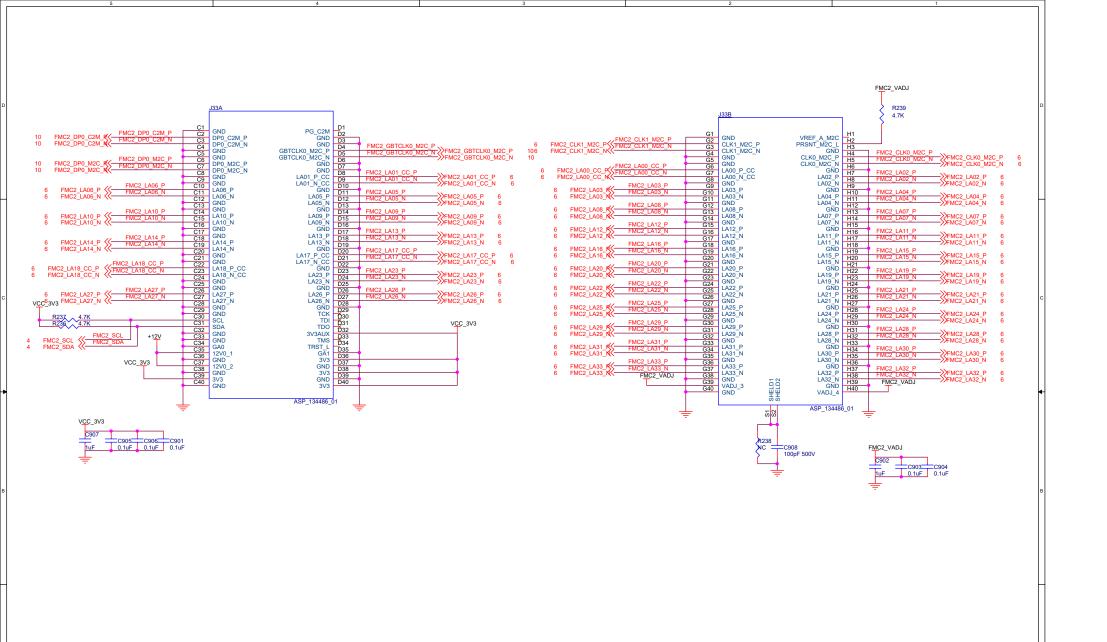








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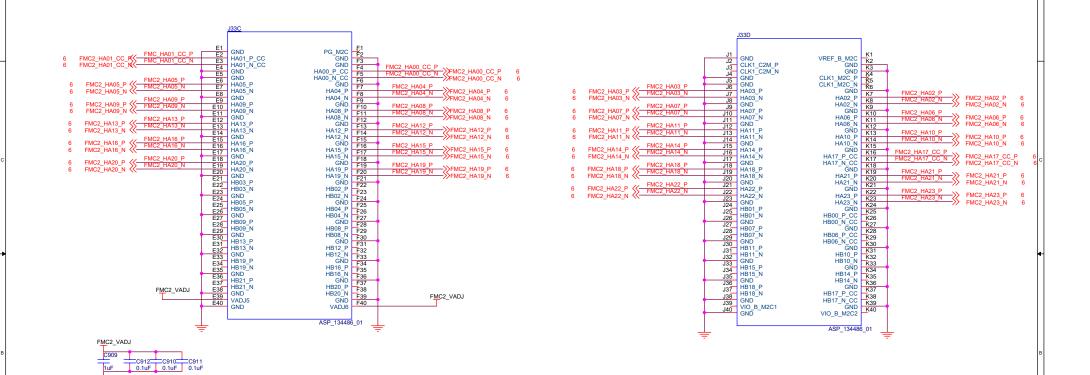


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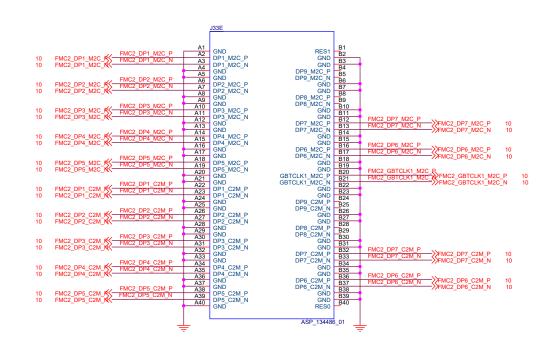


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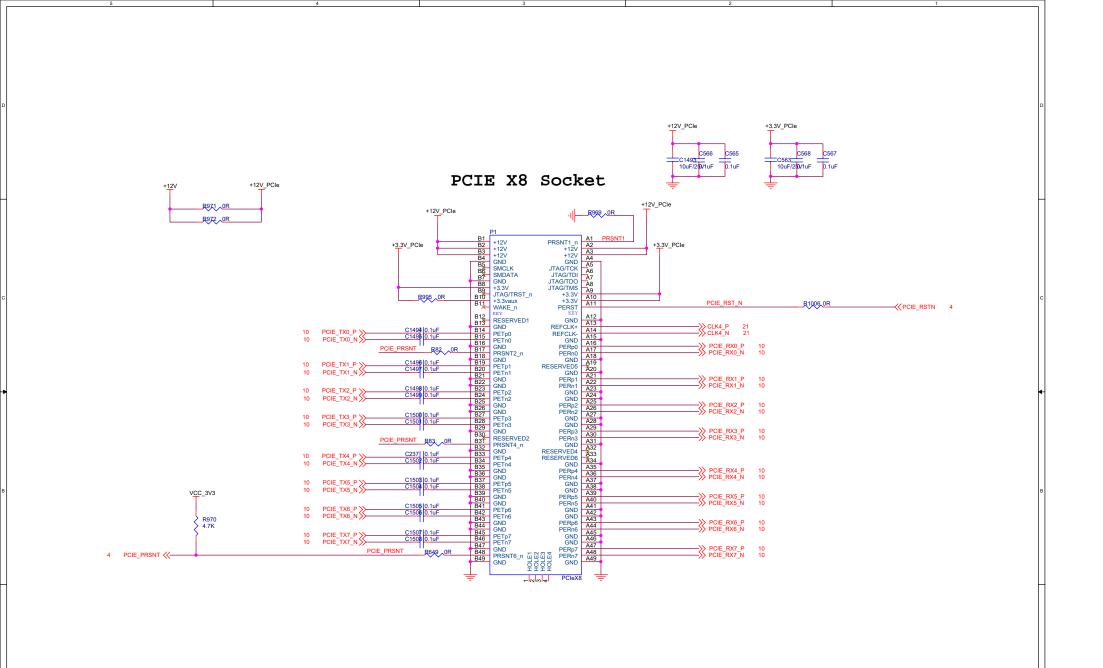


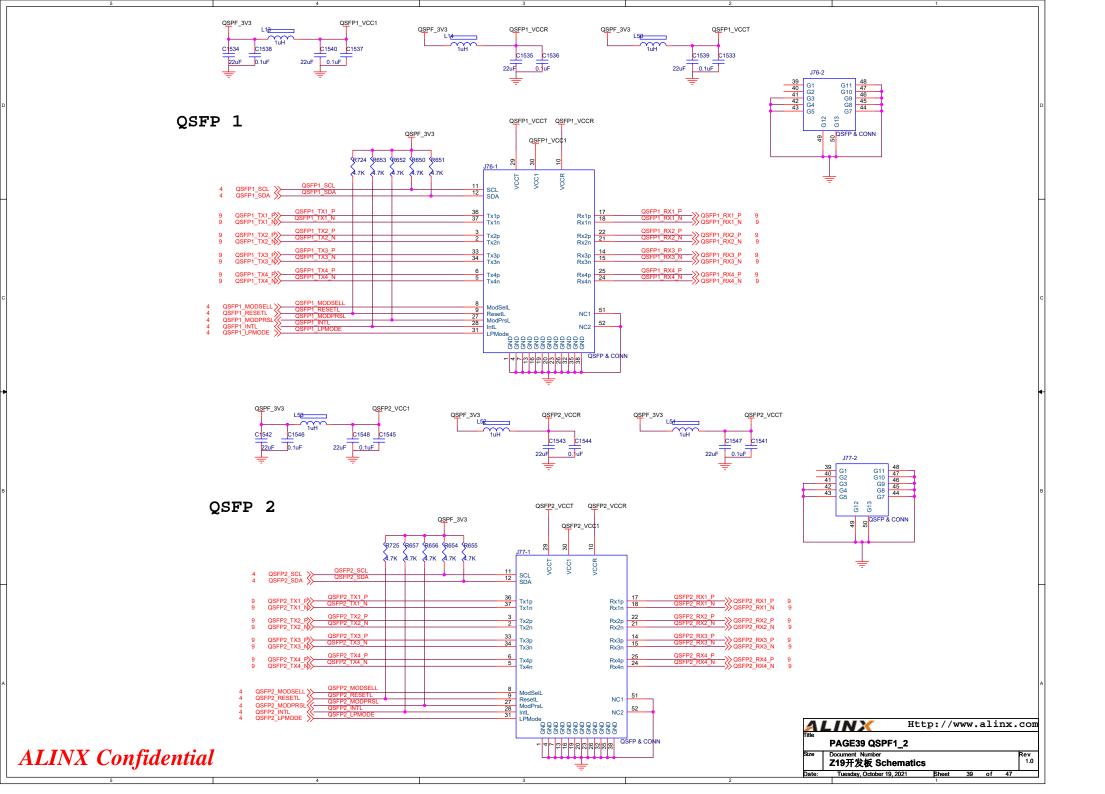
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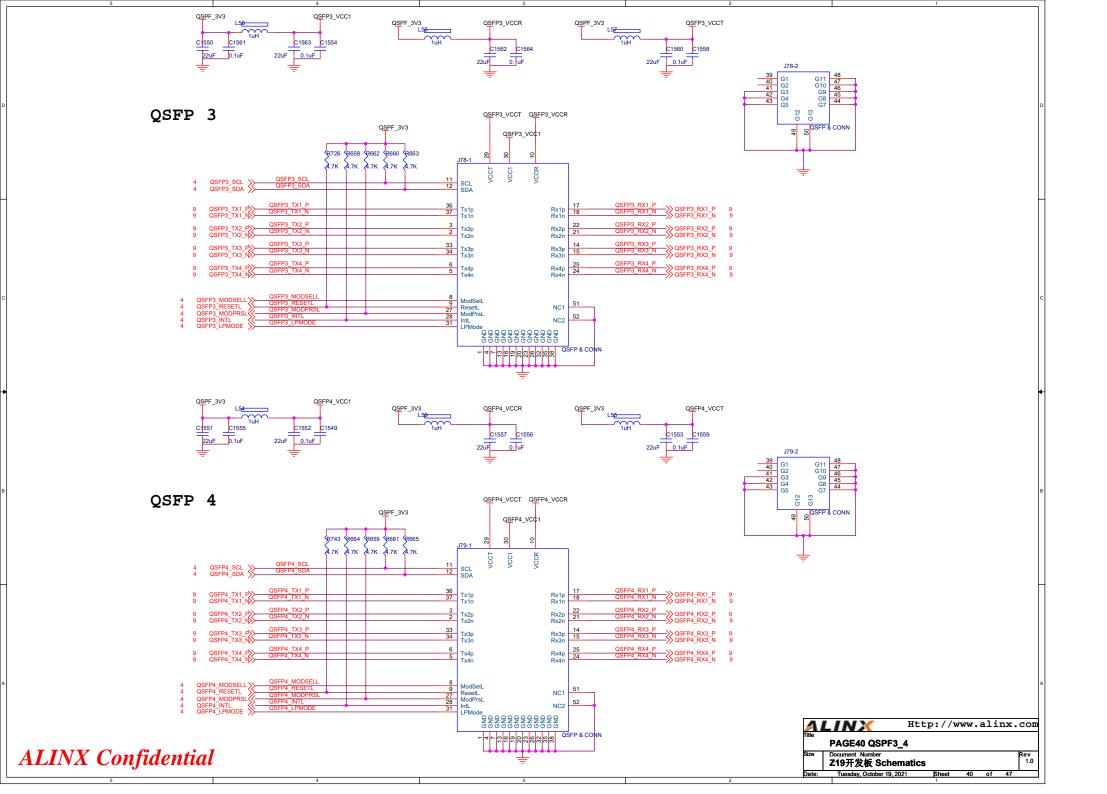
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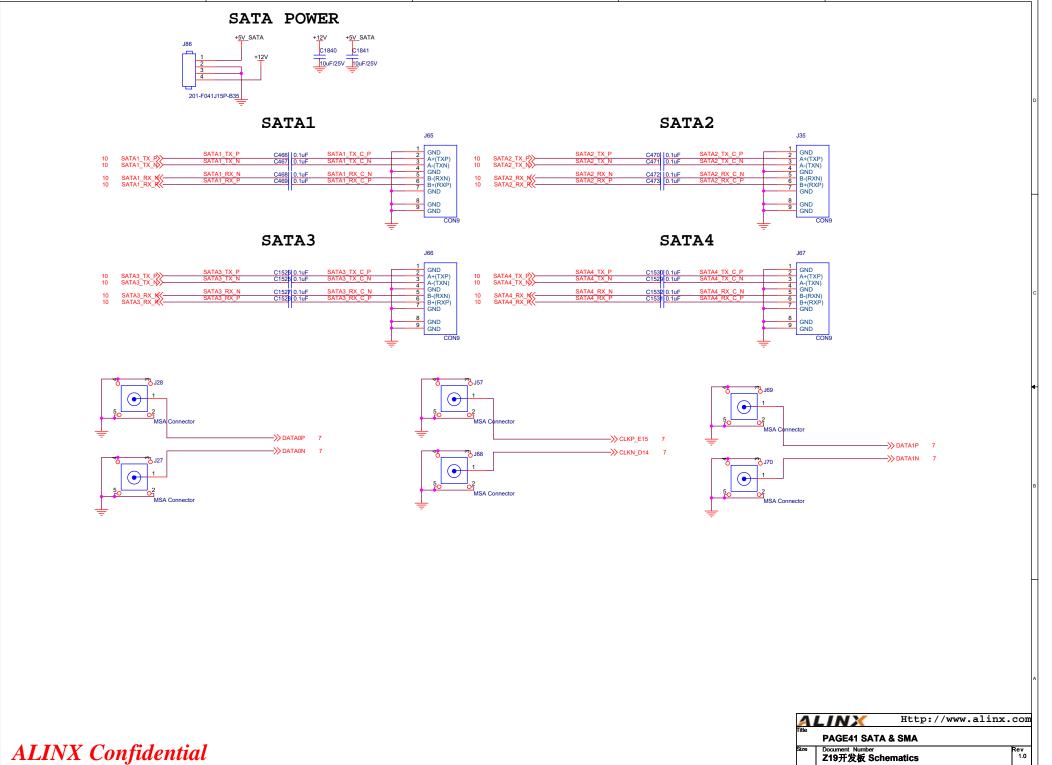
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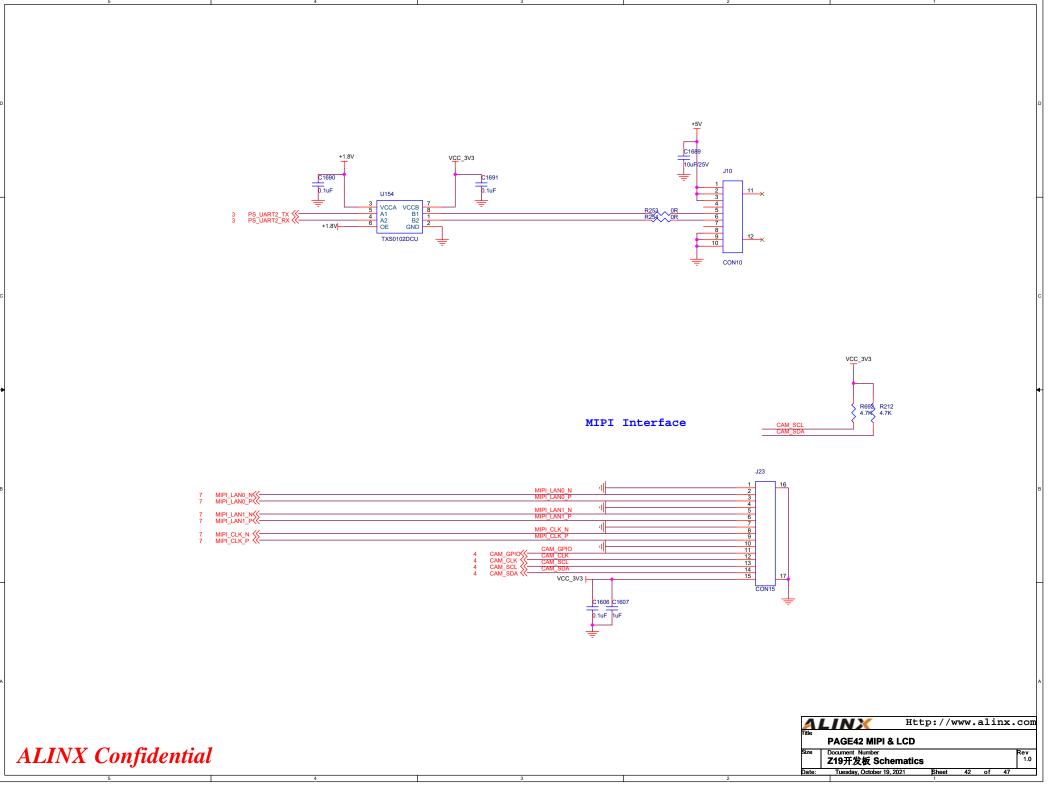


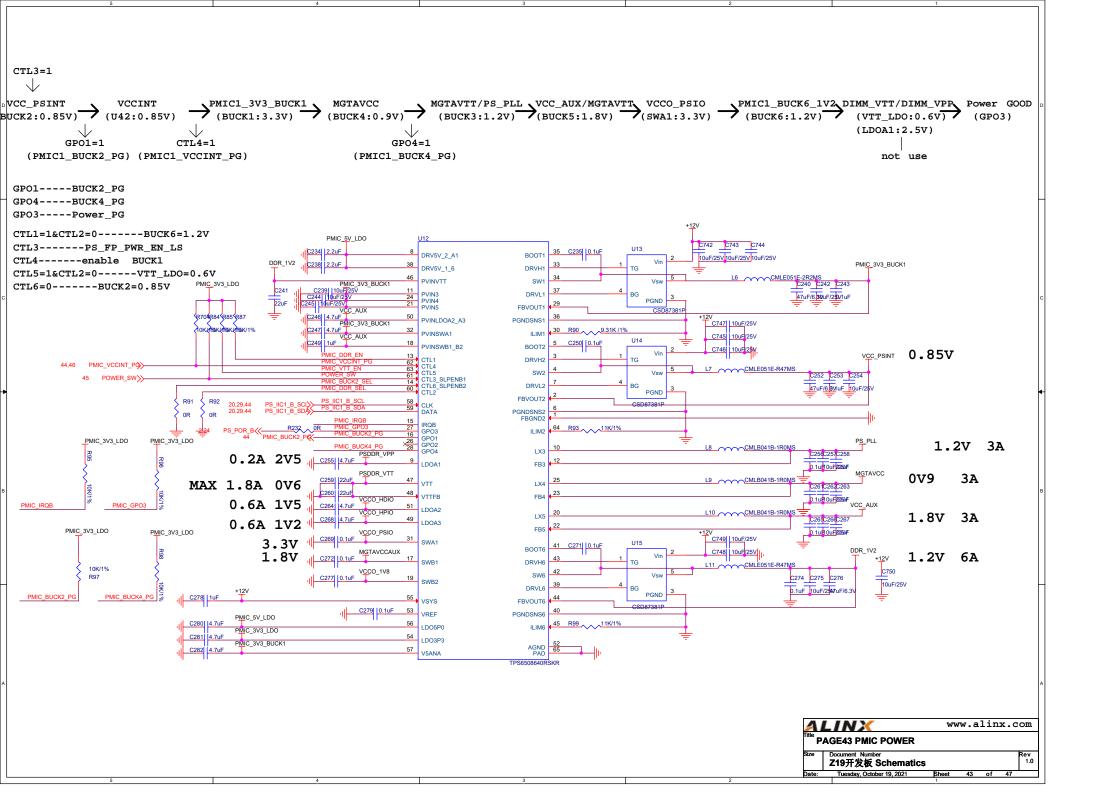






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