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본 수업의 주 교재는 Silberschatz, Galvin, Gagne, "Operating System Concepts Essentials 2nd ed.", Wiley, 또는 한글번역본인 박민규, 조유근, "Operating System Concepts 에센셜 2판", 홍릉과학출판사입니다. 본 강의 동영상의 슬라이드는 이 책의 홈페이지에서 제공하는 것을 사용했음을 밝힙니다 (https://codex.cs.yale.edu/avi/os-book/OSE2/slide-dir/index.html). 다만 강의의 편의를 위해 내용 변경없이 슬라이드 레이아웃을 변경하였고, 진도 관리에 필요한 경우 일부 슬라이드는 생략하였습니다.

Chapter 6: CPU Scheduling

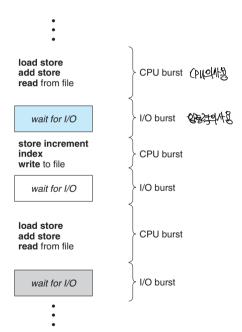
Introduction to CPU scheduling
Various CPU scheduling algorithm
Evaluation criteria for selecting a CPU-scheduling algorithm
scheduling algorithms of several operating systems

Contents

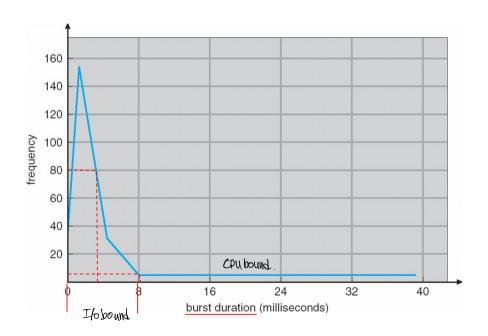
- Basic Concepts
- Scheduling Criteria
- Scheduling Algorithms
- Real-Time CPU Scheduling
- Operating Systems Examples
- Algorithm Evaluation

6.1 Basic Concepts

- CPU burst-I/O Burst Cycle: P rocess execution consists of a cycle of <u>CPU execution</u> and d I/O wait
- CPU burst followed by I/O burst
- CPU burst distribution is of main concern

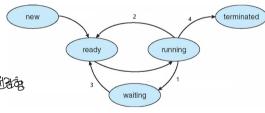


Histogram of CPU-burst Times



CPU Scheduler

- Short-term scheduler selects from among the processes in ready q ueue, and allocates the CPU to one of them હતા.
 - Queue may be ordered in various ways
- CPU scheduling decisions may take place when a process:
 - 1. Switches from running to waiting state
 - 2. Switches from running to ready state
 - 3. Switches from waiting to ready
 - 4. Terminates
- Scheduling under 1 and 4 is nonpreemptive week
- All other scheduling is preemptive
 - Consider access to shared data
 - Consider preemption while in kernel mode
 - Consider interrupts occurring during crucial OS activities



Dispatcher

- Dispatcher module gives control of the CPU to the process se lected by the short-term scheduler; this involves:
 - switching context
 - switching to user mode
 - jumping to the proper location in the user program to restart that p rogram
- Dispatch latency time it takes for the dispatcher to stop on e process and start another running

6.2 Scheduling Criteria

Cpu schedulingal 85217

- **1. CPU utilization** keep the CPU as busy as possible
- 2. Throughput # of processes that complete their execution per time unit
- 3. Turnaround time amount of time to execute a particular process
- **4. Waiting time** amount of time a process has been waiting in the ready queue
- 5. Response time amount of time it takes from when a request was submitted until the first response is produced, not output (for time-sharing environment)

Max CPU utilization 1

是

- Max throughput
- Min turnaround time ↓
- Min waiting time ↓
- Min response time ↓

6.3 Scheduling Algorithms

• First- Come, First-Served (FCFS) Scheduling Will 89,

25HUB 7121	Process	Burst Time	
	P_1	24	
	P_2	3	
	P_3	3	

• Suppose that the processes arrive in the order: P_1 , P_2 , P_3 The Gantt Chart for the schedule is:



- Waiting time for $P_1 = 0$; $P_2 = 24$; $P_3 = 27$
- Average waiting time: (0 + 24 + 27)/3 = 17

FCFS Scheduling (Cont.)

- Suppose that the processes arrive in the order: P₂, P₃, P₁
- The Gantt chart for the schedule is:



- Waiting time for $P_1 = 6$; $P_2 = 0$; $P_3 = 3$
- Average waiting time: (6 + 0 + 3)/3 = 3
- Much better than previous case
- Convoy effect short process behind long process
 - Consider one CPU-bound and many I/O-bound processes

Shortest-Job-First (SJF) Scheduling

- Associate with each process the length of its next CPU burst
 - Use these lengths to schedule the process with the shortest time
- SJF is optimal gives minimum average waiting time for a given set of processes
 - The difficulty is knowing the length of the next CPU request
 - Could ask the user (মপ্র)

Example of SJF

Process	Burst Time		
P_1	6		
P_2	8		
P_3	7		
P_4	3		

• SJF scheduling chart



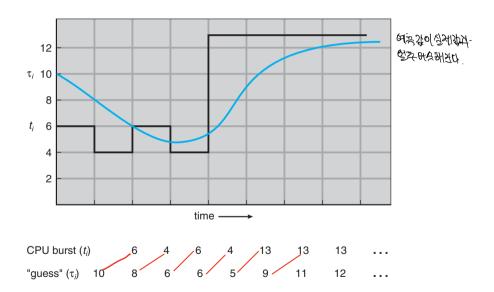
• Average waiting time = (3 + 16 + 9 + 0) / 4 = 7

Determining Length of Next CPU Burst

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- Can only estimate the length should be similar to the previous one
 - Then pick process with shortest predicted next CPU burst
- Can be done by using the length of previous CPU bursts, using exponential averaging
 - 1. t_n = actual length of nth CPU burst থ্যুণা আই-এয়
 - 2. τ_{n+1} = predicted value for the next CPU burst \mathfrak{P}_{n+1}
 - 3. α , $0 \le \alpha \le 1$
 - 4. Define: $\tau_{n+1} = \alpha t_n + (1 \alpha)\tau_n$
- Commonly, α set to $\frac{1}{2}$

Prediction of the Length of the Next CPU Burst



Examples of Exponential Averaging

- $\alpha = 0$ • $\tau_{n+1} = \tau_n$ AARecent history does not count
- $\alpha = 1$
 - $\tau_{n+1} = \alpha t_n$
 - Only the actual last CPU burst counts
- If we expand the formula, we get:

$$\tau_{n+1} = \alpha \ t_n + (1 - \alpha)\alpha \ t_{n-1} + \dots$$

$$+ (1 - \alpha)^n \alpha \ t_{n-j} + \dots$$

$$+ (1 - \alpha)^{n+1} \tau_0$$

• Since both α and (1 - α) are less than or equal to 1, each successive term has less weight than its predecessor

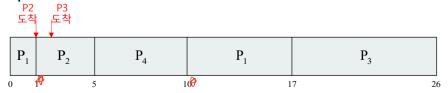
Example of Shortest-remaining-time-first

SJF9 Tybe.

Now we add the concepts of varying arrival times and preemption to the e analysis
 Process Arrival Time Burst Time

Process	Arrival Time	Burst Time	
P_1	0	8 97	
P_2	1	4 4	
P_3	2	9	
P_4	3	5	

• Preemptive SJF Gantt Chart



• Average waiting time = [(0-0)+(10-1)+(1-1)+(1-2)+(5-3)]/4 = 26/4 = 6. 5 msec P₁ P₄

Priority Scheduling

- A priority number (integer) is associated with each process. The CP U is allocated to the process with the highest priority (smallest integer = highest priority)
- Preemptive vs. nonpreemptive
- SJF is priority scheduling where priority is the inverse of predicted next CPU burst time
- ♣ Problem ≡ Starvation low priority processes may never execute
- Solution = Aging as time progresses increase the priority of the process

Example of Priority Scheduling

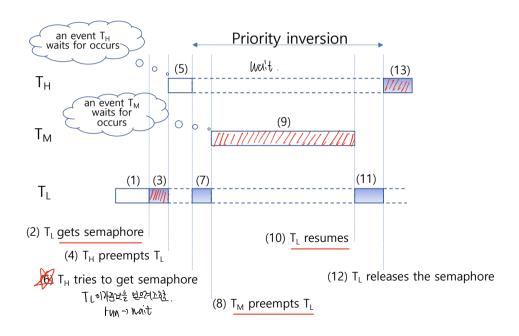
Process	Burst Time	Priority	
P_1	10	3	
P_2	1	1	
P_3	2	4	
P_4	1	5	
P_5	5	2	

• Priority scheduling Gantt Chart

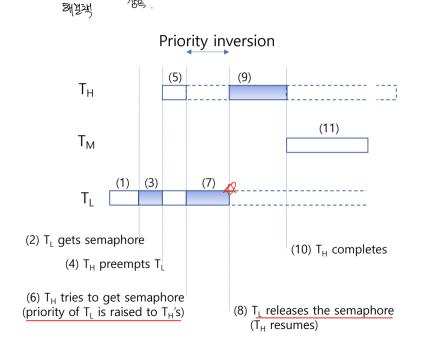


• Average waiting time = 8.2 msec

Priority Inversion কোনিয় নহাল প্রান্থ কর্মা প্রান্থ করে



Priority Inheritance (Donation) Protocol



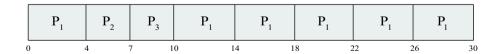
Round Robin (RR) Scheduling

- Each process gets a small unit of CPU time (time quantum q), usu ally 10-100 milliseconds. After this time has elapsed, the process is preempted and added to the end of the ready queue.
- If there are *n* processes in the ready queue and the time quantum is *q*, then each process gets 1/*n* of the CPU time in chunks of at most *q* time units at once. No process waits more than (*n*-1)*q* time units.
- Timer interrupts every quantum to schedule next process
- Performance
 - $q \text{ large} \Rightarrow \text{FIFO}$
 - q small $\Rightarrow q$ must be large with respect to context switch, otherwise overhead is too high

Example of RR with Time Quantum = 4

Process	Burst Time		
P_1	24		
P_2	3		
P_3	3		

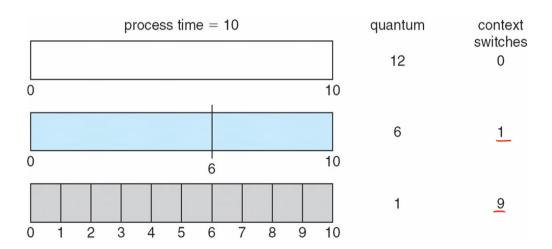
• The Gantt chart is:



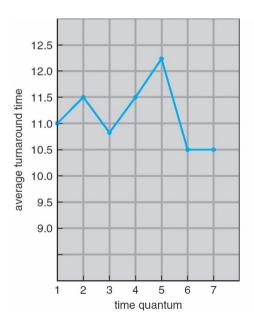
Typically, higher average turnaround than SJF, but better response

Time Quantum and Context Switch Time

• q should be large compared to context switch time; q usually 10ms to 100ms, context switch < 10 usec



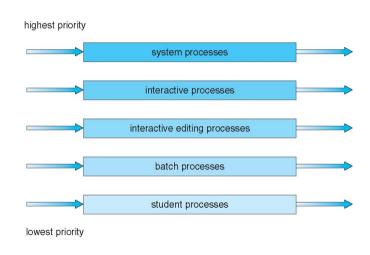
Turnaround Time Varies With The Time Quantum



process	time
P ₁	6
P_2	3
P_3	1
P_4	7

Multilevel Queue

- Ready queue is partitioned into separate queues, eg:
 - foreground (interactive) দাস্ত
 - background (batch)
- Process permanently in a given queue
- <u>Each queue</u> has its own scheduling algor ithm:
 - foreground RR
 - background FCFS
- Scheduling must be done between the q ueues: প্রস্তিগ্রহ্ণ এই স্কর্মন স্কর্মন ক্রামন্ত্র্যা এই বিষয় স্কর্মন ক্রামন্ত্র্যা এই বিষয় স্কর্মন ক্রামন্ত্র্যা ক্রামন্ত্র্যা বিষয় বিষয় বিষয় স্কর্মন ক্রামন্ত্র্যা বিষয় বিষয়
 - Fixed priority scheduling; (i.e., serve all from foreground then from background). Possibility of starvation.
 - Time slice each queue gets a certain amo unt of CPU time which it can schedule amo ngst its processes; i.e., 80% to foreground i n RR and 20% to background in FCFS



Multilevel Feedback Queue

- A process can move between the various queues; aging can be implemented this way
- Multilevel-feedback-queue scheduler defined by the following parameters:
 - number of queues
 - scheduling algorithms for each queue
 - method used to determine when to upgrade a process
 - method used to determine when to demote a process
 - method used to determine which queue a process will enter when t hat process needs service

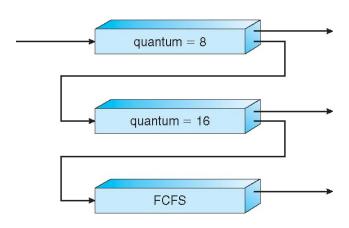
Example of Multilevel Feedback Queue

Three queues:

- Q_0 RR with time quantum 8 milliseconds $\chi \leq 8$
- Q₁ RR time quantum 16 milliseconds をくなくは
- Q₂ FCFS /6 <

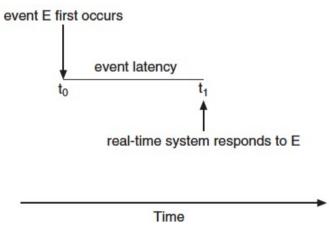
Scheduling

- A new job enters queue Q_0 which is served FCFS
 - When it gains CPU, job receives 8 milliseconds
 - If it does not finish in 8 milliseconds, job is moved to queue Q_1
- At Q₁ job is again served FCFS and receives 16 additional milliseconds
 - If it still does not complete, it is preempted an d moved to queue Q_2



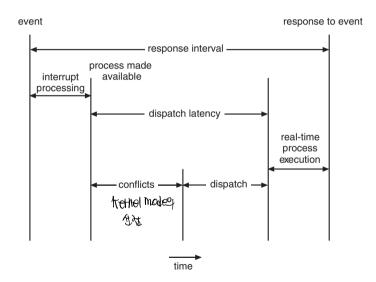
6.6 Real-Time CPU Scheduling

- Real-time system is event-drive
 - Soft real-time systems vs. Hard real-time systems
- Event latency
- Two types of latencies affect performance
 - 1. Interrupt latency time from ar rival of interrupt to start of rout ine that services interrupt
 - 2. Dispatch latency time for sch edule to take current process of f CPU and switch to another



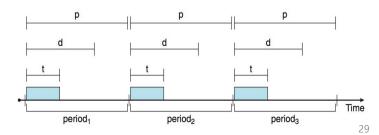
Real-Time CPU Scheduling (Cont.)

- Conflict phase of dispatch lat ency:
 - 1.Preemption of any process run ning in kernel mode
 - 2.Release by low-priority proces s of resources needed by high -priority processes



Priority-based Scheduling

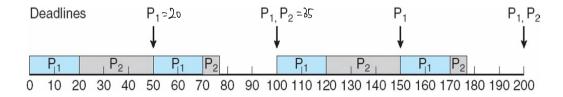
- For real-time scheduling, scheduler must support preemptive, prio rity-based scheduling া ক্রা মে আন ক্রান্ত না
 - But only guarantee soft real-time task = process
- For hard real-time must also provide ability to meet deadlines
- Processes have new characteristics: periodic ones require CPU at constant intervals
 - Has processing time t, deadline d, period p
 - $0 \le t \le d \le p$
 - Rate of periodic task is 1/p



Rate Montonic Scheduling

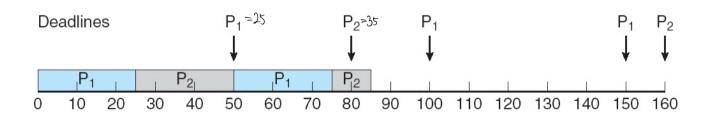
• A priority is assigned based on the inverse of its period

- Shorter periods = higher priority; Longer periods = lower priority
- P₁ is assigned a higher priority than P₂.



Missed Deadlines with Rate Monotonic Scheduling

$$P_1 = \frac{25}{50}$$
 $p_2 = \frac{35}{50}$.

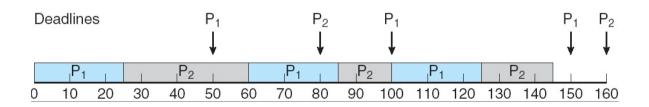


Earliest Deadline First Scheduling (EDF)

Dynamic

Priorities are assigned according to deadlines:

the earlier the deadline, the higher the priority; the later the deadline, the lower the priority



Proportional Share Scheduling

- T shares are allocated among all processes in the system
- An application receives N shares where N < T
- This ensures each application will receive N/T of the total processor time

6.7 Operating System Examples

- Linux scheduling
- Windows scheduling

Linux Scheduling Through Version 2.5

- Prior to kernel version 2.5, ran variation of standard UNIX scheduling algorithm
- Version 2.5 moved to constant order O(1) scheduling time
 - Preemptive, priority based
 - Two priority ranges: time-sharing and real-time
 - ₩ Real-time range from 0 to 99 and nice value from 100 to 140
 - Map into global priority with numerically lower values indicating higher priority
 - Higher priority gets larger q
 - Task run-able as long as time left in time slice (active)
 - If no time left (expired), not run-able until all other tasks use their slices
 - All run-able tasks tracked in per-CPU runqueue data structure
 - Worked well, but poor response times for interactive processes

Linux Scheduling in Version 2.6.23 +

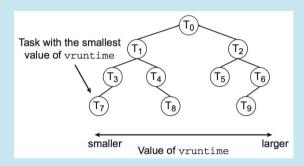
Completely Fair Scheduler (CFS)



- Each has specific priority
- Scheduler picks highest priority task in highest scheduling class
- 2 scheduling classes included, others can be added: default and real-time
- Rather than quantum based on fixed time allotments, based on proportion of CPU time 般沙心.
 - Quantum calculated based on nice value from -20 to +19
 - Lower value is higher priority
 - Calculates target latency interval of time during which task should run at least once
 - Target latency can increase if say number of active tasks increases
- CFS scheduler maintains per task virtual run time in variable vruntime
 - Associated with decay factor based on priority of task lower priority is higher decay rate
 - Normal default priority yields virtual run time = actual run time
- To decide next task to run, scheduler picks task with lowest virtual run time

CFS Performance binary seach thee key: Vhuntime.

The Linux CFS scheduler provides an efficient algorithm for selecting which task to run next. Each runnable task is placed in a red-black tree—a balanced binary search tree whose key is based on the value of vruntime. This tree is shown below:



When a task becomes runnable, it is added to the tree. If a task on the tree is not runnable (for example, if it is blocked while waiting for I/O), it is removed. Generally speaking, tasks that have been given less processing time (smaller values of vruntime) are toward the left side of the tree, and tasks that have been given more processing time are on the right side. According to the properties of a binary search tree, the leftmost node has the smallest key value, which for the sake of the CFS scheduler means that it is the task with the highest priority. Because the red-black tree is balanced, navigating it to discover the leftmost node will require O(lgN) operations (where N is the number of nodes in the tree). However, for efficiency reasons, the Linux scheduler caches this value in the variable rb_leftmost, and thus determining which task to run next requires only retrieving the cached value.

Windows Scheduling

- Windows uses priority-based preemptive scheduling
- Highest-priority thread runs next
- Dispatcher is scheduler
- Thread runs until (1) blocks, (2) uses time slice, (3) preempted by h igher-priority thread
- Real-time threads can preempt non-real-time
- 32-level priority scheme
- Variable class is 1-15, real-time class is 16-31 ০২ দেশতান্য শুখ্ৰ
- Priority 0 is memory-management thread
- Queue for each priority
- If no run-able thread, runs idle thread

Windows Priority Classes

- Win32 API identifies several priority classes to which a process can belong
 - REALTIME_PRIORITY_CLASS, HIGH_PRIORITY_CLASS, ABOVE_NORMAL_PRIORITY_CLASS, NORMAL_PRIORITY_CLASS, BELOW_NORMAL_PRIORITY_CLASS, IDLE_PRIORITY_CLASS
 - All are variable except REALTIME
- A thread within a given priority class has a relative priority one class of the c
 - TIME_CRITICAL, HIGHEST, ABOVE_NORMAL, NORMAL, BELOW_NORMAL, L OWEST, IDLE ⇒ b¾
- Priority class and relative priority combine to give numeric priority
- Base priority is NORMAL within the class
- If quantum expires, priority lowered, but never below base

Windows Priorities

	real- time	high	above normal	normal	below normal	idle priority
time-critical	31	15	15	15	15	15
highest	26	15	12	10	8	6
above normal	25	14	11	9	7	5
normal	24	13	10	8	6	4
below normal	23	12	9	7	5	3
lowest	22	11	8	6	4	2
idle	16	1	1	1	1	1