Consolidated UCF for the Complete Board

Clock and Reset

```
net "CLK_4M" loc = "p76";
net "CLK_OPT" loc = "p77";
```

```
net "RESET" loc = "p108";
```

Test LEDs

```
net "OL<0>" loc = "p139";
net "OL<1>" loc = "p140";
net "OL<2>" loc = "p137";
net "OL<3>" loc = "p138";
net "OL<4>" loc = "p133";
net "OL<5>" loc = "p135";
net "OL<6>" loc = "p131";
net "OL<7>" loc = "p132";
net "OL<8>" loc = "p149";
net "OL<9>" loc = "p150";
net "OL<10>" loc = "p147";
net "OL<11>" loc = "p148";
net "OL<12>" loc = "p144";
net "OL<13>" loc = "p146";
net "OL<14>" loc = "p141";
net "OL<15>" loc = "p143";
net "OL<16>" loc = "p181";
net "OL<17>" loc = "p176";
net "OL<18>" loc = "p172";
net "OL<19>" loc = "p175";
net "OL<20>" loc = "p169";
net "OL<21>" loc = "p168";
net "OL<22>" loc = "p167";
net "OL<23>" loc = "p171";
net "OL<24>" loc = "p165";
net "OL<25>" loc = "p166";
net "OL<26>" loc = "p161";
net "OL<27>" loc = "p162";
net "OL<28>" loc = "p155";
net "OL<29>" loc = "p156";
net "OL<30>" loc = "p152";
net "OL<31>" loc = "p154";
```

LCD Interface

Data Lines

```
net "LCD_D<0>" loc = "p46";
net "LCD_D<1>" loc = "p43";
```

net "LCD_D<2>"	loc = "p44";
net "LCD_D<3>"	loc = "p40";
net "LCD_D<4>"	loc = "p42";
net "LCD_D<5>"	loc = "p37";
net "LCD_D<6>"	loc = "p39";
net "LCD_D<7>"	loc = "p35";
Control Signals	
net "LCD_E"	loc = "p45";
net "LCD_RS"	loc = "p48";
net "LCD_RW"	loc = "p50";

RS232 Interface

net "RS232_RXD"	loc = "p111";
net "RS232_TXD"	loc = "p109";

Seven Segment Interface

7 Segments	
net "SEGA"	loc = "p120";
net "SEGB"	loc = "p124";
net "SEGC"	loc = "p123";
net "SEGD"	loc = "p126";
net "SEGDP"	loc = "p122";
net "SEGE"	loc = "p125";
net "SEGF"	loc = "p130";
net "SEGG"	loc = "p128";
Enable Signals	
net "DIS<0>"	loc = "p117";
net "DIS<1>"	loc = "p119";
net "DIS<2>"	loc = "p115";
net "DIS<3>"	loc = "p116";
net "DIS<4>"	loc = "p114";
net "DIS<5>"	loc = "p113";

Keys

Return Lines	
net "RL<0>"	loc = "p189";
net "RL<1>"	loc = "p182";
net "RL<2>"	loc = "p183";
net "RL<3>"	loc = "p180";
Scan Lines	
net "SL<0>"	loc = "p178";
net "SL<1>"	loc = "p184";
net "SL<2>"	loc = "p185";
net "SL<3>"	loc = "p187";

Input Switches

net "IL<0>"	loc = "p191";
net "IL<1>"	loc = "p190";

net "IL<2>"	loc = "p196";
net "IL<3>"	loc = "p194";
net "IL<4>"	loc = "p198";
net "IL<5>"	loc = "p197";
net "IL<6>"	loc = "p200";
net "IL<7>"	loc = "p199";
net "IL<8>"	loc = "p5";
net "IL<9>"	loc = "p7";
net "IL<10>"	loc = "p3";
net "IL<11>"	loc = "p4";
net "IL<12>"	loc = "p205";
net "IL<13>"	loc = "p2";
net "IL<14>"	loc = "p203";
net "IL<15>"	loc = "p204";
net "IL<16>"	loc = "p27";
net "IL<17>"	loc = "p28";
net "IL<18>"	loc = "p24";
net "IL<19>"	loc = "p26";
net "IL<20>"	loc = "p21";
net "IL<21>"	loc = "p22";
net "IL<22>"	loc = "p19";
net "IL<23>"	loc = "p20";
net "IL<24>"	loc = "p10";
net "IL<25>"	loc = "p9";
net "IL<26>"	loc = "p12";
net "IL<27>"	loc = "p11";
net "IL<28>"	loc = "p15";
net "IL<29>"	loc = "p13";
net "IL<30>"	loc = "p18";
net "IL<31>"	loc = "p16";