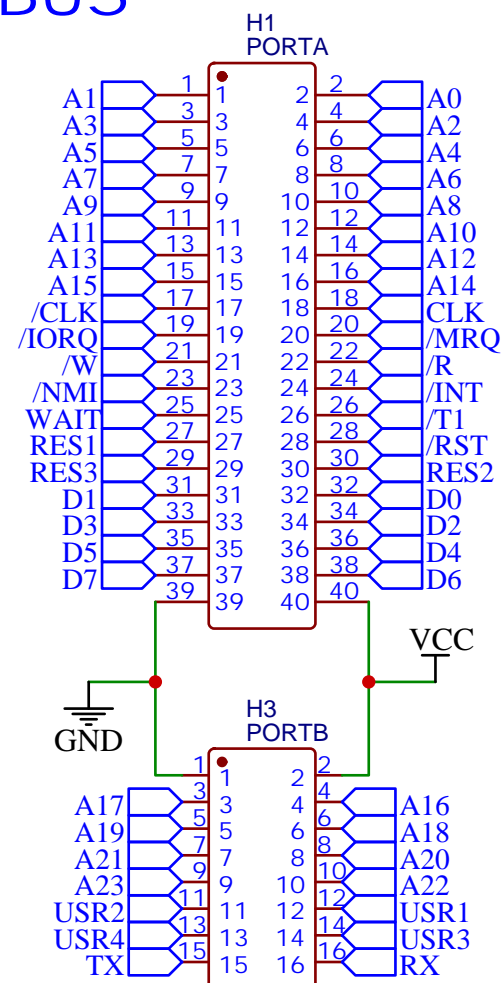
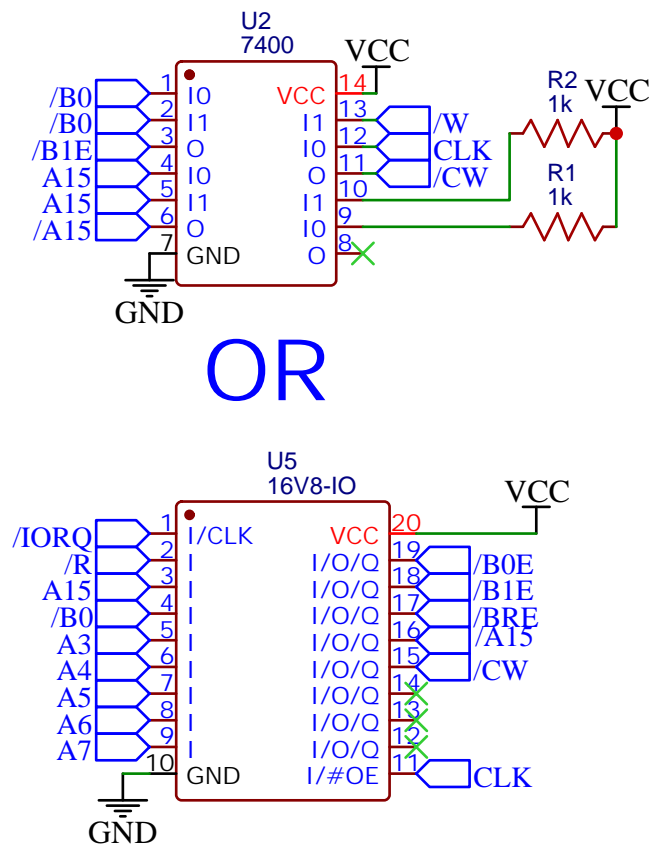


## BUS



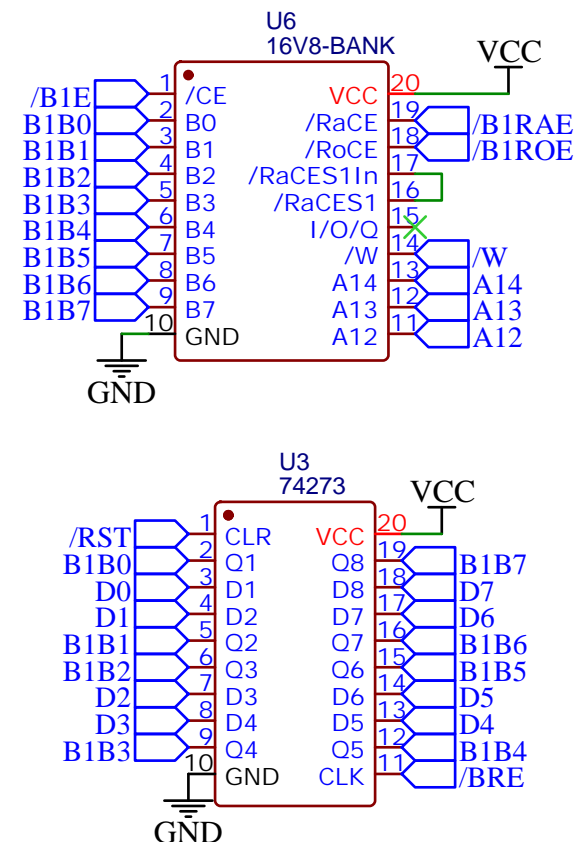
## I/O CTRL



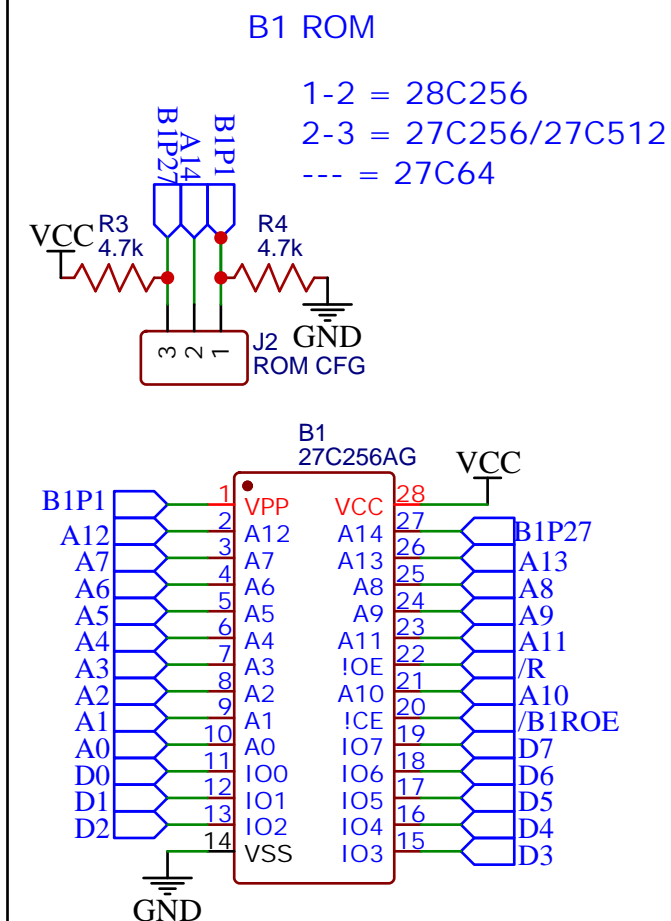
OR

(Required if using B1 Control)

## BANK 1 CONTROL (Optional)



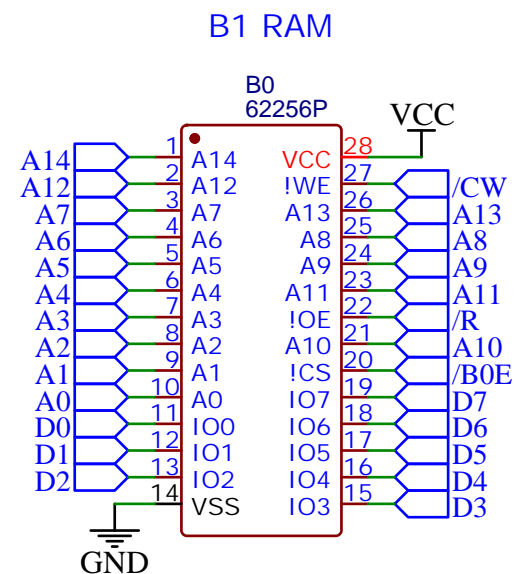
## BANK 1



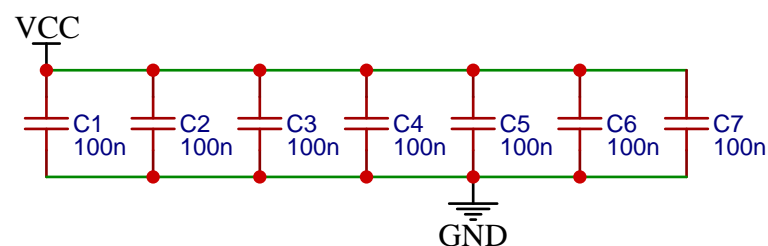
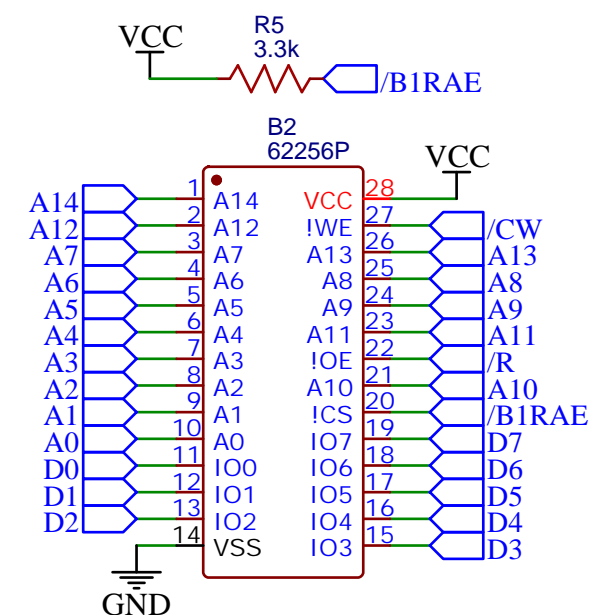
## CONFIG



## BANK 0



## B1 RAM (Optional)



TITLE:

HBC-56 RAM/ROM Card

REV: 1.0

EasyEDA

Company: Troy Schrapel

Sheet: 1/1

Date: 2022-03-17 Drawn By: visrealm