Avant Electronics. SBN1661G Command Summary Table.
Dot-matrix STN LCD Driver with 32-row x 80-column Display Data Memory

	Command				CODE									Function
		A0	RD(E)	WR(RW)	HEX	D7	D6	D5	D4	D3	D2	D1	D0	
1	Display ON/OFF	0	1	0	AE(Off) AF(On)	1	0	1	0	1	1	1	0/1	Turns display 1:On, 0:Off
2	Display Start Line	0	1	0	C0+0~31	1	1	0	Display start address (0 to 31)					Specifies RAM line corresponding to top line of display.
3	Set Page Address	0	1	0	B8+0~3	1	0	1	1	1	0	Page (0 to 3)		Sets display RAM page in page address register.
4	Set Column Address	0	1	0	0 ~ 4F	0	Column address (0 to 79)							Sets display RAM column address in column address register.
5	Set Column/Segment Mapping	0	1	0	A0(Normal) A1(Inverted)	1	0	1	0	0	0	0	0/1	Mapping between Memory Cloumns and Segments and the Column/Segment Mapping Register
6	Static Drive ON/OFF	0	1	0	A4(Off) A5(On)	1	0	1	0	0	1	0	0/1	Selects static driving operation. 1: Static drive, 0: Normal drive
7	Select Duty	0	1	0	A8(1/16) A9(1/32)	1	0	1	0	1	0	0	0/1	Selects LCD duty cycle: 1: 1/32, 0: 1/16
8	Read Status	0	0	1		Busy	ADC	ON/OFF	Reset	0	0	0	0	Reads the following status: BUSY 1: Busy 0: Ready ADC 1: CW output 0: CCW output ON/OFF 1: Display off 0: Display on RESET 1: Being reset 0: Normal
9	Write Display Data	1	1	0		Write data								Writes data from data bus into display RAM.
10	Read Display Data	1	0	1		Read data								Reads data from display RAM into data bus.
11	Read-Modify-Write	0	1	0	E0	1	1	1	0	0	0	0	0	Read-Modify-Write ON
12	End	0	1	0	EE	1	1	1	0	1	1	1	0	Read-Modify-Write OFF
13	Reset	0	1	0	E2	1	1	1	0	0	0	1	0	Software reset