

Matthew Genovese

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Summary

Well-connected in the Austin-area technology community with an excellent rapport among local technical, human resources, and executive personnel. Solid engineering background from software to semiconductors with an understanding of technology talent shortages and associated problems with the hiring process encountered by both employers and candidates.

Network

Beyond local employers, have established relationships with key individuals at the following organizations:

Austin Chamber of Commerce	Central Texas Angel Network	St. Edward's University
Austin Community College	City of Austin Economic Development	Tech Ranch Austin
Austin Java Users Group	Door64	Texas Office of the Governor
Austin Technology Council	Goodwill	Texas State Technical College
Austin Ventures	Houston Technology Center	Texas Workforce Commission
Campus2Careers	IEEE	University of Texas at Austin
Capital Factory	Skillpoint Alliance	Workforce Solutions

Professional Experience

2007 – 2012

Founder and CEO · Door64, Austin, Texas.

- ❖ Founded Door64 to serve the Austin-area technology professionals through high-value career-oriented events focused on connecting individuals and employers. Worked with clients like Samsung, eBay, Oracle, PayPal, AT&T, Freescale, Rackspace, Texas Instruments, and many more.
- ❖ Innovated the standard technical recruiting process by creating unique face-to-face engagements between Austin technology professionals and employers. Amplified employers' recruiting efficiency, often decreasing the time required to find and hire otherwise hard-to-reach tech talent.
- ❖ Over five years, successfully marketed and grew the Door64 membership to 30,000 local technology professionals, and was recognized as the top Business and Networking organization by the *Austin Business Journal* for two consecutive years (2011-2012).
- ❖ Designed and executed surveys and met with employers to specifically understand the critical technology hiring needs in the Austin-area. Results published and used to create focused events to address the needs of local employers and candidates.
- ❖ Growing to a paid staff of four, Door64 hosted over 100 events including focused job fairs, networking happy hours, SXSW™ Interactive events, technical conferences, and tech-focused meet-ups.
- ❖ Recognized and interviewed by print newspaper articles and online blogs, and regularly gave talks and participated in panels covering topics from career issues faced by technology professionals to growing online communities.

2009 – 2011

CEO · JobCannon, Austin, Texas.

- ❖ Joined JobCannon team to help design and refine an online software tool that helps job seekers manage and drive their job search progress.
- ❖ Met with job seekers to understand problems and translate into requirements, features, and written specification, and managed the software development team's efforts.
- ❖ Created training program along with volunteering users, and oversaw class for new JobCannon users to encourage adoption.
- ❖ Developed marketing website, including design, copy, and complete implementation.

Additional Tenures

2006 – 2009

Software Architect and Developer Consultant · The Learning Labs, Inc., Austin, Texas.

- ❖ From concept to implementation, designed a stand-alone Java GUI application using Eclipse and SWT to create a rich-client platform application intended for students and professors in a university educational environment.
- ❖ The overall solution was a combination of hardware and software. Architected and developed the desktop software application from the user-experience down to hardware driver interface.
- ❖ Managed two off-shore development teams who implemented driver, firmware, licensing, and installation package development.

2002 – 2009

VLSI Design Verification Engineer · PowerPC Design Center, Freescale Semiconductor, Austin, Texas.

- ❖ As a member of multiple verification teams, verified designs on a variety of chip design projects on PowerPC-based cores and SoC's.
- ❖ Specialized in test methodology and test bench development, creating verification IP in Verilog, C++, Vera, SystemVerilog, and SystemVerilog Assertions (SVA).
- ❖ Engaged in constant communication with design team colleagues around the world, and occasionally traveled internationally.
- ❖ Authored customer programming application notes, and presented technical talks in academic settings.

1998 – 2002

Product & Test Engineer · Motorola, Austin, Texas.

- ❖ Designed and executed experiments, and authored reports on the MPC860 SoC to increase fabrication and final test yield, outgoing quality, and reliability of the device in the field.
- ❖ Executed device characterization experiments on multiple projects often resulted in increased sales for higher performance chips, and debugged bring-up, production, and customer issues on testers, both in wafer probe and final test.
- ❖ Mentored and trained all junior engineers after rising to level of senior engineer, and routinely called upon for expert advice for fabrication, assembly, and final test issues.
- ❖ Authored formal reports and data sheets, as well as tutorials for internal evaluation of engineering experiments.
- ❖ Traveled to Malaysia and China to debug production problems or to bring up new devices.

Education**M.S. Electrical Engineering**, December 2005 (GPA: 4.0 / 4.0)

The University of Texas at Austin, Austin, Texas

Project & Report: *A Tool for Dynamic Data Capture and Visualization in Heterogeneous Simulation Environments***Object-Oriented Design & Programming in C++**, 2002

Ten week immersive course, Glenn Downing, Professor, The University of Texas at Austin

B.S. Computer Engineering, November 1997

Rochester Institute of Technology, Rochester, New York

Senior Project: *Real-time Autonomous Laser Tracking System (custom-made)***Personal Strengths***Strategic, Empathy, Ideation, Developer, and Learner* (as indicated by the *Strengths Finder* assessment)

Professional Affiliations

Member, Institute for Electrical and Electronics Engineers (IEEE)
Advisory board member, Rochester Institute of Technology (RIT) Department of Computer Engineering

Selected Articles

Blog, "From Tech Unemployment to Workforce Development". Matt Genovese, IEEE The Institute.
<http://theinstitute.ieee.org/ieee-roundup/opinions/ieee-roundup/from-tech-unemployment-to-workforce-development>

Article, "How Door64 became an Austin tech hub". Lori Hawkins, Austin American-Statesman.
<http://www.statesman.com/news/technology/how-door64-became-an-austin-tech-hub/nRpXx/>

Blog, "Austin, Texas needs software engineers". Matt Genovese, IEEE The Institute.
<http://theinstitute.ieee.org/ieee-roundup/opinions/ieee-roundup/austin-texas-needs-software-engineers>

Blog, "Software Talent Tops The List In Door64's First Hiring Survey". George Dearing, AustinStartup.
<http://austinstartup.com/2012/04/software-talent-tops-the-list-in-door64s-first-hiring-survey/>

Article, "Cultivating an Austin Technology Community". Matt Genovese, Forefront Austin.
<http://www.forefrontaustin.com/feature/door64-austin-technology-community>

Article, "Door64 aims to build better tech community". Austin Kurth, Austin American-Statesman.
<http://www.statesman.com/news/business/door64-fair-aims-to-build-better-tech-community/nRfkx/>

Speaking

Speaker, "Building Community: Lessons from Door64". Various engagements, Austin, Texas (2010-2011).
Speaker, "Using Drupal for Community Websites". PubCon South, Austin, Texas (2011).
Panel moderator, "Community Management". Interactive Austin 2009 Conference, Austin, Texas (2009).
Panelist, "The Art of Networking". Society for Women Engineers, Austin, Texas (2009).
Panelist, "Consumer and Community Generated Content". PubCon South Conference, Austin, Texas (2009).
Guest speaker, "Networking – The Missing College Course". Multiple universities (2009).
Guest lecturer, "Driver Development". Embedded System Design graduate course, The University of Texas at Austin, Austin, Texas (2008).
Guest speaker, "Functional Verification Methodology at Freescale Semiconductor". IEEE Computer Society, Rochester, New York (2006).

Publications

M. McDermott, M. Ravel, M. Genovese, "A Design Engineering Education Platform for SYSTEMatic Learning" (poster). Presented at the IEEE International Conference on Microelectronic Systems Education, San Diego, CA (2007).
M. Genovese, "A Tool for Dynamic Data Capture and Visualization in Heterogeneous Simulation Environments." Report detailing tool created during Masters program at The University of Texas at Austin (2005).
M. Genovese, "A Quick-Start Guide for Learning SystemC." Tutorial presented at The University of Texas at Austin as culmination of an independent study (2004).
M. Genovese, "A Modular Approach for Data Extraction Using Perl," Teradyne Users Group Conference 2001, San Antonio, Texas (2001).