

ECG-HRM

Generated by Doxygen 1.9.7

1 Module Index	1
1.1 Modules	1
2 Data Structure Index	2
2.1 Data Structures	2
3 File Index	2
3.1 File List	2
4 Module Documentation	3
4.1 Device Drivers	3
4.1.1 Detailed Description	5
4.1.2 ADC 	5
4.1.3 GPIO 	5
4.1.4 ILI9341 	8
4.1.5 PLL 	8
4.1.6 SPI 	9
4.1.7 SysTick 	9
4.1.8 Timer 	10
4.1.9 UART 	12
4.2 Application Software	14
4.3 Program Threads	14
4.3.1 Detailed Description	15
4.3.2 Function Documentation	15
5 Data Structure Documentation	15
5.1 FIFO_buffer_t Struct Reference	15
5.1.1 Detailed Description	15
6 File Documentation	16
6.1 Debug.h File Reference	16
6.1.1 Detailed Description	16
6.2 Filter.h File Reference	17
6.2.1 Detailed Description	17
6.3 QRS.h File Reference	18
6.3.1 Detailed Description	18
6.4 UserCtrl.h File Reference	19
6.4.1 Detailed Description	19
6.4.2 Function Documentation	20
6.5 fifo_buff.c File Reference	20
6.5.1 Detailed Description	21
6.5.2 Function Documentation	21
6.6 fifo_buff.h File Reference	22
6.6.1 Detailed Description	24

6.6.2 Function Documentation	24
6.7 ADC.c File Reference	25
6.7.1 Detailed Description	26
6.8 ADC.h File Reference	26
6.8.1 Detailed Description	27
6.9 GPIO.h File Reference	27
6.9.1 Detailed Description	28
6.10 ILI9341.h File Reference	29
6.10.1 Detailed Description	29
6.11 isr.c File Reference	30
6.11.1 Detailed Description	30
6.12 PLL.c File Reference	30
6.12.1 Detailed Description	31
6.13 PLL.h File Reference	31
6.13.1 Detailed Description	32
6.14 SPI.h File Reference	32
6.14.1 Detailed Description	33
6.15 SysTick.c File Reference	33
6.15.1 Detailed Description	34
6.16 SysTick.h File Reference	34
6.16.1 Detailed Description	35
6.17 Timer.h File Reference	35
6.17.1 Detailed Description	36
6.18 UART.c File Reference	37
6.18.1 Detailed Description	37
6.19 UART.h File Reference	38
6.19.1 Detailed Description	39
6.20 main.c File Reference	39
6.20.1 Detailed Description	39
Index	41

1 Module Index

1.1 Modules

Here is a list of all modules:

Device Drivers	3
ADC 	5
GPIO 	5

ILI9341
	8
PLL
	8
SPI
	9
SysTick
	9
Timer
	10
UART
	12
Application Software	14
Program Threads	14

2 Data Structure Index

2.1 Data Structures

Here are the data structures with brief descriptions:

FIFO_buffer_t	
Array-based FIFO buffer type	15

3 File Index

3.1 File List

Here is a list of all documented files with brief descriptions:

Debug.h	
Functions to output debugging information to a serial port via UART	16
Filter.h	
Functions to implement digital filters via linear constant coefficient difference equations (LC-CDEs)	17
QRS.h	
QRS detection algorithm functions	18
UserCtrl.h	
Interface for user control module	19
fifo_buff.c	
Source code file for FIFO buffer type	20
fifo_buff.h	
Header file for FIFO buffer type	22
ADC.c	25
ADC.h	
Driver module for analog-to-digital conversion (ADC)	26

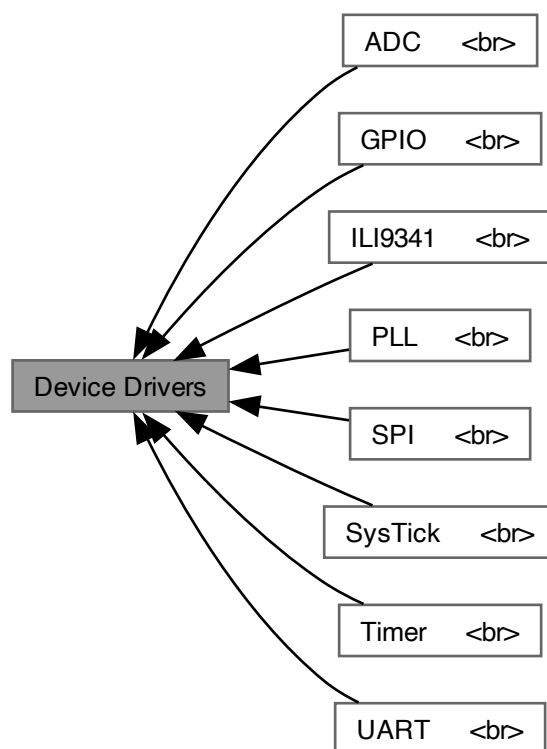
GPIO.h	
Driver module for using the LaunchPad's onboard switches and RGB LEDs for GPIO and interrupts	27
ILI9341.h	
Driver module for interfacing with an ILI9341 LCD driver	29
isr.c	
Source code for interrupt service routines (ISRs)	30
PLL.c	
Implementation details for phase-lock-loop (PLL) functions	30
PLL.h	
Driver module for activating the phase-locked-loop (PLL)	31
SPI.h	
Driver module for using the serial peripheral interface (SPI) protocol	32
SysTick.c	
Implementation details for SysTick functions	33
SysTick.h	
Driver module for using SysTick-based timing and/or interrupts	34
Timer.h	
Implementation for timer module	35
UART.c	
Source code for UART module	37
UART.h	
Driver module for UART1	38
main.c	
Main program file for ECG-HRM	39

4 Module Documentation

4.1 Device Drivers

Device driver modules.

Collaboration diagram for Device Drivers:



Modules

- [ADC
](#)
Analog-to-digital conversion module.
- [GPIO
](#)
GPIO Port F module.
- [ILI9341
](#)
Module for interfacing ILI9341-based RGB LCD via [SPI](#) .
- [PLL
](#)
Phase-locked loop module.
- [SPI
](#)
Serial peripheral interface module.
- [SysTick
](#)
SysTick timing module.
- [Timer
](#)
Timer0A module.
- [UART
](#)
UART0 module.

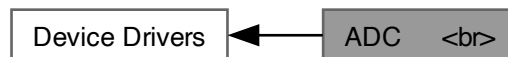
4.1.1 Detailed Description

Device driver modules.

4.1.2 ADC

Analog-to-digital conversion module.

Collaboration diagram for ADC
:



Files

- file [ADC.h](#)
Driver module for analog-to-digital conversion (ADC)

4.1.2.1 Detailed Description

Analog-to-digital conversion module.

4.1.3 GPIO

GPIO Port F module.

Collaboration diagram for GPIO
:



Files

- file [GPIO.h](#)
Driver module for using the LaunchPad's onboard switches and RGB LEDs for GPIO and interrupts.

Functions

- void `GPIO_PF_Init` (void)
Initialize GPIO Port F.
- void `GPIO_PF_LED_Init` (void)
Initialize PF1-3 to interface the LaunchPad's onboard RGB LED.
- void `GPIO_PF_LED_Write` (uint8_t color_mask, uint8_t on_or_off)
Write a 1 or 0 to the selected LED(s).
- void `GPIO_PF_LED_Toggle` (uint8_t color_mask)
Toggle the selected LED(s).
- void `GPIO_PF_Sw_Init` (void)
Initialize PF0/4 to interface the LaunchPad's onboard switches. PF4 is Sw1, and PF0 is Sw2.
- void `GPIO_PF_Interrupt_Init` (void)
Initialize GPIO Port F interrupts via Sw1 and Sw2.

4.1.3.1 Detailed Description

GPIO Port F module.

4.1.3.2 Function Documentation

`GPIO_PF_Init()`

```
void GPIO_PF_Init (  
    void )
```

Initialize GPIO Port F.

`GPIO_PF_Interrupt_Init()`

```
void GPIO_PF_Interrupt_Init (  
    void )
```

Initialize GPIO Port F interrupts via Sw1 and Sw2.

Here is the call graph for this function:



GPIO_PF_LED_Init()

```
void GPIO_PF_LED_Init (
    void )
```

Initialize PF1-3 to interface the LaunchPad's onboard RGB LED.

Here is the call graph for this function:

**GPIO_PF_LED_Toggle()**

```
void GPIO_PF_LED_Toggle (
    uint8_t color_mask )
```

Toggle the selected LED(s).

Parameters

<i>color_mask</i>	Hex. number of LED pin(s) to write to. 0x02 (PF1) – RED; 0x04 (PF2) – BLUE; 0x08 (PF3) – GREEN
-------------------	--

GPIO_PF_LED_Write()

```
void GPIO_PF_LED_Write (
    uint8_t color_mask,
    uint8_t on_or_off )
```

Write a 1 or 0 to the selected LED(s).

Parameters

<i>color_mask</i>	Hex. number of LED pin(s) to write to. 0x02 (PF1) – RED; 0x04 (PF2) – BLUE; 0x08 (PF3) – GREEN
<i>on_or_off</i>	=0 for OFF, >=1 for ON

GPIO_PF_Sw_Init()

```
void GPIO_PF_Sw_Init (
    void )
```

Initialize PF0/4 to interface the LaunchPad's onboard switches. PF4 is Sw1, and PF0 is Sw2.

Here is the call graph for this function:



4.1.4 ILI9341

Module for interfacing ILI9341-based RGB LCD via [SPI](#) .

Collaboration diagram for ILI9341
:

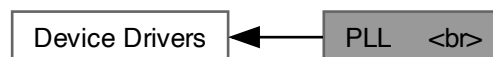


Module for interfacing ILI9341-based RGB LCD via [SPI](#) .

4.1.5 PLL

Phase-locked loop module.

Collaboration diagram for PLL
:



Functions

- void [PLL_Init](#) (void)
Initializes the phase-locked-loop (PLL), allowing a bus frequency of 80[MHz].

4.1.5.1 Detailed Description

Phase-locked loop module.

4.1.5.2 Function Documentation

PLL_Init()

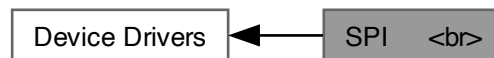
```
void PLL_Init (
    void )
```

Initializes the phase-locked-loop (PLL), allowing a bus frequency of 80[MHz].

4.1.6 SPI

Serial peripheral interface module.

Collaboration diagram for SPI
:



Serial peripheral interface module.

4.1.7 SysTick

SysTick timing module.

Collaboration diagram for SysTick
:



Functions

- void `SysTick_Timer_Init` (void)
Initialize SysTick for timing purposes.
- void `SysTick_Wait1ms` (uint32_t delay_ms)
Delay for specified amount of time in [ms]. Assumes f_bus = 80[MHz].
- void `SysTick_Interrupt_Init` (uint32_t time_ms)
Initialize SysTick for interrupts.

4.1.7.1 Detailed Description

SysTick timing module.

4.1.7.2 Function Documentation

`SysTick_Interrupt_Init()`

```
void SysTick_Interrupt_Init (
    uint32_t time_ms )
```

Initialize SysTick for interrupts.

Parameters

<code>time_ms</code>	Time in [ms] between interrupts. Cannot be more than 200[ms].
----------------------	---

`SysTick_Timer_Init()`

```
void SysTick_Timer_Init (
    void )
```

Initialize SysTick for timing purposes.

4.1.8 Timer

Timer0A module.

Collaboration diagram for Timer
:



Functions

- void `Timer0A_Init` (void)
Initialize timer 0 as 32-bit, one-shot, countdown timer.
- void `Timer0A_Start` (uint32_t time_ms)
Count down starting from the inputted value.
- uint8_t `Timer0A_isCounting` (void)
Returns 1 if Timer0 is still counting and 0 if not.
- void `Timer0A_Wait1ms` (uint32_t time_ms)
Wait for the specified amount of time in [ms].
- void `Timer1A_Init` (uint32_t time_ms)
Initialize timer 1 as a 32-bit, periodic, countdown timer with interrupts.

4.1.8.1 Detailed Description

Timer0A module.

4.1.8.2 Function Documentation

Timer0A_Init()

```
void Timer0A_Init (
    void )
```

Initialize timer 0 as 32-bit, one-shot, countdown timer.

Timer0A_isCounting()

```
uint8_t Timer0A_isCounting (
    void )
```

Returns 1 if Timer0 is still counting and 0 if not.

Returns

uint8_t status

Timer0A_Start()

```
void Timer0A_Start (
    uint32_t time_ms )
```

Count down starting from the inputted value.

Parameters

<code>time_ms</code>	Time in [ms] to load into Timer 0. Must be ≤ 53 seconds.
----------------------	---

Timer0A_Wait1ms()

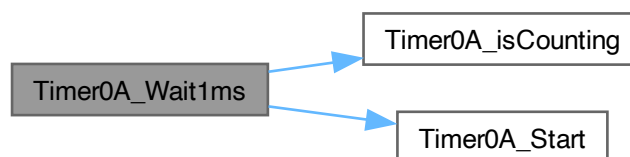
```
void Timer0A_Wait1ms (
    uint32_t time_ms )
```

Wait for the specified amount of time in [ms].

Parameters

<i>time_ms</i>	Time in [ms] to load into Timer 0. Must be ≤ 53 seconds.
----------------	---

Here is the call graph for this function:



Timer1A_Init()

```
void Timer1A_Init (
    uint32_t time_ms )
```

Initialize timer 1 as a 32-bit, periodic, countdown timer with interrupts.

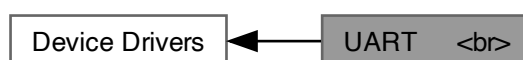
Parameters

<i>time_ms</i>	Time in [ms] between interrupts. Must be ≤ 53 seconds.
----------------	---

4.1.9 UART

UART0 module.

Collaboration diagram for UART
:



Functions

- void `UART0_Init` (void)
Initialize UART0 to a baud rate of 115200, 8-bit data length, 1 start bit, and 1 stop bit.
- unsigned char `UART0_ReadChar` (void)
Read a single character from UART0.
- void `UART0_WriteChar` (unsigned char input_char)
Write a single character to UART0.
- void `UART0_WriteStr` (unsigned char *str_ptr)
Write a C string to UART0.

4.1.9.1 Detailed Description

UART0 module.

4.1.9.2 Function Documentation

UART0_Init()

```
void UART0_Init (
    void )
```

Initialize UART0 to a baud rate of 115200, 8-bit data length, 1 start bit, and 1 stop bit.

Given the bus frequency (f_{bus}) and desired baud rate (BR), the baud rate divisor (BRD) can be calculated:
 $BRD = f_{bus} / (16 * BR)$

The integer BRD (IBRD) is simply the integer part of the BRD: $IBRD = int(BRD)$

The fractional BRD (FBRD) is calculated using the fractional part ($mod(BRD, 1)$) of the BRD: $FBRD = int((mod(BRD, 1) * 64) + 0.5)$

NOTE: LCRH must be accessed *AFTER* setting the BRD register0

UART0_ReadChar()

```
unsigned char UART0_ReadChar (
    void )
```

Read a single character from UART0.

Returns

input_char

This function uses busy-wait synchronization to read a character from UART0.

UART0_WriteChar()

```
void UART0_WriteChar (
    unsigned char input_char )
```

Write a single character to UART0.

Parameters

<code>input_char</code>	
-------------------------	--

This function uses busy-wait synchronization to write a character to UART0.

UART0_WriteStr()

```
void UART0_WriteStr (
    unsigned char * str_ptr )
```

Write a C string to UART0.

Parameters

<code>str_ptr</code>	pointer to C string
----------------------	---------------------

This function uses [UART0_WriteChar\(\)](#) function to write a C string to UART0. The function writes until either the entire string has been written or a null-terminated character has been reached. Here is the call graph for this function:



4.2 Application Software

Application-specific modules.

Application-specific modules.

4.3 Program Threads

Program Threads.

Functions

- void [GPIO_PortF_Handler](#) ()
ISR for facilitating user control of program state.
- void [SysTick_Handler](#) ()
ISR for collecting ECG samples @ $f_s = 200$ [Hz].
- void [Timer1A_Handler](#) ()
ISR for updating the LCD @ $f_s = 30$ [Hz].
- int **main** ()

4.3.1 Detailed Description

Program Threads.

4.3.2 Function Documentation

GPIO_PortF_Handler()

```
void GPIO_PortF_Handler ( )
```

ISR for facilitating user control of program state.

SysTick_Handler()

```
void SysTick_Handler ( )
```

ISR for collecting ECG samples @ f_s = 200 [Hz].

Timer1A_Handler()

```
void Timer1A_Handler ( )
```

ISR for updating the LCD @ f_s = 30 [Hz].

5 Data Structure Documentation

5.1 FIFO_buffer_t Struct Reference

Array-based FIFO buffer type.

Data Fields

- volatile uint16_t * **front_ptr**
- volatile uint16_t * **rear_ptr**
- volatile uint32_t **curr_size**
- uint32_t **MAX_SIZE**

5.1.1 Detailed Description

Array-based FIFO buffer type.

Parameters

<i>front_ptr</i>	pointer to the first element of the buffer.
<i>rear_ptr</i>	pointer to the last element of the buffer.
<i>curr_size</i>	current number of elements within the buffer.
<i>MAX_SIZE</i>	maximum number of elements allowed within buffer.

The documentation for this struct was generated from the following file:

- [fifo_buff.c](#)

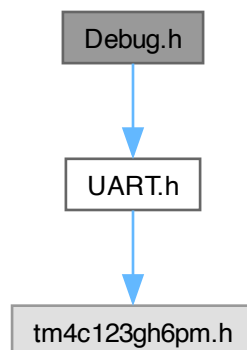
6 File Documentation

6.1 Debug.h File Reference

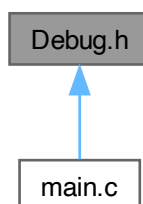
Functions to output debugging information to a serial port via UART.

```
#include "UART.h"
```

Include dependency graph for Debug.h:



This graph shows which files directly or indirectly include this file:



6.1.1 Detailed Description

Functions to output debugging information to a serial port via UART.

Author

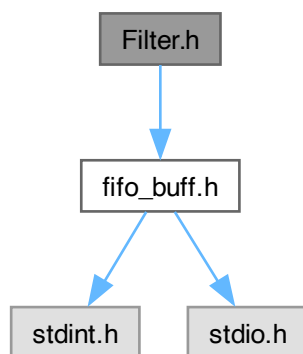
Bryan McElvy

6.2 Filter.h File Reference

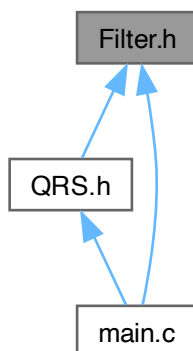
Functions to implement digital filters via linear constant coefficient difference equations (LCCDEs).

```
#include "fifo_buff.h"
```

Include dependency graph for Filter.h:



This graph shows which files directly or indirectly include this file:



6.2.1 Detailed Description

Functions to implement digital filters via linear constant coefficient difference equations (LCCDEs).

Author

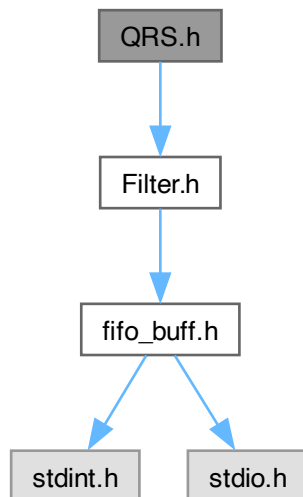
Bryan McElvy

6.3 QRS.h File Reference

QRS detection algorithm functions.

```
#include "Filter.h"
```

Include dependency graph for QRS.h:



This graph shows which files directly or indirectly include this file:



6.3.1 Detailed Description

QRS detection algorithm functions.

Author

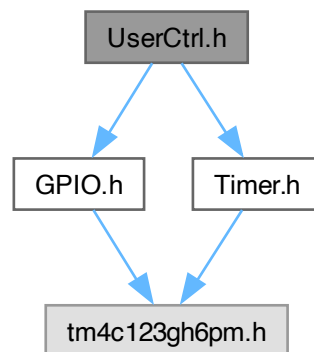
Bryan McElvy

This module contains functions for detecting heart rate (HR) using a simplified version of the Pan-Tompkins algorithm.

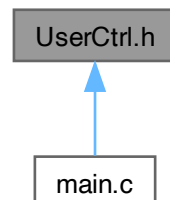
6.4 UserCtrl.h File Reference

Interface for user control module.

```
#include "GPIO.h"
#include "Timer.h"
Include dependency graph for UserCtrl.h:
```



This graph shows which files directly or indirectly include this file:



Functions

- void [UserCtrl_Init](#) ()
Initializes the UserCtrl module and its dependencies (Timer0B and GPIO_PortF)

6.4.1 Detailed Description

Interface for user control module.

Author

Bryan McElvy

6.4.2 Function Documentation

UserCtrl_Init()

```
void UserCtrl_Init ( )
```

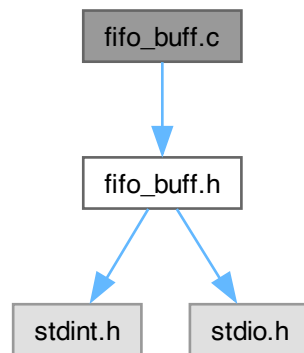
Initializes the UserCtrl module and its dependencies (Timer0B and GPIO_PortF)

6.5 fifo_buff.c File Reference

Source code file for FIFO buffer type.

```
#include "fifo_buff.h"
```

Include dependency graph for fifo_buff.c:



Data Structures

- struct [FIFO_buffer_t](#)
Array-based FIFO buffer type.

Functions

- `FIFO_buffer_t` [FIFO_init](#) (uint32_t buffer_size)
Initializes a FIFO buffer with the specified size.
- void [FIFO_add_sample](#) (FIFO_buffer_t *FIFO_ptr, uint16_t sample)
Adds a 16-bit sample to the end of the FIFO buffer at the specified address.
- uint16_t [FIFO_rem_sample](#) (FIFO_buffer_t *FIFO_ptr)
Removes the first element of the FIFO buffer at the specified address.
- uint32_t [FIFO_get_size](#) (FIFO_buffer_t *FIFO_ptr)
Gets the size of the FIFO buffer at the specified address.
- void [FIFO_show_data](#) (FIFO_buffer_t *FIFO_ptr)
Shows all of the items in the FIFO buffer at the specified address. NOTE: Intended for debugging purposes only.

6.5.1 Detailed Description

Source code file for FIFO buffer type.

Author

Bryan McElvy

6.5.2 Function Documentation

FIFO_add_sample()

```
void FIFO_add_sample (
    FIFO_buffer_t * FIFO_ptr,
    uint16_t sample )
```

Adds a 16-bit sample to the end of the FIFO buffer at the specified address.

Parameters

<i>FIFO_buffer</i>	pointer to FIFO buffer
<i>sample</i>	data sample to be added

Returns

None

FIFO_get_size()

```
uint32_t FIFO_get_size (
    FIFO_buffer_t * FIFO_ptr )
```

Gets the size of the FIFO buffer at the specified address.

Parameters

<i>FIFO_ptr</i>	pointer to FIFO buffer
-----------------	------------------------

Returns

curr_size

FIFO_init()

```
FIFO_buffer_t FIFO_init (
    uint32_t buffer_size )
```

Initializes a FIFO buffer with the specified size.

Parameters

<i>buffer_size</i>	desired buffer size.
--------------------	----------------------

Returns

[FIFO_buffer](#)

FIFO_rem_sample()

```
uint16_t FIFO_rem_sample (
    FIFO_buffer_t * FIFO_ptr )
```

Removes the first element of the FIFO buffer at the specified address.

Parameters

<i>FIFO_ptr</i>	pointer to FIFO buffer
-----------------	------------------------

Returns

uint16_t

FIFO_show_data()

```
void FIFO_show_data (
    FIFO_buffer_t * FIFO_ptr )
```

Shows all of the items in the FIFO buffer at the specified address. NOTE: Intended for debugging purposes only.

Parameters

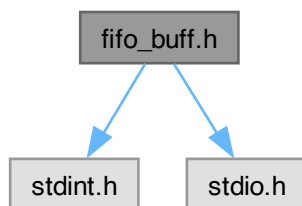
<i>FIFO_ptr</i>	pointer to FIFO buffer
-----------------	------------------------

6.6 fifo_buff.h File Reference

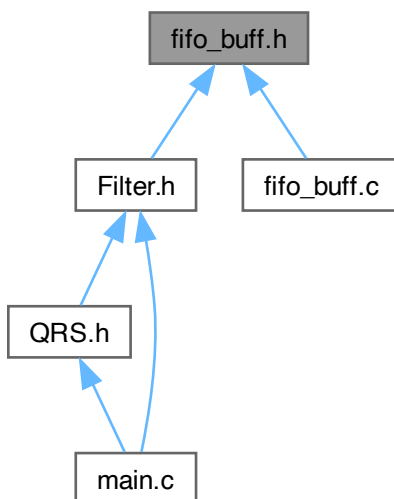
Header file for FIFO buffer type.

```
#include <stdint.h>
#include <stdio.h>
```


Include dependency graph for `fifo_buff.h`:



This graph shows which files directly or indirectly include this file:



Functions

- `FIFO_buffer_t` [FIFO_init](#) (`uint32_t` buffer_size)
Initializes a FIFO buffer with the specified size.
- `void` [FIFO_add_sample](#) (`FIFO_buffer_t` *FIFO_ptr, `uint16_t` sample)
Adds a 16-bit sample to the end of the FIFO buffer at the specified address.
- `uint16_t` [FIFO_rem_sample](#) (`FIFO_buffer_t` *FIFO_ptr)
Removes the first element of the FIFO buffer at the specified address.
- `uint32_t` [FIFO_get_size](#) (`FIFO_buffer_t` *FIFO_ptr)
Gets the size of the FIFO buffer at the specified address.
- `void` [FIFO_show_data](#) (`FIFO_buffer_t` *FIFO_ptr)
Shows all of the items in the FIFO buffer at the specified address. NOTE: Intended for debugging purposes only.

6.6.1 Detailed Description

Header file for FIFO buffer type.

Author

Bryan McElvy

6.6.2 Function Documentation

FIFO_add_sample()

```
void FIFO_add_sample (
    FIFO_buffer_t * FIFO_ptr,
    uint16_t sample )
```

Adds a 16-bit sample to the end of the FIFO buffer at the specified address.

Parameters

<i>FIFO_buffer</i>	pointer to FIFO buffer
<i>sample</i>	data sample to be added

Returns

None

FIFO_get_size()

```
uint32_t FIFO_get_size (
    FIFO_buffer_t * FIFO_ptr )
```

Gets the size of the FIFO buffer at the specified address.

Parameters

<i>FIFO_ptr</i>	pointer to FIFO buffer
-----------------	------------------------

Returns

curr_size

FIFO_init()

```
FIFO_buffer_t FIFO_init (
    uint32_t buffer_size )
```

Initializes a FIFO buffer with the specified size.

Parameters

<i>buffer_size</i>	desired buffer size.
--------------------	----------------------

Returns

[FIFO_buffer](#)

FIFO_rem_sample()

```
uint16_t FIFO_rem_sample (
    FIFO_buffer_t * FIFO_ptr )
```

Removes the first element of the FIFO buffer at the specified address.

Parameters

<i>FIFO_ptr</i>	pointer to FIFO buffer
-----------------	------------------------

Returns

uint16_t

FIFO_show_data()

```
void FIFO_show_data (
    FIFO_buffer_t * FIFO_ptr )
```

Shows all of the items in the FIFO buffer at the specified address. NOTE: Intended for debugging purposes only.

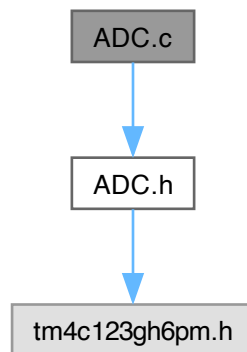
Parameters

<i>FIFO_ptr</i>	pointer to FIFO buffer
-----------------	------------------------

6.7 ADC.c File Reference

```
#include "ADC.h"
```

Include dependency graph for ADC.c:



6.7.1 Detailed Description

Author

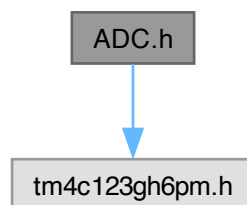
Bryan McElvy

6.8 ADC.h File Reference

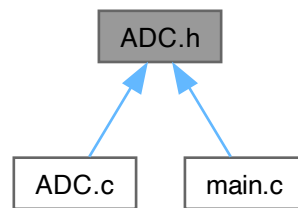
Driver module for analog-to-digital conversion (ADC)

```
#include "tm4c123gh6pm.h"
```

Include dependency graph for ADC.h:



This graph shows which files directly or indirectly include this file:



6.8.1 Detailed Description

Driver module for analog-to-digital conversion (ADC)

Author

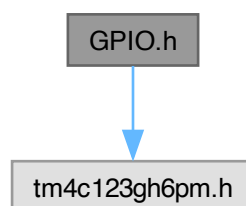
Bryan McElvy

6.9 GPIO.h File Reference

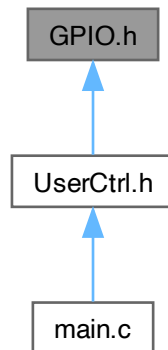
Driver module for using the LaunchPad's onboard switches and RGB LEDs for GPIO and interrupts.

```
#include "tm4c123gh6pm.h"
```

Include dependency graph for GPIO.h:



This graph shows which files directly or indirectly include this file:



Functions

- void [GPIO_PF_Init](#) (void)
Initialize GPIO Port F.
- void [GPIO_PF_LED_Init](#) (void)
Initialize PF1-3 to interface the LaunchPad's onboard RGB LED.
- void [GPIO_PF_LED_Write](#) (uint8_t color_mask, uint8_t on_or_off)
Write a 1 or 0 to the selected LED(s).
- void [GPIO_PF_LED_Toggle](#) (uint8_t color_mask)
Toggle the selected LED(s).
- void [GPIO_PF_Sw_Init](#) (void)
Initialize PF0/4 to interface the LaunchPad's onboard switches. PF4 is Sw1, and PF0 is Sw2.
- void [GPIO_PF_Interrupt_Init](#) (void)
Initialize GPIO Port F interrupts via Sw1 and Sw2.

6.9.1 Detailed Description

Driver module for using the LaunchPad's onboard switches and RGB LEDs for GPIO and interrupts.

Author

Bryan McElvy

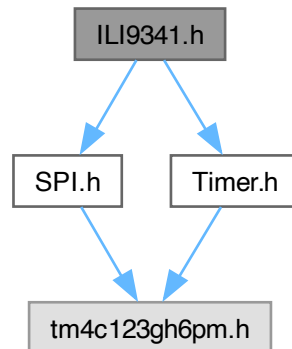
6.10 ILI9341.h File Reference

Driver module for interfacing with an ILI9341 LCD driver.

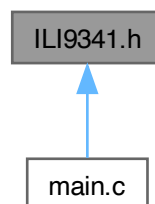
```
#include "SPI.h"
```

```
#include "Timer.h"
```

Include dependency graph for ILI9341.h:



This graph shows which files directly or indirectly include this file:



6.10.1 Detailed Description

Driver module for interfacing with an ILI9341 LCD driver.

Author

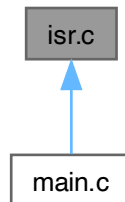
Bryan McElvy

This module contains functions for initializing and outputting graphical data to a 240RGBx320 resolution, 262K color-depth liquid crystal display (LCD). The module interfaces the LaunchPad (or any other board featuring the TM4C123GH6PM microcontroller) with an ILI9341 LCD driver chip via the SPI (serial peripheral interface) protocol.

6.11 isr.c File Reference

Source code for interrupt service routines (ISRs)

This graph shows which files directly or indirectly include this file:



Functions

- void [GPIO_PortF_Handler](#) ()
ISR for facilitating user control of program state.
- void [SysTick_Handler](#) ()
ISR for collecting ECG samples @ $f_s = 200$ [Hz].
- void [Timer1A_Handler](#) ()
ISR for updating the LCD @ $f_s = 30$ [Hz].

6.11.1 Detailed Description

Source code for interrupt service routines (ISRs)

Author

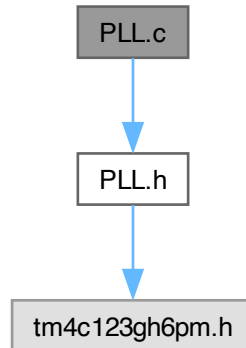
Bryan McElvy

6.12 PLL.c File Reference

Implementation details for phase-lock-loop (PLL) functions.


```
#include "PLL.h"
```

Include dependency graph for PLL.c:



Functions

- void [PLL_Init](#) (void)
Initializes the phase-locked-loop (PLL), allowing a bus frequency of 80[MHz].

6.12.1 Detailed Description

Implementation details for phase-lock-loop (PLL) functions.

Author

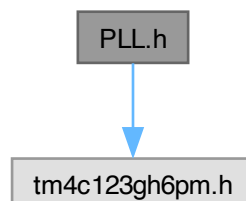
Bryan McElvy

6.13 PLL.h File Reference

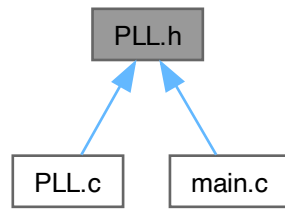
Driver module for activating the phase-locked-loop (PLL).

```
#include "tm4c123gh6pm.h"
```

Include dependency graph for PLL.h:



This graph shows which files directly or indirectly include this file:



Functions

- void `PLL_Init` (void)
Initializes the phase-locked-loop (PLL), allowing a bus frequency of 80[MHz].

6.13.1 Detailed Description

Driver module for activating the phase-locked-loop (PLL).

Author

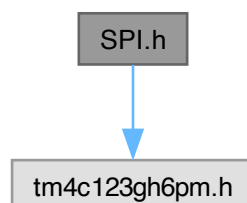
Bryan McElvy

6.14 SPI.h File Reference

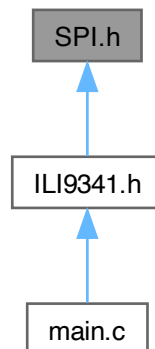
Driver module for using the serial peripheral interface (SPI) protocol.

```
#include "tm4c123gh6pm.h"
```

Include dependency graph for `SPI.h`:



This graph shows which files directly or indirectly include this file:



6.14.1 Detailed Description

Driver module for using the serial peripheral interface (SPI) protocol.

Author

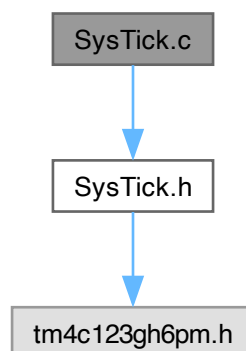
Bryan McElvy

6.15 SysTick.c File Reference

Implementation details for SysTick functions.

```
#include "SysTick.h"
```

Include dependency graph for SysTick.c:



Functions

- void `SysTick_Timer_Init` (void)
Initialize SysTick for timing purposes.
- void `SysTick_Wait1ms` (uint32_t time_ms)
Delay for specified amount of time in [ms]. Assumes $f_{bus} = 80$ [MHz].
- void `SysTick_Interrupt_Init` (uint32_t time_ms)
Initialize SysTick for interrupts.

6.15.1 Detailed Description

Implementation details for SysTick functions.

Author

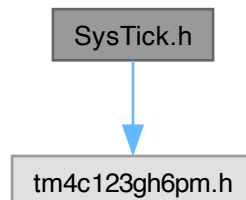
Bryan McElvy

6.16 SysTick.h File Reference

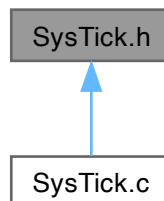
Driver module for using SysTick-based timing and/or interrupts.

```
#include "tm4c123gh6pm.h"
```

Include dependency graph for SysTick.h:



This graph shows which files directly or indirectly include this file:



Functions

- void [SysTick_Timer_Init](#) (void)
Initialize SysTick for timing purposes.
- void **SysTick_Wait1ms** (uint32_t delay_ms)
Delay for specified amount of time in [ms]. Assumes f_bus = 80[MHz].
- void [SysTick_Interrupt_Init](#) (uint32_t time_ms)
Initialize SysTick for interrupts.

6.16.1 Detailed Description

Driver module for using SysTick-based timing and/or interrupts.

Author

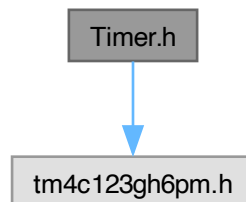
Bryan McElvy

6.17 Timer.h File Reference

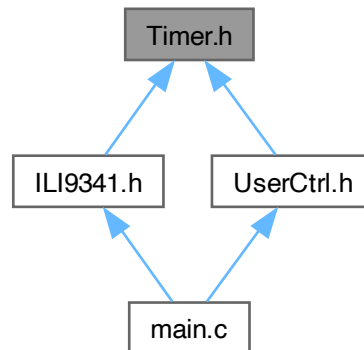
Implementation for timer module.

```
#include "tm4c123gh6pm.h"
```

Include dependency graph for Timer.h:



This graph shows which files directly or indirectly include this file:



Functions

- void [Timer0A_Init](#) (void)
Initialize timer 0 as 32-bit, one-shot, countdown timer.
- void [Timer0A_Start](#) (uint32_t time_ms)
Count down starting from the inputted value.
- uint8_t [Timer0A_isCounting](#) (void)
Returns 1 if Timer0 is still counting and 0 if not.
- void [Timer0A_Wait1ms](#) (uint32_t time_ms)
Wait for the specified amount of time in [ms].
- void [Timer1A_Init](#) (uint32_t time_ms)
Initialize timer 1 as a 32-bit, periodic, countdown timer with interrupts.

6.17.1 Detailed Description

Implementation for timer module.

Driver module for timing (Timer0) and interrupts (Timer1).

Author

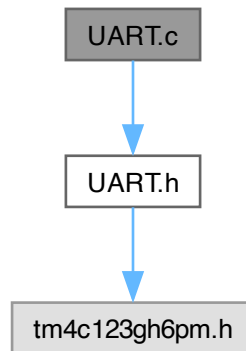
Bryan McElvy
Bryan McElvy

6.18 UART.c File Reference

Source code for UART module.

```
#include "UART.h"
```

Include dependency graph for UART.c:



Functions

- void [UART0_Init](#) (void)
Initialize UART0 to a baud rate of 115200, 8-bit data length, 1 start bit, and 1 stop bit.
- unsigned char [UART0_ReadChar](#) (void)
Read a single character from UART0.
- void [UART0_WriteChar](#) (unsigned char input_char)
Write a single character to UART0.
- void [UART0_WriteStr](#) (unsigned char *str_ptr)
Write a C string to UART0.

6.18.1 Detailed Description

Source code for UART module.

Author

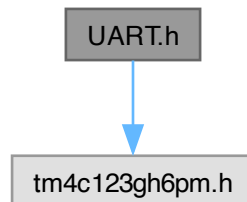
Bryan McElvy

6.19 UART.h File Reference

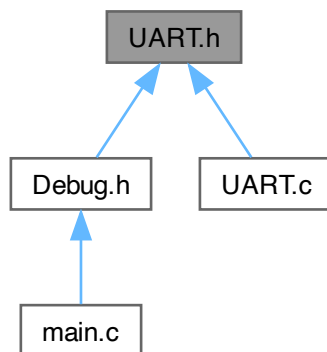
Driver module for UART1.

```
#include "tm4c123gh6pm.h"
```

Include dependency graph for UART.h:



This graph shows which files directly or indirectly include this file:



Functions

- void [UART0_Init](#) (void)
Initialize UART0 to a baud rate of 115200, 8-bit data length, 1 start bit, and 1 stop bit.
- unsigned char [UART0_ReadChar](#) (void)
Read a single character from UART0.
- void [UART0_WriteChar](#) (unsigned char input_char)
Write a single character to UART0.
- void [UART0_WriteStr](#) (unsigned char *str_ptr)
Write a C string to UART0.

6.19.1 Detailed Description

Driver module for UART1.

Author

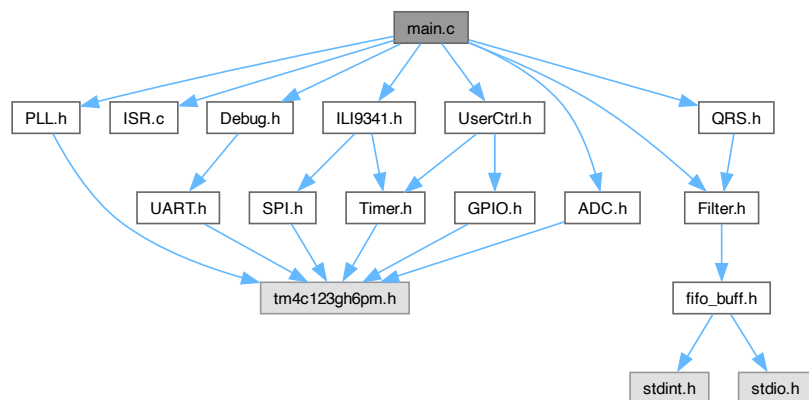
Bryan McElvy

6.20 main.c File Reference

Main program file for ECG-HRM.

```
#include "ADC.h"
#include "ISR.c"
#include "ILI9341.h"
#include "PLL.h"
#include "Debug.h"
#include "Filter.h"
#include "QRS.h"
#include "UserCtrl.h"
```

Include dependency graph for main.c:



Functions

- int **main** ()

6.20.1 Detailed Description

Main program file for ECG-HRM.

Author

Bryan McElvy

Index

ADC
, 5
ADC.c, 25
ADC.h, 26
Application Software, 14

Debug.h, 16
Device Drivers, 3

FIFO_add_sample
 fifo_buff.c, 21
 fifo_buff.h, 24
fifo_buff.c, 20
 FIFO_add_sample, 21
 FIFO_get_size, 21
 FIFO_init, 21
 FIFO_rem_sample, 22
 FIFO_show_data, 22
fifo_buff.h, 22
 FIFO_add_sample, 24
 FIFO_get_size, 24
 FIFO_init, 24
 FIFO_rem_sample, 25
 FIFO_show_data, 25
FIFO_buffer_t, 15
FIFO_get_size
 fifo_buff.c, 21
 fifo_buff.h, 24
FIFO_init
 fifo_buff.c, 21
 fifo_buff.h, 24
FIFO_rem_sample
 fifo_buff.c, 22
 fifo_buff.h, 25
FIFO_show_data
 fifo_buff.c, 22
 fifo_buff.h, 25
Filter.h, 17

GPIO
, 5
 GPIO_PF_Init, 6
 GPIO_PF_Interrupt_Init, 6
 GPIO_PF_LED_Init, 6
 GPIO_PF_LED_Toggle, 7
 GPIO_PF_LED_Write, 7
 GPIO_PF_Sw_Init, 7
GPIO.h, 27
GPIO_PF_Init
 GPIO
, 6
GPIO_PF_Interrupt_Init
 GPIO
, 6
GPIO_PF_LED_Init
 GPIO
, 6
GPIO_PF_LED_Toggle
 GPIO
, 7
GPIO_PF_LED_Write
 GPIO
, 7
GPIO_PF_Sw_Init
 GPIO
, 7
GPIO_PortF_Handler
 Program Threads, 15

ILI9341
, 8
ILI9341.h, 29
isr.c, 30

main.c, 39

PLL
, 8
 PLL_Init, 9
PLL.c, 30
PLL.h, 31
PLL_Init
 PLL
, 9
Program Threads, 14
 GPIO_PortF_Handler, 15
 SysTick_Handler, 15
 Timer1A_Handler, 15

QRS.h, 18

SPI
, 9
SPI.h, 32
SysTick
, 9
 SysTick_Interrupt_Init, 10
 SysTick_Timer_Init, 10
SysTick.c, 33
SysTick.h, 34
SysTick_Handler
 Program Threads, 15
SysTick_Interrupt_Init
 SysTick
, 10
SysTick_Timer_Init
 SysTick
, 10

Timer
, 10
 Timer0A_Init, 11
 Timer0A_isCounting, 11
 Timer0A_Start, 11
 Timer0A_Wait1ms, 12
 Timer1A_Init, 12
Timer.h, 35
Timer0A_Init
 Timer
, 11
Timer0A_isCounting
 Timer
, 11
Timer0A_Start
 Timer
, 11
Timer0A_Wait1ms
 Timer
, 12
Timer1A_Handler
 Program Threads, 15
Timer1A_Init
 Timer
, 12

- UART
, [12](#)
 - UART0_Init, [13](#)
 - UART0_ReadChar, [13](#)
 - UART0_WriteChar, [13](#)
 - UART0_WriteStr, [14](#)
- UART.c, [37](#)
- UART.h, [38](#)
- UART0_Init
 - UART
, [13](#)
- UART0_ReadChar
 - UART
, [13](#)
- UART0_WriteChar
 - UART
, [13](#)
- UART0_WriteStr
 - UART
, [14](#)
- UserCtrl.h, [19](#)
 - UserCtrl_Init, [20](#)
- UserCtrl_Init
 - UserCtrl.h, [20](#)