# ECG-HRM

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## 1 Electrocardiogram-based Heart Rate Monitor (ECG-HRM)

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## 1.1 Navigation

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- [/data](data) ECG sample data from the publically available MIT-BIH Arrhythmia Database.
- [/docs](docs) Documentation for both the project itself and resources used in creating it.
  - [/app\_notes](app notes) Application notes.
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  - [/doxygen\_files](doxygen\_files) Files used for documentation generation via Doxygen.
  - [/help](help) Help text for a few of the command line-based applications used in this project.
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  - /CMSIS Core CMSIS library by ARM for Cortex-M devices.
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- [/src](src) Source code for the software modules written for this project.
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  - [/middleware](middleware) Software modules for interfacing with external hardware via device drivers.
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  - [/test](test) Scripts used for manual on-target testing.
- [/test](test) CppUTest-based unit test suite.
  - [/mocks](mocks) CppUMock-based mock functions used to substitute a module's dependencies during unit tests.
  - [/src](src) Source code for unit tests.
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- [/tools](tools) Miscellaneous tools used or created for this project.
  - [/cppcheck](cppcheck) Suppressions list for Cppcheck.
  - [/data](data) Original files from MIT-BIH Arrhythmia Database, as well as a Python script to convert them to csv files.
  - [/filter\_design](filter\_design) Python scripts/notebooks used to design the digital filters used in this project.
  - [/JDS6600](JDS6600) Scripts for interfacing a JDS6600 DDS Signal Generator/Counter.
  - [/lookup\_table](lookup\_table) Script for generating the lookup table used in the ADC module.

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## 4 File Index

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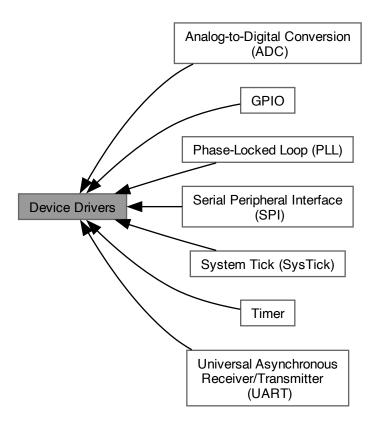
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# 5 Topic Documentation

## 5.1 Device Drivers

Low level device driver modules.

Collaboration diagram for Device Drivers:



## Modules

- Analog-to-Digital Conversion (ADC)
- GPIO
- Phase-Locked Loop (PLL)
- Serial Peripheral Interface (SPI)
- System Tick (SysTick)
- Timer
- Universal Asynchronous Receiver/Transmitter (UART)

## 5.1.1 Detailed Description

Low level device driver modules.

These modules contain functions for interfacing with peripherals available on the TM4C123GH6PM microcontroller.

## 5.1.2 Analog-to-Digital Conversion (ADC)

Collaboration diagram for Analog-to-Digital Conversion (ADC):



## **Files**

• file ADC.c

Source code for ADC module.

• file ADC.h

Driver module for analog-to-digital conversion (ADC).

#### **Functions**

void ADC\_Init (void)

Initialize ADC0 as a single-input analog-to-digital converter.

• void ADC\_InterruptEnable (void)

Enable the ADC interrupt.

void ADC\_InterruptDisable (void)

Disable the ADC interrupt.

• float32\_t ADC\_ConvertToVolts (uint16\_t raw\_sample)

Convert a raw ADC sample to voltage in [mV].

## 5.1.2.1 Detailed Description

Functions for differential-input analog-to-digital conversion.

## 5.1.2.2 Function Documentation

## ADC\_ConvertToVolts()

Convert a raw ADC sample to voltage in [mV].

#### **Parameters**

raw_sample	12-bit unsigned ADC value. s	sample =	[0,	0xFFF]
------------	------------------------------	----------	-----	--------

#### Returns

double Voltage value in range [-5.5, 5.5) [mV].

## 5.1.3 GPIO

Collaboration diagram for GPIO:



Functions for using general-purpose input/output (GPIO) ports.

## 5.1.4 Phase-Locked Loop (PLL)

Collaboration diagram for Phase-Locked Loop (PLL):



#### **Files**

- file PLL.c
  - Implementation details for phase-lock-loop (PLL) functions.
- file PLL.h

Driver module for activating the phase-locked-loop (PLL).

#### **Functions**

void PLL\_Init (void)

Initializes the phase-locked-loop (PLL), allowing a bus frequency of 80[MHz].

## 5.1.4.1 Detailed Description

Function for initializing the phase-locked loop.

#### 5.1.5 Serial Peripheral Interface (SPI)

Collaboration diagram for Serial Peripheral Interface (SPI):



#### Files

• file SPI.c

Source code for SPI module.

• file SPI.h

Driver module for using the serial peripheral interface (SPI) protocol.

#### **Macros**

- #define NVIC SSI0 NUM 7
- #define SPI\_INT\_START() (NVIC\_SW\_TRIG\_R = (NVIC\_SW\_TRIG\_R & ~(0xFF)) | NVIC\_SSIO\_NUM)
- #define **SPI SET DC**() (GPIO PORTA DATA R |= 0x40)
- #define SPI\_CLEAR\_DC() (GPIO\_PORTA\_DATA\_R &=  $\sim$ (0x40))
- #define SPI\_IS\_BUSY (SSI0\_SR\_R & 0x10)
- #define SPI\_TX\_ISNOTFULL ((bool) (SSI0\_SR\_R & 0x02))
- #define SPI\_BUFFER\_SIZE 9

## **Enumerations**

enum {
 SPI\_CLK\_PIN = GPIO\_PIN2 , SPI\_CS\_PIN = GPIO\_PIN3 , SPI\_RX\_PIN = GPIO\_PIN4 , SPI\_TX\_PIN = GPIO\_PIN5 ,
 SPI\_DC\_PIN = GPIO\_PIN6 , SPI\_RESET\_PIN = GPIO\_PIN7 , SPI\_SSIO\_PINS = (SPI\_CLK\_PIN | SPI\_CS\_PIN | SPI\_RX\_PIN | SPI\_TX\_PIN) , SPI\_GPIO\_PINS = (SPI\_DC\_PIN | SPI\_RESET\_PIN) ,

SPI\_ALL\_PINS = (SPI\_SSI0\_PINS | SPI\_GPIO\_PINS) }

## **Functions**

void SPI\_Init (void)

Initialize SSI0 to act as an SPI Controller (AKA Master) in mode 0.

• uint8\_t SPI\_Read (void)

Read data from the peripheral.

• void SPI\_WriteCmd (uint8\_t cmd)

Write an 8-bit command to the peripheral.

void SPI\_WriteData (uint8\_t data)

Write 8-bit data to the peripheral.

void SPI\_IRQ\_WriteCmd (uint8\_t cmd)

Add an 8-bit command to the SPI queue. If no data or other command is written, should directly precede a call to SPI\_IRQ\_StartWriting().

· void SPI IRQ WriteData (uint8 t data)

Add 8-bit data to the SPI queue. Should directly precede either another call to the same function or a call to SPI\_IRQ\_StartWriting().

void SPI\_IRQ\_StartWriting (void)

Start writing data to the Tx FIFO. Should be used after 1+ calls to SPI\_IRQ\_WriteCmd() and/or SPI\_IRQ\_WriteData(). If unused, writing will start when the SPI queue is full.

void SSI0 Handler (void)

Sends parameters (data or commands) over SPI via SSI0.

#### 5.1.5.1 Detailed Description

Functions for SPI-based communication via SSI0 peripheral.

#### 5.1.5.2 Macro Definition Documentation

#### NVIC\_SSI0\_NUM

#define NVIC\_SSIO\_NUM 7

TM4C Pin	Function	ILI9341 Pin	Description
PA2	SSI0Clk	CLK	Serial clock signal
PA3	SSI0Fss	CS	Chip select signal
PA4	SSI0Rx	MISO	TM4C (M) input, LCD (S) output
PA5	SSI0Tx	MOSI	TM4C (M) output, LCD (S) input
PA6	GPIO	D/C	Data = 1, Command = 0
PA7	GPIO	RESET	Reset the display (negative logic/active LOW)

```
Clk. Polarity = steady state low (0)
```

Clk. Phase = rising clock edge (0)

## 5.1.5.3 Function Documentation

#### SPI Init()

```
void SPI_Init (
     void )
```

Initialize SSI0 to act as an SPI Controller (AKA Master) in mode 0.

The bit rate BR is set using the (positive, even-numbered) clock prescale divisor CPSDVSR and the SCR field in the SSI Control 0 (CR0) register:

$$BR = f_{bus}/(CPSDVSR * (1 + SCR))$$

The ILI9341 driver has a min. read cycle of 150 [ns] and a min. write cycle of 100 [ns], so the bit rate BR is set to be equal to the bus frequency (  $f_{bus}=80[MHz]$ ) divided by 8, allowing a bit rate of 10 [MHz], or a period of 100 [ns].

## SPI\_IRQ\_WriteCmd()

Add an 8-bit command to the SPI queue. If no data or other command is written, should directly precede a call to SPI\_IRQ\_StartWriting().

## **Parameters**

cmd command for peripheral

## SPI\_IRQ\_WriteData()

Add 8-bit data to the SPI queue. Should directly precede either another call to the same function or a call to  $SPI\_IRQ\_StartWriting()$ .

#### **Parameters**

data input data for peripheral

## SPI\_Read()

Read data from the peripheral.

#### Returns

uint8\_t

## SPI\_WriteCmd()

Write an 8-bit command to the peripheral.

#### **Parameters**

cmd | command for peripheral

#### SPI\_WriteData()

Write 8-bit data to the peripheral.

#### **Parameters**

```
data input data for peripheral
```

## SSI0\_Handler()

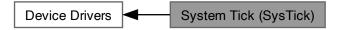
```
void SSI0_Handler (
     void )
```

Sends parameters (data or commands) over SPI via SSI0.

```
The interrupt is enabled by the 'SPI_Init()' function and triggered by a call to 'SPI_IRQ_StartWriting()'. The handler determines whether to signal for data or a command via the D/C pin, and then writes to the data register. The interrupt is unpended at the start of the function.
```

## 5.1.6 System Tick (SysTick)

Collaboration diagram for System Tick (SysTick):



#### **Files**

• file SysTick.c

Implementation details for SysTick functions.

file SysTick.h

Driver module for using SysTick-based timing and/or interrupts.

## **Functions**

void SysTick\_Timer\_Init (void)

Initialize SysTick for timing purposes.

void SysTick\_Wait1ms (uint32\_t delay\_ms)

Delay for specified amount of time in [ms]. Assumes f\_bus = 80[MHz].

void SysTick\_Interrupt\_Init (uint32\_t time\_ms)

Initialize SysTick for interrupts.

#### 5.1.6.1 Detailed Description

Functions for timing and periodic interrupts via SysTick.

#### 5.1.6.2 Function Documentation

## SysTick\_Interrupt\_Init()

Initialize SysTick for interrupts.

#### **Parameters**

*time\_ms* Time in [ms] between interrupts. Cannot be more than 200[ms].

#### 5.1.7 Timer

Collaboration diagram for Timer:



#### **Files**

• file Timer.c

Implementation for timer module.

• file Timer.h

Driver module for general-purpose timer modules.

## Timer0A

void Timer0A\_Init (void)

Initialize timer 0 as 32-bit, one-shot, countdown timer.

void Timer0A\_Start (uint32\_t time\_ms)

Count down starting from the inputted value.

uint8\_t Timer0A\_isCounting (void)

Returns 1 if Timer0 is still counting and 0 if not.

• void Timer0A\_Wait1ms (uint32\_t time\_ms)

Wait for the specified amount of time in [ms].

#### Timer1A

void Timer1A\_Init (uint32\_t time\_ms)

Initialize timer 1 as a 32-bit, periodic, countdown timer with interrupts.

#### Timer2A

· void Timer2A Init (void)

Initialize timer 2 as 32-bit, one-shot, countdown timer.

void Timer2A\_Start (uint32\_t time\_ms)

Count down starting from the inputted value.

uint8\_t Timer2A\_isCounting (void)

Returns 1 if Timer2 is still counting and 0 if not.

void Timer2A\_Wait1ms (uint32\_t time\_ms)

Wait for the specified amount of time in [ms].

void Timer3A\_Init (uint32\_t time\_ms)

Initialize Timer3A as a 32-bit, periodic, countdown timer that triggers ADC sample capture.

## 5.1.7.1 Detailed Description

Functions for timing and periodic interrupts via general-purpose timer modules (GPTM).

#### 5.1.7.2 Function Documentation

## Timer0A\_isCounting()

Returns 1 if Timer0 is still counting and 0 if not.

Returns

uint8\_t status

## Timer0A\_Start()

Count down starting from the inputted value.

**Parameters** 

*time\_ms* Time in [ms] to load into Timer 0. Must be <= 53 seconds.

## Timer0A\_Wait1ms()

Wait for the specified amount of time in [ms].

**Parameters** 

```
time\_ms Time in [ms] to load into Timer 0. Must be \leq 53 seconds.
```

## Timer1A\_Init()

Initialize timer 1 as a 32-bit, periodic, countdown timer with interrupts.

**Parameters** 

```
time_ms | Time in [ms] between interrupts. Must be <= 53 seconds.
```

## Timer2A\_isCounting()

Returns 1 if Timer2 is still counting and 0 if not.

Returns

uint8\_t status

## Timer2A\_Start()

Count down starting from the inputted value.

**Parameters** 

time\_ms | Time in [ms] to load into Timer 2. Must be <= 53 seconds.

## Timer2A\_Wait1ms()

Wait for the specified amount of time in [ms].

#### **Parameters**

```
time_ms | Time in [ms] to load into Timer 2. Must be <= 53 seconds.
```

## Timer3A\_Init()

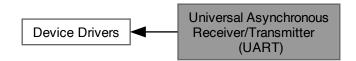
Initialize Timer3A as a 32-bit, periodic, countdown timer that triggers ADC sample capture.

#### **Parameters**

	time_ms	Time in [ms] to load into Timer3A. Must be $\leq$ 53 seconds.	
--	---------	---	--

## 5.1.8 Universal Asynchronous Receiver/Transmitter (UART)

Collaboration diagram for Universal Asynchronous Receiver/Transmitter (UART):



## **Files**

• file UART.c

Source code for UART module.

• file UART.h

Driver module for serial communication via UART0 and UART 1.

#### Macros

- #define ASCII\_CONVERSION 0x30
- #define UART0 TX FULL (UART0 FR R & 0x20)
- #define **UARTO\_BUFFER\_SIZE** 16
- #define UART0\_INTERRUPT\_NUM 5

#### **Functions**

void UART0\_Init (void)

Initialize UART0 to a baud rate of 115200, 8-bit data length, 1 start bit, and 1 stop bit.

• unsigned char UARTO ReadChar (void)

Read a single character from UARTO.

void UART0\_WriteChar (unsigned char input\_char)

Write a single character to UARTO.

void UARTO WriteStr (void \*input str)

Write a C string to UARTO.

void UART0\_WriteInt (uint32\_t n)

Write a 32-bit unsigned integer to UARTO.

• void UARTO WriteFloat (double n, uint8 t num decimals)

Write a floating-point number to UARTO.

void UARTO IRQ AddChar (unsigned char input char)

Add a single character to UARTO's FIFO.

• void UART0\_IRQ\_AddStr (void \*input\_str)

Add a string to UARTO's FIFO.

• void UART0\_IRQ\_AddInt (uint32\_t n)

Add an integer to UART0's FIFO.

void UARTO IRQ Start (void)

Transmit the UARTO's FIFO's contents via interrupt.

- void UARTO\_Handler (void)
- void UART1\_Init (void)

Initialize UART1 to a baud rate of 115200, 8-bit data length, 1 start bit, and 1 stop bit.

• unsigned char UART1 ReadChar (void)

Read a single character from UART1.

void UART1\_WriteChar (unsigned char input\_char)

Write a single character to UART1.

void UART1\_WriteStr (void \*input\_str)

Write a C string to UART1.

## 5.1.8.1 Detailed Description

Functions for UART-based communication.

## 5.1.8.2 Function Documentation

#### UART0\_Init()

```
void UARTO_Init (
```

Initialize UART0 to a baud rate of 115200, 8-bit data length, 1 start bit, and 1 stop bit.

Given the bus frequency (f\_bus) and desired baud rate (BR), the baud rate divisor (BRD) can be calculated:  $BRD = f_{bus}/(16*BR)$ 

The integer BRD (IBRD) is simply the integer part of the BRD: IBRD = int(BRD)

The fractional BRD (FBRD) is calculated using the fractional part (mod (BRD, 1)) of the BRD: FBRD = int((mod(BRD,1)\*64)+0.5)

## UART0\_IRQ\_AddChar()

Add a single character to UART0's FIFO.

**Parameters** 

```
input_char | ASCII character.
```

## UARTO\_IRQ\_AddInt()

Add an integer to UART0's FIFO.

**Parameters** 

n 32-bit integer to be converted and transmitted.

## UART0\_IRQ\_AddStr()

Add a string to UART0's FIFO.

**Parameters** 

```
input_str (Pointer to) array of ASCII characters.
```

## UART0\_IRQ\_Start()

Transmit the UART0's FIFO's contents via interrupt.

This function writes to the Software Trigger Interrupt (SWTRIG) register to activate the UARTO\_Handler() function rather than relying on the TM4C123's built-in UARTO interrupt sources.

## UART0\_ReadChar()

```
unsigned char UARTO_ReadChar ( void \quad )
```

Read a single character from UART0.

#### Returns

```
input_char
```

This function uses busy-wait synchronization to read a character from UARTO.

## UART0\_WriteChar()

```
void UARTO_WriteChar ( \label{eq:writeChar} \mbox{unsigned char } input\_char \mbox{ )}
```

Write a single character to UART0.

#### **Parameters**

```
input_char
```

This function uses busy-wait synchronization to write a character to UARTO.

## UART0\_WriteFloat()

Write a floating-point number to UART0.

#### **Parameters**

n	Floating-point number to be converted and transmitted.
num_decimals	Number of digits after the decimal point to include.

## UART0\_WriteInt()

Write a 32-bit unsigned integer to UART0.

## **Parameters**

n 32-bit unsigned integer to be converted and transmitted

## UART0\_WriteStr()

Write a C string to UART0.

#### **Parameters**

```
input_str (Pointer to) array of ASCII characters.
```

This function uses UART0\_WriteChar() function to write a C string to UART0. The function writes until either the entire string has been written or a null-terminated character has been reached.

## UART1\_Init()

```
void UART1_Init (
```

Initialize UART1 to a baud rate of 115200, 8-bit data length, 1 start bit, and 1 stop bit.

Given the bus frequency (f\_bus) and desired baud rate (BR), the baud rate divisor (BRD) can be calculated:  $BRD = f_{bus}/(16*BR)$ 

The integer BRD (IBRD) is simply the integer part of the BRD: IBRD = int(BRD)

The fractional BRD (FBRD) is calculated using the fractional part (mod (BRD, 1)) of the BRD: FBRD = int((mod(BRD,1)\*64)+0.5)

NOTE: LCRH must be accessed AFTER setting the BRD register

#### UART1\_ReadChar()

Read a single character from UART1.

Returns

input\_char

This function uses busy-wait synchronization to read a character from UART1.

## UART1\_WriteChar()

Write a single character to UART1.

**Parameters** 

input\_char

This function uses busy-wait synchronization to write a character to UART1.

## UART1\_WriteStr()

Write a C string to UART1.

#### **Parameters**

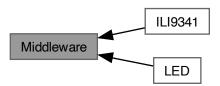
```
input_str C string
```

This function uses UART1\_WriteChar() function to write a C string to UART1. The function writes until either the entire string has been written or a null-terminated character has been reached.

#### 5.2 Middleware

High-level device driver modules.

Collaboration diagram for Middleware:



#### Modules

- ILI9341
- LED

## 5.2.1 Detailed Description

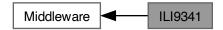
High-level device driver modules.

These modules contain functions for interfacing with external devices/peripherals via the use of low-level drivers.

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#### 5.2.2 ILI9341

Collaboration diagram for ILI9341:



#### **Files**

• file ILI9341.c

Source code for ILI9341 module.

file ILI9341.h

Driver module for interfacing with an ILI9341 LCD driver.

#### **Macros**

- #define NUM\_COLS (uint16\_t) 240
- #define NUM\_ROWS (uint16\_t) 320

#### **Enumerations**

```
    enum Cmd_t {
    NOP = 0x00 , SWRESET = 0x01 , SPLIN = 0x10 , SPLOUT = 0x11 ,
    PTLON = 0x12 , NORON = 0x13 , DINVOFF = 0x20 , DINVON = 0x21 ,
    CASET = 0x2A , PASET = 0x2B , RAMWR = 0x2C , DISPOFF = 0x28 ,
    DISPON = 0x29 , PLTAR = 0x30 , VSCRDEF = 0x33 , MADCTL = 0x36 ,
    VSCRSADD = 0x37 , IDMOFF = 0x38 , IDMON = 0x39 , PIXSET = 0x3A ,
    FRMCTR1 = 0xB1 , FRMCTR2 = 0xB2 , FRMCTR3 = 0xB3 , PRCTR = 0xB5 ,
    IFCTL = 0xF6 }
```

## **Functions**

• void ILI9341\_Init (void)

Initialize the LCD driver, the SPI module, and Timer2A.

void ILI9341\_resetHard (void)

Perform a hardware reset of the LCD driver.

void ILI9341 resetSoft (void)

Perform a software reset of the LCD driver.

void ILI9341\_setSleepMode (bool isSleeping)

Enter or exit sleep mode. The LCD driver is in sleep mode by default upon powering on or either kind of reset.

void ILI9341 setDispMode (bool isNormal, bool isFullColors)

Set the display area and color expression.

void ILI9341\_setPartialArea (uint16\_t rowStart, uint16\_t rowEnd)

Set the partial display area for partial mode. Call before activating partial mode via ILI9341\_setDisplayMode().

void ILI9341\_setDispInversion (bool is\_ON)

Toggle display inversion. Turning ON causes colors to be inverted on the display.

void ILI9341\_setDispOutput (bool is\_ON)

Turn display output ON or OFF. This function clears the display and stops outputting to the display area, but does not affect frame memory or power.

void ILI9341\_setScrollArea (uint16\_t topFixedArea, uint16\_t vertScrollArea, uint16\_t bottFixedArea)

Set the vertical scrolling area of the display. The sum of the three parameters should be equal to the max number of rows  $NUM\_ROWS = 320$ .

void ILI9341 setScrollStart (uint16 t startRow)

Set the start row for vertical scrolling.

void ILI9341\_setMemAccessCtrl (bool areRowsFlipped, bool areColsFlipped, bool areRowsAndCols
 — Switched, bool isVertRefreshFlipped, bool isColorOrderFlipped, bool isHorRefreshFlipped)

Set how data is converted from memory to display.

· void ILI9341 setColorDepth (bool is 16bit)

Set the pixel format to be 16-bit (65K colors) or 18-bit (262K colors).

void ILI9341\_NoOpCmd (void)

Send the "No Operation" command ( $NOP = 0 \times 00$ ) to the LCD driver. Can be used to terminate the "Memory Write" (RAMWR) and "Memory Read" (RAMRD) commands, but does nothing otherwise.

void ILI9341 setFrameRateNorm (uint8 t divisionRatio, uint8 t clocksPerLine)

TODO: Write brief.

void ILI9341 setFrameRateIdle (uint8 t divisionRatio, uint8 t clocksPerLine)

TODO: Write brief.

· void ILI9341\_setInterface (void)

Sets the interface for the ILl9341. The parameters for this command are hard-coded, so it only needs to be called once upon initialization.

void ILI9341 setRowAddress (uint16 t startRow, uint16 t endRow)

not using backlight, so these aren't necessary

void ILI9341\_setColAddress (uint16\_t startCol, uint16\_t endCol)

Sets the start/end rows to be written to.

void ILI9341\_writeMemCmd (void)

Sends the "Write Memory" (RAMWR) command to the LCD driver, signalling that incoming data should be written to memory.

void ILI9341\_writePixel (uint8\_t red, uint8\_t green, uint8\_t blue, bool is\_16bit)

Write a single pixel to frame memory.

• void **ILI9341\_setBlankingPorch** (uint8\_t vpf, uint8\_t vbp, uint8\_t hfp, uint8\_t hbp)

TODO: Write.

#### 5.2.2.1 Detailed Description

Functions for interfacing an ILI9341-based 240RGBx320 LCD via Serial Peripheral Interface (SPI).

#### 5.2.2.2 Enumeration Type Documentation

#### Cmd\_t

enum Cmd\_t

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#### Enumerator

No Operation.
Software Reset.
Enter Sleep Mode.
Sleep Out (i.e. Exit Sleep Mode)
Partial Display Mode ON.
Normal Display Mode ON.
Display Inversion OFF.
Display Inversion ON.
Column Address Set.
Page Address Set.
Memory Write.
Display OFF.
Display ON.
Partial Area.
Vertical Scrolling Definition.
Memory Access Control.
Vertical Scrolling Start Address.
Idle Mode OFF.
Idle Mode ON.
Pixel Format Set.
Frame Rate Control Set (Normal Mode)
Frame Rate Control Set (Idle Mode)
Frame Rate Control Set (Partial Mode)
Blanking Porch Control.

## 5.2.2.3 Function Documentation

## ILI9341\_resetHard()

```
void ILI9341_resetHard ( void\ )
```

Perform a hardware reset of the LCD driver.

The LCD driver's RESET pin requires a negative logic (i.e. active LOW) signal for >= 10 [us] and an additional 5 [ms] before further commands can be sent.

## ILI9341\_resetSoft()

Perform a software reset of the LCD driver.

the driver needs 5 [ms] before another command

#### ILI9341\_setColAddress()

Sets the start/end rows to be written to.

```
Should be called along with 'ILI9341_setRowAddress()' and before 'ILI9341_writeMemCmd()'.
```

#### **Parameters**

startCol	<pre>0 &lt;= startCol &lt;= endCol</pre>
endCol	startCol <= endCol < 240

This function is simply an interface to ILI9341\_setAddress(). To work correctly,  $start\_col$  must be no greater than end\_col, and end\_col cannot be greater than the max column number (default 240).

## ILI9341\_setColorDepth()

```
void ILI9341_setColorDepth ( bool is\_16bit )
```

Set the pixel format to be 16-bit (65K colors) or 18-bit (262K colors).

## **Parameters**

```
is_16bit
```

16-bit requires 2 transfers and allows for 65K colors. 18-bit requires 3 transfers and allows for 262K colors.

## ILI9341\_setDispInversion()

```
void ILI9341_setDispInversion ( bool \ is\_ON \ )
```

Toggle display inversion. Turning ON causes colors to be inverted on the display.

#### **Parameters**

```
is_ON true to turn ON, false to turn OFF
```

TODO: Write description

## ILI9341\_setDispMode()

```
void ILI9341\_setDispMode (
```

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```
bool isNormal,
bool isFullColors )
```

Set the display area and color expression.

```
Normal mode is the default and allows output to the full display area. Partial mode should be activated after calling 'ILI9341_setPartialArea()'.

Setting 'isFullColors' to 'false' restricts the color expression to 8 colors, determined by the MSB of the R/G/B values.
```

#### **Parameters**

isNormal	true for normal mode, false for partial mode
isFullColors	true for full colors, false for 8 colors

## ILI9341\_setDispOutput()

```
void ILI9341_setDispOutput ( bool is\_ON )
```

Turn display output ON or OFF. This function clears the display and stops outputting to the display area, but does not affect frame memory or power.

#### **Parameters**

TODO: Write description

#### ILI9341\_setFrameRateIdle()

TODO: Write brief.

TODO: Write description

#### ILI9341\_setFrameRateNorm()

TODO: Write brief.

TODO: Write description

#### ILI9341\_setInterface()

Sets the interface for the ILI9341. The parameters for this command are hard-coded, so it only needs to be called once upon initialization.

This function implements the "Interface Control" IFCTL command from p. 192-194 of the ILI9341 datasheet, which controls how the LCD driver handles 16-bit data and what interfaces (internal or external) are used.

Name	Bit #	Param #	Effect when set = 1					
MY_EOR	7		flips value of corresponding MADCTL bit					
MX_EOR	6		flips value of corresponding MADCTL bit					
MV_EOR	5	0	flips value of corresponding MADCTL bit					
BGR_EOR	3		flips value of corresponding MADCTL bit					
WEMODE	0		overflowing pixel data is not ignored					
EPF[1:0]	5:4	1	controls 16 to 18-bit pixel data conversion					
MDT[1:0]	1:0	'	controls display data transfer method					
ENDIAN	5		host sends LSB first					
DM[1:0]	3:2	selects display operation mode						
RM	1	selects GRAM interface mode						
RIM	0		specifies RGB interface-specific details					

The first param's bits are cleared so that the corresponding MADCTL bits (ILl9341\_setMemoryAccessCtrl()) are unaffected and overflowing pixel data is ignored. The EPF bits are cleared so that the LSB of the R and B values is copied from the MSB when using 16-bit color depth. The TM4C123 sends the MSB first, so the ENDIAN bit is cleared. The other bits are cleared and/or irrelevant since the RGB and VSYNC interfaces aren't used.

## ILI9341\_setMemAccessCtrl()

Set how data is converted from memory to display.

## Parameters

in	areRowsFlipped	
in	areColsFlipped	
in	areRowsAndColsSwitched	
in	isVertRefreshFlipped	
in	isColorOrderFlipped	
in	isHorRefreshFlipped	

This function implements the "Memory Access Control" (MADCTL) command from p. 127-128 of the ILI9341

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datasheet, which controls how the LCD driver displays data upon writing to memory.

Name	Bit #	Effect when set = 1
MY	7	flip row (AKA "page") addresses
MX	6	flip column addresses
MV	5	exchange rows and column addresses
ML	4	reverse horizontal refresh order
BGR	3	reverse color input order (RGB -> BGR)
МН	2	reverse vertical refresh order

All bits are clear after powering on or HWRESET.

## ILI9341\_setPartialArea()

Set the partial display area for partial mode. Call before activating partial mode via ILI9341\_setDisplayMode().

#### **Parameters**

rowStart	
rowEnd	

## ILI9341\_setRowAddress()

not using backlight, so these aren't necessary

Sets the start/end rows to be written to.

```
Should be called along with 'ILI9341_setColAddress()' and before 'ILI9341_writeMemCmd()'.
```

## Parameters

startRow	0 <= startRow <= endRow
endRow	startRow <= endRow < 320

This function is simply an interface to ILI9341\_setAddress(). To work correctly,  $start_{row}$  must be no greater than end\_row, and end\_row cannot be greater than the max row number (default 320).

## ILI9341\_setScrollArea()

Set the vertical scrolling area of the display. The sum of the three parameters should be equal to the max number of rows NUM ROWS = 320.

#### **Parameters**

topFixedArea	Number of rows fixed at the top of the screen.
vertScrollArea	Number of rows that scroll.
bottFixedArea	Number of rows fixed at the bottom of the screen.

## ILI9341\_setScrollStart()

Set the start row for vertical scrolling.

#### **Parameters**

## ILI9341\_setSleepMode()

Enter or exit sleep mode. The LCD driver is in sleep mode by default upon powering on or either kind of reset.

## **Parameters**

```
isSleeping | true to enter sleep mode, false to exit
```

This function turns sleep mode ON or OFF depending on the value of  $is\_sleeping$ . Either way, the MCU must wait >= 5 [ms] before sending further commands.

It's also necessary to wait 120 [ms] before sending SPLOUT after sending SPLIN or a reset, so this function waits 120 [ms] regardless of the preceding event.

## ILI9341\_writeMemCmd()

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Sends the "Write Memory" (RAMWR) command to the LCD driver, signalling that incoming data should be written to memory.

Should be called after setting the row (ILI9341\_setRowAddress()) and/or and/or column (ILI9341\_setRowAddress()) addresses, but before writing image data (ILI9341\_writePixel()).

## ILI9341\_writePixel()

Write a single pixel to frame memory.

```
Call 'ILI9341_writeMemCmd()' before this one.
```

#### **Parameters**

red	5 or 6-bit R value
green	5 or 6-bit G value
blue	5 or 6-bit B value
is_16bit	true for 16-bit (65K colors, 2 transfers) color depth, false for 18-bit (262K colors, 3 transfer) color depth NOTE: set color depth via ILI9341_setColorDepth()

This function sends one pixel to the display. Because the serial interface (SPI) is used, each pixel requires 2 transfers in 16-bit mode and 3 transfers in 18-bit mode.

The following table (adapted from p. 63 of the datasheet) visualizes how the RGB data is sent to the display when using 16-bit color depth.

Transfer	1										2	2				
Bit #	7	6	5	4	3	2	1	0	7	6	5	4	3	2	1	0
Value	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B4	В3	B2	B1	B0

The following table (adapted from p. 64 of the datasheet) visualizes how the RGB data is sent to the display when using 18-bit color depth.

Transfer		1					2				
Bit #	7	6	5	4	3	2	1	0	7	6	
Value	R5	R4	R3	R2	R1	R0	0/1	0/1	G5	G4	

#### 5.2.3 LED

Collaboration diagram for LED:



#### **Files**

• file Led.c

Source code for LED module.

· file Led.h

Interface for LED module.

#### **Data Structures**

• struct Led\_t

#### **Macros**

• #define LED\_POOL\_SIZE 3

## **Functions**

```
• Led_t * Led_Init (GPIO_Port_t *gpioPort, GPIO_Pin_t pin)
```

Initialize a light-emitting diode (LED) as an Led\_t.

GPIO\_Port\_t \* Led\_GetPort (Led\_t \*led)

Get the GPIO port associated with the LED.

• GPIO\_Pin\_t Led\_GetPin (Led\_t \*led)

Get the GPIO pin associated with the LED.

bool Led\_isOn (Led\_t \*led)

Check the LED's status.

void Led\_TurnOn (Led\_t \*led)

Turn the LED ON.

void Led\_TurnOff (Led\_t \*led)

Turn the LED OFF.

void Led\_Toggle (Led\_t \*led)

Toggle the LED (i.e.  $OFF \rightarrow ON \text{ or } ON \rightarrow OFF$ ).

## 5.2.3.1 Detailed Description

Functions for driving light-emitting diodes (LEDs) via GPIO.

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## 5.2.3.2 Function Documentation

## Led\_GetPin()

Get the GPIO pin associated with the LED.

## **Parameters**

in	led	Pointer to LED data structure.
out	GPIO_←	GPIO pin associated with the LED.
	Pin_t	

## Led\_GetPort()

Get the GPIO port associated with the LED.

#### **Parameters**

in	led	Pointer to LED data structure.
out	GPIO_Port←	Pointer to a GPIO port data structure.
	_ <i>t</i> *	

## Led\_Init()

Initialize a light-emitting diode (LED) as an Led\_t.

## Parameters

in	gpioPort	Pointer to a struct representing a GPIO port.
in	pin	GPIO pin to use.
out	Led_t*	Pointer to LED data structure.

## Led\_isOn()

```
bool Led_isOn ( \label{led_t*led} \begin{tabular}{ll} $\operatorname{Led_t} * \operatorname{led} \end{tabular}
```

Check the LED's status.

# **Parameters**

in	led Pointer to LED data structure	
out	true	the LED is ON.
out	false	the LED is OFF.

# Led\_Toggle()

```
void Led_Toggle (
    Led_t * led )
```

Toggle the LED (i.e.  $OFF \rightarrow ON \text{ or } ON \rightarrow OFF$ ).

### **Parameters**

in led Pointer to LED data structure
--------------------------------------

# Led\_TurnOff()

Turn the LED OFF.

# **Parameters**

in <i>le</i>	Pointer to LED data structur	e.
--------------	------------------------------	----

# Led\_TurnOn()

```
void Led_TurnOn (
    Led_t * led )
```

Turn the LED  $\ensuremath{\mathtt{ON}}.$ 

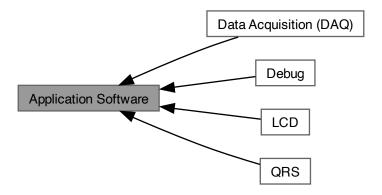
## **Parameters**

in	led	Pointer to LED data structure.

# 5.3 Application Software

Application-specific software modules.

Collaboration diagram for Application Software:



## Modules

- Data Acquisition (DAQ)
- Debug
- LCD
- QRS

# 5.3.1 Detailed Description

Application-specific software modules.

These modules contain functions specifically built for this project's purposes.

## 5.3.2 Data Acquisition (DAQ)

Collaboration diagram for Data Acquisition (DAQ):



### **Files**

• file DAQ.c

Source code for DAQ module.

• file DAQ.h

Application software for handling data acquision (DAQ) functions.

· file lookup.c

Lookup table source code.

· file lookup.h

Lookup table API.

#### **Macros**

• #define SAMPLING\_PERIOD\_MS 5

sampling period in ms (  $T_s = 1/f_s$ )

- #define LOOKUP\_ADC\_MAX (float32\_t) 5.5
- #define LOOKUP\_ADC\_MIN (float32\_t)(-5.5)

## **Typedefs**

• typedef arm\_biquad\_casd\_df1\_inst\_f32 filt\_t

### **Enumerations**

• enum {

```
NUM_STAGES_LOWPASS = 4 , NUM_COEFF_LOWPASS = NUM_STAGES_LOWPASS * 5 , STATE_ ← BUFF_SIZE_LOWPASS = NUM_STAGES_LOWPASS * 4 , NUM_STAGES_NOTCH = 6 , NUM_COEFF_NOTCH = NUM_STAGES_NOTCH * 5 , STATE_BUFF_SIZE_NOTCH = NUM_STAGES_ ← NOTCH * 4 }
```

# **Functions**

void DAQ\_Init (void)

Initialize the data acquisition module, including the input filter and timer interrupt-based analog-to-digital conversion (ADC) @  $f_s = 200[Hz]$ .

• float32 t DAQ Filter (volatile float32 t inputSample)

Filter an input sample using a 40 [Hz] low pass filter and a 60 [Hz] notch filter.

const float32\_t \* Lookup\_GetPtr\_ADC (void)

Return a pointer to the ADC lookup table.

### 5.3.2.1 Detailed Description

Module for managing data acquisition (DAQ) functions.

### 5.3.2.2 Function Documentation

# DAQ\_Filter()

Filter an input sample using a 40 [Hz] low pass filter and a 60 [Hz] notch filter.

## **Parameters**

in	inputSample	Raw input sample in range [-5.5, 5.5) [V].	
out	float32_t	Filtered output sample.	7

# Lookup\_GetPtr\_ADC()

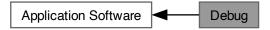
Return a pointer to the ADC lookup table.

Returns

const float32\_t\*

# 5.3.3 Debug

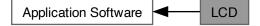
Collaboration diagram for Debug:



Module for debugging functions, including serial output and assertion.

## 5.3.4 LCD

Collaboration diagram for LCD:



#### **Files**

• file LCD.c

Source code for LCD module.

• file LCD.h

Module for outputting the ECG waveform and HR to a liquid crystal display (LCD).

#### **Enumerations**

enum { X MAX = NUM ROWS, Y MAX = NUM COLS }

## **Color Setting Functions**

• enum {

• void LCD\_setColor (uint8\_t R\_val, uint8\_t G\_val, uint8\_t B\_val)

Set the current color value for the display. Only the first 5-6 bits of each inputted value are used.

void LCD\_setColor\_3bit (uint8\_t color\_code)

Set the color value via a 3-bit code.

# Init./Config. Functions

void LCD Init (void)

Initialize the LCD driver and its internal independencies.

void LCD\_toggleOutput (void)

Toggle display output ON or OFF (OFF by default). Turning output OFF prevents the LCD driver from refreshing the display, which can prevent abnormalities like screen tearing while attempting to update the image.

• void LCD\_toggleInversion (void)

Toggle color inversion ON or OFF (OFF by default).

• void LCD\_toggleColorDepth (void)

Toggle 16-bit or 18-bit color depth (16-bit by default).

## **Drawing Area Definition Functions**

```
    void LCD setArea (uint16 t x1 new, uint16 t x2 new, uint16 t y1 new, uint16 t y2 new)
```

Set the area of the display to be written to.  $0 <= x1 <= x2 < X\_MAX 0 <= y1 <= y2 < Y\_MAX$ 

void LCD\_setX (uint16\_t x1\_new, uint16\_t x2\_new)

Set only new x-coordinates to be written to.  $0 <= x1 <= x2 < X\_MAX$ 

void LCD\_setY (uint16\_t y1\_new, uint16\_t y2\_new)

Set only new y-coordinates to be written to.  $0 <= y1 <= y2 < Y\_MAX$ 

## **Drawing Functions**

• void LCD\_Draw (void)

Draw on the LCD display. Call this function after setting the drawable area via LCD\_setArea(), or after individually calling LCD\_setX() and/or LCD\_setY().

void LCD\_Fill (void)

Fill the display with a single color.

• void LCD\_drawHoriLine (uint16\_t yCenter, uint16\_t lineWidth)

Draw a horizontal line across the entire display.

void LCD\_drawVertLine (uint16\_t xCenter, uint16\_t lineWidth)

Draw a vertical line across the entire display.

• void LCD\_drawRectangle (uint16\_t x1, uint16\_t dx, uint16\_t y1, uint16\_t dy, bool isFilled)

Draw a rectangle of size dx x dy onto the display. The bottom-left corner will be located at (x1, y1).

• void LCD\_graphSample (uint16\_t x1, uint16\_t dx, uint16\_t y1, uint16\_t dy, uint16\_t y\_min, uint16\_t y\_max, uint16\_t color\_code)

Draw a rectangle of size dx x dy and blank out all other pixels between y\_min and y\_max.

## 5.3.4.1 Detailed Description

Module for displaying graphs on an LCD via the ILI9341 module.

### 5.3.4.2 Function Documentation

## LCD\_Draw()

```
void LCD_Draw (
          void )
```

Draw on the LCD display. Call this function after setting the drawable area via LCD\_setArea(), or after individually calling LCD\_setX() and/or LCD\_setY().

# LCD\_drawHoriLine()

Draw a horizontal line across the entire display.

#### **Parameters**

yCenter y-coordinate to center the line on lineWidth width of the line; should be a positive		y-coordinate to center the line on
		width of the line; should be a positive, odd number

## LCD\_drawRectangle()

```
uint16_t dx,
uint16_t y1,
uint16_t dy,
bool isFilled )
```

Draw a rectangle of size dx x dy onto the display. The bottom-left corner will be located at (x1, y1).

### **Parameters**

x1	lowest (left-most) x-coordinate	
dx	length (horizontal distance) of the rectangle	
y1	lowest (bottom-most) y-coordinate	
dy	height (vertical distance) of the rectangle	
isFilled	true to fill the rectangle, false to leave it unfilled	

# LCD\_drawVertLine()

Draw a vertical line across the entire display.

### **Parameters**

xCenter	x-coordinate to center the line on	
lineWidth	width of the line; should be a positive, odd number	

# LCD\_graphSample()

Draw a rectangle of size  $\mathtt{dx}\ \mathtt{x}\ \mathtt{dy}$  and blank out all other pixels between  $\mathtt{y}\_\mathtt{min}$  and  $\mathtt{y}\_\mathtt{max}$ .

### **Parameters**

x1	lowest (left-most) x-coordinate	
dx length (horizontal distance) of the d		
y1	y-coordinate of the pixel's bottom side	
dy	height (vertical distance) of the pixel	
y_min	lowest (bottom-most) y-coordinate	
y_max	highest (top-most) y-coordinate	
color_code	3-bit color code	

TODO: Write description

### LCD\_setArea()

Set the area of the display to be written to. 0  $<= x1 <= x2 < X_MAX 0 <= y1 <= y2 < Y_MAX$ 

### **Parameters**

x1_new	left-most x-coordinate	
x2_new	right-most x-coordinate	
y1_new	lowest y-coordinate	
y2_new	highest y-coordinate	

# LCD\_setColor()

Set the current color value for the display. Only the first 5-6 bits of each inputted value are used.

### **Parameters**

R_val	5-bit ([0-31]) R value; 6-bit ([0-63]) if color depth is 18-bit	
G_val	6-bit ([0-63]) G value	
B_val	5-bit ( $[0-31]$ ) B value; 6-bit ( $[0-63]$ ) if color depth is 18-bit	

## LCD\_setColor\_3bit()

Set the color value via a 3-bit code.

#### **Parameters**

```
color_code 3-bit color value to use. Bits 2, 1, 0 correspond to R, G, and B values, respectively.
```

This is simply a convenience function for setting the color using the enum values defined in the header file. The ones with the  $\_{\tt INV}$  suffix should be used when the display colors are inverted.

hex	binary	macro
0x00	000	LCD_BLACK
0x01	001	LCD_BLUE
0x02	010	LCD_GREEN
0x03	011	LCD_CYAN
0x04	100	LCD_RED
0x05	101	LCD_PURPLE
0x06	110	LCD_YELLOW
0x07	111	LCD_WHITE

# LCD\_setX()

```
void LCD_setX ( \label{lcd_lcd_lcd_lcd} \mbox{uint16\_t} \ x1\_new, \\ \mbox{uint16\_t} \ x2\_new \ )
```

Set only new x-coordinates to be written to. 0  $<= x1 <= x2 < X\_MAX$ 

## **Parameters**

x1_new	left-most x-coordinate
x2_new	right-most x-coordinate

# LCD\_setY()

Set only new y-coordinates to be written to. 0 <= y1 <= y2 < Y\_MAX

# Parameters

y1_new	lowest y-coordinate
y2_new	highest y-coordinate

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### 5.3.5 QRS

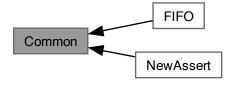
Collaboration diagram for QRS:



Module for analyzing ECG data to determine heart rate.

## 5.4 Common

Collaboration diagram for Common:



# Modules

- FIFO
- NewAssert

# **Files**

· file NewAssert.c

 ${\it Source\ code\ for\ custom\ assert\ implementation}.$ 

· file NewAssert.h

 ${\it Header file for custom} \ {\it assert implementation}.$ 

# **Functions**

• void Assert (bool condition)

 $\textit{Custom} \ \textit{assert implementation that is more lightweight than the one from} \ \textit{newlib}.$ 

# 5.4.1 Detailed Description

Modules that are used by multiple layers and/or don't fit into any one layer.

### 5.4.2 Function Documentation

# Assert()

```
void Assert (
          bool condition )
```

Custom assert implementation that is more lightweight than the one from newlib.

### **Parameters**

in	condition	Conditional to test. Causes an infinite loop if false.
----	-----------	--

### 5.4.3 FIFO

Collaboration diagram for FIFO:



## **Files**

• file FIFO.c

Source code for FIFO buffer module.

• file FIFO.h

FIFO buffer data structure.

# **Data Structures**

• struct FIFO\_t

## Macros

• #define **FIFO\_POOL\_SIZE** 5

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### **Functions**

volatile FIFO\_t \* FIFO\_Init (volatile uint32\_t buffer[], const uint32\_t N)
 Initialize a FIFO buffer of length N.

### **Basic Operations**

void FIFO\_Put (volatile FIFO\_t \*fifo, const uint32\_t val)

Add a value to the end of the buffer.

• uint32\_t FIFO\_Get (volatile FIFO\_t \*fifo)

Remove the first value of the buffer.

• void FIFO\_TransferOne (volatile FIFO\_t \*srcFifo, volatile FIFO\_t \*destFifo)

Transfer a value from one FIFO buffer to another.

## **Bulk Removal**

void FIFO\_Flush (volatile FIFO\_t \*fifo, uint32\_t outputBuffer[])

Empty the FIFO buffer's contents into an array.

void FIFO\_Reset (volatile FIFO\_t \*fifo)

Reset the FIFO buffer.

• void FIFO\_TransferAll (volatile FIFO\_t \*srcFifo, volatile FIFO\_t \*destFifo)

Transfer the contents of one FIFO buffer to another.

### **Peeking**

uint32\_t FIFO\_PeekOne (volatile FIFO\_t \*fifo)

See the first element in the FIFO without removing it.

void FIFO\_PeekAll (volatile FIFO\_t \*fifo, uint32\_t outputBuffer[])

See the FIFO buffer's contents without removing them.

## **Status Checks**

bool FIFO\_isFull (volatile FIFO\_t \*fifo)

Check if the FIFO buffer is full.

bool FIFO\_isEmpty (volatile FIFO\_t \*fifo)

Check if the FIFO buffer is empty.

uint32 t FIFO getCurrSize (volatile FIFO t \*fifo)

Get the current size of the FIFO buffer.

## 5.4.3.1 Detailed Description

Module for using the "first-in first-out (FIFO) buffer" data structure.

## 5.4.3.2 Function Documentation

### FIFO\_Flush()

Empty the FIFO buffer's contents into an array.

## **Parameters**

fifo	Pointer to source FIFO buffer.
outputBuffer	Array to output values to. Should be the same length as the FIFO buffer.

# FIFO\_Get()

Remove the first value of the buffer.

#### **Parameters**

fifo	Pointer to FIFO object
------	------------------------

# Returns

First sample in the FIFO.

# FIFO\_getCurrSize()

Get the current size of the FIFO buffer.

### **Parameters**

fifo	Pointer to the FIFO buffer.
1110	Pointer to the FIFO buller.

# FIFO\_Init()

Initialize a FIFO buffer of length  ${\tt N}.$ 

## **Parameters**

buffer	Array of size $\ensuremath{\mathbb{N}}$ to be used as FIFO buffer
Ν	Length of buffer. Usable length is ${\tt N}-{\tt 1}.$

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### Returns

pointer to the FIFO buffer

TODO: Add details

# FIFO\_isEmpty()

```
bool FIFO_isEmpty ( \label{eq:condition} \mbox{volatile FIFO\_t * $fifo$ )}
```

Check if the FIFO buffer is empty.

### **Parameters**

fifo Pointer to the FIFO buffer.	
----------------------------------	--

### Return values

true	The buffer is empty.
false	The buffer is not empty.

# FIFO\_isFull()

Check if the FIFO buffer is full.

## **Parameters**

fifo Pointer to the FIFO buffer.
----------------------------------

### Return values

true	The buffer is full.
false	The buffer is not full.

# FIFO\_PeekAll()

See the FIFO buffer's contents without removing them.

## **Parameters**

fifo	Pointer to FIFO object
outputBuffer	Array to output values to. Should be the same length as the FIFO buffer.

# FIFO\_PeekOne()

See the first element in the FIFO without removing it.

### **Parameters**

fifo	Pointer to FIFO object
------	------------------------

## Returns

First sample in the FIFO.

# FIFO\_Put()

Add a value to the end of the buffer.

## **Parameters**

fifo	Pointer to FIFO object
val	last value in the buffer

# FIFO\_Reset()

Reset the FIFO buffer.

## **Parameters**

in	fifo	Pointer to FIFO buffer.

# FIFO\_TransferAll()

Transfer the contents of one FIFO buffer to another.

### **Parameters**

srcFifo	Pointer to source FIFO buffer.
destFifo	Pointer to destination FIFO buffer.

# FIFO\_TransferOne()

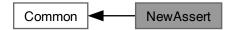
Transfer a value from one FIFO buffer to another.

### **Parameters**

srcFifo	Pointer to source FIFO buffer.
destFifo	Pointer to destination FIFO buffer.

# 5.4.4 NewAssert

Collaboration diagram for NewAssert:



Module for using a custom assert implementation.

# 6 Data Structure Documentation

# 6.1 FIFO\_t Struct Reference

### **Data Fields**

volatile uint32\_t \* buffer

(pointer to) array to use as FIFO buffer

volatile uint32\_t N

length of buffer

volatile uint32\_t front\_idx

idx of front of FIFO

volatile uint32\_t back\_idx

idx of back of FIFO

The documentation for this struct was generated from the following file:

• FIFO.c

# 6.2 GPIO\_Port\_t Struct Reference

## **Data Fields**

- const uint32\_t BASE\_ADDRESS
- const uint32\_t DATA\_REGISTER
- bool islnit

The documentation for this struct was generated from the following file:

• GPIO.c

# 6.3 Led\_t Struct Reference

### **Data Fields**

• GPIO\_Port\_t \* GPIO\_PORT\_PTR

pointer to GPIO port data structure

GPIO\_Pin\_t GPIO\_PIN

GPIO pin number.

bool is\_ON

state indicator

The documentation for this struct was generated from the following file:

• Led.c

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## 7 File Documentation

### 7.1 DAQ.c File Reference

Source code for DAQ module.

```
#include "DAQ.h"
#include "ADC.h"
#include "Timer.h"
#include "FIFO.h"
#include "NewAssert.h"
#include "arm_math_types.h"
#include "dsp/filtering_functions.h"
#include "lookup.h"
#include "tm4c123gh6pm.h"
#include <math.h>
#include <stdbool.h>
#include <stdint.h>
```

## Macros

• #define SAMPLING\_PERIOD\_MS 5 sampling period in ms (  $T_s = 1/f_s$ )

### **Typedefs**

• typedef arm\_biquad\_casd\_df1\_inst\_f32 filt\_t

# **Enumerations**

• enum {

```
NUM_STAGES_LOWPASS = 4 , NUM_COEFF_LOWPASS = NUM_STAGES_LOWPASS * 5 , STATE_ \leftrightarrow BUFF_SIZE_LOWPASS = NUM_STAGES_LOWPASS * 4 , NUM_STAGES_NOTCH = 6 , NUM_COEFF_NOTCH = NUM_STAGES_NOTCH * 5 , STATE_BUFF_SIZE_NOTCH = NUM_STAGES_ \leftrightarrow NOTCH * 4 }
```

#### **Functions**

void DAQ\_Init (void)

Initialize the data acquisition module, including the input filter and timer interrupt-based analog-to-digital conversion (ADC) @  $f_s = 200[Hz]$ .

• float32\_t DAQ\_Filter (volatile float32\_t inputSample)

Filter an input sample using a 40 [Hz] low pass filter and a 60 [Hz] notch filter.

# 7.1.1 Detailed Description

Source code for DAQ module.

**Author** 

Bryan McElvy

## 7.2 DAQ.h File Reference

Application software for handling data acquision (DAQ) functions.

```
#include "ADC.h"
#include "Timer.h"
#include "FIFO.h"
#include "arm_math_types.h"
#include "dsp/filtering_functions.h"
#include "tm4c123gh6pm.h"
#include <stdint.h>
```

### **Functions**

void DAQ Init (void)

Initialize the data acquisition module, including the input filter and timer interrupt-based analog-to-digital conversion (ADC) @  $f_s = 200[Hz]$ .

float32\_t DAQ\_Filter (volatile float32\_t inputSample)

Filter an input sample using a 40 [Hz] low pass filter and a 60 [Hz] notch filter.

# 7.2.1 Detailed Description

Application software for handling data acquision (DAQ) functions.

**Author** 

Bryan McElvy

# 7.3 Debug.h File Reference

Functions to output debugging information to a serial port via UART.

```
#include "UART.h"
#include "NewAssert.h"
#include "tm4c123gh6pm.h"
#include <stdbool.h>
#include <stdint.h>
```

## **Enumerations**

```
enum msg_t {
    START_MSG, DAQ_INIT, QRS_INIT, LCD_INIT,
    ASSERT_FALSE }
```

### **Functions**

void **Debug\_Init** (void)

Init. the Debug module and send a start message to the port.

void Debug\_SendMsg (void \*message)

Send a message to the serial port.

• void Debug\_SendFromList (msg\_t msg)

Send a message from the message list.

void Debug\_WriteFloat (double value)

Write a floating-point value to the serial port.

void Debug\_Assert (bool condition)

Stops program if condition is true. Useful for bug detection during debugging.

### 7.3.1 Detailed Description

Functions to output debugging information to a serial port via UART.

**Author** 

Bryan McElvy

### 7.3.2 Function Documentation

# Debug\_Assert()

```
void Debug_Assert (
          bool condition )
```

Stops program if condition is true. Useful for bug detection during debugging.

#### **Parameters**

condition

## Debug\_SendFromList()

Send a message from the message list.

## **Parameters**

in	msg	Message to send.

### Debug\_SendMsg()

```
void Debug_SendMsg (
     void * message )
```

Send a message to the serial port.

### **Parameters**

```
message (Pointer to) array of ASCII characters.
```

## Debug\_WriteFloat()

Write a floating-point value to the serial port.

#### **Parameters**

in	value	Floating-point value.
----	-------	-----------------------

## 7.4 LCD.c File Reference

Source code for LCD module.

```
#include "LCD.h"
#include "ILI9341.h"
#include "SPI.h"
#include "Timer.h"
#include "NewAssert.h"
#include "tm4c123gh6pm.h"
#include <stdbool.h>
#include <stdint.h>
```

## **Functions**

## Init./Config. Functions

void LCD\_Init (void)

Initialize the LCD driver and its internal independencies.

void LCD\_toggleOutput (void)

Toggle display output ON or OFF (OFF by default). Turning output OFF prevents the LCD driver from refreshing the display, which can prevent abnormalities like screen tearing while attempting to update the image.

void LCD\_toggleInversion (void)

Toggle color inversion ON or OFF (OFF by default).

• void LCD\_toggleColorDepth (void)

Toggle 16-bit or 18-bit color depth (16-bit by default).

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#### **Drawing Area Definition Functions**

```
void LCD_setArea (uint16_t x1_new, uint16_t x2_new, uint16_t y1_new, uint16_t y2_new)

Set the area of the display to be written to. 0 <= x1 <= x2 < X_MAX 0 <= y1 <= y2 < Y_MAX</li>
void LCD_setX (uint16_t x1_new, uint16_t x2_new)

Set only new x-coordinates to be written to. 0 <= x1 <= x2 < X_MAX</li>
void LCD_setY (uint16_t y1_new, uint16_t y2_new)

Set only new y-coordinates to be written to. 0 <= y1 <= y2 < Y_MAX</li>
```

### **Color Setting Functions**

void LCD\_setColor (uint8\_t R\_val, uint8\_t G\_val, uint8\_t B\_val)
 Set the current color value for the display. Only the first 5-6 bits of each inputted value are used.
 void LCD\_setColor\_3bit (uint8\_t color\_code)

Set the color value via a 3-bit code.

### **Drawing Functions**

• void LCD\_Draw (void)

Draw on the LCD display. Call this function after setting the drawable area via LCD\_setArea(), or after individually calling LCD\_setX() and/or LCD\_setY().

void LCD\_Fill (void)

Fill the display with a single color.

• void LCD\_drawHoriLine (uint16\_t yCenter, uint16\_t lineWidth)

Draw a horizontal line across the entire display.

void LCD drawVertLine (uint16 t xCenter, uint16 t lineWidth)

Draw a vertical line across the entire display.

void LCD\_drawRectangle (uint16\_t x1, uint16\_t dx, uint16\_t y1, uint16\_t dy, bool isFilled)

Draw a rectangle of size  $dx \times dy$  onto the display. The bottom-left corner will be located at (x1, y1).

void LCD\_graphSample (uint16\_t x1, uint16\_t dx, uint16\_t y1, uint16\_t dy, uint16\_t y\_min, uint16\_t y\_max, uint16\_t color\_code)

Draw a rectangle of size dx x dy and blank out all other pixels between y\_min and y\_max.

## 7.4.1 Detailed Description

Source code for LCD module.

**Author** 

Bryan McElvy

## 7.5 LCD.h File Reference

Module for outputting the ECG waveform and HR to a liquid crystal display (LCD).

```
#include "ILI9341.h"
#include "SPI.h"
#include "Timer.h"
#include "NewAssert.h"
#include "tm4c123gh6pm.h"
#include <stdbool.h>
#include <stdint.h>
```

#### **Enumerations**

enum { X\_MAX = NUM\_ROWS , Y\_MAX = NUM\_COLS }

#### **Functions**

### Init./Config. Functions

void LCD Init (void)

Initialize the LCD driver and its internal independencies.

void LCD\_toggleOutput (void)

Toggle display output ON or OFF (OFF by default). Turning output OFF prevents the LCD driver from refreshing the display, which can prevent abnormalities like screen tearing while attempting to update the image.

void LCD\_toggleInversion (void)

Toggle color inversion ON or OFF (OFF by default).

void LCD\_toggleColorDepth (void)

Toggle 16-bit or 18-bit color depth (16-bit by default).

## **Drawing Area Definition Functions**

```
    void LCD_setArea (uint16_t x1_new, uint16_t x2_new, uint16_t y1_new, uint16_t y2_new)
```

Set the area of the display to be written to.  $0 <= x1 <= x2 < X_MAX 0 <= y1 <= y2 < Y_MAX$ 

void LCD setX (uint16 t x1 new, uint16 t x2 new)

Set only new x-coordinates to be written to.  $0 <= x1 <= x2 < X\_MAX$ 

void LCD\_setY (uint16\_t y1\_new, uint16\_t y2\_new)

Set only new y-coordinates to be written to.  $0 \le y1 \le y2 \le y\_MAX$ 

## **Drawing Functions**

• void LCD Draw (void)

Draw on the LCD display. Call this function after setting the drawable area via LCD\_setArea(), or after individually calling LCD\_setX() and/or LCD\_setY().

void LCD\_Fill (void)

Fill the display with a single color.

• void LCD\_drawHoriLine (uint16\_t yCenter, uint16\_t lineWidth)

Draw a horizontal line across the entire display.

void LCD\_drawVertLine (uint16\_t xCenter, uint16\_t lineWidth)

Draw a vertical line across the entire display.

void LCD\_drawRectangle (uint16\_t x1, uint16\_t dx, uint16\_t y1, uint16\_t dy, bool isFilled)

Draw a rectangle of size  $dx \times dy$  onto the display. The bottom-left corner will be located at (x1, y1).

• void LCD\_graphSample (uint16\_t x1, uint16\_t dx, uint16\_t y1, uint16\_t dy, uint16\_t y\_min, uint16\_t y\_max, uint16\_t color\_code)

Draw a rectangle of size dx x dy and blank out all other pixels between  $y\_min$  and  $y\_max$ .

# **Color Setting Functions**

```
enum {
```

```
LCD_BLACK = 0x00 , LCD_RED = 0x04 , LCD_GREEN = 0x02 , LCD_BLUE = 0x01 ,
LCD_YELLOW = 0x06 , LCD_CYAN = 0x03 , LCD_PURPLE = 0x05 , LCD_WHITE = 0x07 ,
LCD_BLACK_INV = LCD_WHITE , LCD_RED_INV = LCD_CYAN , LCD_GREEN_INV = LCD_PURPLE ,
LCD_BLUE_INV = LCD_YELLOW ,
LCD_YELLOW_INV = LCD_BLUE , LCD_CYAN_INV = LCD_RED , LCD_PURPLE_INV = LCD_GREEN ,
LCD_WHITE_INV = LCD_BLACK }
```

void LCD\_setColor (uint8\_t R\_val, uint8\_t G\_val, uint8\_t B\_val)

Set the current color value for the display. Only the first 5-6 bits of each inputted value are used.

void LCD\_setColor\_3bit (uint8\_t color\_code)

Set the color value via a 3-bit code.

7.6 QRS.h File Reference 57

## 7.5.1 Detailed Description

Module for outputting the ECG waveform and HR to a liquid crystal display (LCD).

Author

Bryan McElvy

## 7.6 QRS.h File Reference

QRS detection algorithm functions.

```
#include "dsp/filtering_functions_f16.h"
```

## 7.6.1 Detailed Description

QRS detection algorithm functions.

Author

Bryan McElvy

This module contains functions for detecting heart rate (HR) using a simplified version of the Pan-Tompkins algorithm.

## 7.7 UserCtrl.h File Reference

Interface for user control module.

```
#include "GPIO.h"
#include "Timer.h"
```

## **Functions**

void UserCtrl\_Init ()

Initializes the UserCtrl module and its dependencies (Timer0B and GPIO\_PortF)

## 7.7.1 Detailed Description

Interface for user control module.

**Author** 

Bryan McElvy

### 7.8 FIFO.c File Reference

Source code for FIFO buffer module.

```
#include "FIFO.h"
#include "NewAssert.h"
#include <stdint.h>
#include <stdbool.h>
```

#### **Data Structures**

• struct FIFO\_t

### **Functions**

volatile FIFO\_t \* FIFO\_Init (volatile uint32\_t buffer[], const uint32\_t N)
 Initialize a FIFO buffer of length N.

## **Basic Operations**

```
    void FIFO_Put (volatile FIFO_t *fifo, const uint32_t val)
```

Add a value to the end of the buffer.

uint32\_t FIFO\_Get (volatile FIFO\_t \*fifo)

Remove the first value of the buffer.

• void FIFO\_TransferOne (volatile FIFO\_t \*srcFifo, volatile FIFO\_t \*destFifo)

Transfer a value from one FIFO buffer to another.

### **Bulk Removal**

```
• void FIFO_Flush (volatile FIFO_t *fifo, uint32_t outputBuffer[])
```

Empty the FIFO buffer's contents into an array.

• void FIFO\_Reset (volatile FIFO\_t \*fifo)

Reset the FIFO buffer.

void FIFO\_TransferAll (volatile FIFO\_t \*srcFifo, volatile FIFO\_t \*destFifo)

Transfer the contents of one FIFO buffer to another.

### **Peeking**

```
    uint32_t FIFO_PeekOne (volatile FIFO_t *fifo)
```

See the first element in the FIFO without removing it.

void FIFO\_PeekAll (volatile FIFO\_t \*fifo, uint32\_t outputBuffer[])

See the FIFO buffer's contents without removing them.

#### Status Checks

```
    bool FIFO isFull (volatile FIFO t *fifo)
```

Check if the FIFO buffer is full.

• bool FIFO\_isEmpty (volatile FIFO\_t \*fifo)

Check if the FIFO buffer is empty.

uint32\_t FIFO\_getCurrSize (volatile FIFO\_t \*fifo)

Get the current size of the FIFO buffer.

7.9 FIFO.h File Reference 59

## 7.8.1 Detailed Description

Source code for FIFO buffer module.

**Author** 

Bryan McElvy

### 7.9 FIFO.h File Reference

#### FIFO buffer data structure.

```
#include "NewAssert.h"
#include <stdbool.h>
#include <stdint.h>
```

#### **Macros**

• #define FIFO\_POOL\_SIZE 5

### **Functions**

volatile FIFO\_t \* FIFO\_Init (volatile uint32\_t buffer[], const uint32\_t N)
 Initialize a FIFO buffer of length N.

# **Basic Operations**

```
    void FIFO Put (volatile FIFO t *fifo, const uint32 t val)
```

Add a value to the end of the buffer.

• uint32\_t FIFO\_Get (volatile FIFO\_t \*fifo)

Remove the first value of the buffer.

void FIFO\_TransferOne (volatile FIFO\_t \*srcFifo, volatile FIFO\_t \*destFifo)

Transfer a value from one FIFO buffer to another.

### **Bulk Removal**

```
• void FIFO Flush (volatile FIFO t *fifo, uint32 t outputBuffer[])
```

Empty the FIFO buffer's contents into an array.

void FIFO\_Reset (volatile FIFO\_t \*fifo)

Reset the FIFO buffer.

void FIFO TransferAll (volatile FIFO t \*srcFifo, volatile FIFO t \*destFifo)

Transfer the contents of one FIFO buffer to another.

## **Peeking**

```
• uint32_t FIFO_PeekOne (volatile FIFO_t *fifo)
```

See the first element in the FIFO without removing it.

void FIFO\_PeekAll (volatile FIFO\_t \*fifo, uint32\_t outputBuffer[])

See the FIFO buffer's contents without removing them.

### **Status Checks**

```
    bool FIFO_isFull (volatile FIFO_t *fifo)
```

Check if the FIFO buffer is full.

bool FIFO\_isEmpty (volatile FIFO\_t \*fifo)

Check if the FIFO buffer is empty.

uint32\_t FIFO\_getCurrSize (volatile FIFO\_t \*fifo)

Get the current size of the FIFO buffer.

## 7.9.1 Detailed Description

FIFO buffer data structure.

**Author** 

Bryan McElvy

# 7.10 lookup.c File Reference

Lookup table source code.

```
#include "lookup.h"
#include "arm_math_types.h"
```

### **Functions**

const float32\_t \* Lookup\_GetPtr\_ADC (void)
 Return a pointer to the ADC lookup table.

# 7.10.1 Detailed Description

Lookup table source code.

Author

Bryan McElvy

# 7.11 lookup.h File Reference

```
Lookup table API.
```

```
#include "arm_math_types.h"
```

# Macros

- #define LOOKUP\_ADC\_MAX (float32\_t) 5.5
- #define LOOKUP\_ADC\_MIN (float32\_t)(-5.5)

## **Functions**

const float32\_t \* Lookup\_GetPtr\_ADC (void)
 Return a pointer to the ADC lookup table.

### 7.11.1 Detailed Description

Lookup table API.

Author

Bryan McElvy

## 7.12 NewAssert.c File Reference

Source code for custom assert implementation.

```
#include "NewAssert.h"
#include <stdbool.h>
```

### **Functions**

· void Assert (bool condition)

Custom assert implementation that is more lightweight than the one from newlib.

## 7.12.1 Detailed Description

Source code for custom assert implementation.

**Author** 

Bryan McElvy

## 7.13 NewAssert.h File Reference

Header file for custom assert implementation.

```
#include <stdbool.h>
```

### **Functions**

void Assert (bool condition)

Custom assert implementation that is more lightweight than the one from newlib.

## 7.13.1 Detailed Description

Header file for custom assert implementation.

Author

Bryan McElvy

## 7.14 ADC.c File Reference

Source code for ADC module.

```
#include "ADC.h"
#include "GPIO.h"
#include "Timer.h"
#include "lookup.h"
#include "arm_math_types.h"
#include "tm4c123gh6pm.h"
#include <stdint.h>
```

#### **Functions**

· void ADC\_Init (void)

Initialize ADC0 as a single-input analog-to-digital converter.

void ADC\_InterruptEnable (void)

Enable the ADC interrupt.

• void ADC\_InterruptDisable (void)

Disable the ADC interrupt.

float32\_t ADC\_ConvertToVolts (uint16\_t raw\_sample)

Convert a raw ADC sample to voltage in [mV].

## 7.14.1 Detailed Description

Source code for ADC module.

Author

Bryan McElvy

## 7.15 ADC.h File Reference

Driver module for analog-to-digital conversion (ADC).

```
#include "GPIO.h"
#include "Timer.h"
#include "lookup.h"
#include "arm_math_types.h"
#include "tm4c123gh6pm.h"
#include <stdint.h>
```

#### **Functions**

• void ADC\_Init (void)

Initialize ADC0 as a single-input analog-to-digital converter.

void ADC\_InterruptEnable (void)

Enable the ADC interrupt.

void ADC\_InterruptDisable (void)

Disable the ADC interrupt.

float32\_t ADC\_ConvertToVolts (uint16\_t raw\_sample)

Convert a raw ADC sample to voltage in [mV].

## 7.15.1 Detailed Description

Driver module for analog-to-digital conversion (ADC).

**Author** 

Bryan McElvy

### 7.16 GPIO.c File Reference

Source code for GPIO module.

```
#include "GPIO.h"
#include <NewAssert.h>
#include "tm4c123gh6pm.h"
#include <stdbool.h>
#include <stdint.h>
```

#### **Data Structures**

struct GPIO Port t

## Macros

• #define GPIO\_NUM\_PORTS 6

#### **Typedefs**

typedef volatile uint32 t \* register\_t

### **Enumerations**

```
    enum {
        GPIO_PORTA_BASE_ADDRESS = (uint32_t) 0x40004000 , GPIO_PORTB_BASE_ADDRESS = (uint32 ←
        _t) 0x40005000 , GPIO_PORTC_BASE_ADDRESS = (uint32_t) 0x40006000 , GPIO_PORTD_BASE_←
        ADDRESS = (uint32_t) 0x40007000 ,
        GPIO_PORTE_BASE_ADDRESS = (uint32_t) 0x40024000 , GPIO_PORTF_BASE_ADDRESS = (uint32_t) 0x40025000 }
        enum {
             GPIO_DATA_R_OFFSET = (uint32_t) 0x03FC , GPIO_DIR_R_OFFSET = (uint32_t) 0x0400 , GPIO_IS_R←
```

```
GPIO_DATA_R_OFFSET = (uint32_t) 0x03FC , GPIO_DIR_R_OFFSET = (uint32_t) 0x0400 , GPIO_IS_R \leftarrow _OFFSET = (uint32_t) 0x0404 , GPIO_IBE_R_OFFSET = (uint32_t) 0x0408 , GPIO_IEV_R_OFFSET = (uint32_t) 0x040C , GPIO_IM_R_OFFSET = (uint32_t) 0x0410 , GPIO_ICR_R_ \leftarrow OFFSET = (uint32_t) 0x041C , GPIO_AFSEL_R_OFFSET = (uint32_t) 0x0420 , GPIO_DR2R_R_OFFSET = (uint32_t) 0x0500 , GPIO_DR4R_R_OFFSET = (uint32_t) 0x0504 , GPIO_ \leftarrow DR8R_R_OFFSET = (uint32_t) 0x0508 , GPIO_PUR_R_OFFSET = (uint32_t) 0x0510 , GPIO_PDR_R_OFFSET = (uint32_t) 0x051C , GPIO_ \leftarrow LOCK_R_OFFSET = (uint32_t) 0x0520 , GPIO_COMMIT_R_OFFSET = (uint32_t) 0x0524 , GPIO_AMSEL_R_OFFSET = (uint32_t) 0x052C }
```

#### **Functions**

GPIO Port t \* GPIO InitPort (GPIO PortName t portName)

Initialize a GPIO Port and return a pointer to its struct.

• bool GPIO\_isPortInit (GPIO\_Port\_t \*gpioPort)

Check if the GPIO port is initialized.

void GPIO\_ConfigDirOutput (GPIO\_Port\_t \*gpioPort, GPIO\_Pin\_t pinMask)

Configure the direction of the specified GPIO pins. All pins are configured to INPUT by default, so this function should only be called to specify OUTPUT pins.

• void GPIO ConfigDirInput (GPIO\_Port\_t \*gpioPort, GPIO\_Pin\_t pinMask)

Configure the specified GPIO pins as INPUT pins. All pins are configured to INPUT by default, so this function is technically unnecessary, but useful for code readability.

void GPIO\_ConfigPullUp (GPIO\_Port\_t \*gpioPort, GPIO\_Pin\_t pinMask)

Activate the specified pins' internal pull-up resistors.

• void GPIO\_ConfigPullDown (GPIO\_Port\_t \*gpioPort, GPIO\_Pin\_t pinMask)

Activate the specified pins' internal pull-down resistors.

void GPIO\_ConfigDriveStrength (GPIO\_Port\_t \*gpioPort, GPIO\_Pin\_t pinMask, uint8\_t drive\_mA)

Configure the specified pins' drive strength. Pins are initialized with 2[mA] drive strength, so this is only needed for a drive strength of 4[mA] or 8[mA].

void GPIO\_EnableDigital (GPIO\_Port\_t \*gpioPort, GPIO\_Pin\_t pinMask)

Enable digital I/O for the specified pins.

void GPIO\_DisableDigital (GPIO\_Port\_t \*gpioPort, GPIO\_Pin\_t pinMask)

Disable digital I/O for the specified pins.

void GPIO\_ConfigInterrupts\_Edge (GPIO\_Port\_t \*gpioPort, GPIO\_Pin\_t pinMask, bool risingEdge)

Configure the specified GPIO pins to trigger an interrupt on the rising or falling edge of an input.

void GPIO\_ConfigInterrupts\_BothEdges (GPIO\_Port\_t \*gpioPort, GPIO\_Pin\_t pinMask)

Configure the specified GPIO pins to trigger an interrupt on both edges of an input.

void GPIO\_ConfigInterrupts\_LevelTrig (GPIO\_Port\_t \*gpioPort, GPIO\_Pin\_t pinMask, bool highLevel)

Configure the specified GPIO pins to trigger an interrupt on a high level or low level pulse.

void GPIO\_ConfigNVIC (GPIO\_Port\_t \*gpioPort, uint8\_t priority)

Configure interrupts for the selected port in the NVIC.

uint8\_t GPIO\_ReadPins (GPIO\_Port\_t \*gpioPort, GPIO\_Pin\_t pinMask)

Read from the specified GPIO pin.

void GPIO WriteHigh (GPIO Port t \*gpioPort, GPIO Pin t pinMask)

Write a 1 to the specified GPIO pins.

void GPIO\_WriteLow (GPIO\_Port\_t \*gpioPort, GPIO\_Pin\_t pinMask)

Write a 0 to the specified GPIO pins.

• void GPIO\_Toggle (GPIO\_Port\_t \*gpioPort, GPIO\_Pin\_t pinMask)

Toggle the specified GPIO pins.

void GPIO ConfigAltMode (GPIO Port t \*gpioPort, GPIO Pin t pinMask)

Activate the alternate mode for the specified pins.

void GPIO ConfigPortCtrl (GPIO Port t \*gpioPort, GPIO Pin t pinMask, uint8 t fieldEncoding)

Specify the alternate mode to use for the specified pins.

void GPIO\_ConfigAnalog (GPIO\_Port\_t \*gpioPort, GPIO\_Pin\_t pinMask)

Activate analog mode for the specified GPIO pins.

## 7.16.1 Detailed Description

Source code for GPIO module.

**Author** 

Bryan McElvy

### 7.16.2 Function Documentation

## **GPIO\_ConfigAltMode()**

Activate the alternate mode for the specified pins.

### **Parameters**

in	gpioPort	Pointer to the specified GPIO port.
in	pinMask	Bit mask corresponding to the intended pin(s).

# GPIO\_ConfigAnalog()

Activate analog mode for the specified GPIO pins.

#### **Parameters**

in	gpioPort	Pointer to the specified GPIO port.
in	pinMask	Bit mask corresponding to the intended pin(s).

# GPIO\_ConfigDirInput()

Configure the specified GPIO pins as INPUT pins. All pins are configured to INPUT by default, so this function is technically unnecessary, but useful for code readability.

## Parameters

in	gpioPort	Pointer to the specified GPIO port.
in	bitMask	Bit mask corresponding to the intended INPUT pin(s).

# **GPIO\_ConfigDirOutput()**

Configure the direction of the specified GPIO pins. All pins are configured to INPUT by default, so this function should only be called to specify OUTPUT pins.

#### **Parameters**

in	gpioPort	Pointer to the specified GPIO port.
in	bitMask	Bit mask corresponding to the intended OUTPUT pin(s).

# **GPIO\_ConfigDriveStrength()**

Configure the specified pins' drive strength. Pins are initialized with 2[mA] drive strength, so this is only needed for a drive strength of 4[mA] or 8[mA].

#### **Parameters**

	in	gpioPort	Pointer to the specified GPIO port.
Ī	in	pinMask	Bit mask corresponding to the intended pin(s).
Ī	in	drive_mA	Drive strength in [mA]. Should be 2, 4, or 8 [mA].

# **GPIO\_ConfigInterrupts\_BothEdges()**

Configure the specified GPIO pins to trigger an interrupt on both edges of an input.

#### **Parameters**

in	gpioPort	Pointer to the specified GPIO port.
in	pinMask	Bit mask corresponding to the intended pin(s).

# **GPIO\_ConfigInterrupts\_Edge()**

Configure the specified GPIO pins to trigger an interrupt on the rising or falling edge of an input.

### **Parameters**

	in	gpioPort	Pointer to the specified GPIO port.	
	in	pinMask	Bit mask corresponding to the intended pin(s).	
ľ	in	in risingEdge true for rising edge, false for fallin		

## **GPIO\_ConfigInterrupts\_LevelTrig()**

Configure the specified GPIO pins to trigger an interrupt on a high level or low level pulse.

#### **Parameters**

in	gpioPort	Pointer to the specified GPIO port.
in	pinMask	Bit mask corresponding to the intended pin(s).
in	highLevel	true for high level, false for low level

# GPIO\_ConfigNVIC()

Configure interrupts for the selected port in the NVIC.

### **Parameters**

in	gpioPort	Pointer to the specified GPIO port.	
in	priority	Priority number between 0 (highest) and 7 (lowest).	

# GPIO\_ConfigPortCtrl()

Specify the alternate mode to use for the specified pins.

# **Parameters**

in	gpioPort	Pointer to the specified GPIO port.
in	pinMask	Bit mask corresponding to the intended pin(s).
in	fieldEncoding	Number corresponding to intended alternate mode.

# GPIO\_ConfigPullDown()

Activate the specified pins' internal pull-down resistors.

## **Parameters**

in	gpioPort	Pointer to the specified GPIO port.
in	pinMask	Bit mask corresponding to the intended pin(s).

# GPIO\_ConfigPullUp()

Activate the specified pins' internal pull-up resistors.

### **Parameters**

in	gpioPort	Pointer to the specified GPIO port.	
in	pinMask	Bit mask corresponding to the intended pin(s).	

# GPIO\_DisableDigital()

Disable digital I/O for the specified pins.

## **Parameters**

ſ	in	gpioPort Pointer to the specified GPIO port.	
ſ	in	pinMask	Bit mask corresponding to the intended pin(s).

# GPIO\_EnableDigital()

Enable digital I/O for the specified pins.

## **Parameters**

in	gpioPort	Pointer to the specified GPIO port.
in	pinMask	Bit mask corresponding to the intended pin(s).

# GPIO\_InitPort()

Initialize a GPIO Port and return a pointer to its  ${\tt struct}.$ 

### **Parameters**

in	portName	Name of the chosen port.

# Returns

GPIO\_Port\_t\* Pointer to the GPIO port's struct.

# GPIO\_isPortInit()

Check if the GPIO port is initialized.

# **Parameters**

in	gpioPort	Pointer to the specified GPIO port.
out	true	The GPIO port is initialized.
out	false The GPIO port has not been initialize	

## GPIO\_ReadPins()

Read from the specified GPIO pin.

## **Parameters**

in	gpioPort	Pointer to the specified GPIO port.	
in	pinMask	Bit mask corresponding to the intended pin(s).	

## GPIO\_Toggle()

Toggle the specified GPIO pins.

### **Parameters**

i	n	gpioPort	Pointer to the specified GPIO port.
i	n	pinMask	Bit mask corresponding to the intended pin(s).

## GPIO\_WriteHigh()

Write a  $\ensuremath{\mathbb{1}}$  to the specified GPIO pins.

#### **Parameters**

in	gpioPort	Pointer to the specified GPIO port.
in	pinMask	Bit mask corresponding to the intended pin(s).

## GPIO\_WriteLow()

Write a 0 to the specified GPIO pins.

### **Parameters**

in	gpioPort	Pointer to the specified GPIO port.
in	pinMask	Bit mask corresponding to the intended pin(s).

### 7.17 GPIO.h File Reference

Header file for general-purpose input/output (GPIO) device driver.

```
#include <NewAssert.h>
#include "tm4c123gh6pm.h"
#include <stdbool.h>
#include <stdint.h>
```

#### **Enumerations**

```
    enum GPIO_Pin_t {
        GPIO_PIN0 = ((uint8_t) 1) , GPIO_PIN1 = ((uint8_t) (1 << 1)) , GPIO_PIN2 = ((uint8_t) (1 << 2)) , GPIO ←
        _PIN3 = ((uint8_t) (1 << 3)) ,
        GPIO_PIN4 = ((uint8_t) (1 << 4)) , GPIO_PIN5 = ((uint8_t) (1 << 5)) , GPIO_PIN6 = ((uint8_t) (1 << 6)) ,
        GPIO_PIN7 = ((uint8_t) (1 << 7)) ,
        GPIO_ALL_PINS = ((uint8_t) (0xFF)) }
        enum {
        LED_RED = GPIO_PIN1 , LED_GREEN = GPIO_PIN3 , LED_BLUE = GPIO_PIN2 , LED_YELLOW =
        (LED_RED + LED_GREEN) ,
        LED_CYAN = (LED_BLUE + LED_GREEN) , LED_PURPLE = (LED_RED + LED_BLUE) , LED_WHITE =
        (LED_RED + LED_BLUE + LED_GREEN) }
        enum GPIO_PortName_t {
            A , B , C , D ,
            E , F }
        </li>
```

#### **Functions**

GPIO\_Port\_t \* GPIO\_InitPort (GPIO\_PortName\_t portName)

Initialize a GPIO Port and return a pointer to its struct.

bool GPIO\_isPortInit (GPIO\_Port\_t \*gpioPort)

Check if the GPIO port is initialized.

void GPIO ConfigDirOutput (GPIO Port t \*gpioPort, GPIO Pin t pinMask)

Configure the direction of the specified GPIO pins. All pins are configured to INPUT by default, so this function should only be called to specify OUTPUT pins.

• void GPIO\_ConfigDirInput (GPIO\_Port\_t \*gpioPort, GPIO\_Pin\_t pinMask)

Configure the specified GPIO pins as INPUT pins. All pins are configured to INPUT by default, so this function is technically unnecessary, but useful for code readability.

void GPIO\_ConfigPullUp (GPIO\_Port\_t \*gpioPort, GPIO\_Pin\_t pinMask)

Activate the specified pins' internal pull-up resistors.

void GPIO ConfigPullDown (GPIO Port t \*gpioPort, GPIO Pin t pinMask)

Activate the specified pins' internal pull-down resistors.

• void GPIO\_ConfigDriveStrength (GPIO\_Port\_t \*gpioPort, GPIO\_Pin\_t pinMask, uint8\_t drive\_mA)

Configure the specified pins' drive strength. Pins are initialized with 2[mA] drive strength, so this is only needed for a drive strength of 4[mA] or 8[mA].

void GPIO EnableDigital (GPIO Port t \*gpioPort, GPIO Pin t pinMask)

Enable digital I/O for the specified pins.

void GPIO\_DisableDigital (GPIO\_Port\_t \*gpioPort, GPIO\_Pin\_t pinMask)

Disable digital I/O for the specified pins.

void GPIO ConfigInterrupts Edge (GPIO Port t \*gpioPort, GPIO Pin t pinMask, bool risingEdge)

Configure the specified GPIO pins to trigger an interrupt on the rising or falling edge of an input.

• void GPIO ConfigInterrupts BothEdges (GPIO Port t \*gpioPort, GPIO Pin t pinMask)

Configure the specified GPIO pins to trigger an interrupt on both edges of an input.

void GPIO\_ConfigInterrupts\_LevelTrig (GPIO\_Port\_t \*gpioPort, GPIO\_Pin\_t pinMask, bool highLevel)

Configure the specified GPIO pins to trigger an interrupt on a high level or low level pulse.

void GPIO ConfigNVIC (GPIO Port t \*gpioPort, uint8 t priority)

Configure interrupts for the selected port in the NVIC.

uint8\_t GPIO\_ReadPins (GPIO\_Port\_t \*gpioPort, GPIO\_Pin\_t pinMask)

Read from the specified GPIO pin.

void GPIO WriteHigh (GPIO Port t \*gpioPort, GPIO Pin t pinMask)

Write a 1 to the specified GPIO pins.

void GPIO\_WriteLow (GPIO\_Port\_t \*gpioPort, GPIO\_Pin\_t pinMask)

Write a 0 to the specified GPIO pins.

• void GPIO\_Toggle (GPIO\_Port\_t \*gpioPort, GPIO\_Pin\_t pinMask)

Toggle the specified GPIO pins.

void GPIO\_ConfigAltMode (GPIO\_Port\_t \*gpioPort, GPIO\_Pin\_t pinMask)

Activate the alternate mode for the specified pins.

• void GPIO\_ConfigPortCtrl (GPIO\_Port\_t \*gpioPort, GPIO\_Pin\_t pinMask, uint8\_t fieldEncoding)

Specify the alternate mode to use for the specified pins.

void GPIO\_ConfigAnalog (GPIO\_Port\_t \*gpioPort, GPIO\_Pin\_t pinMask)

Activate analog mode for the specified GPIO pins.

### 7.17.1 Detailed Description

Header file for general-purpose input/output (GPIO) device driver.

**Author** 

Bryan McElvy

#### 7.17.2 Function Documentation

## GPIO\_ConfigAltMode()

Activate the alternate mode for the specified pins.

### **Parameters**

in	gpioPort	Pointer to the specified GPIO port.
in	pinMask	Bit mask corresponding to the intended pin(s).

### **GPIO\_ConfigAnalog()**

Activate analog mode for the specified GPIO pins.

### **Parameters**

in	gpioPort	Pointer to the specified GPIO port.
in	pinMask	Bit mask corresponding to the intended pin(s).

### GPIO\_ConfigDirInput()

Configure the specified GPIO pins as INPUT pins. All pins are configured to INPUT by default, so this function is technically unnecessary, but useful for code readability.

#### **Parameters**

in	gpioPort	Pointer to the specified GPIO port.
in	bitMask	Bit mask corresponding to the intended INPUT pin(s).

## **GPIO\_ConfigDirOutput()**

Configure the direction of the specified GPIO pins. All pins are configured to INPUT by default, so this function should only be called to specify OUTPUT pins.

#### **Parameters**

in	gpioPort	Pointer to the specified GPIO port.
in	bitMask	Bit mask corresponding to the intended OUTPUT pin(s).

## **GPIO\_ConfigDriveStrength()**

Configure the specified pins' drive strength. Pins are initialized with 2[mA] drive strength, so this is only needed for a drive strength of 4[mA] or 8[mA].

#### **Parameters**

	in	gpioPort	Pointer to the specified GPIO port.
	in	pinMask	Bit mask corresponding to the intended pin(s).
Ī	in	drive_mA	Drive strength in [mA]. Should be 2, 4, or 8 [mA].

### GPIO\_ConfigInterrupts\_BothEdges()

```
{\tt void \ GPIO\_ConfigInterrupts\_BothEdges \ (}
```

```
GPIO_Port_t * gpioPort,
GPIO_Pin_t pinMask )
```

Configure the specified GPIO pins to trigger an interrupt on both edges of an input.

### **Parameters**

	in	gpioPort	Pointer to the specified GPIO port.
ſ	in	pinMask	Bit mask corresponding to the intended pin(s).

## **GPIO\_ConfigInterrupts\_Edge()**

Configure the specified GPIO pins to trigger an interrupt on the rising or falling edge of an input.

#### **Parameters**

in	gpioPort	Pointer to the specified GPIO port.
in	pinMask	Bit mask corresponding to the intended pin(s).
in	risingEdge	true for rising edge, false for falling edge

### **GPIO\_ConfigInterrupts\_LevelTrig()**

Configure the specified GPIO pins to trigger an interrupt on a high level or low level pulse.

### **Parameters**

in	gpioPort	Pointer to the specified GPIO port.
in	pinMask	Bit mask corresponding to the intended pin(s).
in	highLevel	true for high level, false for low level

### GPIO\_ConfigNVIC()

Configure interrupts for the selected port in the NVIC.

#### **Parameters**

in	gpioPort	Pointer to the specified GPIO port.
in	priority	Priority number between 0 (highest) and 7 (lowest).

## GPIO\_ConfigPortCtrl()

Specify the alternate mode to use for the specified pins.

#### **Parameters**

in	gpioPort	Pointer to the specified GPIO port.
in	pinMask	Bit mask corresponding to the intended pin(s).
in	fieldEncoding	Number corresponding to intended alternate mode.

## GPIO\_ConfigPullDown()

Activate the specified pins' internal pull-down resistors.

### Parameters

in	gpioPort	Pointer to the specified GPIO port.
in	pinMask	Bit mask corresponding to the intended pin(s).

## GPIO\_ConfigPullUp()

Activate the specified pins' internal pull-up resistors.

### **Parameters**

in	gpioPort	Pointer to the specified GPIO port.
in	pinMask	Bit mask corresponding to the intended pin(s).

## GPIO\_DisableDigital()

Disable digital I/O for the specified pins.

### **Parameters**

in	gpioPort	Pointer to the specified GPIO port.
in	pinMask	Bit mask corresponding to the intended pin(s).

## GPIO\_EnableDigital()

Enable digital I/O for the specified pins.

#### **Parameters**

	in	gpioPort	Pointer to the specified GPIO port.
ĺ	in	pinMask	Bit mask corresponding to the intended pin(s).

### GPIO InitPort()

Initialize a GPIO Port and return a pointer to its struct.

#### **Parameters**

in portName Name of the chosen port.
--------------------------------------

#### Returns

GPIO\_Port\_t\* Pointer to the GPIO port's struct.

## GPIO\_isPortInit()

Check if the GPIO port is initialized.

### **Parameters**

in	gpioPort	Pointer to the specified GPIO port.
out	true	The GPIO port is initialized.
out	false	The GPIO port has not been initialized.

## GPIO\_ReadPins()

Read from the specified GPIO pin.

### **Parameters**

in	gpioPort	Pointer to the specified GPIO port.
in	pinMask	Bit mask corresponding to the intended pin(s).

## GPIO\_Toggle()

Toggle the specified GPIO pins.

## **Parameters**

in	gpioPort	Pointer to the specified GPIO port.
in	pinMask	Bit mask corresponding to the intended pin(s).

## GPIO\_WriteHigh()

Write a  $\ensuremath{\mathbb{1}}$  to the specified GPIO pins.

#### **Parameters**

in	gpioPort	Pointer to the specified GPIO port.
in	pinMask	Bit mask corresponding to the intended pin(s).

## GPIO\_WriteLow()

Write a 0 to the specified GPIO pins.

### **Parameters**

in	gpioPort	Pointer to the specified GPIO port.
in	pinMask	Bit mask corresponding to the intended pin(s).

### 7.18 PLL.c File Reference

Implementation details for phase-lock-loop (PLL) functions.

```
#include "PLL.h"
#include "tm4c123gh6pm.h"
#include <stdint.h>
```

#### **Functions**

void PLL\_Init (void)

Initializes the phase-locked-loop (PLL), allowing a bus frequency of 80[MHz].

### 7.18.1 Detailed Description

Implementation details for phase-lock-loop (PLL) functions.

**Author** 

Bryan McElvy

## 7.19 PLL.h File Reference

Driver module for activating the phase-locked-loop (PLL).

```
#include "tm4c123gh6pm.h"
#include <stdint.h>
```

## **Functions**

• void PLL\_Init (void)

Initializes the phase-locked-loop (PLL), allowing a bus frequency of 80[MHz].

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### 7.19.1 Detailed Description

Driver module for activating the phase-locked-loop (PLL).

**Author** 

Bryan McElvy

#### 7.20 SPI.c File Reference

Source code for SPI module.

```
#include "SPI.h"
#include "GPIO.h"
#include "FIFO.h"
#include "tm4c123gh6pm.h"
#include <stdbool.h>
#include <stdint.h>
```

#### **Macros**

- #define NVIC SSI0 NUM 7
- #define  $SPI\_INT\_START()$  (NVIC\_SW\_TRIG\_R = (NVIC\_SW\_TRIG\_R &  $\sim$ (0xFF)) |  $NVIC\_SSI0\_NUM$ )
- #define **SPI\_SET\_DC**() (GPIO\_PORTA\_DATA\_R |= 0x40)
- #define **SPI\_CLEAR\_DC**() (GPIO\_PORTA\_DATA\_R &=  $\sim$ (0x40))
- #define SPI\_IS\_BUSY (SSI0\_SR\_R & 0x10)
- #define SPI\_TX\_ISNOTFULL ((bool) (SSI0\_SR\_R & 0x02))
- #define SPI\_BUFFER\_SIZE 9

### **Enumerations**

```
    enum {
    SPI_CLK_PIN = GPIO_PIN2 , SPI_CS_PIN = GPIO_PIN3 , SPI_RX_PIN = GPIO_PIN4 , SPI_TX_PIN = GPIO_PIN5 ,
    SPI_DC_PIN = GPIO_PIN6 , SPI_RESET_PIN = GPIO_PIN7 , SPI_SSIO_PINS = (SPI_CLK_PIN | SPI_CS_PIN | SPI_RX_PIN | SPI_TX_PIN) , SPI_GPIO_PINS = (SPI_DC_PIN | SPI_RESET_PIN) ,
    SPI_ALL_PINS = (SPI_SSIO_PINS | SPI_GPIO_PINS) }
```

#### **Functions**

void SPI\_Init (void)

Initialize SSI0 to act as an SPI Controller (AKA Master) in mode 0.

• uint8 t SPI Read (void)

Read data from the peripheral.

void SPI\_WriteCmd (uint8\_t cmd)

Write an 8-bit command to the peripheral.

void SPI WriteData (uint8 t data)

Write 8-bit data to the peripheral.

void SPI\_IRQ\_WriteCmd (uint8\_t cmd)

Add an 8-bit command to the SPI queue. If no data or other command is written, should directly precede a call to SPI\_IRQ\_StartWriting().

void SPI\_IRQ\_WriteData (uint8\_t data)

Add 8-bit data to the SPI queue. Should directly precede either another call to the same function or a call to SPI\_IRQ\_StartWriting().

· void SPI IRQ StartWriting (void)

Start writing data to the Tx FIFO. Should be used after 1+ calls to SPI\_IRQ\_WriteCmd() and/or SPI\_IRQ\_WriteData(). If unused, writing will start when the SPI queue is full.

· void SSI0 Handler (void)

Sends parameters (data or commands) over SPI via SSI0.

### 7.20.1 Detailed Description

Source code for SPI module.

**Author** 

Bryan McElvy

### 7.21 SPI.h File Reference

Driver module for using the serial peripheral interface (SPI) protocol.

```
#include "GPIO.h"
#include "FIFO.h"
#include "tm4c123gh6pm.h"
#include <stdbool.h>
#include <stdint.h>
```

## **Functions**

void SPI\_Init (void)

Initialize SSI0 to act as an SPI Controller (AKA Master) in mode 0.

• uint8 t SPI Read (void)

Read data from the peripheral.

• void SPI\_WriteCmd (uint8\_t cmd)

Write an 8-bit command to the peripheral.

void SPI\_WriteData (uint8\_t data)

Write 8-bit data to the peripheral.

void SPI\_IRQ\_WriteCmd (uint8\_t cmd)

Add an 8-bit command to the SPI queue. If no data or other command is written, should directly precede a call to SPI\_IRQ\_StartWriting().

void SPI IRQ WriteData (uint8 t data)

Add 8-bit data to the SPI queue. Should directly precede either another call to the same function or a call to  $SPI\_IRQ\_StartWriting()$ .

void SPI\_IRQ\_StartWriting (void)

Start writing data to the Tx FIFO. Should be used after 1+ calls to  $SPI\_IRQ\_WriteCmd()$  and/or  $SPI\_IRQ\_WriteData()$ . If unused, writing will start when the SPI queue is full.

### 7.21.1 Detailed Description

Driver module for using the serial peripheral interface (SPI) protocol.

**Author** 

Bryan McElvy

## 7.22 SysTick.c File Reference

Implementation details for SysTick functions.

```
#include "SysTick.h"
#include "tm4c123gh6pm.h"
#include <stdint.h>
```

#### **Functions**

• void SysTick\_Timer\_Init (void)

Initialize SysTick for timing purposes.

void SysTick\_Wait1ms (uint32\_t delay\_ms)

Delay for specified amount of time in [ms]. Assumes f\_bus = 80[MHz].

void SysTick\_Interrupt\_Init (uint32\_t time\_ms)
 Initialize SysTick for interrupts.

### 7.22.1 Detailed Description

Implementation details for SysTick functions.

**Author** 

Bryan McElvy

## 7.23 SysTick.h File Reference

Driver module for using SysTick-based timing and/or interrupts.

```
#include "tm4c123gh6pm.h"
#include <stdint.h>
```

### **Functions**

void SysTick\_Timer\_Init (void)

Initialize SysTick for timing purposes.

void SysTick\_Wait1ms (uint32\_t delay\_ms)

Delay for specified amount of time in [ms]. Assumes f\_bus = 80[MHz].

void SysTick\_Interrupt\_Init (uint32\_t time\_ms)

Initialize SysTick for interrupts.

### 7.23.1 Detailed Description

Driver module for using SysTick-based timing and/or interrupts.

**Author** 

Bryan McElvy

#### 7.24 Timer.c File Reference

Implementation for timer module.

```
#include "Timer.h"
#include "tm4c123gh6pm.h"
#include <stdint.h>
```

#### **Functions**

#### Timer0A

void Timer0A\_Init (void)

Initialize timer 0 as 32-bit, one-shot, countdown timer.

• void Timer0A\_Start (uint32\_t time\_ms)

Count down starting from the inputted value.

uint8\_t Timer0A\_isCounting (void)

Returns 1 if Timer0 is still counting and 0 if not.

void Timer0A\_Wait1ms (uint32\_t time\_ms)

Wait for the specified amount of time in [ms].

#### Timer1A

void Timer1A\_Init (uint32\_t time\_ms)
 Initialize timer 1 as a 32-bit, periodic, countdown timer with interrupts.

#### Timer2A

• void Timer2A\_Init (void)

Initialize timer 2 as 32-bit, one-shot, countdown timer.

void Timer2A\_Start (uint32\_t time\_ms)

Count down starting from the inputted value.

• uint8\_t Timer2A\_isCounting (void)

Returns 1 if Timer2 is still counting and 0 if not.

void Timer2A\_Wait1ms (uint32\_t time\_ms)

Wait for the specified amount of time in [ms].

void Timer3A\_Init (uint32\_t time\_ms)

Initialize Timer3A as a 32-bit, periodic, countdown timer that triggers ADC sample capture.

## 7.24.1 Detailed Description

Implementation for timer module.

**Author** 

Bryan McElvy

#### 7.25 Timer.h File Reference

Driver module for general-purpose timer modules.

```
#include "tm4c123gh6pm.h"
#include <stdint.h>
```

#### **Functions**

#### Timer0A

void Timer0A\_Init (void)

Initialize timer 0 as 32-bit, one-shot, countdown timer.

• void Timer0A Start (uint32 t time ms)

Count down starting from the inputted value.

uint8\_t Timer0A\_isCounting (void)

Returns 1 if Timer0 is still counting and 0 if not.

void Timer0A\_Wait1ms (uint32\_t time\_ms)

Wait for the specified amount of time in [ms].

#### Timer1A

void Timer1A\_Init (uint32\_t time\_ms)

Initialize timer 1 as a 32-bit, periodic, countdown timer with interrupts.

### Timer2A

• void Timer2A\_Init (void)

Initialize timer 2 as 32-bit, one-shot, countdown timer.

• void Timer2A Start (uint32 t time ms)

Count down starting from the inputted value.

• uint8\_t Timer2A\_isCounting (void)

Returns 1 if Timer2 is still counting and 0 if not.

void Timer2A\_Wait1ms (uint32\_t time\_ms)

Wait for the specified amount of time in [ms].

void Timer3A\_Init (uint32\_t time\_ms)

Initialize Timer3A as a 32-bit, periodic, countdown timer that triggers ADC sample capture.

### 7.25.1 Detailed Description

Driver module for general-purpose timer modules.

#### Author

### Bryan McElvy

Timer	Function
0A	Debouncing
1A	LCD Interrupts
2A	ILI9341 Resets
3A	ADC Interrupts

#### 7.26 UART.c File Reference

Source code for UART module.

```
#include "UART.h"
#include "GPIO.h"
#include "FIFO.h"
#include "tm4c123gh6pm.h"
#include <stdbool.h>
#include <stdint.h>
```

#### **Macros**

- #define ASCII CONVERSION 0x30
- #define UART0\_TX\_FULL (UART0\_FR\_R & 0x20)
- #define UART0\_BUFFER\_SIZE 16
- #define UART0\_INTERRUPT\_NUM 5

#### **Functions**

void UART0\_Init (void)

Initialize UART0 to a baud rate of 115200, 8-bit data length, 1 start bit, and 1 stop bit.

• unsigned char UART0\_ReadChar (void)

Read a single character from UARTO.

· void UART0\_WriteChar (unsigned char input\_char)

Write a single character to UARTO.

• void UART0\_WriteStr (void \*input\_str)

Write a C string to UARTO.

• void UART0\_WriteInt (uint32\_t n)

Write a 32-bit unsigned integer to UARTO.

• void UART0\_WriteFloat (double n, uint8\_t num\_decimals)

Write a floating-point number to UART0.

void UART0\_IRQ\_AddChar (unsigned char input\_char)

Add a single character to UARTO's FIFO.

• void UART0\_IRQ\_AddStr (void \*input\_str)

Add a string to UARTO's FIFO.

• void UART0\_IRQ\_AddInt (uint32\_t n)

Add an integer to UARTO's FIFO.

void UART0\_IRQ\_Start (void)

Transmit the UART0's FIFO's contents via interrupt.

- void UART0\_Handler (void)
- void UART1\_Init (void)

Initialize UART1 to a baud rate of 115200, 8-bit data length, 1 start bit, and 1 stop bit.

• unsigned char UART1\_ReadChar (void)

Read a single character from UART1.

void UART1\_WriteChar (unsigned char input\_char)

Write a single character to UART1.

void UART1\_WriteStr (void \*input\_str)

Write a C string to UART1.

### 7.26.1 Detailed Description

Source code for UART module.

**Author** 

Bryan McElvy

#### 7.27 UART.h File Reference

Driver module for serial communication via UART0 and UART 1.

```
#include "GPIO.h"
#include "FIFO.h"
#include "tm4c123gh6pm.h"
#include <stdbool.h>
#include <stdint.h>
```

#### **Functions**

void UARTO Init (void)

Initialize UART0 to a baud rate of 115200, 8-bit data length, 1 start bit, and 1 stop bit.

unsigned char UART0\_ReadChar (void)

Read a single character from UARTO.

void UARTO WriteChar (unsigned char input char)

Write a single character to UARTO.

• void UART0\_WriteStr (void \*input\_str)

Write a C string to UARTO.

void UART0\_WriteInt (uint32\_t n)

Write a 32-bit unsigned integer to UARTO.

void UART0\_WriteFloat (double n, uint8\_t num\_decimals)

Write a floating-point number to UARTO.

void UART0\_IRQ\_AddChar (unsigned char input\_char)

Add a single character to UARTO's FIFO.

• void UART0\_IRQ\_AddStr (void \*input\_str)

Add a string to UARTO's FIFO.

void UART0\_IRQ\_AddInt (uint32\_t n)

Add an integer to UART0's FIFO.

• void UART0\_IRQ\_Start (void)

Transmit the UART0's FIFO's contents via interrupt.

void UART1\_Init (void)

Initialize UART1 to a baud rate of 115200, 8-bit data length, 1 start bit, and 1 stop bit.

• unsigned char UART1\_ReadChar (void)

Read a single character from UART1.

· void UART1\_WriteChar (unsigned char input\_char)

Write a single character to UART1.

void UART1\_WriteStr (void \*input\_str)

Write a C string to UART1.

### 7.27.1 Detailed Description

Driver module for serial communication via UART0 and UART 1.

**Author** 

### Bryan McElvy

```
UARTO uses PAO and PA1, which are not broken out but can connect to a PC's serial port via USB.  
UART1 uses PBO (Rx) and PB1 (Tx), which are broken out but do not connect to a serial port.
```

#### 7.28 main.c File Reference

Main program file for ECG-HRM.

```
#include "DAQ.h"
#include "Debug.h"
#include "LCD.h"
#include "QRS.h"
#include "PLL.h"
```

#### **Functions**

- int main (void)
- void ADC0 SS3 Handler (void)

Interrupt service routine (ISR) for collecting ADC samples.

void Timer1A\_Handler (void)

Interrupt service routine (ISR) for outputting data to the LCD.

## 7.28.1 Detailed Description

Main program file for ECG-HRM.

**Author** 

Bryan McElvy

## 7.29 ILI9341.c File Reference

Source code for ILI9341 module.

```
#include "ILI9341.h"
#include "SPI.h"
#include "Timer.h"
#include "FIFO.h"
#include "tm4c123gh6pm.h"
#include <stdbool.h>
#include <stdint.h>
```

#### **Enumerations**

```
    enum Cmd_t {
    NOP = 0x00 , SWRESET = 0x01 , SPLIN = 0x10 , SPLOUT = 0x11 ,
    PTLON = 0x12 , NORON = 0x13 , DINVOFF = 0x20 , DINVON = 0x21 ,
    CASET = 0x2A , PASET = 0x2B , RAMWR = 0x2C , DISPOFF = 0x28 ,
    DISPON = 0x29 , PLTAR = 0x30 , VSCRDEF = 0x33 , MADCTL = 0x36 ,
    VSCRSADD = 0x37 , IDMOFF = 0x38 , IDMON = 0x39 , PIXSET = 0x3A ,
    FRMCTR1 = 0xB1 , FRMCTR2 = 0xB2 , FRMCTR3 = 0xB3 , PRCTR = 0xB5 ,
    IFCTL = 0xF6 }
```

#### **Functions**

void ILI9341\_Init (void)

Initialize the LCD driver, the SPI module, and Timer2A.

void ILI9341 resetHard (void)

Perform a hardware reset of the LCD driver.

void ILI9341 resetSoft (void)

Perform a software reset of the LCD driver.

void ILI9341\_setSleepMode (bool isSleeping)

Enter or exit sleep mode. The LCD driver is in sleep mode by default upon powering on or either kind of reset.

void ILI9341 setDispMode (bool isNormal, bool isFullColors)

Set the display area and color expression.

void ILI9341 setPartialArea (uint16 t rowStart, uint16 t rowEnd)

Set the partial display area for partial mode. Call before activating partial mode via ILI9341\_setDisplayMode().

void ILI9341\_setDispInversion (bool is\_ON)

Toggle display inversion. Turning ON causes colors to be inverted on the display.

void ILI9341 setDispOutput (bool is ON)

Turn display output ON or OFF. This function clears the display and stops outputting to the display area, but does not affect frame memory or power.

void ILI9341\_setScrollArea (uint16\_t topFixedArea, uint16\_t vertScrollArea, uint16\_t bottFixedArea)

Set the vertical scrolling area of the display. The sum of the three parameters should be equal to the max number of rows  $NUM_ROWS = 320$ .

void ILI9341\_setScrollStart (uint16\_t startRow)

Set the start row for vertical scrolling.

void ILI9341\_setMemAccessCtrl (bool areRowsFlipped, bool areColsFlipped, bool areRowsAndCols
 — Switched, bool isVertRefreshFlipped, bool isColorOrderFlipped, bool isHorRefreshFlipped)

Set how data is converted from memory to display.

· void ILI9341 setColorDepth (bool is 16bit)

Set the pixel format to be 16-bit (65K colors) or 18-bit (262K colors).

void ILI9341\_NoOpCmd (void)

Send the "No Operation" command ( $NOP = 0 \times 00$ ) to the LCD driver. Can be used to terminate the "Memory Write" (RAMWR) and "Memory Read" (RAMRD) commands, but does nothing otherwise.

void ILI9341\_setFrameRateNorm (uint8\_t divisionRatio, uint8\_t clocksPerLine)

TODO: Write brief.

• void ILI9341\_setFrameRateIdle (uint8\_t divisionRatio, uint8\_t clocksPerLine)

TODO: Write brief.

void ILI9341\_setInterface (void)

Sets the interface for the ILl9341. The parameters for this command are hard-coded, so it only needs to be called once upon initialization.

void ILI9341 setRowAddress (uint16 t startRow, uint16 t endRow)

not using backlight, so these aren't necessary

• void ILI9341\_setColAddress (uint16\_t startCol, uint16\_t endCol)

Sets the start/end rows to be written to.

void ILI9341 writeMemCmd (void)

Sends the "Write Memory" (RAMWR) command to the LCD driver, signalling that incoming data should be written to memory.

• void ILI9341\_writePixel (uint8\_t red, uint8\_t green, uint8\_t blue, bool is\_16bit)

Write a single pixel to frame memory.

#### 7.29.1 Detailed Description

Source code for ILI9341 module.

**Author** 

Bryan McElvy

#### 7.30 ILI9341.h File Reference

Driver module for interfacing with an ILI9341 LCD driver.

```
#include "SPI.h"
#include "Timer.h"
#include "FIFO.h"
#include "tm4c123gh6pm.h"
#include <stdbool.h>
#include <stdint.h>
```

#### **Macros**

- #define NUM\_COLS (uint16\_t) 240
- #define NUM\_ROWS (uint16 t) 320

#### **Functions**

• void ILI9341\_Init (void)

Initialize the LCD driver, the SPI module, and Timer2A.

void ILI9341\_resetHard (void)

Perform a hardware reset of the LCD driver.

void ILI9341\_resetSoft (void)

Perform a software reset of the LCD driver.

void ILI9341 setSleepMode (bool isSleeping)

Enter or exit sleep mode. The LCD driver is in sleep mode by default upon powering on or either kind of reset.

void ILI9341 setDispMode (bool isNormal, bool isFullColors)

Set the display area and color expression.

void ILI9341\_setPartialArea (uint16\_t rowStart, uint16\_t rowEnd)

Set the partial display area for partial mode. Call before activating partial mode via ILI9341\_setDisplayMode().

void ILI9341\_setDispInversion (bool is\_ON)

Toggle display inversion. Turning ON causes colors to be inverted on the display.

void ILI9341\_setDispOutput (bool is\_ON)

Turn display output ON or OFF. This function clears the display and stops outputting to the display area, but does not affect frame memory or power.

void ILI9341 setScrollArea (uint16 t topFixedArea, uint16 t vertScrollArea, uint16 t bottFixedArea)

Set the vertical scrolling area of the display. The sum of the three parameters should be equal to the max number of rows  $NUM_ROWS = 320$ .

void ILI9341\_setScrollStart (uint16\_t startRow)

Set the start row for vertical scrolling.

void ILI9341\_setMemAccessCtrl (bool areRowsFlipped, bool areColsFlipped, bool areRowsAndCols
 — Switched, bool isVertRefreshFlipped, bool isColorOrderFlipped, bool isHorRefreshFlipped)

Set how data is converted from memory to display.

· void ILI9341\_setColorDepth (bool is\_16bit)

Set the pixel format to be 16-bit (65K colors) or 18-bit (262K colors).

void ILI9341\_NoOpCmd (void)

Send the "No Operation" command ( $NOP = 0 \times 00$ ) to the LCD driver. Can be used to terminate the "Memory Write" (RAMWR) and "Memory Read" (RAMRD) commands, but does nothing otherwise.

• void ILI9341\_setFrameRateNorm (uint8\_t divisionRatio, uint8\_t clocksPerLine)

TODO: Write brief.

• void ILI9341\_setFrameRateIdle (uint8\_t divisionRatio, uint8\_t clocksPerLine)

TODO: Write brief.

• void ILI9341\_setBlankingPorch (uint8 t vpf, uint8 t vbp, uint8 t hfp, uint8 t hbp)

TODO: Write.

void ILI9341 setInterface (void)

Sets the interface for the ILl9341. The parameters for this command are hard-coded, so it only needs to be called once upon initialization.

void ILI9341\_setRowAddress (uint16\_t startRow, uint16\_t endRow)

not using backlight, so these aren't necessary

void ILI9341\_setColAddress (uint16\_t startCol, uint16\_t endCol)

Sets the start/end rows to be written to.

void ILI9341 writeMemCmd (void)

Sends the "Write Memory" (RAMWR) command to the LCD driver, signalling that incoming data should be written to memory.

void ILI9341\_writePixel (uint8\_t red, uint8\_t green, uint8\_t blue, bool is\_16bit)

Write a single pixel to frame memory.

### 7.30.1 Detailed Description

Driver module for interfacing with an ILI9341 LCD driver.

Author

Bryan McElvy

This module contains functions for initializing and outputting graphical data to a 240RGBx320 resolution, 262K color-depth liquid crystal display (LCD). The module interfaces the LaunchPad (or any other board featuring the TM4C123GH6PM microcontroller) with an ILI9341 LCD driver chip via the SPI (serial peripheral interface) protocol.

### 7.31 Led.c File Reference

Source code for LED module.

```
#include "Led.h"
#include "GPIO.h"
#include "NewAssert.h"
#include <stdbool.h>
#include <stdint.h>
```

#### **Data Structures**

• struct Led\_t

#### **Functions**

### 7.31.1 Detailed Description

Source code for LED module.

**Author** 

Bryan McElvy

## 7.32 Led.h File Reference

Interface for LED module.

```
#include "GPIO.h"
#include "NewAssert.h"
#include <stdbool.h>
#include <stdint.h>
```

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#### **Macros**

• #define LED\_POOL\_SIZE 3

### **Functions**

### 7.32.1 Detailed Description

Interface for LED module.

**Author** 

Bryan McElvy

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