(Syllabus)

[1] (Basic Information)									
(Course Information)									
/ (Year/Semester)	2020	/ 1	(Cam	pus)	(Seoul Campus)				
(Course No.)	067	57	(Class No.)	02	(Credit)	3			
(Course Title)	(MICROCO SYSTE		(Time/F	/ Room)	207 () 604 < / > 3 / 3,4(College of engineering1 604 < / > TUE3 / THU3,4)				
(Course Classification)	(M	lajor)	(Lecture	Type)	(Lone-te	aching course)			
(Course Type)	(Theoreti	cal course)	(Medit Instruc						
(Accreditation)			(Accreditation of Engineering Education)		(Engineering subject-related course)				
(College)	ICT (College of ICT Engineering)		() (Department)		ICT (School of Electrical and Electronics Engineering)				
e-class (Usage of e-class)	Yes								
(Instructor Infor	mation)								
(Name)	(Kim JunSeong)		(Department)		(School of Electrical and Electronics Engineering)				
(Office Phone No.)	02-820-5294		(Contact No.)		02-820-5294				
E-mail (E-mail)	junkim@cau.ac.kr		(Department Phone No.)		02-820-5333				
가 (Office Hour)	TBD		(Office Location)		#207-730				
(Course Web-site)			-						

	[2] /	(1	Learning Objec	ctives/Outcomes)				
(Course Description)								
To build the foundation of microcomputer a microcomputer system design and interfac		class	provides a compreh	nensive treatment of Assembly language programming,				
(Prerequisites and Co-requisites)								
; ;								
(Learning Objectives)								
To understand the design and interfacing of level language and assembly language pro			•	. Hardware and sofrware organization of a microcomputer; high oheral devices;				
(Learning Outcomes)								
(1) 40% + (2) 20% + 가	(3) 40% 1) 3)	,	,	2)				
	[3]		(Course	Methods)				
(Teaching and Lea	(Teaching and Learning Methods)							
(Teaching and Learning Methods)			가	(Additional Description)				
(Lecture)	x86		•	, ARM				
(Assignments)								

(Textbook	s, Readir	g, and other Mat	erials)						
(Textbook/Reference	(Title)		(Author)	/ (Year of Publication/etc)	/ (Publisher/Name of Journal)	/ (No. of Edition)			
(Main Textbook)	(Main Leythook) Language Design And			M. A. Mazidi, J. G. Pearson lazidi and D. Causey					
[4] 가 (Student Assessment)									
가 (Assessment Item) 기 (%)(Assessment Ratio)			t	가 (Addition	nal Description)				
(Attendance) 5									
/ (Participation/Attitude) 10									
(Mid-term Exam) 35									
(Final Exam) 40									

가	(Assessment Item)	가 (%)(Assessment Ratio)	가 (Additional Description)
	(Assignment)	10	

[5] (Course Schedule)

(We ek)	(Instructor)	(Topic & Content)	(Student Assignment)	가 (Additional Description & Instructor Assignment)
1		introduction to computing; The x86 microprocessor; IC Technology and System Design;		- e
2		Memory and Memory Interfacing; SDK-86 System Design Kit;		- e - QnA
3		Memory and Memory Interfacing; SDK-86 System Design Kit;		- e
4		8088/86 microprocessors and supporting chips;		- e
5		8088/86 microprocessors and supporting chips;		- e
6		8088/86 microprocessors and supporting chips;		- e
7		8088/86 microprocessors and supporting chips;		- e - QnA
8		midterm () / 8088/86 microprocessors and supporting chips;		- e
9		Assembly Language Programming;		- e
10		Assembly Language Programming; 80x86 Instructions and Modular Programming;		- e
11		Assembly Language Programming; 80x86 Instructions and Modular Programming;		- e
12		Assembly Language Programming; 80x86 Instructions and Modular Programming;		- e
13		8253/54 Timer; Interrupts and the 8259 Chip;		- e
14		I/0, 8255 and Device Interfacing; SDK-86 System Design Kit;		- e - QnA
15		final exam.		- e - QnA
16		final exam.		

[6] (Guide to Learning)

You MUST read a keep your attentio	Il the class materials in and interest	S.		100%						
-	가			10076	•					
 2011 28.7%(35 /122	. 2018 1	, 4 ,		. 3			122	(71.3%, 87	가 /122)	
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' ()	, , 가	. 8086	· · · · 가		NASA	x86 가	,		' (') '
			(P	revious	Exam Sam	nples)				
		< 가	>	(<down< b=""> 기</down<>	load Additi	ional Sa	mple>)			
				(1	Engineerin	g Educa	ition)			
				(Le	arning Out	comes)				
	: 40		: 20		: 40					
	(Title)									
n/a										
	(Objective)									
n/a										
	(Restrictions)									
n/a	(A	Madler IV								
<u>가</u> n/a	(Assessment I	vietrioa)								
,										

71 **[]** 6 47 **[**]

(In pursuant to the Article 71 "Discipline" of the Chung-Ang University Regulations, and Article 47 "Punishment for Cheating during Examination" under Chapter 6 of the Academic Affairs Management Rules, any student caught engaging in academic misconduct during an exam will be subject to disciplinary action.)

In this class, students with disabilities are eligible for reasonable accommodations depending on the type and severity of disability. If you wish to receive accommodations listed below, please contact the Support Center for Students with Disabilities.

- 1. Visual Impairment: Braille, large print, electronic class materials, volunteer note-taker, adjustments in assessment practices, etc.
- 2. Hearing Impairment: Volunteer note-taker, stenographer, adjustments in assessment practices, etc.
- 3. Physical Disabilities/Brain Lesions: Classrooms with wheelchair access, volunteer note-taker, adjustments in assessment practices, etc.
- 4. Accommodations for students with other psychiatric disabilities or health impairments can be arranged through the Support Center for Students with Disabilities after consultation

Inquiry: 02-820-6577~9 (Seoul Campus), 031-670-4816 (Anseong Campus)

- KakaoTalk Plus Friend ID: @cauable