Resume

J. Caleb Wherry

Software Architect

CONTACT

(931) 338 - 1071

caleb@calebwherry.com

linkedin.com/in/calebwherry

calebwherry.com

Atlanta, Georgia

SKILLS

- Software Architecture
- Mathematical Analysis
- Scientific Computing
- High-performance Computing
- Machine Learning Infrastructure

EXPERTISE

- Modern C++ (11-20)
- C# (.NET Core)
- Python
- GPGPU

EDUCATION

MASTER OF SCIENCE

Computer Science

Georgia Tech

2014 – TBD (On Hiatus)

BACHELOR OF SCIENCE

Computer Science

Austin Peay State University

2006 – 2011

PROFILE

A versatile Software Architect & Leader with over a decade of experience productizing research-driven technologies. Expert in rapidly prototyping machine learning & mathematical algorithms from first principles into production-hardened systems that scale horizontally and vertically. Technical leader and innovator that pushes the boundaries of technologies and research capabilities for core product development.

NOTABLE EXPERIENCE

Tech Lead / Engineering Manager

Microsoft / Atlanta, GA / Jul 2021 – Current

Manager and Tech Lead for an ML inference engine in E+D serving Bing, Windows, and O365 products. Low latency (sub-millisecond), high throughout (millions of QPS) inference engine running on CPUs, GPUs, & FPGAs using TensorFlow, ONNX, & other engines for ML workloads: Deep Learning (DNN, CNN), Classifiers (SVM, Decision Tree), and more.

Tech Lead / Senior Software Engineer

Microsoft / Atlanta, GA / Jun 2020 – Jun 2021

Technical Lead for vertical team on Azure Compute tackling long term performance enhancements and re-architectures.

Principal Software Engineer

Nexidia, Inc | NICE / Atlanta, GA / Mar 2015 – May 2020

Technical Lead for Research architecture and infrastructure. Rapid prototyping of C++, C# (.NET Core), & Python applications to provide efficient and robust solutions to kick-start productization of core research technologies. Architect for greenfield, cross-platform, containerized, gRPC-based product to modernize core research technologies for the entire tech stack.

Research Scientist

Georgia Tech Research Institute / Atlanta, GA / Jun 2014 – Mar 2015

Software Architect for FPGA analysis tools using large-scale graph analytics.