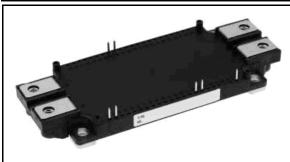


< IGBT MODULES >

CM300DX-24S

HIGH POWER SWITCHING USE INSULATED TYPE

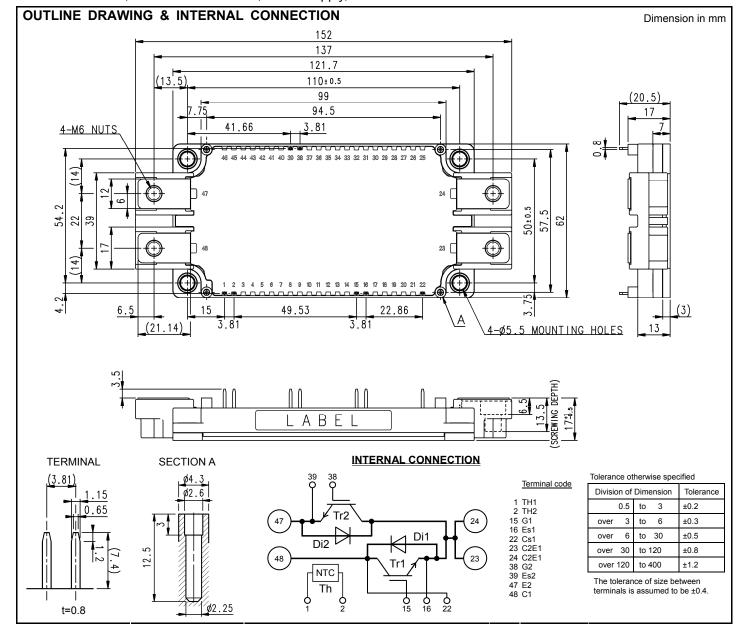


Dual switch (Half-Bridge)

- Flat base Type
- Copper base plate (non-plating)
- •Tin plating pin terminals
- •RoHS Directive compliant
- •Recognized under UL1557, File E323585

APPLICATION

AC Motor Control, Motion/Servo Control, Power supply, etc.



< IGBT MODULES > CM300DX-24S HIGH POWER SWITCHING USE INSULATED TYPE

MAXIMUM RATINGS (T_j =25 °C, unless otherwise specified) INVERTER PART IGBT/DIODE

Symbol	Item	Conditions	Rating	Unit
V _{CES}	Collector-emitter voltage	G-E short-circuited	1200	V
V_{GES}	Gate-emitter voltage	C-E short-circuited	± 20	V
Ic	Collector current	DC, T _C =119 °C (Note2, 4)	300	Α
I _{CRM}	Collector current	Pulse, Repetitive (Note3)	600	^
P _{tot}	Total power dissipation	T _C =25 °C (Note2, 4)	2270	W
I _E (Note1)	Emitter current	DC (Note2)	300	۸
I _{ERM} (Note1)	Linitlei Current	Pulse, Repetitive (Note3)	600	Α

MODULE

Symbol	Item	Conditions	Rating	Unit
V _{isol}	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	2500	V
T_{jmax}	Maximum junction temperature	Instantaneous event (overload)	175	°C
T _{Cmax}	Maximum case temperature	(Note4)	125	
T _{jop}	Operating junction temperature	Continuous operation (under switching)	-40 ~ +150	°C
T _{stg}	Storage temperature	-	-40 ~ +125	

ELECTRICAL CHARACTERISTICS (T_j=25 °C, unless otherwise specified)

INVERTER PART IGBT/DIODE

Symbol	Item	Conditions			Limits		Unit
Gyllibol	item	Conditions		Min.	Тур.	Max.	Offic
I _{CES}	Collector-emitter cut-off current	V _{CE} =V _{CES} , G-E short-circuited		-	-	1.0	mA
I _{GES}	Gate-emitter leakage current	V _{GE} =V _{GES} , C-E short-circuited		-	-	0.5	μΑ
$V_{GE(th)}$	Gate-emitter threshold voltage	I _C =30 mA, V _{CE} =10 V		5.4	6.0	6.6	V
		I _C =300 A, V _{GE} =15 V,	T _j =25 °C	-	1.80	2.25	
V _{CEsat} (Terminal)		Refer to the figure of test circuit	T _j =125 °C	-	2.00	-	V
(Terminar)	Callantar arcittar actuation valtars	(Note5)	T _j =150 °C	-	2.05	-	
	Collector-emitter saturation voltage	I _C =300 A,	T _j =25 °C	-	1.70	2.15	
V _{CEsat}		V _{GE} =15 V,	T _j =125 °C	-	1.90	-	V
(Chip)		(Note5)	T _j =150 °C	-	1.95	-	
Cies	Input capacitance		1	-	-	30	
Coes	Output capacitance	V _{CE} =10 V, G-E short-circuited		-	-	6.0	nF
Cres	Reverse transfer capacitance			-	-	0.5	
Q _G	Gate charge	V _{CC} =600 V, I _C =300 A, V _{GE} =15 V		-	700	-	nC
t _{d(on)}	Turn-on delay time	V _{CC} =600 V, I _C =300 A, V _{GE} =±15 V,		-	-	800	no
tr	Rise time			-	-	200	
t _{d(off)}	Turn-off delay time	T		-	-	600	ns
tf	Fall time	$R_G=0 \Omega$, Inductive load		-	-	300	
(Note1)		I _E =300 A, G-E short-circuited,	T _j =25 °C	-	1.80	2.25	
V _{EC} (Note1)		Refer to the figure of test circuit T_j =	T _j =125 °C	-	1.80	-	V
(Terminal)	Foritten cellecter wells		T _j =150 °C	-	1.80	-	
(Note1)	Emitter-collector voltage	I _E =300 A,	T _i =25 °C	-	1.70	2.15	
V _{EC} (Note1)		G-E short-circuited,	T _i =125 °C	-	1.70	-	V
(Chip)		(Note5)	T _j =150 °C	-	1.70	-	
t _{rr} (Note1)	Reverse recovery time	V _{CC} =600 V, I _E =300 A, V _{GE} =±15 V,	1	-	-	300	ns
Q _{rr} (Note1)	Reverse recovery charge	R _G =0 Ω, Inductive load		-	16	-	μC
E _{on}	Turn-on switching energy per pulse	V _{CC} =600 V, I _C =I _E =300 A,		-	41	-	·
E _{off}	Turn-off switching energy per pulse	V_{GE} =±15 V, R_{G} =0 Ω , T_{i} =150 °C,		-	32	-	mJ
E _{rr} (Note1)	Reverse recovery energy per pulse	Inductive load		-	22	-	mJ
R _{cc'+EE'}	Internal lead resistance	Main terminals-chip, per switch, T _C =25 °C (Note4)		-	-	0.9	mΩ
r_g	Internal gate resistance	Per switch		-	6.5	-	Ω

< IGBT MODULES > CM300DX-24S HIGH POWER SWITCHING USE INSULATED TYPE

ELECTRICAL CHARACTERISTICS (cont.; T_j =25 °C, unless otherwise specified) NTC THERMISTOR PART

Symbol	Item	Conditions	Limits			Unit
	item	Conditions	Min.	Min. Typ. M	Max.	Offic
R ₂₅	Zero-power resistance	T _C =25 °C (Note4)	4.85	5.00	5.15	kΩ
ΔR/R	Deviation of resistance	R ₁₀₀ =493 Ω, T _C =100 °C (Note4)	-7.3	-	+7.8	%
B _(25/50)	B-constant	Approximate by equation (Note6)	-	3375	-	K
P ₂₅	Power dissipation	T _C =25 °C (Note4)	-	-	10	mW

THERMAL RESISTANCE CHARACTERISTICS

Symbol	Item	Conditions	Limits			Unit
	item		Min.	Тур.	Max.	Offic
$R_{th(j-c)Q}$	Thermal resistance	Junction to case, per Inverter IGBT (Note4)	-	-	66	K/kW
$R_{th(j-c)D}$		Junction to case, per Inverter DIODE (Note4)	-	-	120	I N/KVV
R _{th(c-s)}	Contact thermal resistance	Case to heat sink, per 1 module,		15	-	K/kW
	Contact thermal resistance	Thermal grease applied (Note4, 7)	-	- 15		

MECHANICAL CHARACTERISTICS

Symbol	Item	Conditions	Conditions		Limits		
		Conditions			Тур.	Max.	Unit
M _t	Mounting torque	Main terminals	M 6 screw	3.5	4.0	4.5	N·m
Ms	Mounting torque	Mounting to heat sink	M 5 screw	2.5	3.0	3.5	N·m
m	mass	-		-	350	-	g
d	Creepage distance	Terminal to terminal		11.26	-	-	- mm
d _s		Terminal to base plate		12.46	-	-	
d	Clearance	Terminal to terminal		10	-	-	mm
da	Clearance	Terminal to base plate		10.12	-	-	mm
ec	Flatness of base plate	On the centerline X, Y (Note8)		±0	-	+100	μm

Note1. Represent ratings and characteristics of the anti-parallel, emitter-collector free wheeling diode (DIODE)

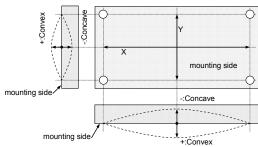
- 2. Junction temperature (T_j) should not increase beyond T_{jmax} rating.
- 3. Pulse width and repetition rate should be such that the device junction temperature (T_j) dose not exceed T_{jmax} rating.
- 4. Case temperature (T_C) and heat sink temperature (T_s) are defined on the each surface (mounting side) of base plate and heat sink just under the chips. Refer to the figure of chip location.
- 5. Pulse width and repetition rate should be such as to cause negligible temperature rise.

6.
$$B_{(25/50)} = In(\frac{R_{25}}{R_{50}})/(\frac{1}{T_{25}} - \frac{1}{T_{50}})$$
,

R₂₅: resistance at absolute temperature T₂₅ [K]; T₂₅=25 [°C]+273.15=298.15 [K]

 R_{50} : resistance at absolute temperature T_{50} [K]; T_{50} =50 [°C]+273.15=323.15 [K]

- 7. Typical value is measured by using thermally conductive grease of λ=0.9 W/(m·K).
- 8. The base plate (mounting side) flatness measurement points (X, Y) are as follows of the following figure.



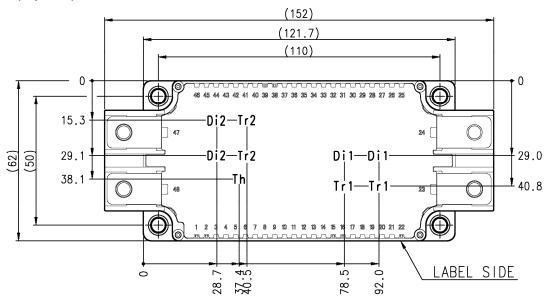
- Use the following screws when mounting the printed circuit board (PCB) on the stand offs.
 "φ2.6×10 or φ2.6×12 B1 tapping screw"
 - The length of the screw depends on thickness (t1.6~t2.0) of the PCB.

RECOMMENDED OPERATING CONDITIONS

Symbol	Item	Conditions	Limits			Unit
	item	Conditions	Min. Typ. Max.			
V _{CC}	(DC) Supply voltage	Applied across C1-E2 terminals	-	600	850	V
V_{GEon}	Gate (-emitter drive) voltage	Applied across G1-Es1/G2-Es2 terminals	13.5	15.0	16.5	V
R _G	External gate resistance	Per switch	0	-	14	Ω

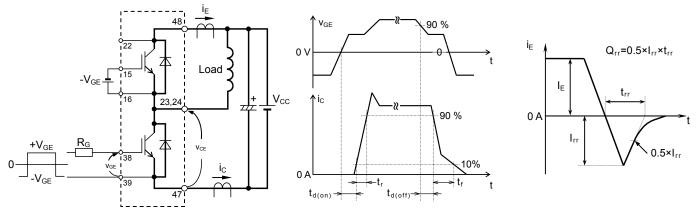
CHIP LOCATION (Top view)

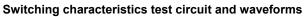
Dimension in mm, tolerance: ±1 mm

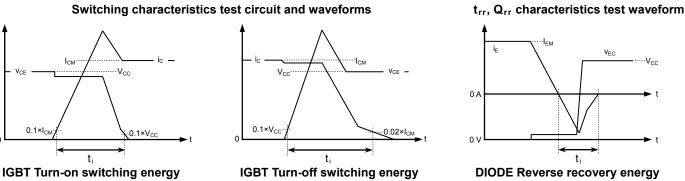


Tr1/Tr2: IGBT, Di1/Di2: DIODE, Th: NTC thermistor

TEST CIRCUIT AND WAVEFORMS

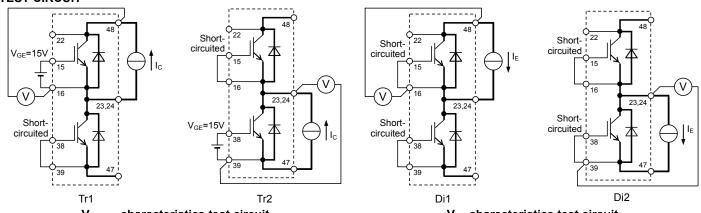






Turn-on / Turn-off switching energy and Reverse recovery energy test waveforms (Integral time instruction drawing)



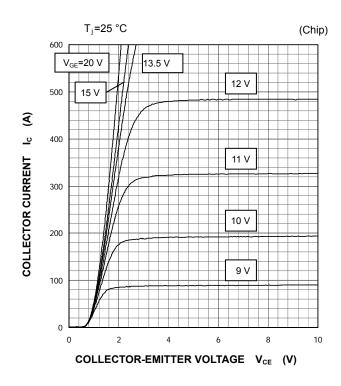


V_{CEsat} characteristics test circuit

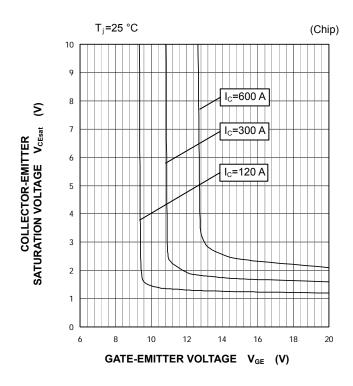
V_{EC} characteristics test circuit

PERFORMANCE CURVES INVERTER PART

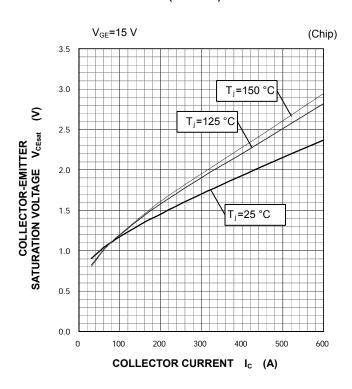
OUTPUT CHARACTERISTICS (TYPICAL)



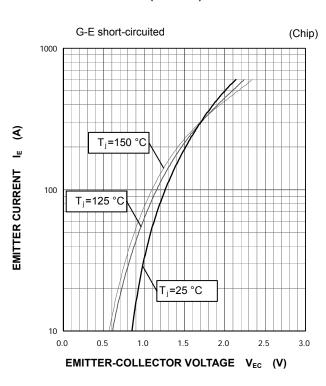
COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



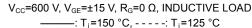
FREE WHEELING DIODE FORWARD CHARACTERISTICS (TYPICAL)

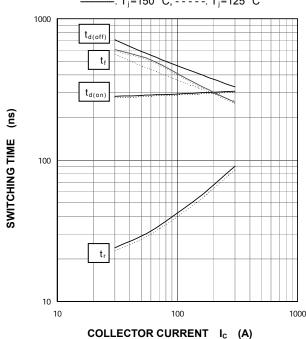


PERFORMANCE CURVES

INVERTER PART

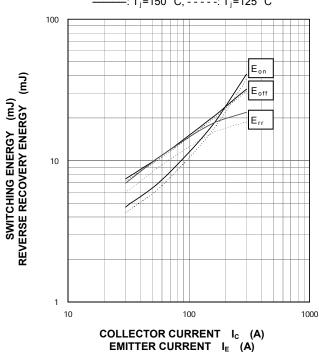
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



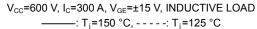


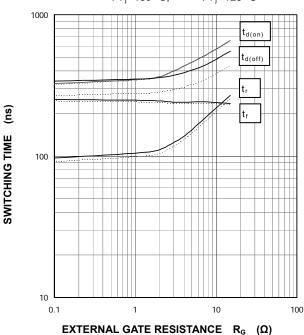
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

 V_{CC} =600 V, V_{GE} =±15 V, R_{G} =0 Ω , INDUCTIVE LOAD, PER PULSE: T_i =150 °C, - - - - : T_i =125 °C



HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

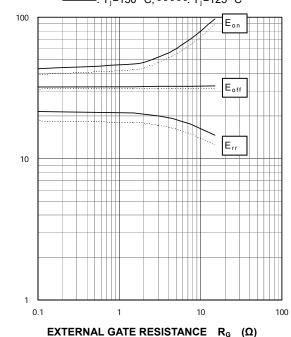




HALF-BRIDGE SWITCHING CHARACTERISTICS

(TYPICAL) V_{CC} =600 V, V_{GE} =±15 V, I_C/I_E =300 A, INDUCTIVE LOAD, PER PULSE

-----: T_i=150 °C, - - - -: T_i=125 °C

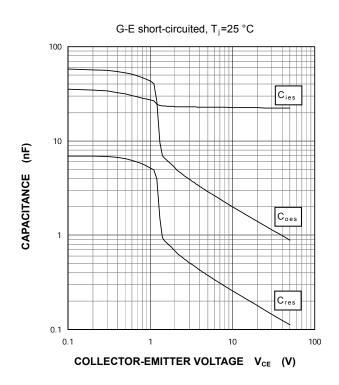


SWITCHING ENERGY (mJ)
REVERSE RECOVERY ENERGY (mJ)

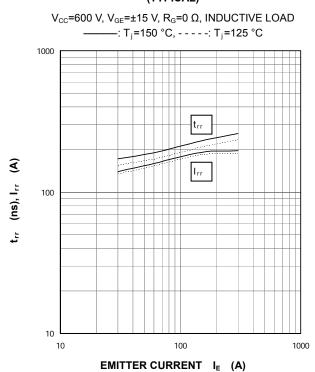
PERFORMANCE CURVES

INVERTER PART

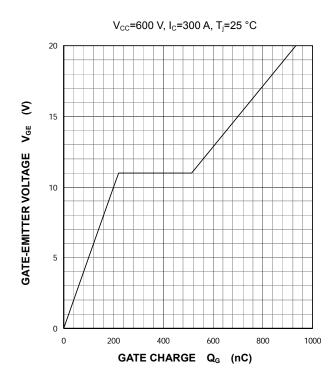
CAPACITANCE CHARACTERISTICS (TYPICAL)



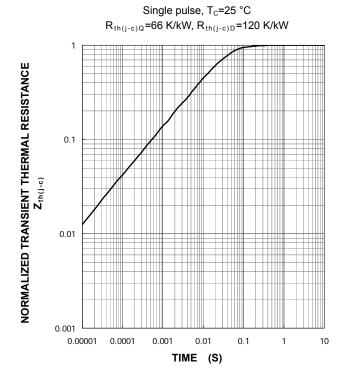
FREE WHEELING DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)



GATE CHARGE CHARACTERISTICS (TYPICAL)



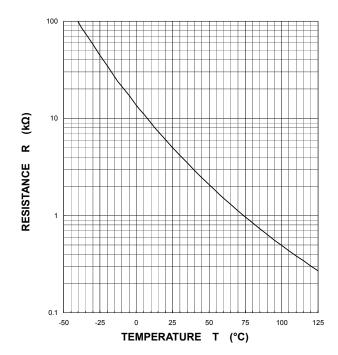
TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (MAXIMUM)



PERFORMANCE CURVES

NTC thermistor part

TEMPERATURE CHARACTERISTICS (TYPICAL)



Keep safety first in your circuit designs!

Mitsubishi Electric Corporation puts the maximum effort into making semiconductor products better and more reliable, but there is always the possibility that trouble may occur with them. Trouble with semiconductors may lead to personal injury, fire or property damage. Remember to give due consideration to safety when making your circuit designs, with appropriate measures such as (i) placement of substitutive, auxiliary circuits, (ii) use of non-flammable material or (iii) prevention against any malfunction or mishap.

Notes regarding these materials

- •These materials are intended as a reference to assist our customers in the selection of the Mitsubishi semiconductor product best suited to the customer's application; they do not convey any license under any intellectual property rights, or any other rights, belonging to Mitsubishi Electric Corporation or a third party.
- •Mitsubishi Electric Corporation assumes no responsibility for any damage, or infringement of any third-party's rights, originating in the use of any product data, diagrams, charts, programs, algorithms, or circuit application examples contained in these materials.
- •All information contained in these materials, including product data, diagrams, charts, programs and algorithms represents information on products at the time of publication of these materials, and are subject to change by Mitsubishi Electric Corporation without notice due to product improvements or other reasons. It is therefore recommended that customers contact Mitsubishi Electric Corporation or an authorized Mitsubishi Semiconductor product distributor for the latest product information before purchasing a product listed herein.

The information described here may contain technical inaccuracies or typographical errors. Mitsubishi Electric Corporation assumes no responsibility for any damage, liability, or other loss rising from these inaccuracies or errors.

Please also pay attention to information published by Mitsubishi Electric Corporation by various means, including the Mitsubishi Semiconductor home page (www.MitsubishiElectric.com/semiconductors/).

- •When using any or all of the information contained in these materials, including product data, diagrams, charts, programs, and algorithms, please be sure to evaluate all information as a total system before making a final decision on the applicability of the information and products. Mitsubishi Electric Corporation assumes no responsibility for any damage, liability or other loss resulting from the information containedherein.
- •Mitsubishi Electric Corporation semiconductors are not designed or manufactured for use in a device or system that is used under circumstances in which human life is potentially at stake. Please contact Mitsubishi Electric Corporation or an authorized Mitsubishi Semiconductor product distributor when considering the use of a product contained herein for any specific purposes, such as apparatus or systems for transportation, vehicular, medical, aerospace, nuclear, or undersea repeater use.
- •The prior written approval of Mitsubishi Electric Corporation is necessary to reprint or reproduce in whole or in part these materials.
- •If these products or technologies are subject to the Japanese export control restrictions, they must be exported under a license from the Japanese government and cannot be imported into a country other than the approved destination.
- Any diversion or reexport contrary to the export control laws and regulations of Japan and/or the country of destination is prohibited.
- •Please contact Mitsubishi Electric Corporation or an authorized Mitsubishi Semiconductor product distributor for further details on these materials or the products contained therein.