## CmpE 443 Final Project Design Document Group Name: Tezla

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#### Contents

1	System Level Structural Diagram (Block Diagram)	2
2	Sequence Diagram	3
3	LED Connections	4
1	Motor - Speed Sensor Connection	5
5	Motor - Driver Connection	6
3	Driver - Board Connection	7

## 1 System Level Structural Diagram (Block Diagram)

This is where system level structural diagram will be put. Do not forget to describe functionality of the components in the scope of the given problem.

#### 2 Sequence Diagram

Sequence Diagram would be here. You should show the high level relationships between modules clearly (you do not need to give details such as variable names and function names). The relationship between the hardwares and the software must be demonstrated in this diagram. Explain each module as in your labs. Example Sequence Diagram for Lab 7 is provided below Figure:

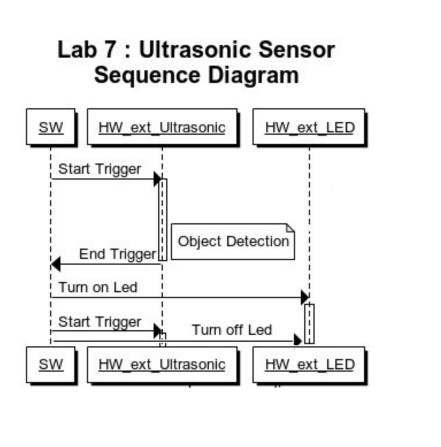


Figure 1: Sample Sequence Diagram.

#### 3 LED Connections

All the components which are controlled via LPC4088 should be connected to the board. Therefore, you should determine the pins and their functionalities. Draw the described table in the term project interim report description document. After you determine the all the pins, draw the circuit schematic for the LED circuits.

## 4 Motor - Speed Sensor Connection

You should determine the pins of sensors and their functionalities. Draw a table which shows the pins and their functionalities as described in the project description document.

## 5 Motor - Driver Connection

Here is an example figure for you to use the LaTeX format.

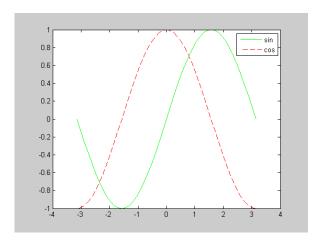


Figure 2: Sin and Cosine.

## 6 Driver - Board Connection

Here is an example table for you to use the LaTeX format.

Algorithm	Feature 1	Feature 2	Feature 3	Feature n
Alg 1	1	2	23	68.7%
Alg 2	9	2	37	75.68%
Alg 3	10	3	51	96.34%

Table 1: Should be a caption