





74HC595  
1-7,15 Q0-Q7 Output Pins  
8 GND  
9 Q7 Serial Out [DATAOUT]  
10 MR Master Reclear, active low  
11 SH\_CP Shift register clock [CLOCK]  
12 ST\_CP Storage register clock [LATCH]  
13 OE Output enable, active low  
14 DS Serial data input [DATA IN]  
16 Vcc

DIMM\_OUT  
U5 11  
U6 13  
U3 11

DIMMIN  
9,10

ULN2003

74HC595

74HC00

74HC595

ULN2003

DATAIN  
14

DATAOUT  
9

CLKOUT  
U3 11,12  
U6 11, 12  
U5 6

CLKIN  
1,2

SURE  
DE-DP  
Ver1.1

1	+12V	6	+5V
2	GND	7	GND
3	GND	8	GND
4	DIMMOUT	9	GND
5	DATAOUT	10	CLKOUT

1	GND	6	+12V
2	GND	7	GND
3	GND	8	GND
4	DIMMIN	9	GND
5	DATAIN	10	CLKIN