



Daren Wang

2B Electrical Engineering



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darenwang.me

Summary of Qualifications

- ▶ Experience working with **Oscilloscopes, power supplies, DMMs**, and other lab equipment
- ▶ Programming experience with **C++, C#, Objective-C, Java, HTML, CSS, JavaScript**
- ▶ Proficient in gate level circuit design with **FPGA** and **VHDL** technologies

Experience

SOFTWARE DEVELOPER

SMART TECHNOLOGIES, CALGARY, AB

MAY - AUG 2016

- ▶ Developed SMART Notebook, SMART's flagship macOS and Windows desktop applications
- ▶ Created shippable educational activity during 3 week intern project, complete with unit and integration tests
- ▶ Participated in company wide hackathon, team was awarded "executive award" for the project which leveraged functionality in SMART Notebook to allow student to explore the fundamentals of programming
- ▶ Took on an individual project to create a functional prototype for a journaling application on Android, project required use of JNI layer to use native libraries on Android
- ▶ Technologies used during term: C++, Objective-C, Java, JS

QUALITY ASSURANCE ENGINEERING

ZYNGA, TORONTO, ON

SEPT - DEC 2015

- ▶ Executed feature, regression and release testing for mobile games on iOS and Android platforms
- ▶ Participated in the agile development cycle using tools including JIRA and Crashlytics to track and promote project development
- ▶ Tasked with preparing product status reports and build release sign offs

TRANSIT MANAGEMENT SYSTEMS

REGIONAL MUNICIPALITY OF YORK, VAUGHN, ON

JAN - APR 2015

- ▶ A member of the On-Board systems team, responsible for maintaining and improving CAD/AVL (computer aided dispatch/ automatic vehicle location) systems and Seon CCTV systems on York Region Transit's 10 types of public buses.
- ▶ Worked daily with electrical circuits and testing equipment to troubleshoot and improve on-board systems
- ▶ Exposed to corporate networking and data management technologies such as VPN, Microsoft Server 2012, eDocs and Access SQL

MEMBER OF STUDENT DESIGN TEAM

UNIVERSITY OF WATERLOO BAJA SAE OFF-ROAD RACING

SEPT 2014 - JAN 2015

- ▶ Developed collaboration skills by being involved in a small team dedicated to the design of electrical systems on the vehicle
- ▶ Corporate communication experience gained through participation on sponsorship team

Education

- ▶ University of Waterloo – Candidate for Bachelor of Applied Science, Electrical Engineering - 2019
 - ▶ 3.7/4.0 GPA
 - ▶ Relevant courses: Data Structures and Algorithms, Embedded Microprocessor Systems