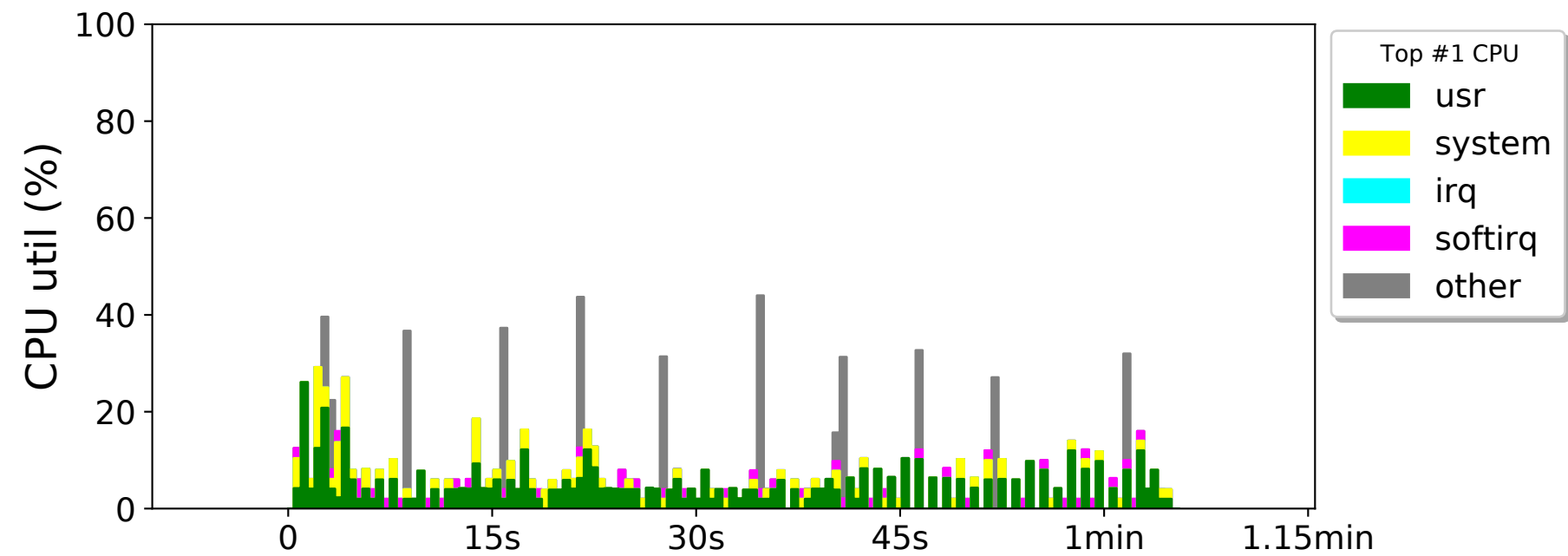
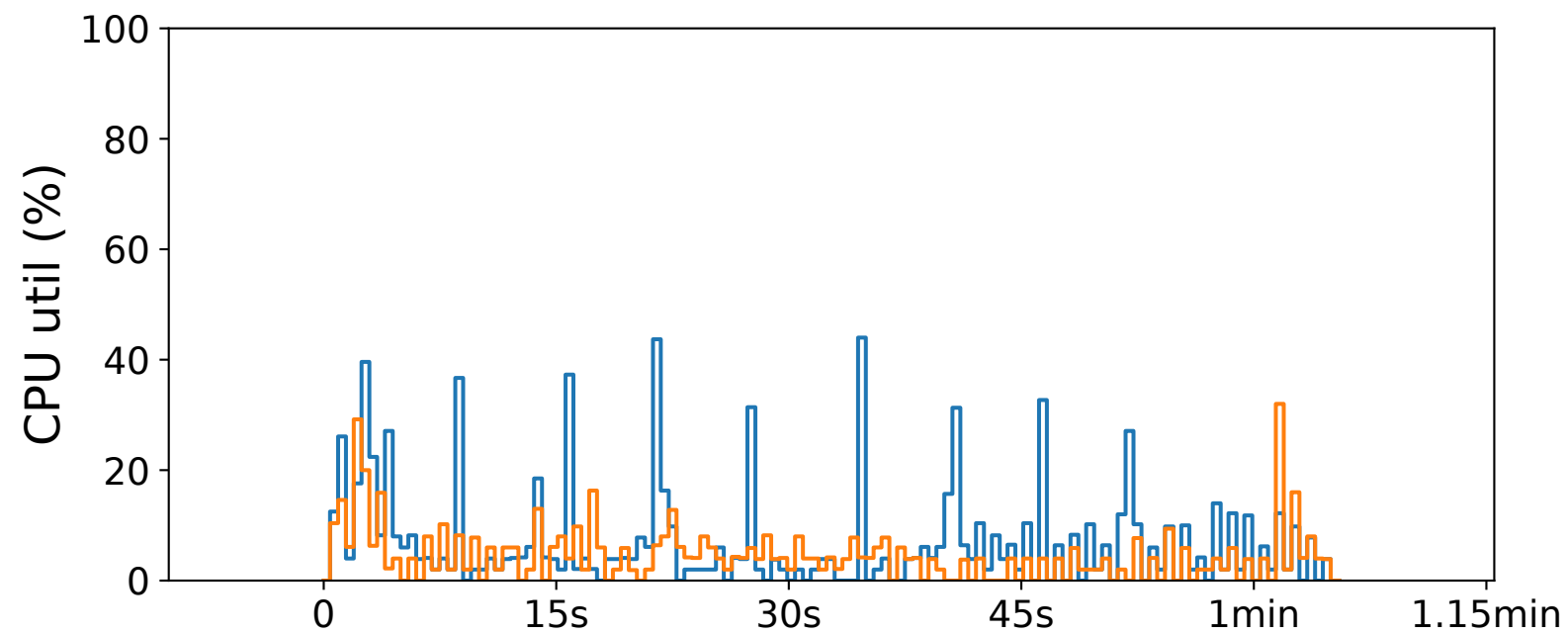


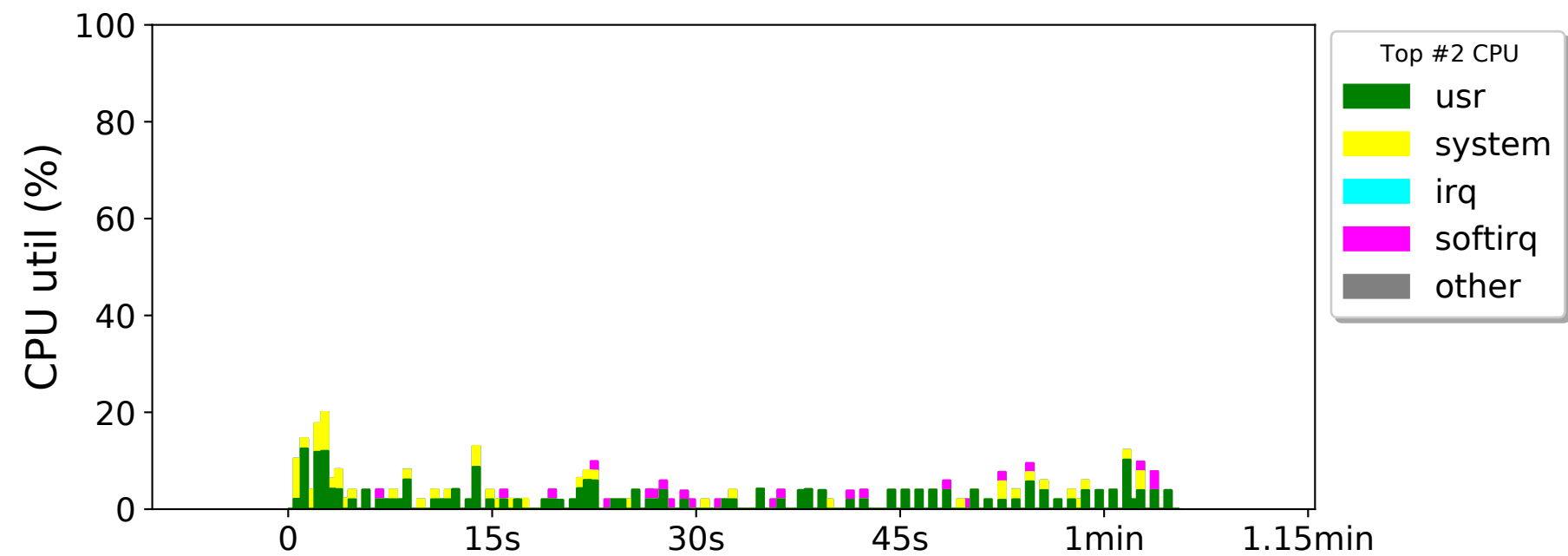
sv1-eth0 (send) sv1-eth1 (send) sv1-eth2 (send)  
sv1-eth0 (receive) sv1-eth1 (receive) sv1-eth2 (receive)



Top #1 CPU  
usr  
system  
irq  
softirq  
other



CPU0  
CPU1



Top #2 CPU  
usr  
system  
irq  
softirq  
other