

Inputs

- clk\_s
- rst\_s
- func\_s[10:0]
- op\_code\_s[5:0]

1° Stage

- en1\_s
- rf1\_s
- rf2\_s

2° Stage

- alu\_sel\_s[1:0]
- en2\_s
- s1\_s
- s2\_s

3° Stage

- en3\_s
- rm\_s
- wm\_s
- s3\_s
- wf1\_s

Internal

- upc
- icount

