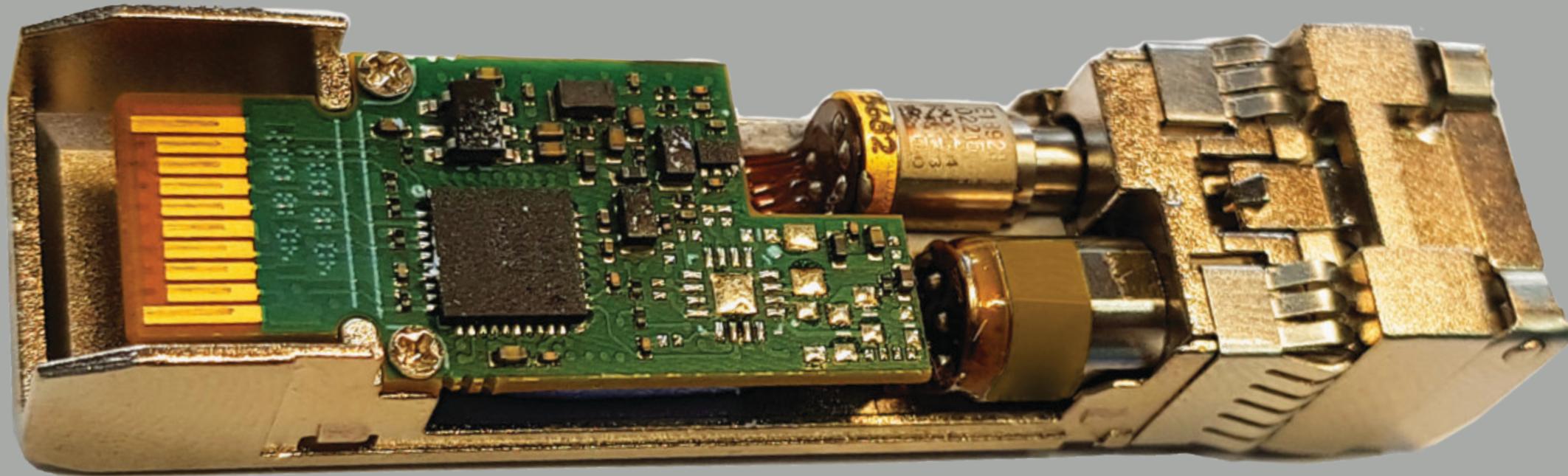


the complexity of hyper speed transceivers

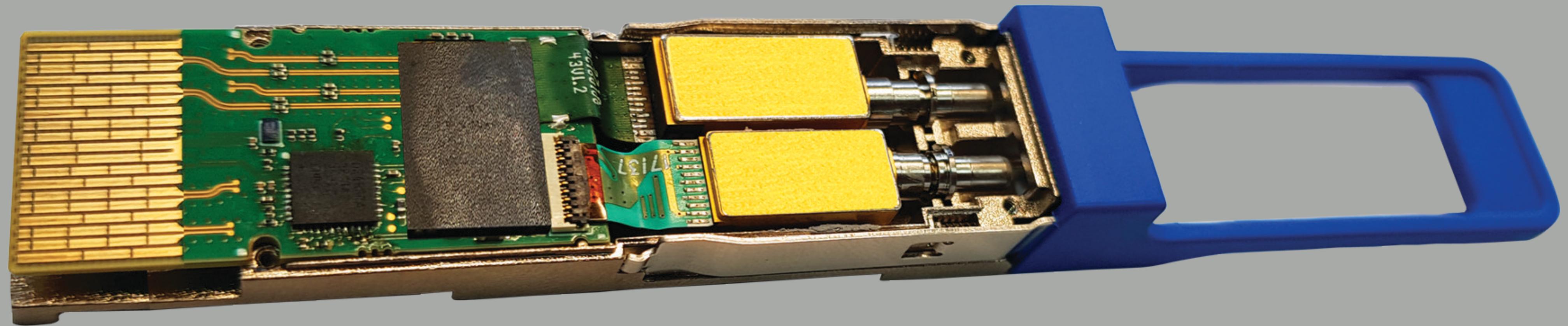


lets make it

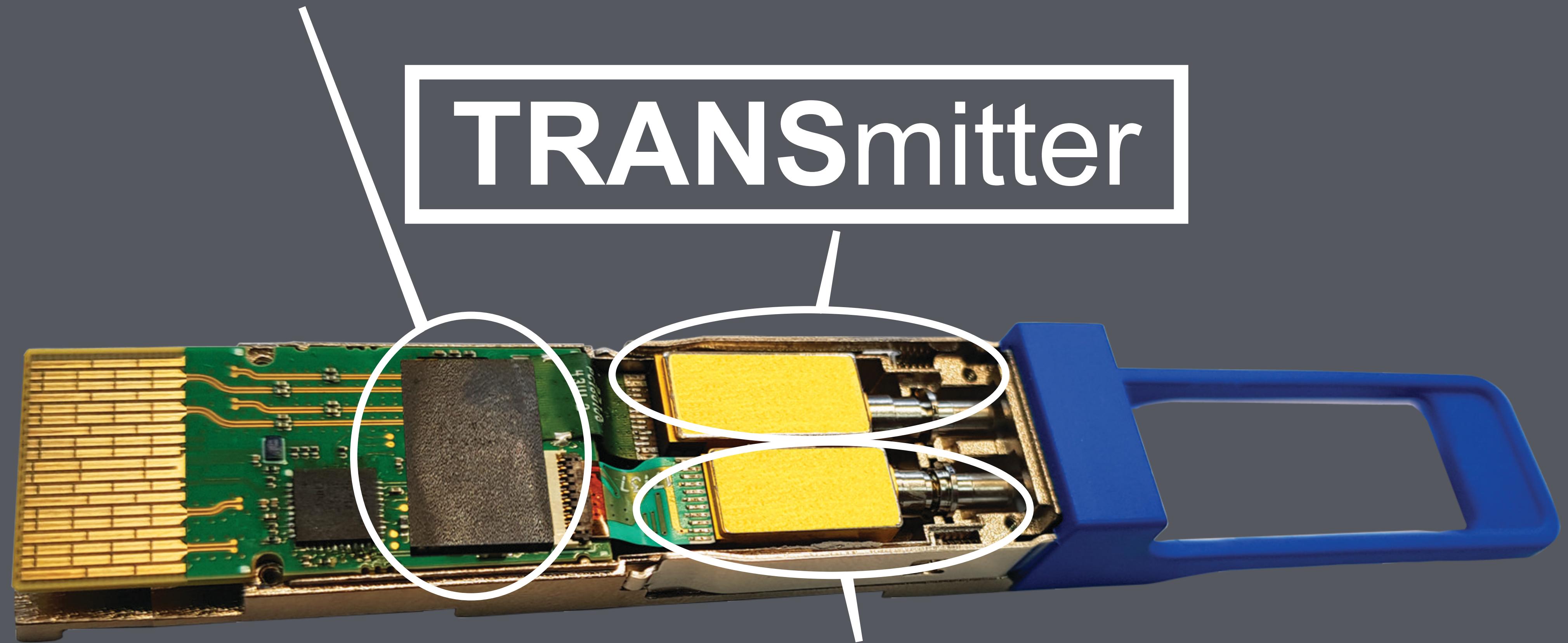
10G



400G



Digital Signal Processor



TRANSmitter

reCEIVER

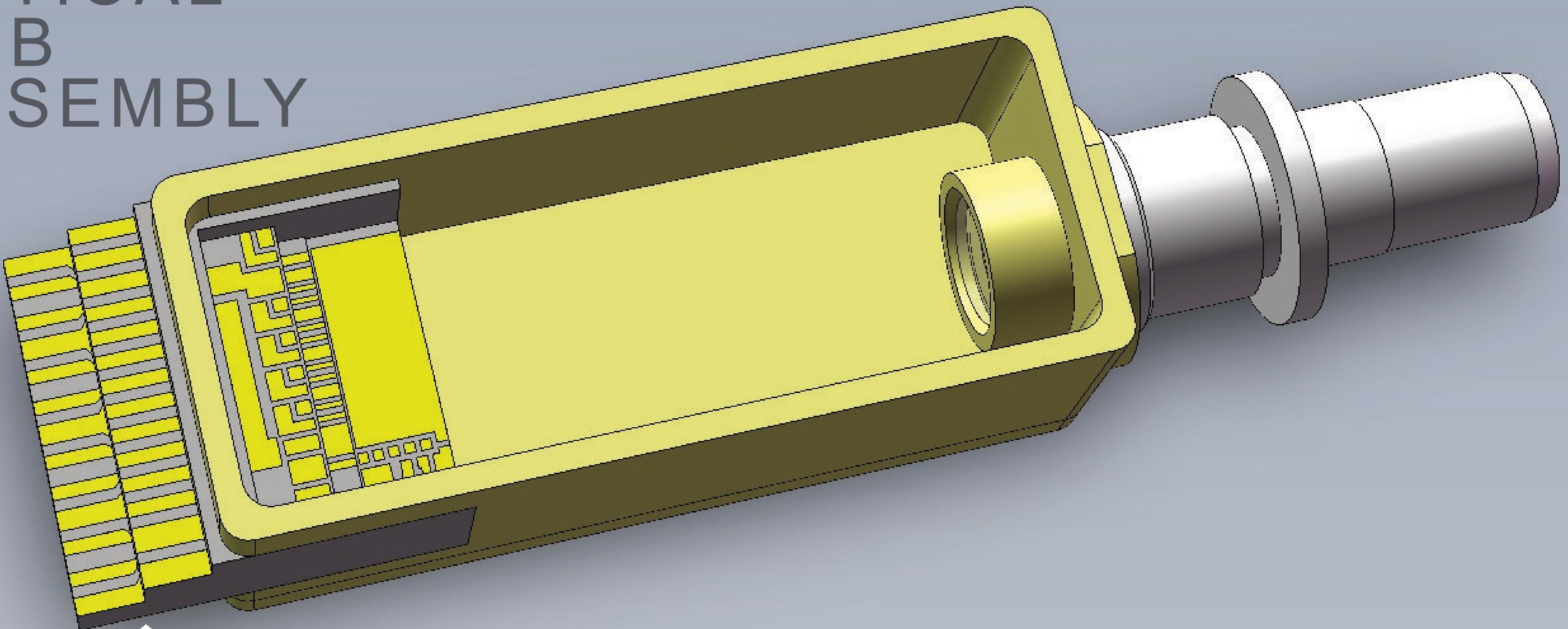
TRANSMITTER

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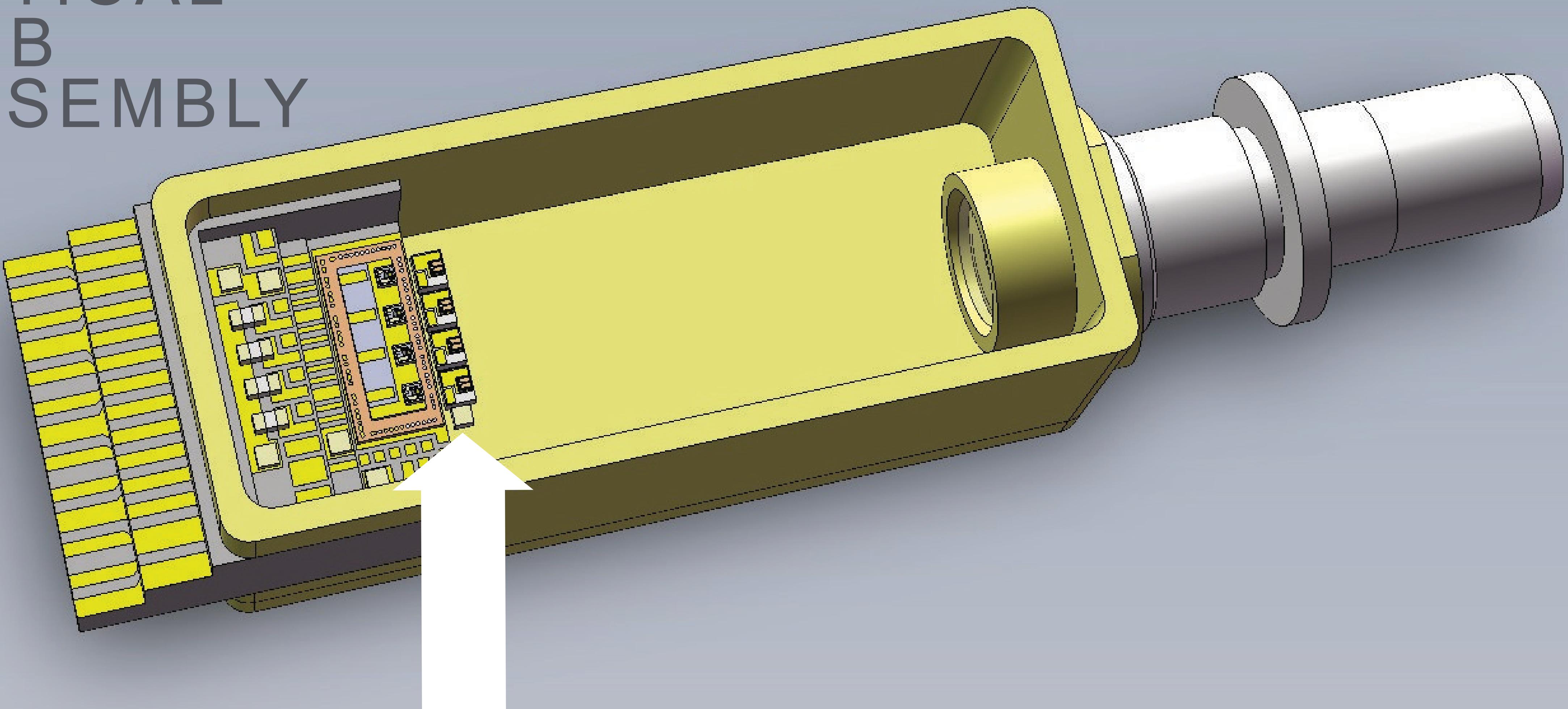
electrical connector

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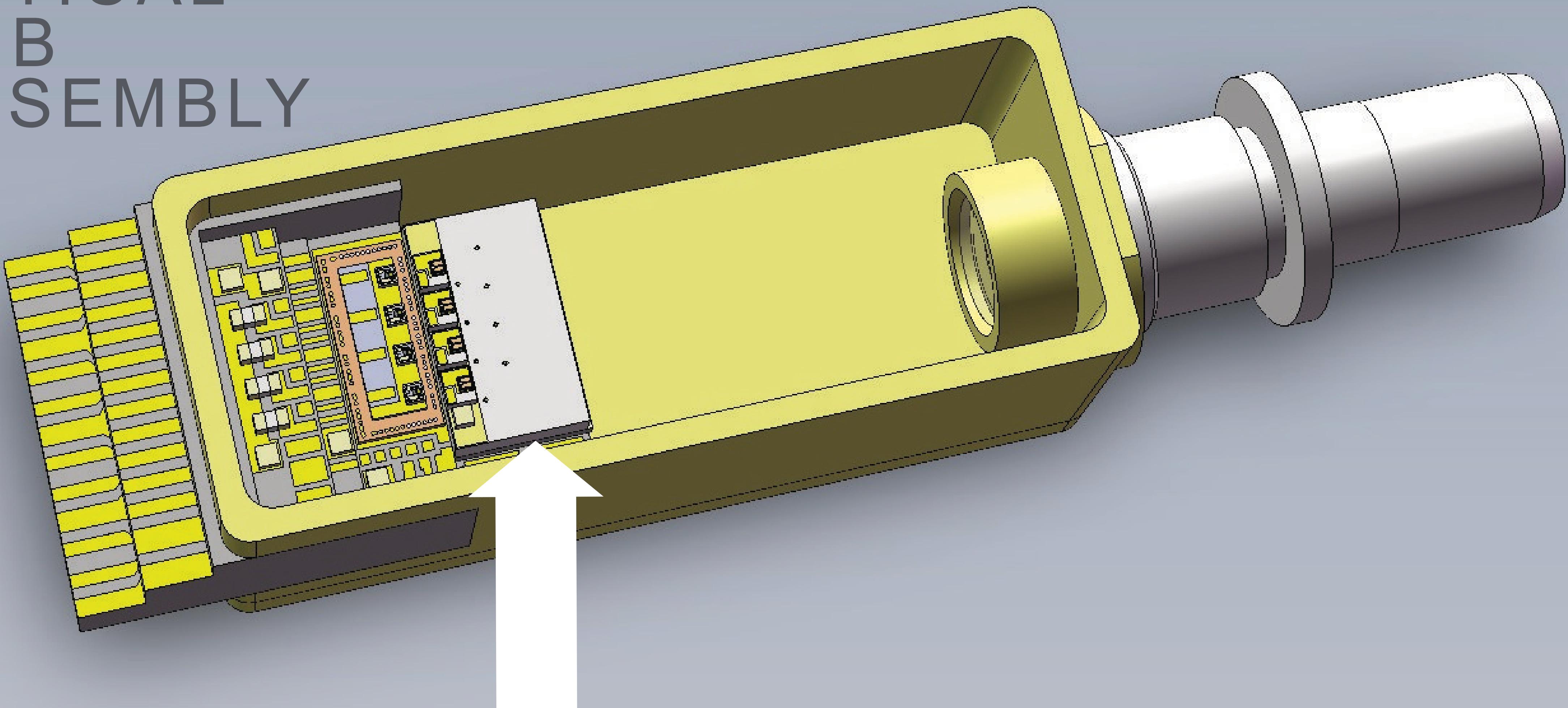
laser diodes

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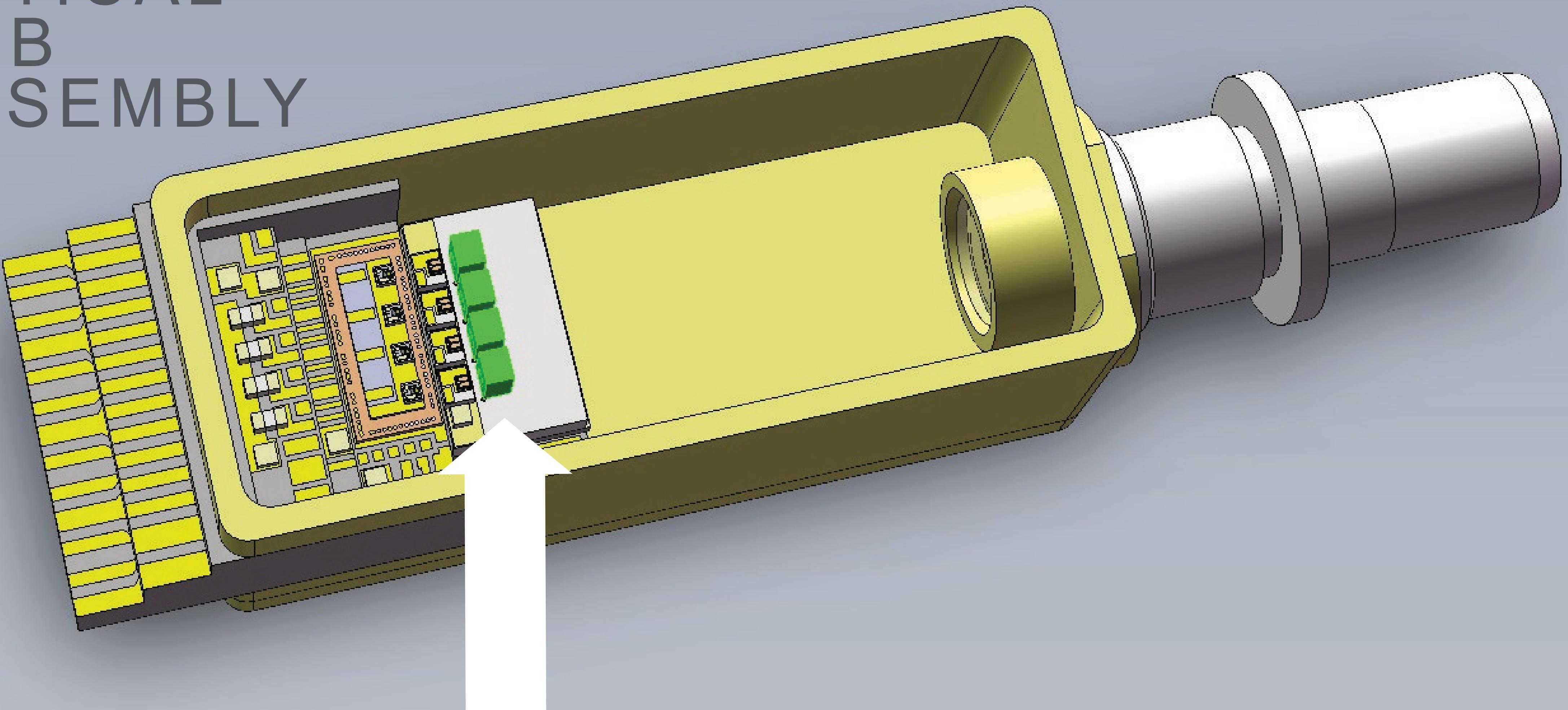
cooler

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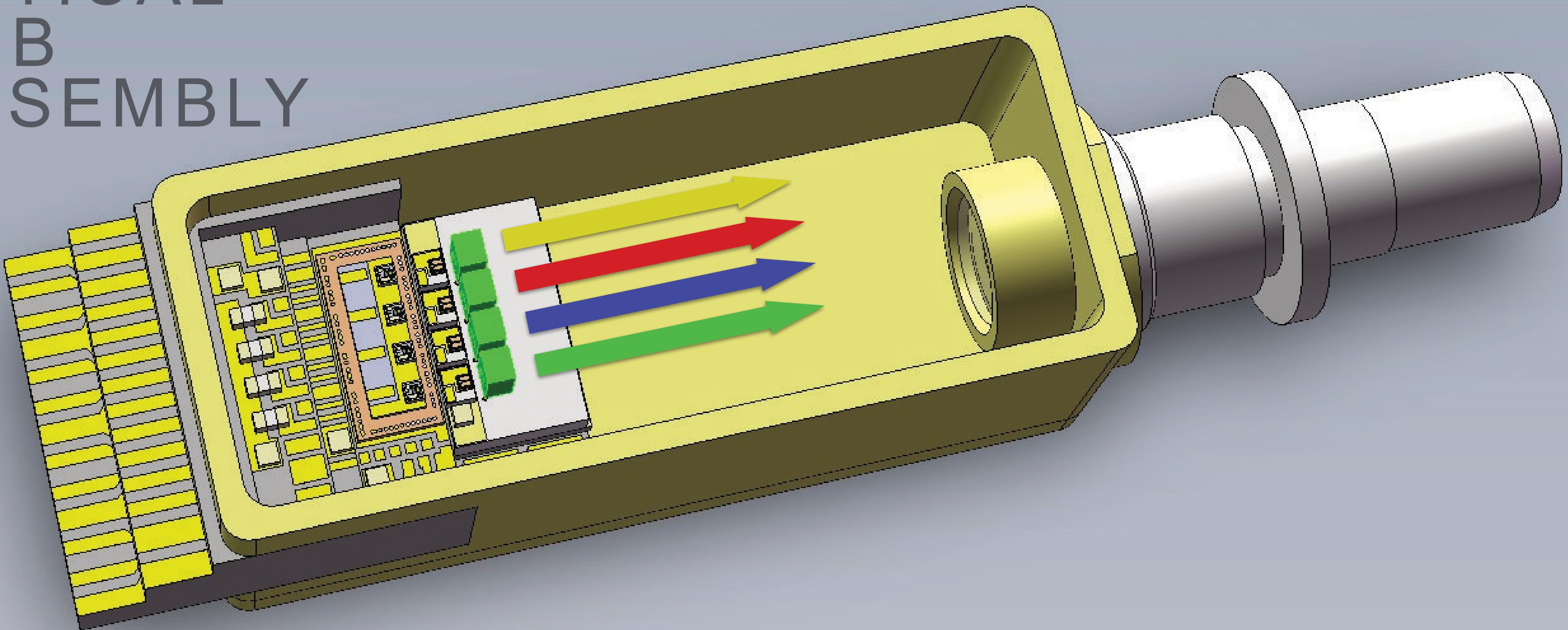
S
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optical lenses

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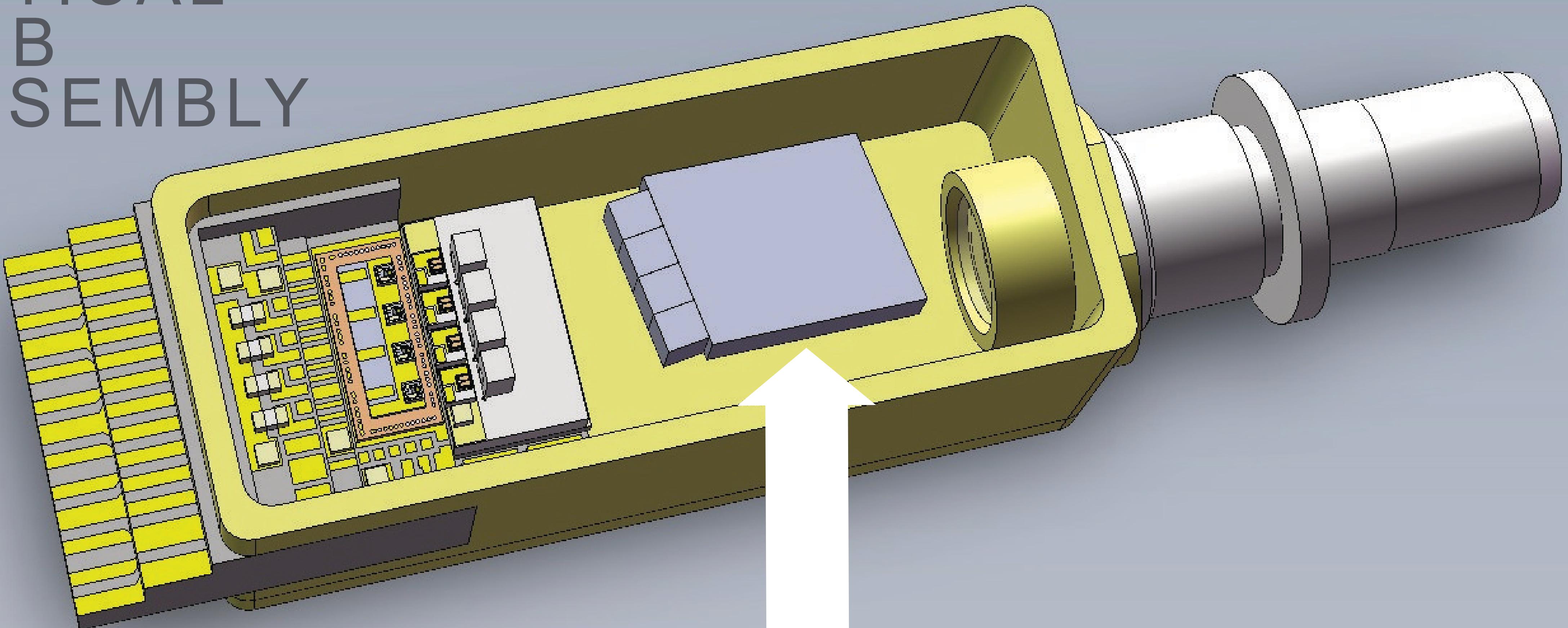


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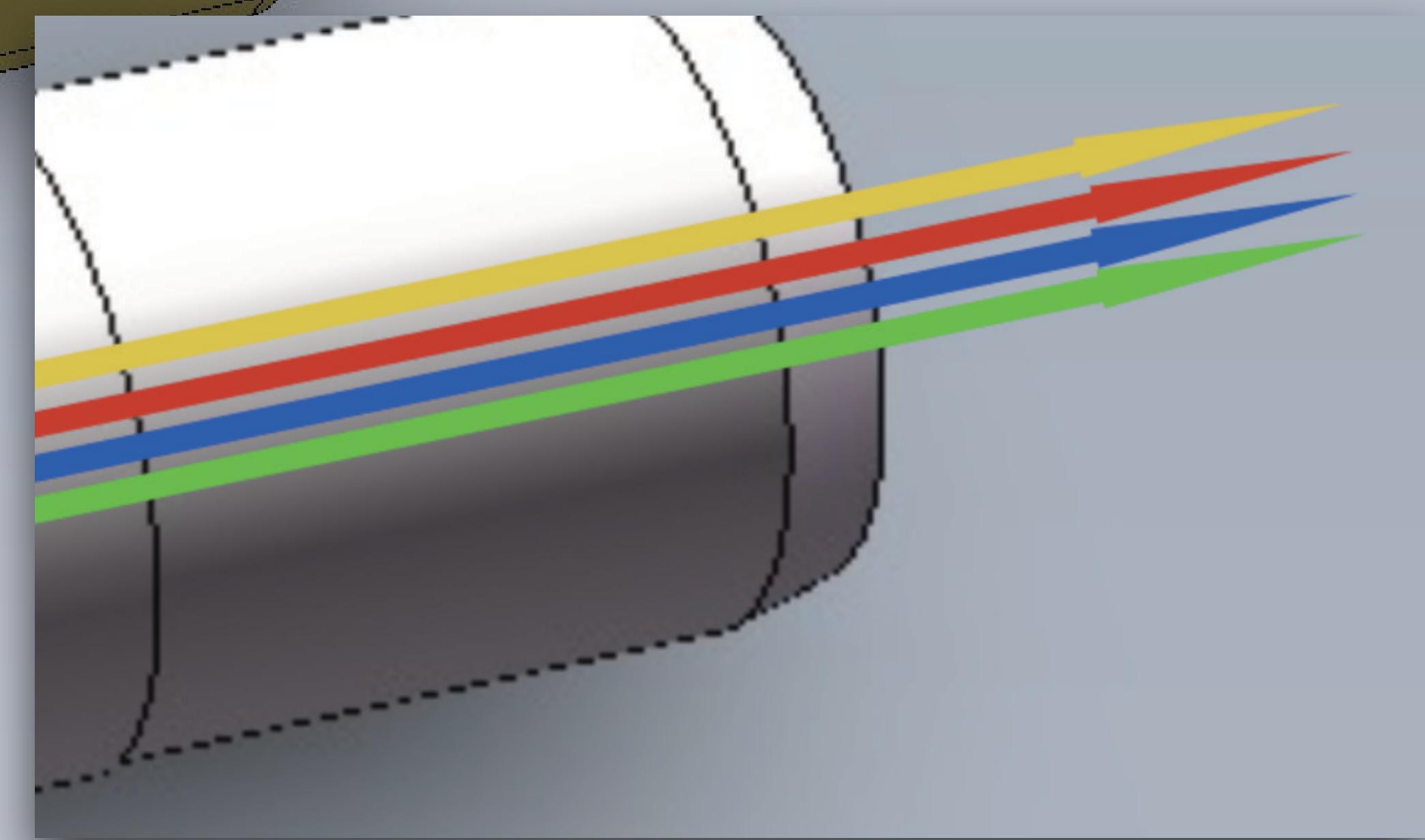
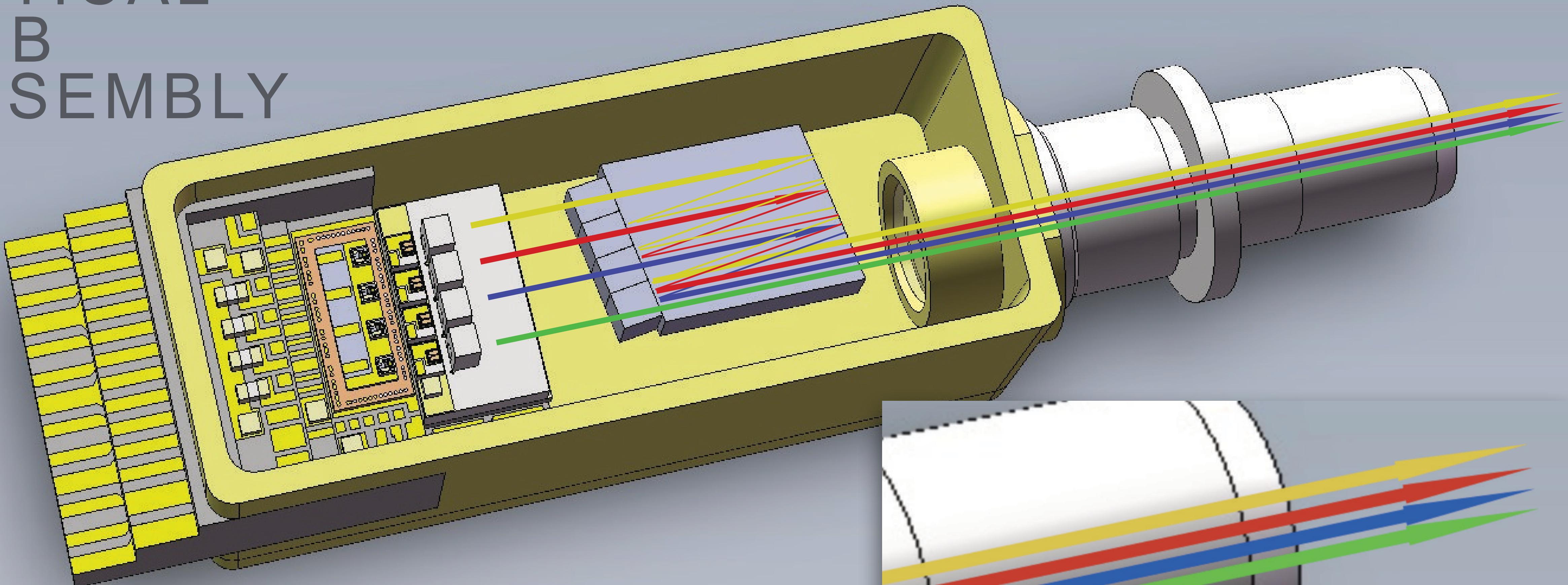
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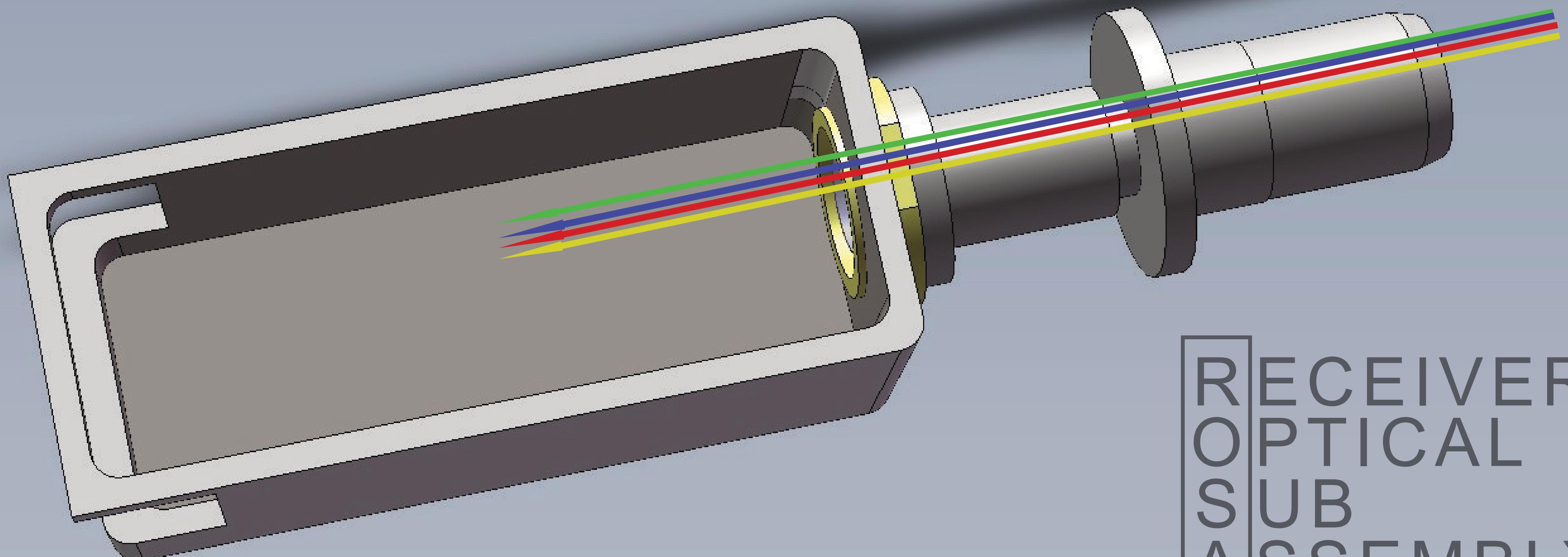
optical multiplexer

T
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TRANSMITTER
OPTICAL
SUB
ASSEMBLY



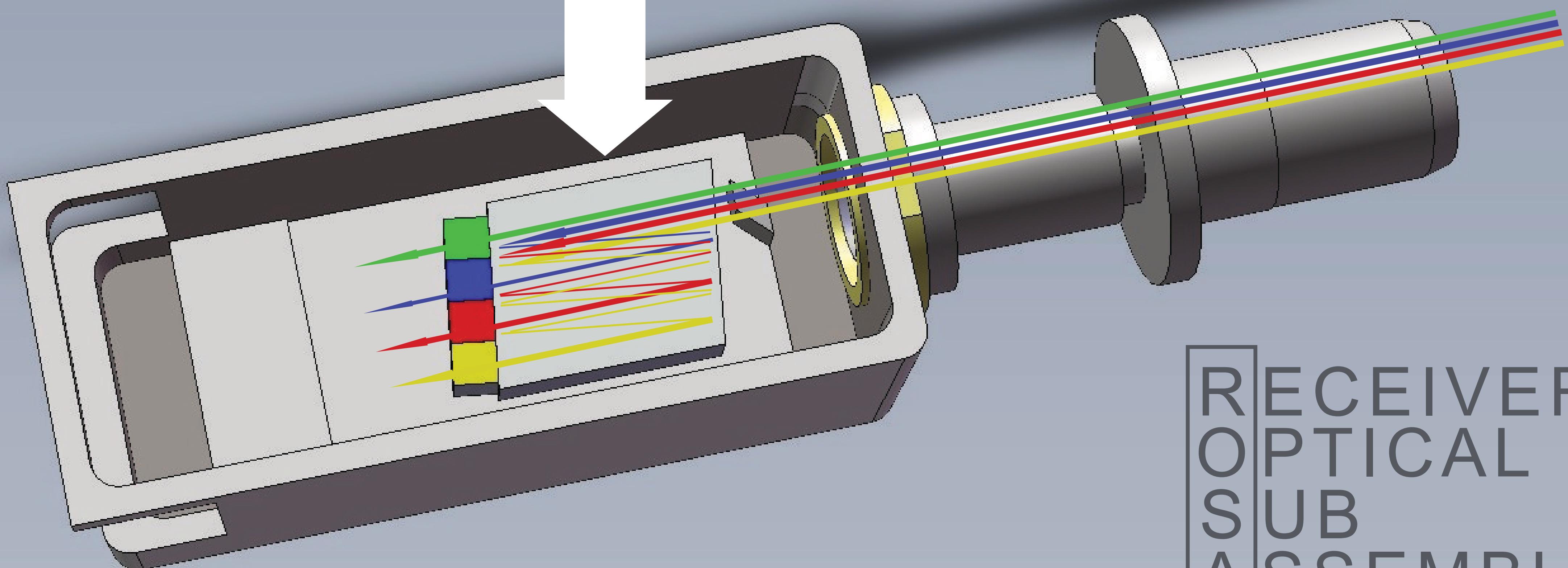
RECEIVER

optical receptacle



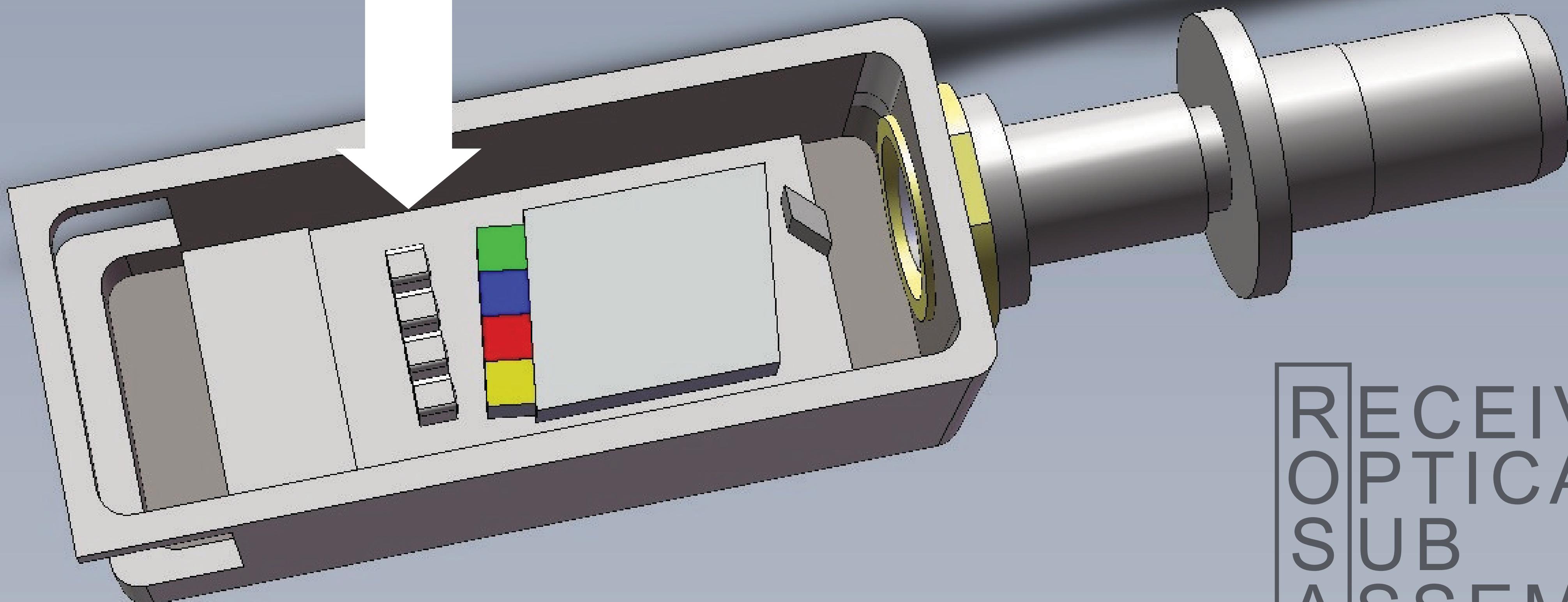
RECEIVER
OPTICAL
SUB
ASSEMBLY

optical demultiplexer



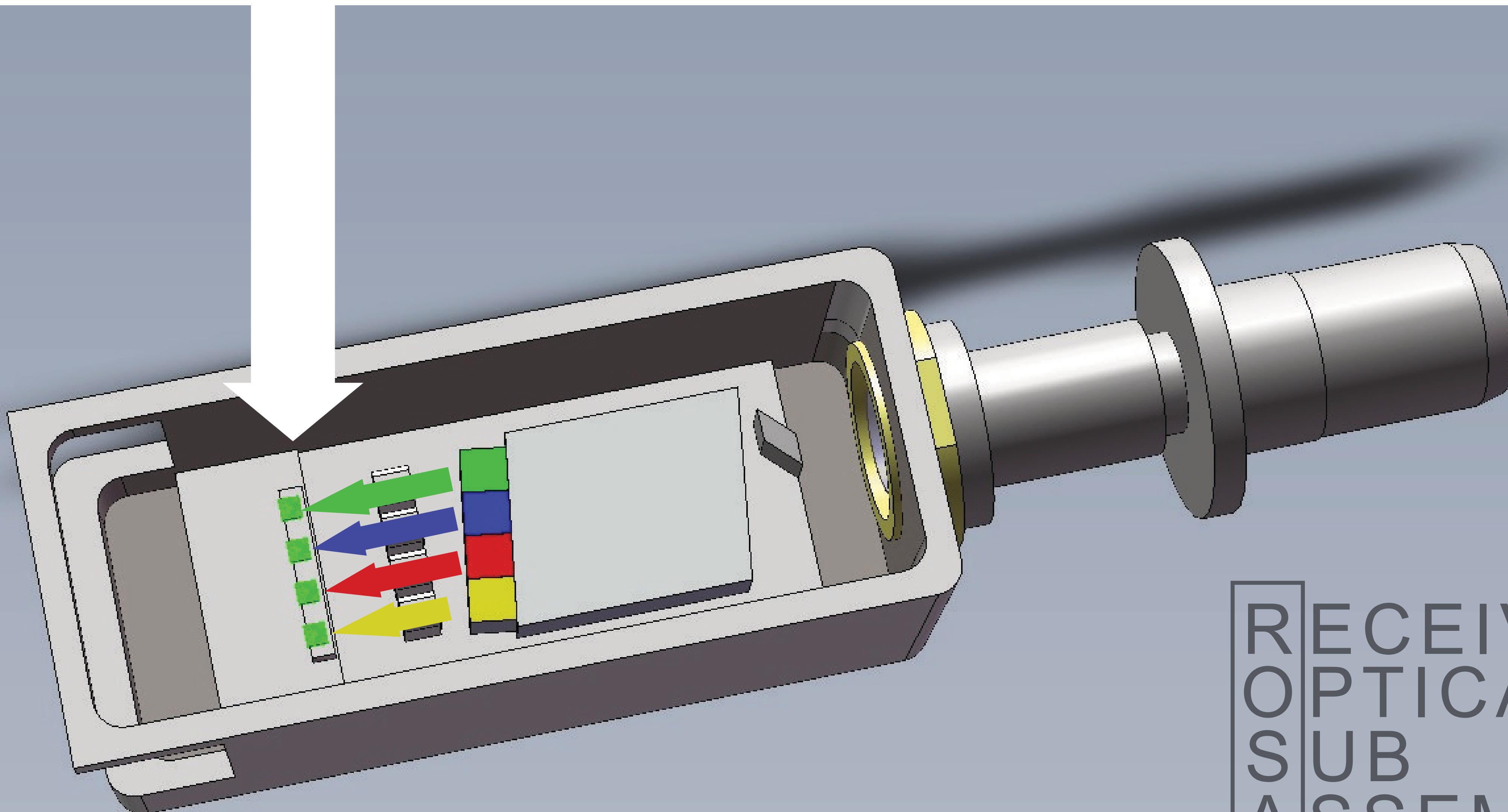
RECEIVER
OPTICAL
SUB
ASSEMBLY

optical lenses



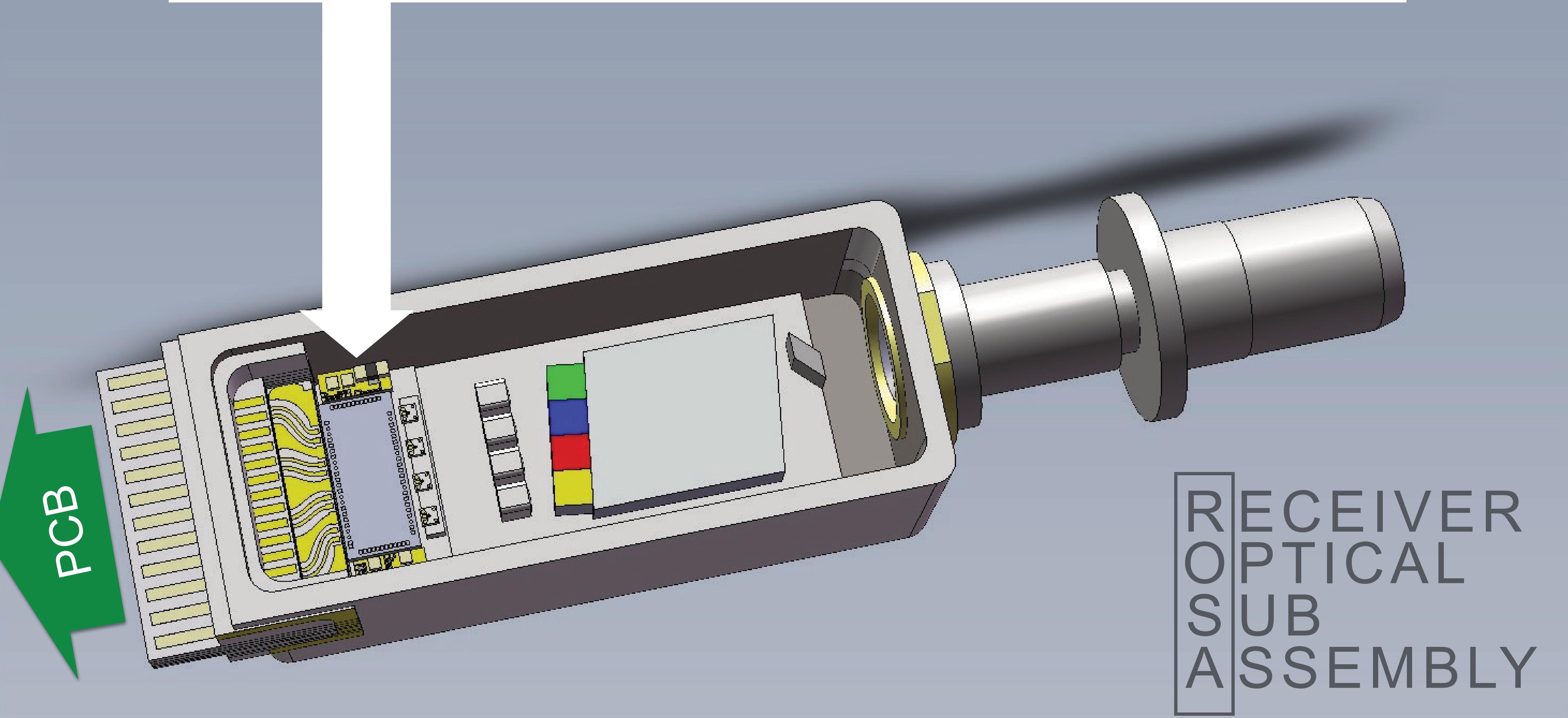
RECEIVER
OPTICAL
SUB
ASSEMBLY

PIN diode



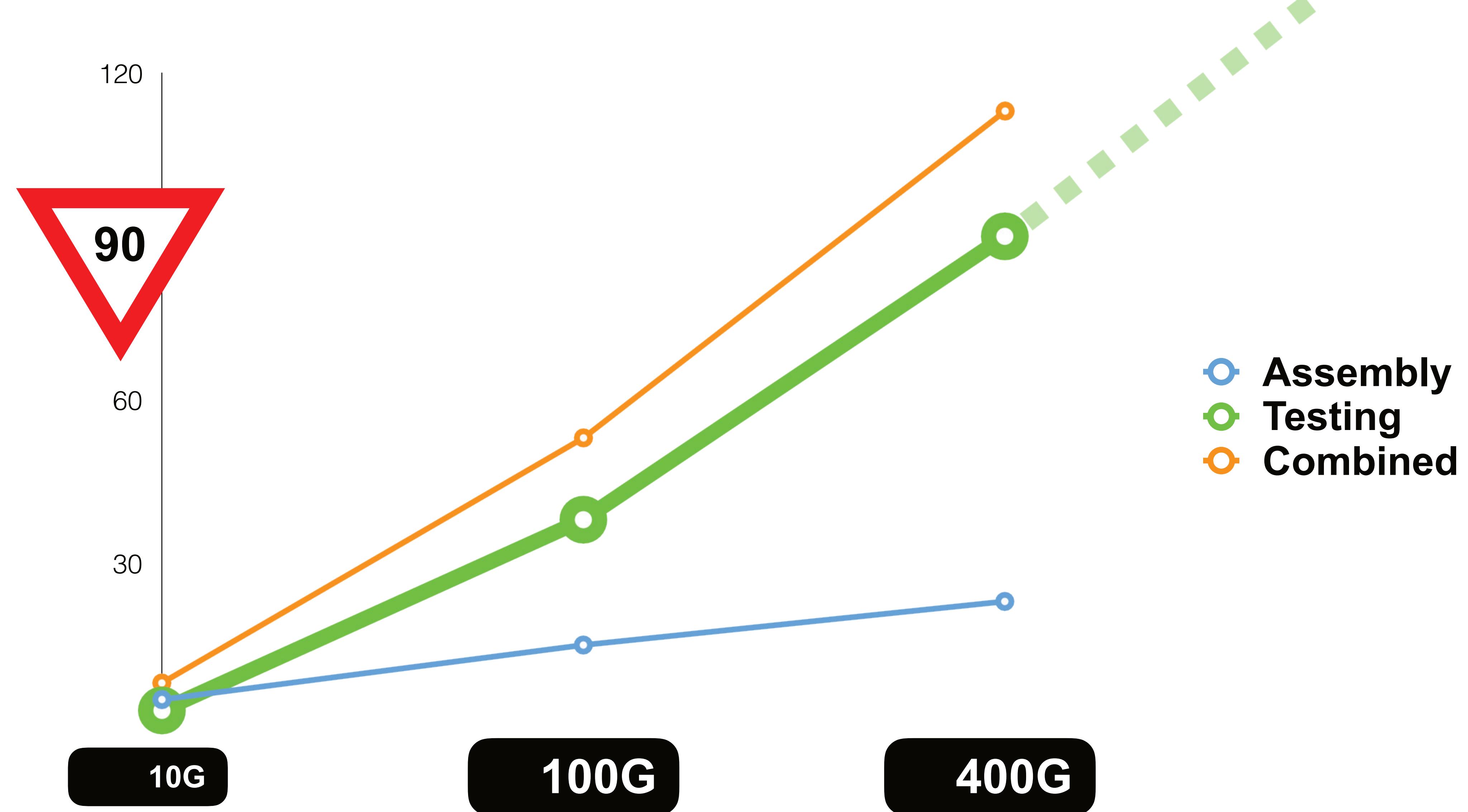
RECEIVER
OPTICAL
SUB
ASSEMBLY

amplifier

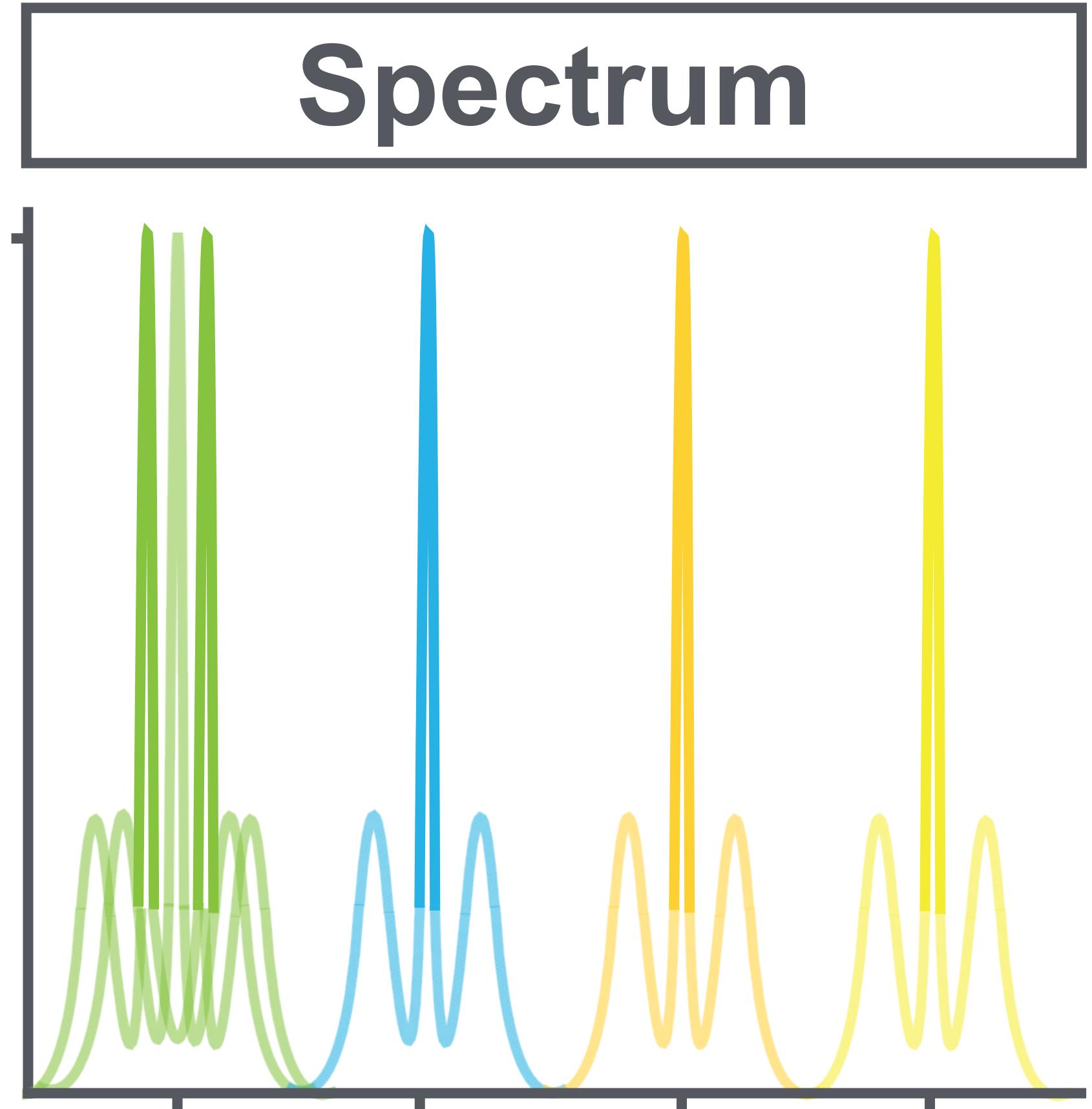


production





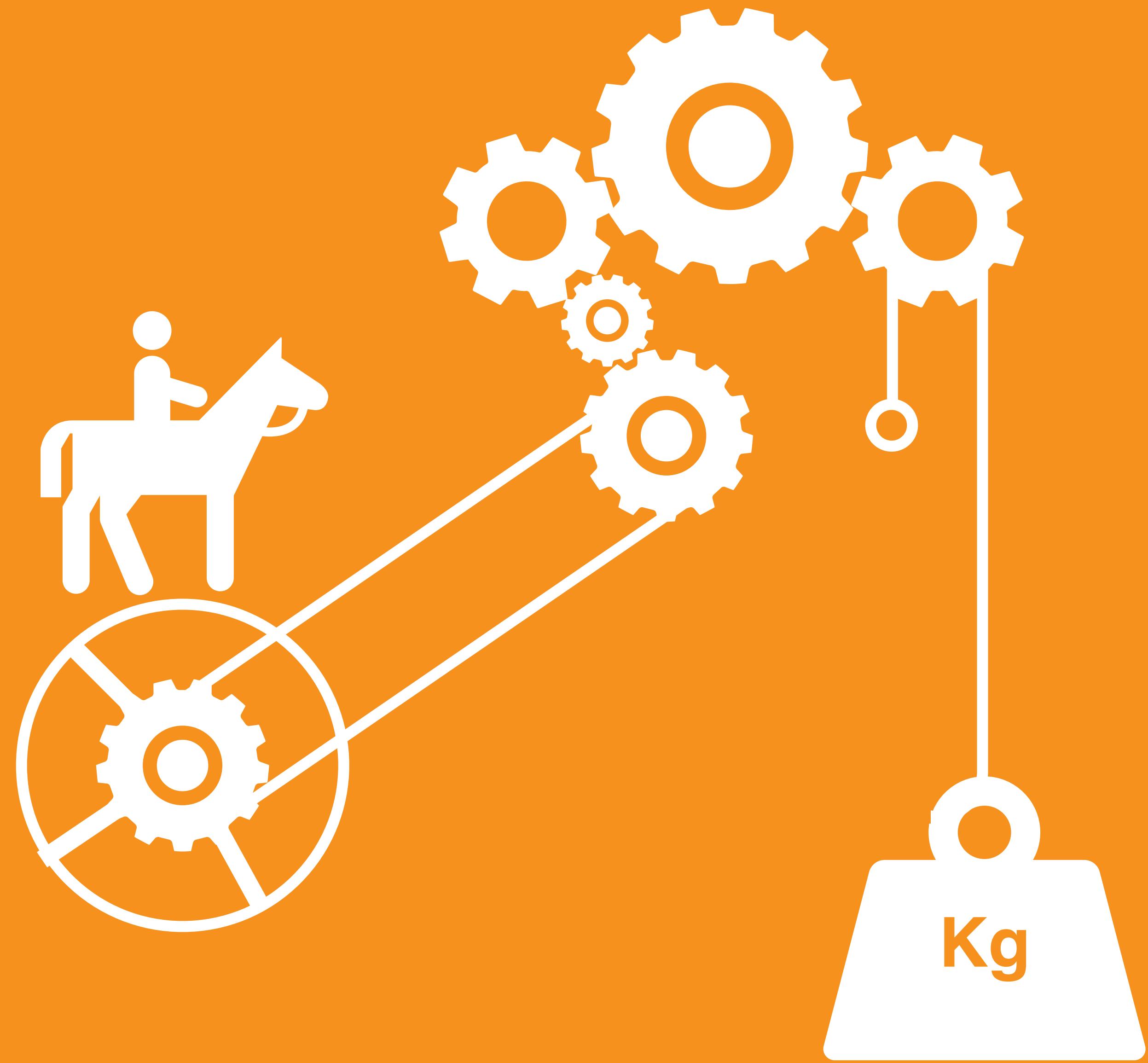
T I M I N G

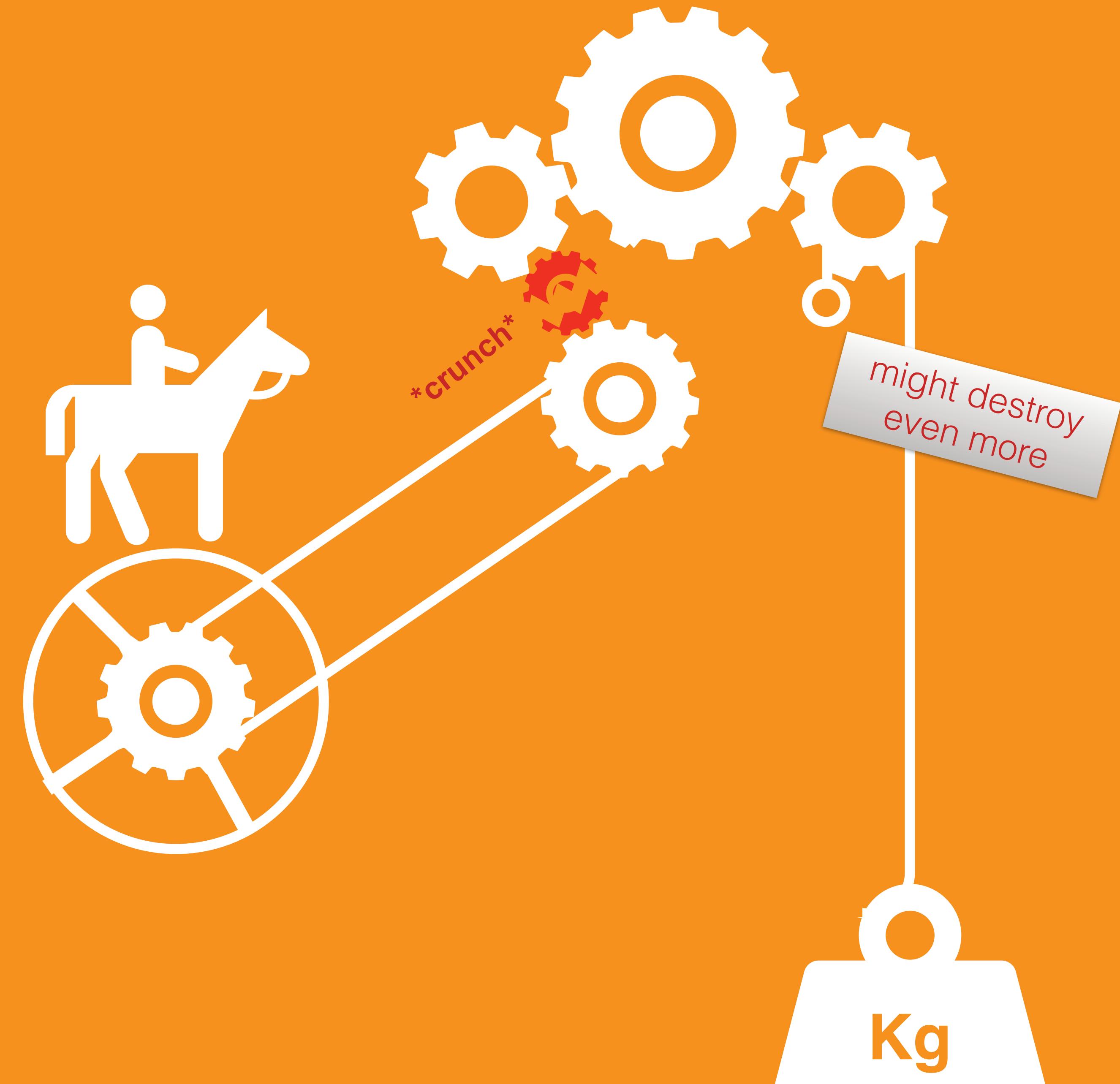


0°C

+70°C

TEMPERATURE





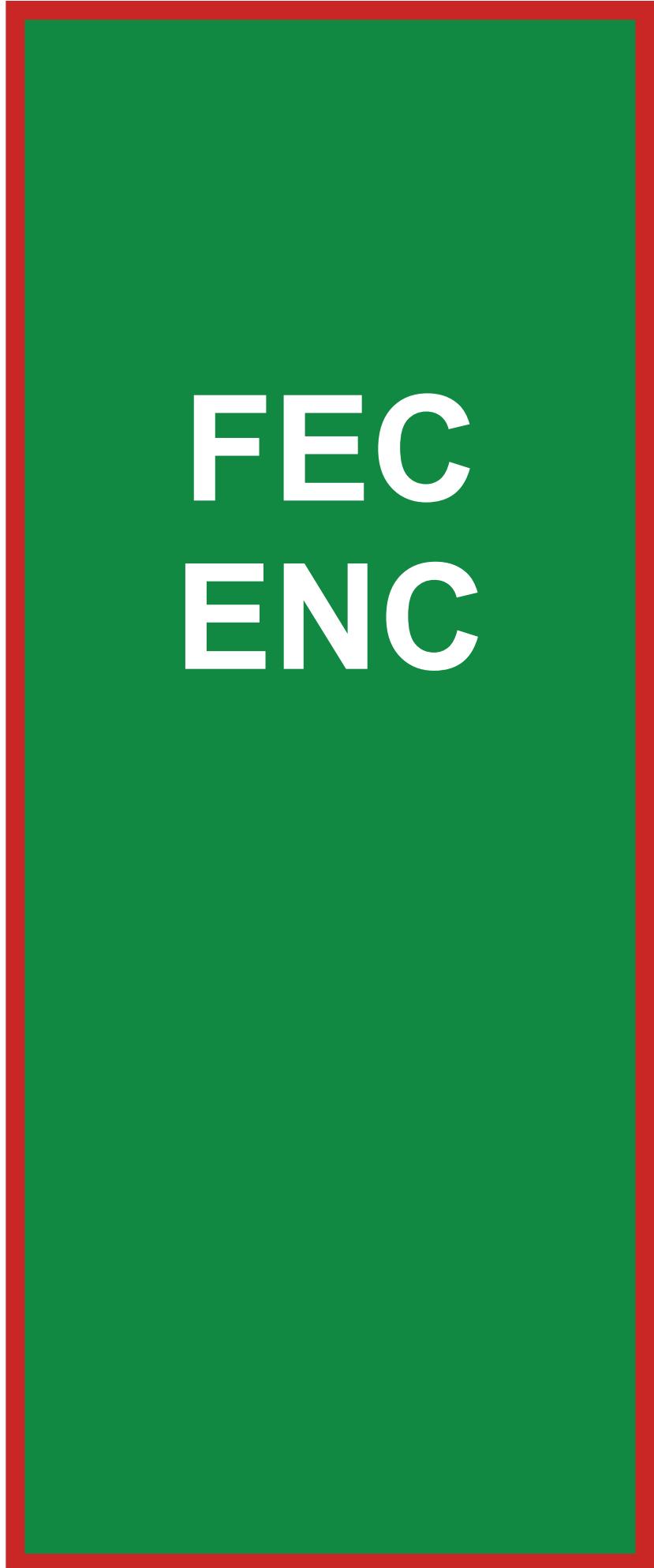
production



Forward Error Correction



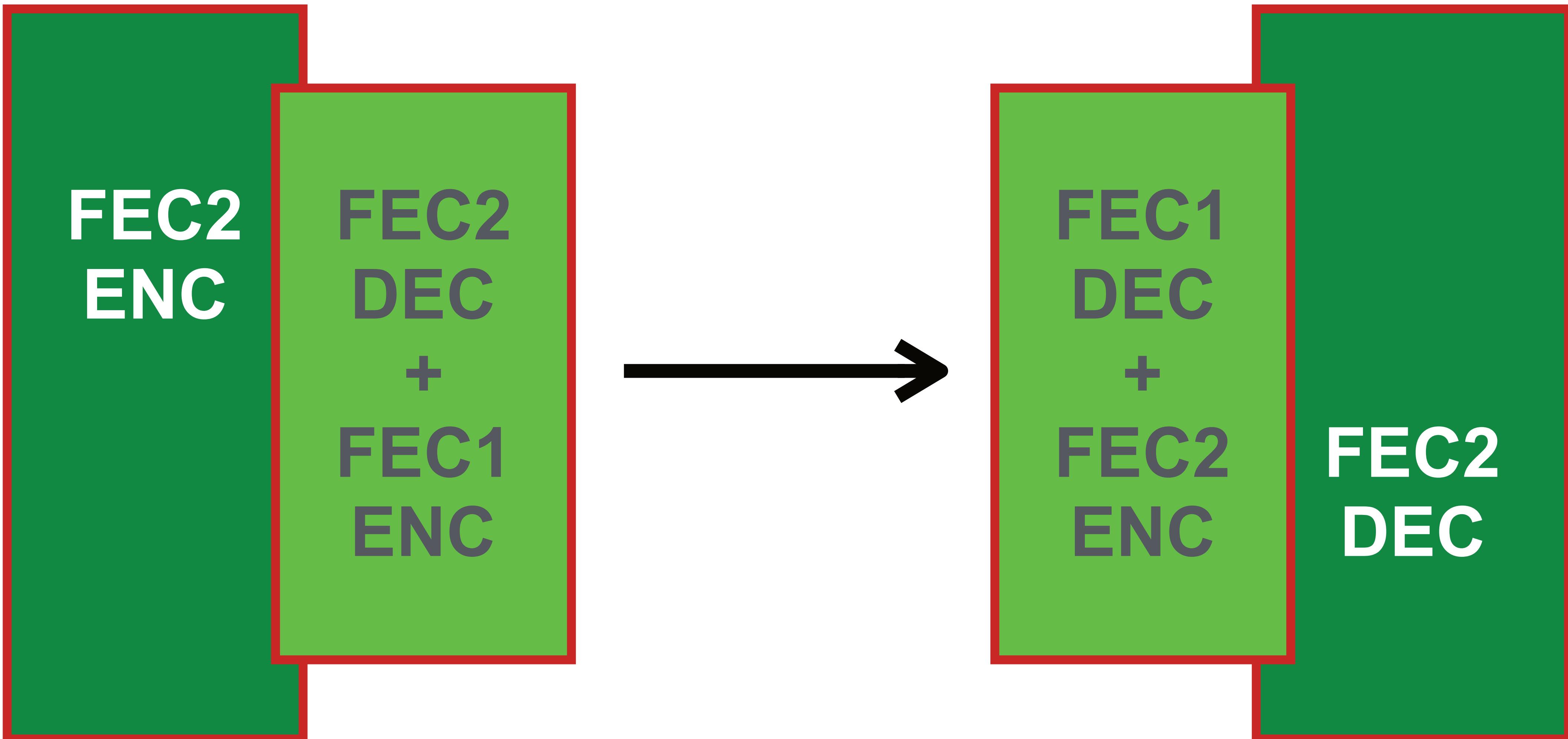
100G



Switch

Switch

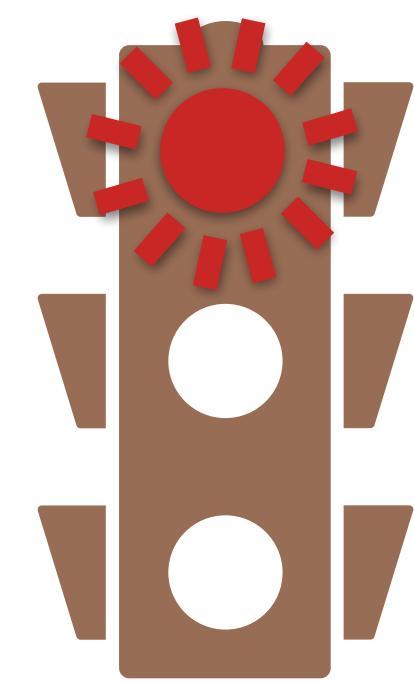




Switch
QSFP-DD
OSFP

QSFP-DD
OSFP **Switch**

400G



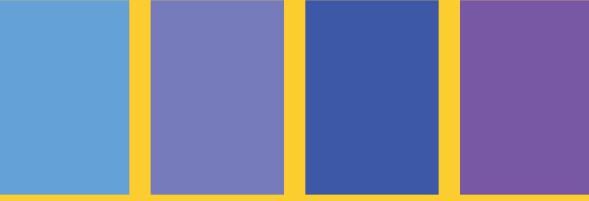
unequal # of
FEC's in line



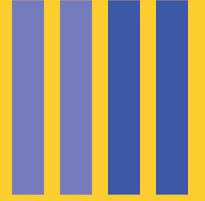
new



400G LR4 as CWDM4



or LAN-WDM



100G ER4 + ZR4



50G ER



40G DWDM LR

25G DWDM LR

A



THE FLEXBOX CREATORS

THOMAS.WEIBLE@FLEXOPTIX.NET



1
revolutionary
development



56
flexperts



48322
transceiver
compatibilities





THANK YOU

