

Question L

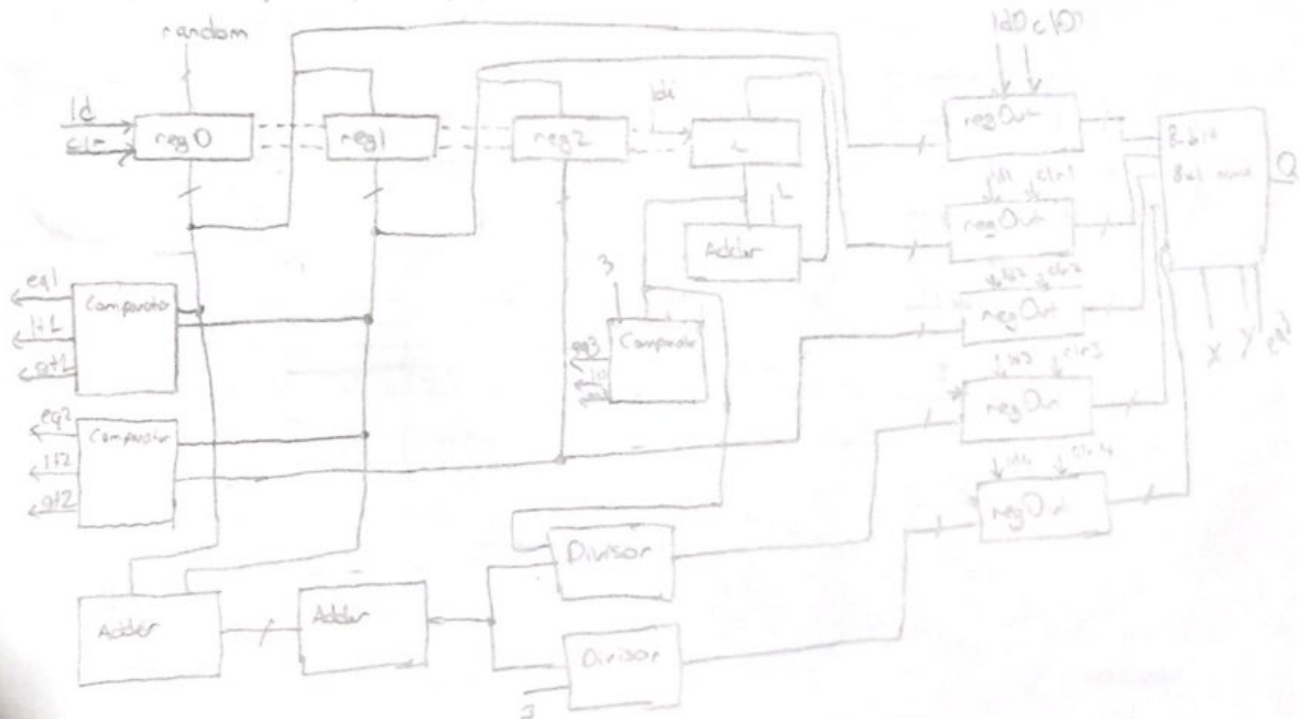
Inputs: byte random, bit X, bit Y

Outputs: byte Q

Registers: byte reg0, byte regL, byte reg2, byte regOut, bit i



+ 5 local registers, comparator, adder, divisor



Question 2

Inputs: byte $A[128]$, bit go

Outputs: byte max , byte min , byte ave , bit $done$

Local registers: byte $regMax$, $regMin$, $regAv$, bit i

