

Wireless Building Power Monitor (Hardware Design and Packaging)

Pak Lam (Jack) Yung
Mentor: Professor Thomas B. Jones
Group Member: Ka Hei (David) Fung
Date: July 27, 2012

Abstract:

The goal of this project is to develop a wireless sensor device that gathers power quality parameters for a building or residence. It then uploads the data to a cloud computer. Any portable device that has access to the cloud can then monitor the power characteristics of the circuit. Our first testing point will be in the Hopeman Engineering Building in University of Rochester.

My work focuses on hardware design, such as circuit layout, noise control, and packaging design.

Smart Grid:

Combines traditional power hardware with sensing, communication and monitoring technology to:

- Provide a systematic communication between suppliers and consumers.
- Motivate consumers to get involved in Energy management and savings.
- Improve fault detection and allow self-healing of the network.
- Make the grid highly controllable.

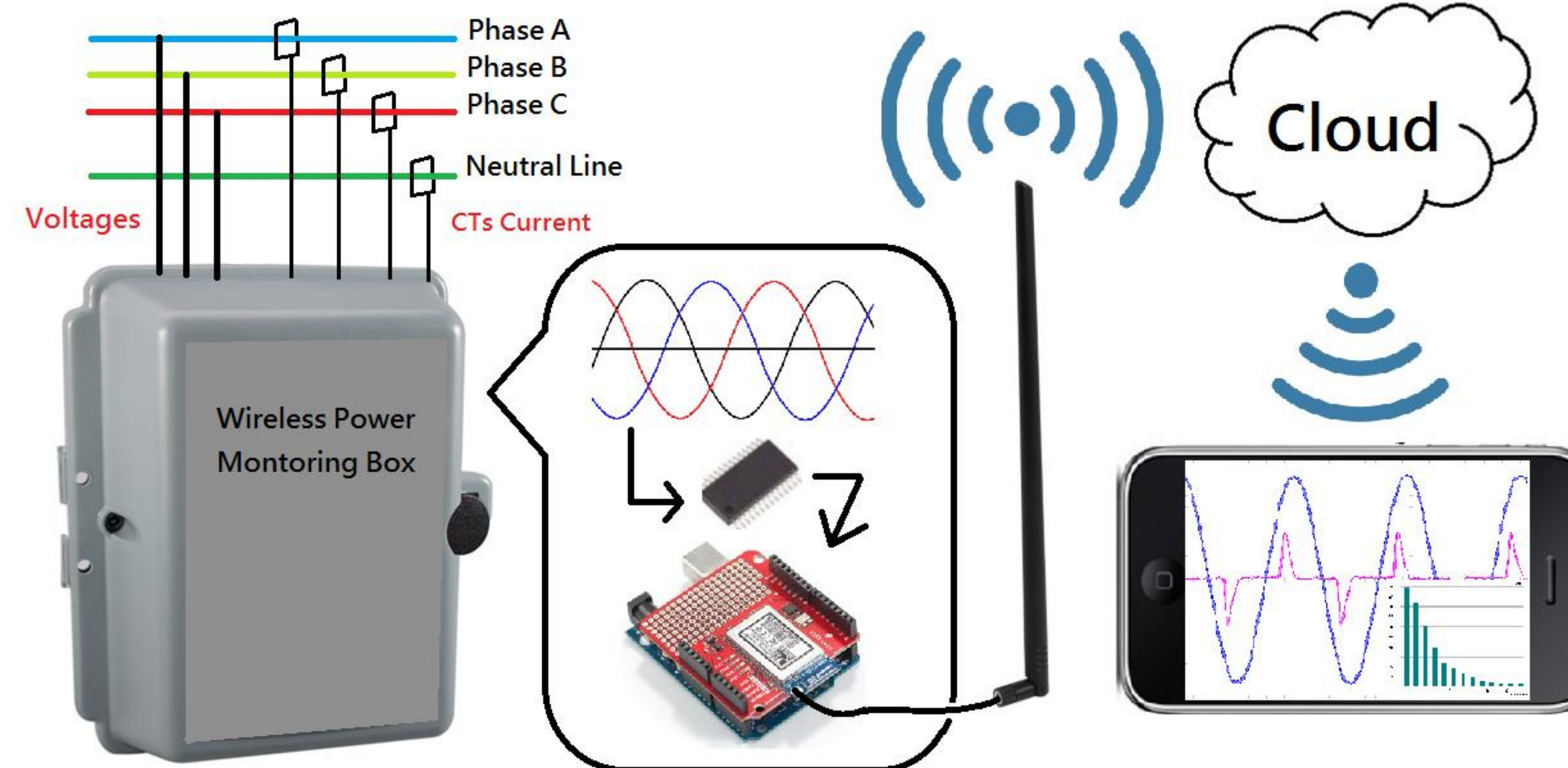
This Wireless Power Monitoring device helps consumers to adopt Demand Side Management (DSM) in order to:

- Encourage the consumer to use less energy during peak hours, or move the time of energy use to off-peak time such as nighttime and weekends.
- Average out the total city power consumption which can reduce the need for investments in networks and power plants

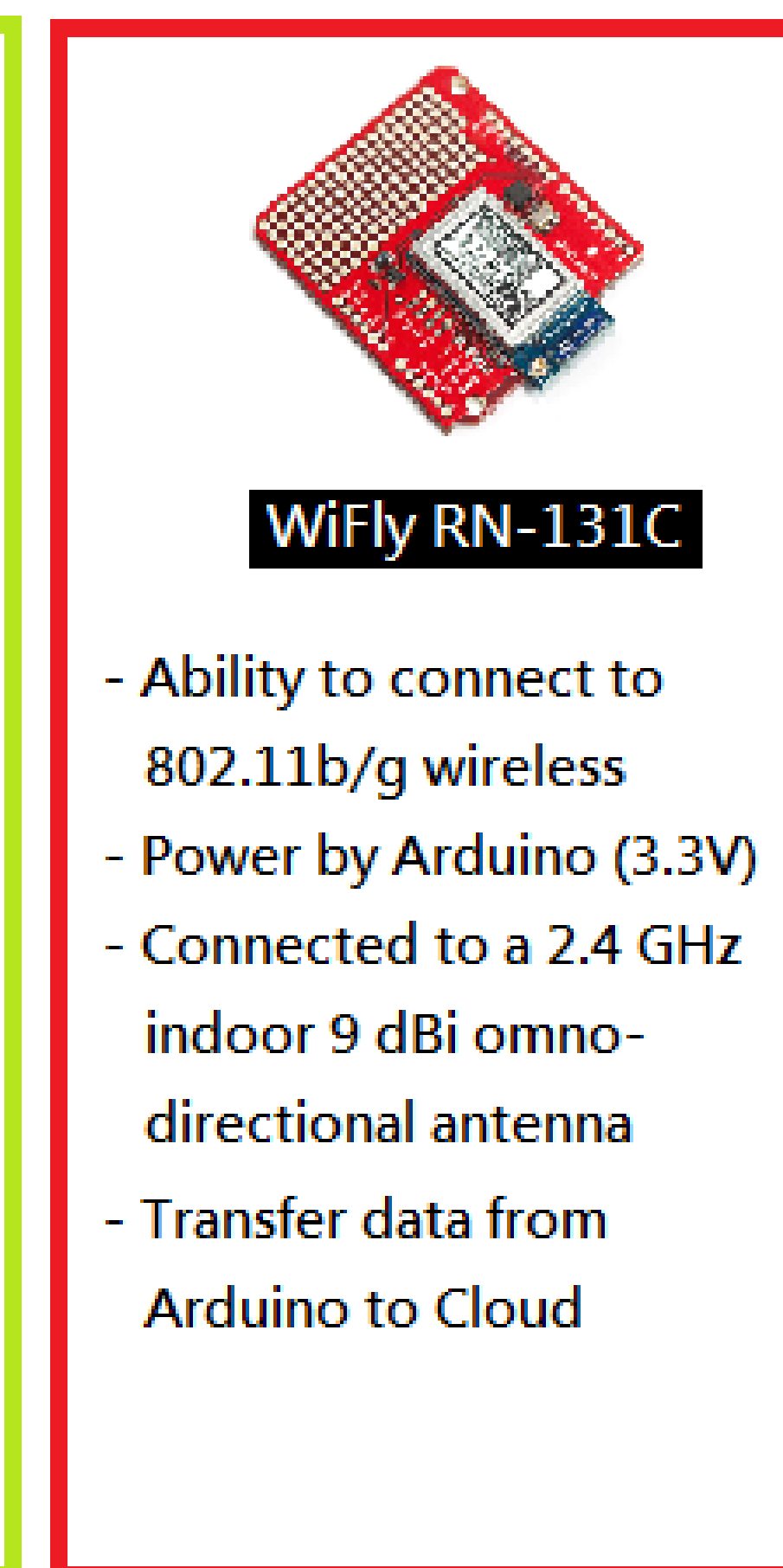
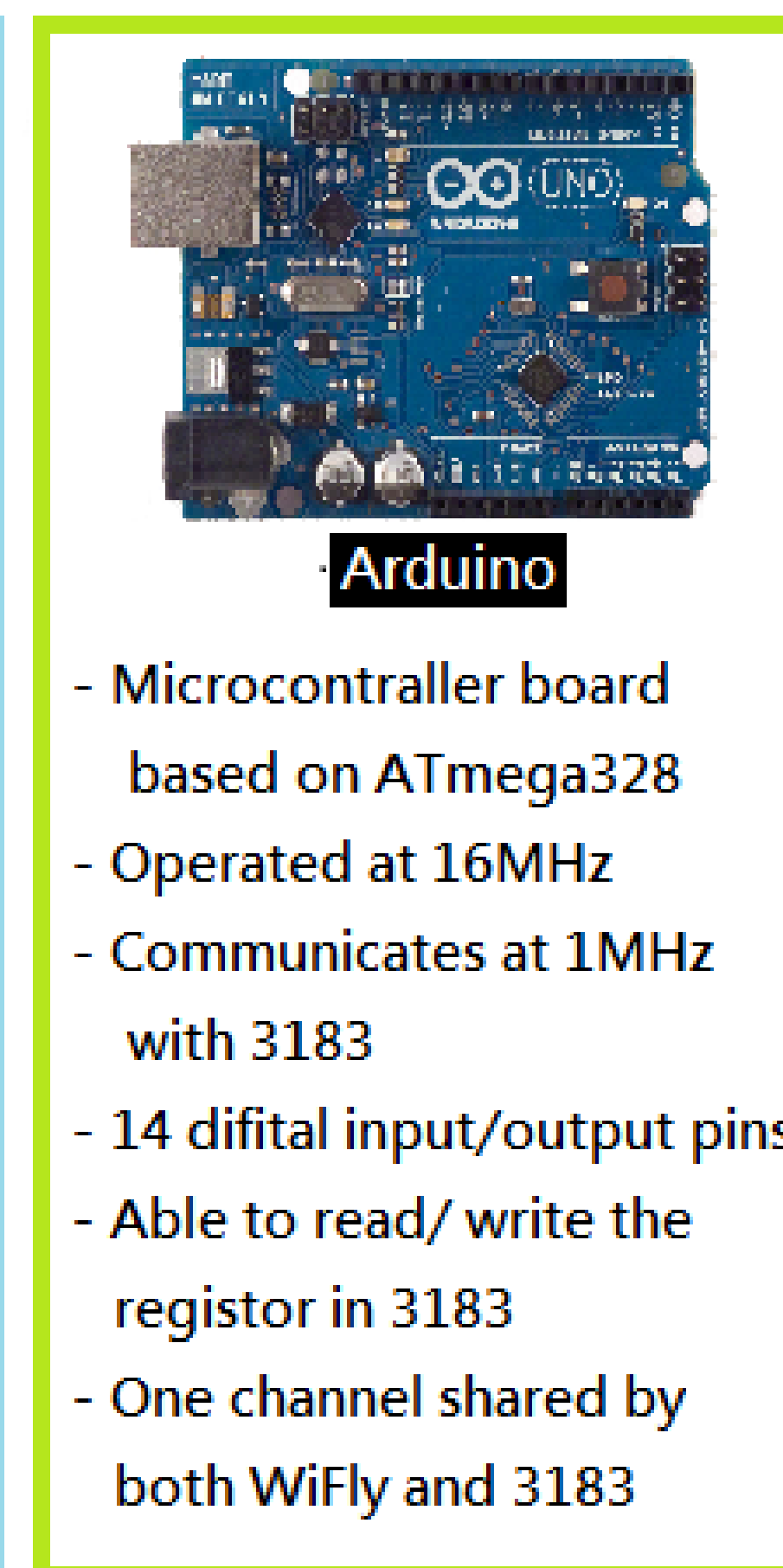
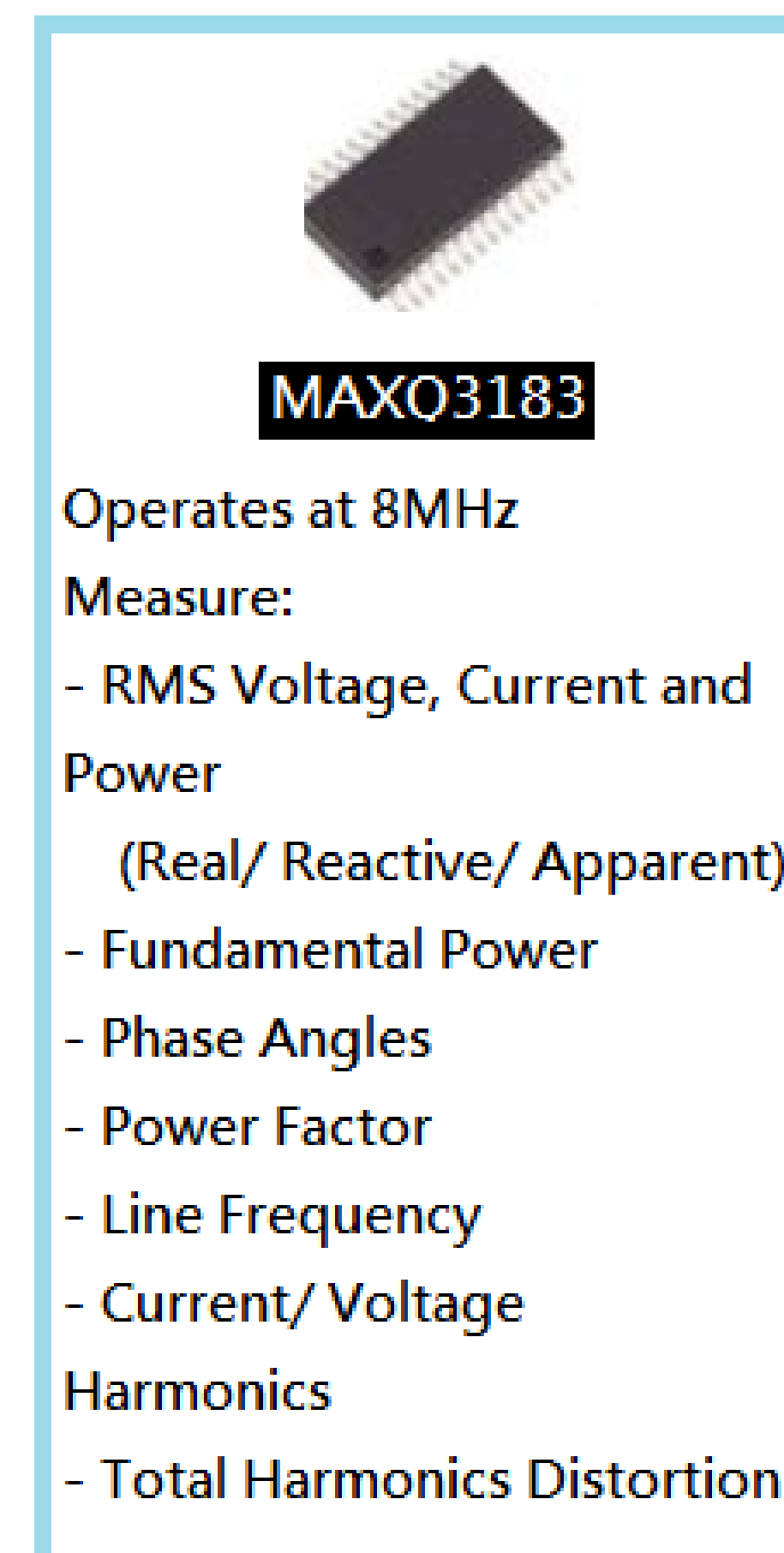
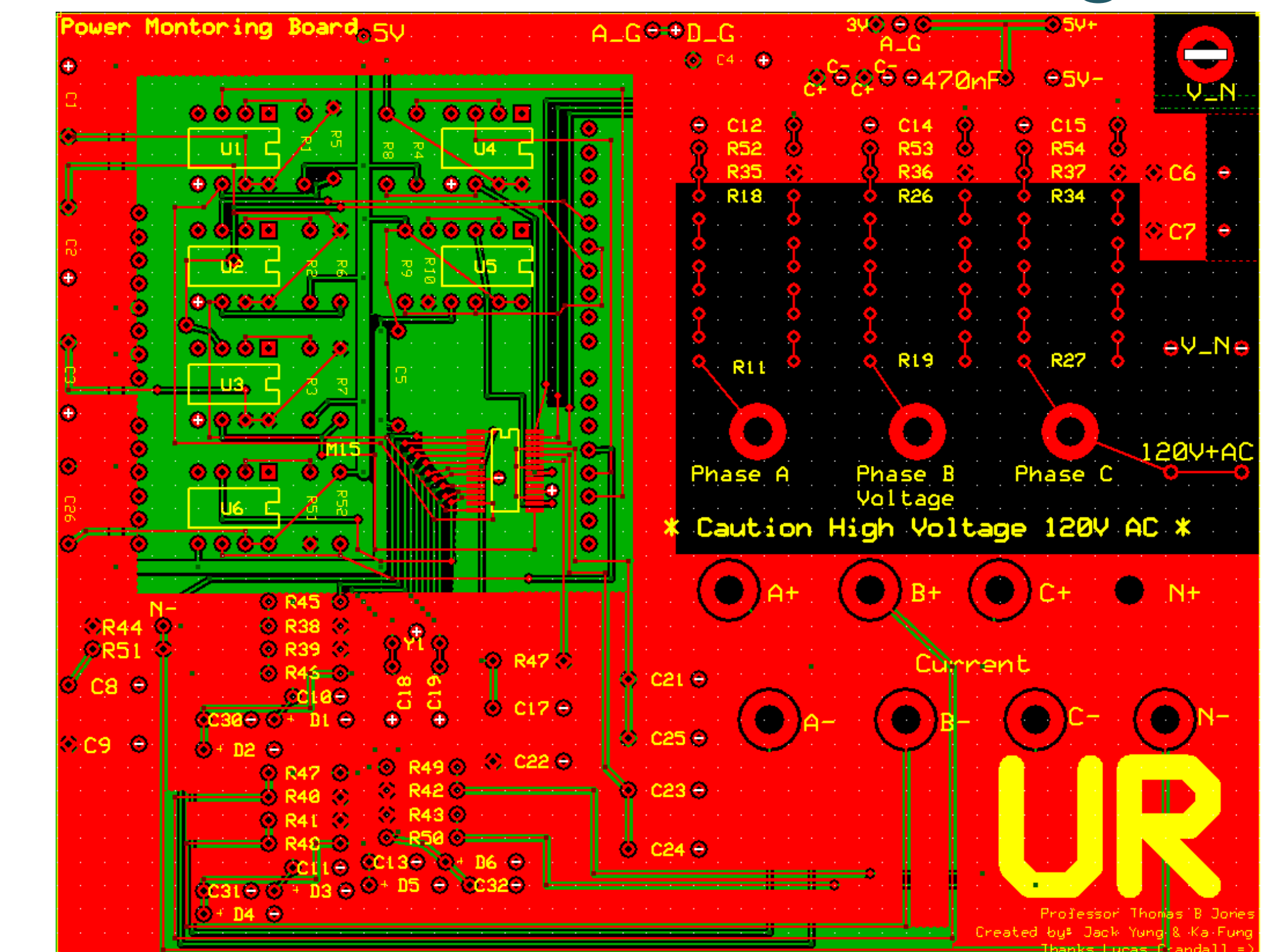
Electrical Power Harmonics:

When a linear electrical load is connected to an AC system, the voltage and current vary sinusoidally at 60Hz. If a non-linear load, such as a DC power supply (rectifier), LED light, or motor, is connected to the system, it draws currents containing harmonic components. No matter how complex the current waveform becomes, Fourier Series Analysis can decompose the waveform into a series of simple sinusoids.

System Description:



Circuit and PCB Design:



Noise Control:

To reduce the noise, there are copper shields that are connected to either one of: the 3.3V power input, neutral of the current transformer, digital ground and analog ground.

Voltage Measurement:

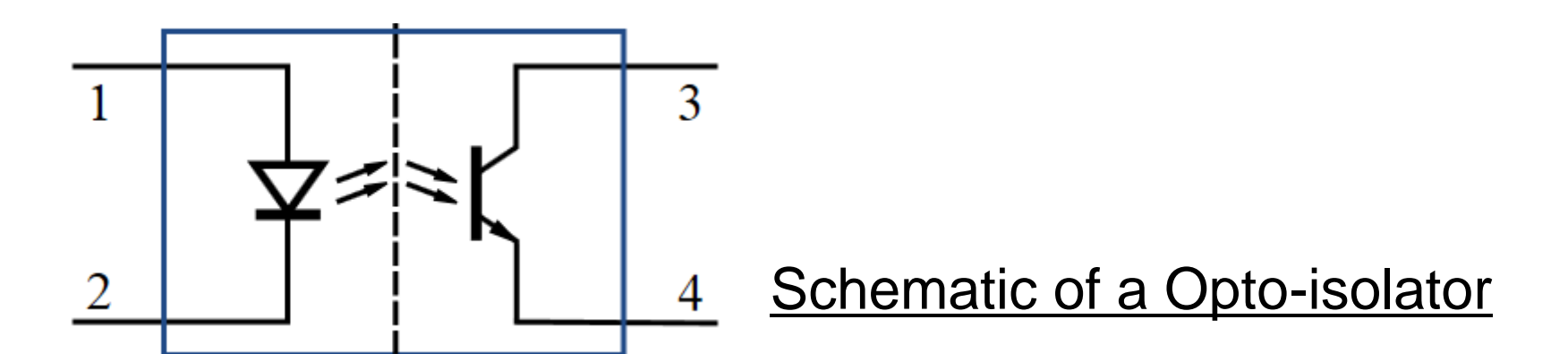
To get low voltages from the power lines to the Maxim chip, we use voltage dividers, calibrating their output voltages in the chip, so that we can get the right ratio between the output of the dividers and the voltages of the three lines.

Current Measurement:

Four 5 Amp to 0.333V current transformers are used to measure the line currents.

Opto-isolator:

Six opto-couplers has been used to isolate the low voltage part of the board from the high voltage part.



Acknowledgements

This work is supported in part by the National Energy Technology Laboratory (STEPS program) of the U.S. Department of Energy. We thank Professor Wendi Heinzelman for her assistance with the wireless and Cloud part of the design. We also acknowledge Gabriel Unger, Lucas Crandall and Sara Lickers for their contributions to this project.

Reference:
- www.wikipedia.org
- www.sparkfun.com
- www.maxim-ic.com
- www.arduino.cc

