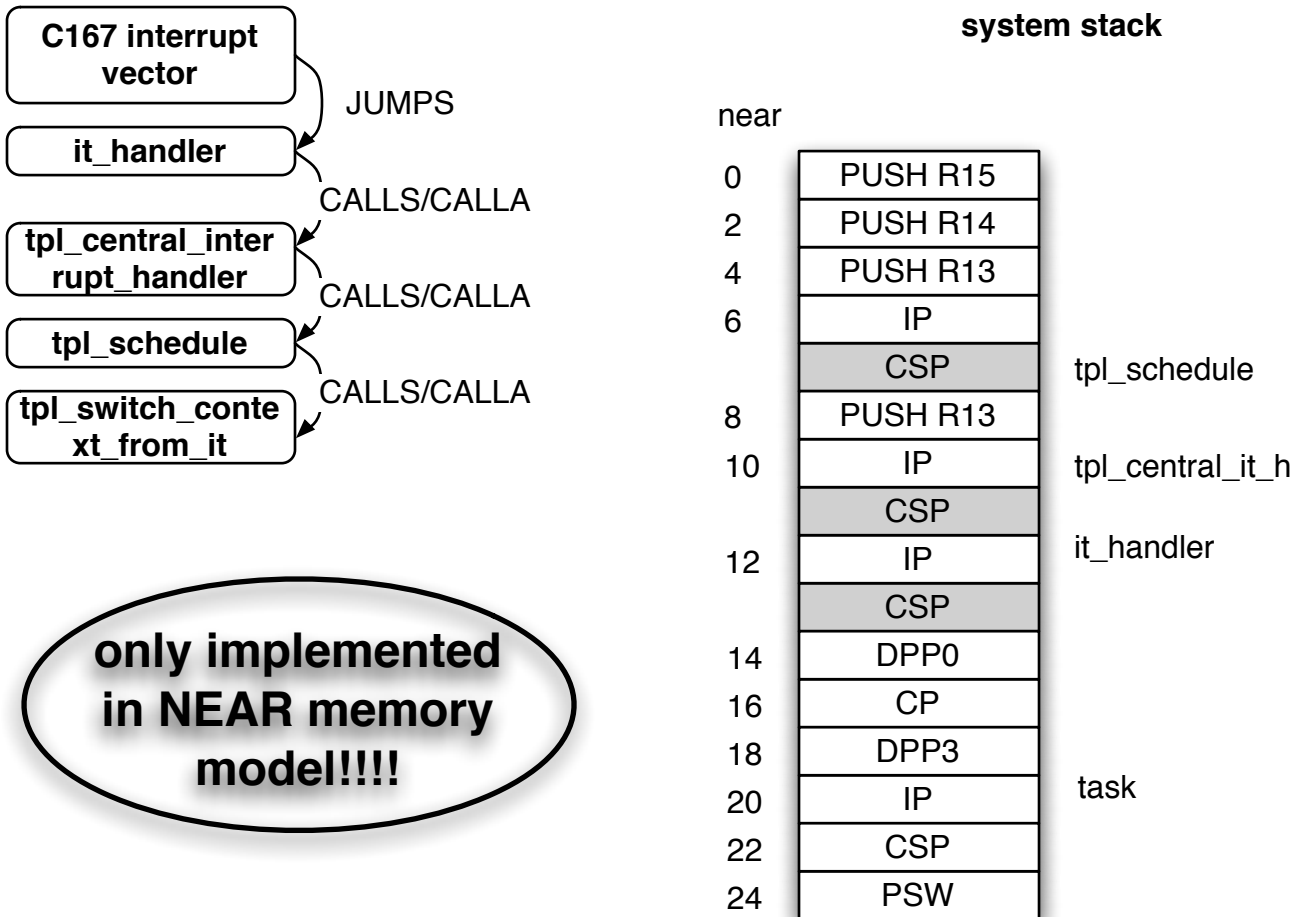# interrupt management for the C167

To process an interrupt, the CPU suspends its program execution and branches to the interrupt vector and push IP, PSW and CSP in segmented mode.

C167 interrupt vector

JUMPS

it_handler

CALLS/CALLA

tpl_central_inter rupt_handler

CALLS/CALLA

tpl_schedule

CALLS/CALLA

tpl_switch_conte xt_from_it

only implemented in NEAR memory model!!!!

system stack

near

0	PUSH R15	
2	PUSH R14	
4	PUSH R13	
6	IP	
	CSP	tpl_schedule
8	PUSH R13	
10	IP	tpl_central_it_h
	CSP	
12	IP	it_handler
	CSP	
14	DPP0	
16	CP	
18	DPP3	
20	IP	task
22	CSP	
24	PSW	

only if FAR mode (32 bit pointers)
always on the stack