

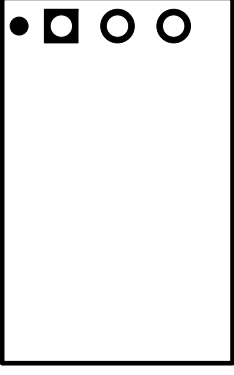
NC 0V ADC7 ICP3 NC NC ADC3 16 15 14 J4

VIN 0V 0V 5V NC nRST IOREF NC

9\_ALT 0V PWR 7.36V J2

SPI  
0V RESET  
MOSI SCK  
IOREF MISO J10

SHIELD VIN IS  
CNTRLD BY D2 (IO2)



R10 OPEN  
JMP R8

D2  
J11 I2C  
SCL SDA  
5V

DTR OR RTS  
HOST\_RX < TX  
HOST\_TX > RX  
+5V  
NC BOOTLOAD  
MISO

J12

ICP1 LOW IF > 8mA ON PL1  
ICP1 HIGH IF < 5mA ON PL1  
PL3 SAME FOR ICP3  
PL4 SAME FOR ICP4

J13 22mA ADC0 0V ADC1 22mA ANALOG 0V ADC2 22mA  
5\_EN 14 ICP4 23 RX TX 2 3 4 5 6 7  
6\_EN 16 7\_EN

J1 ICP1 9 SS INPUT CAPTURE 0V NC SDA SCL J9  
3\_EN PL1 4\_EN PL3 23\_EN PL4

J6 0V 5V  
RPUBUS.ORG  
17341^0  
RPUicp