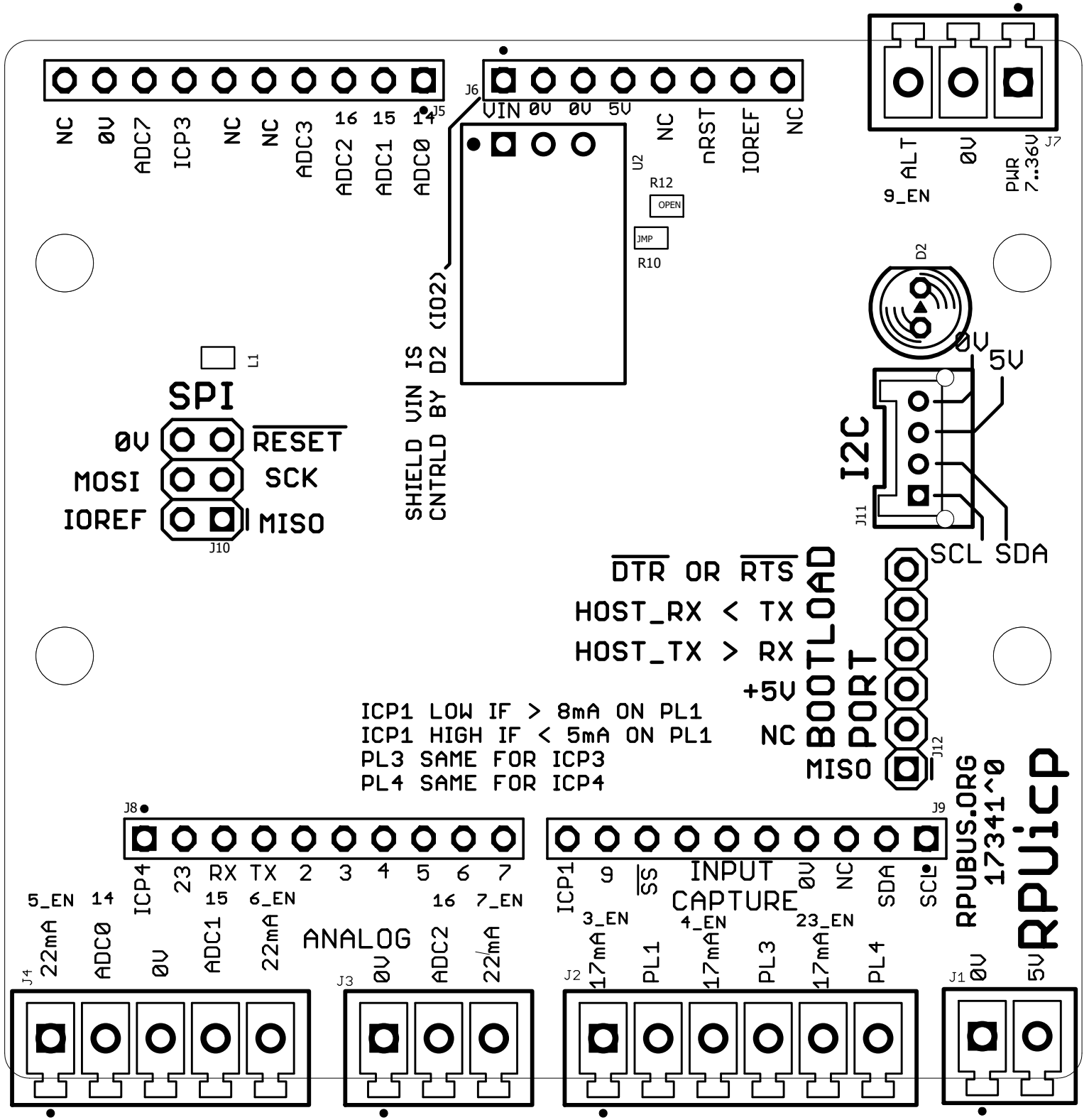
NC
0V
ADC7
ICP3
NC
NC
ADC3
16
ADC2
15
ADC1
14
ADC0
J5
J6
VIN
0V
0V
5V
NC
nRST
IOREF
NC
U2
R12
OPEN
JMP
R10
SHIELD VIN IS
CNTRLD BY D2 (IO2)
J7
ALT
9_EN
0V
PWR
7..36V
D2
L1
SPI
0V
RESET
MOSI
SCK
IOREF
MISO
J10
0V
5V
I2C
J11
SCL SDA
DTR OR RTS
HOST_RX < TX
HOST_TX > RX
+5V
NC
MISO
BOOTLOAD
PORT
J12
ICP1 LOW IF > 8mA ON PL1
ICP1 HIGH IF < 5mA ON PL1
PL3 SAME FOR ICP3
PL4 SAME FOR ICP4
J8
ICP4
23
RX
TX
2
3
4
5
6
7
5_EN
14
15
6_EN
16
7_EN
J9
ICP1
9
SS
INPUT
CAPTURE
0V
NC
SDA
SCL
3_EN
4_EN
23_EN
RPUBUS.ORG
17341^0
RPUicp
J4
22mA
ADC0
0V
ADC1
22mA
ANALOG
J3
0V
ADC2
22mA
J2
17mA
PL1
17mA
PL3
17mA
PL4
J1
0V
5V