

INTRODUCTION

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Specifications for Phase 2

- Phase 2 is still ~ 10 years away
- But the TDR is quite near (~ 2017)
- And some important design decisions need to be taken by then
- *We need to guide the designs by providing specifications or proposals that are driven by the physics we want to do at Phase 2*
- We need time for discussions, to converge on these aspects

What we want to help deciding

- Dynamic ranges
- Linearities
- Noise at different points in the chain
- ADC size
- ADC sampling rate

Twiki

- I will put as much information as possible on a Twiki page
- that will be kept updated
- <https://twiki.cern.ch/twiki/bin/viewauth/AtlasProtected/LArPhaseIIHardwareReq>

Schedule

- Goal is to have ~ 1 meeting per month
- With ample time for discussions
- Next meeting will be during LAr week in September (not enough time for discussions in Phase2 electronics general meeting)
- Exact slot still in discussion
- Main option is Monday, beginning of afternoon

Agenda

- Sophie on EM dynamic range and linearity
- Evgeny on HEC
- Yum on digital signal processing
- Time to go through the Twiki and discuss some open points