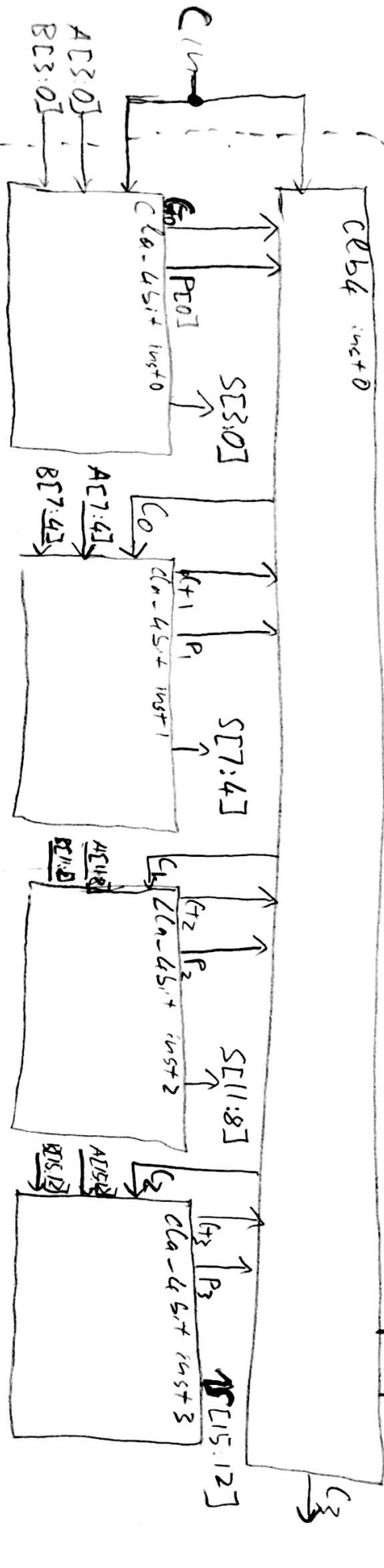
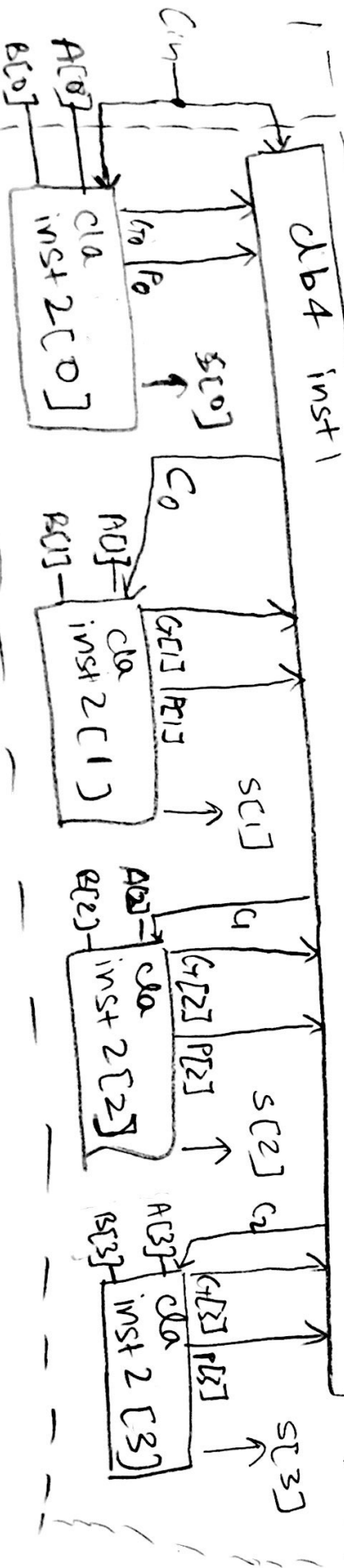


CEA-16 bit

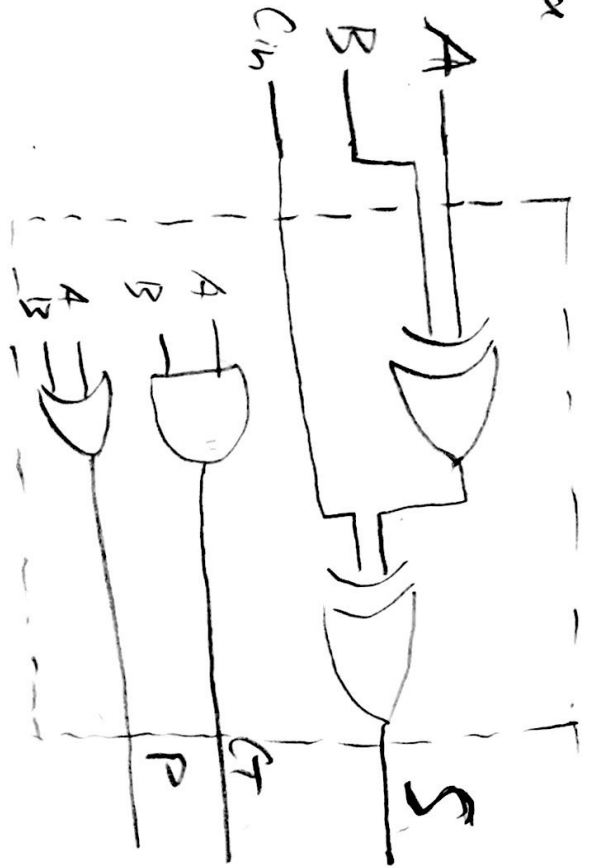
~~A[15:0]~~
~~B[15:0]~~



CEA-4 bit



clea



celb4

