

fifo-counter.v

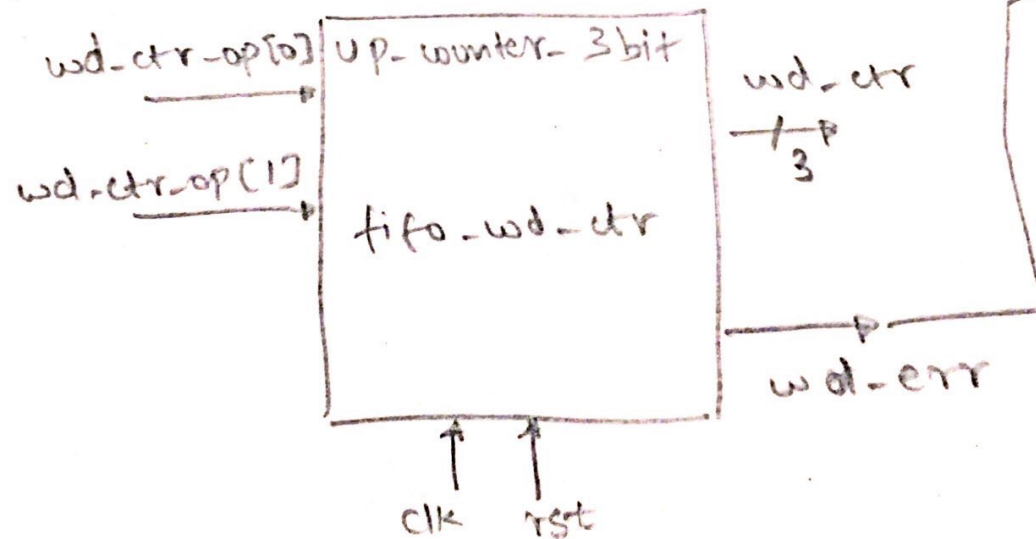
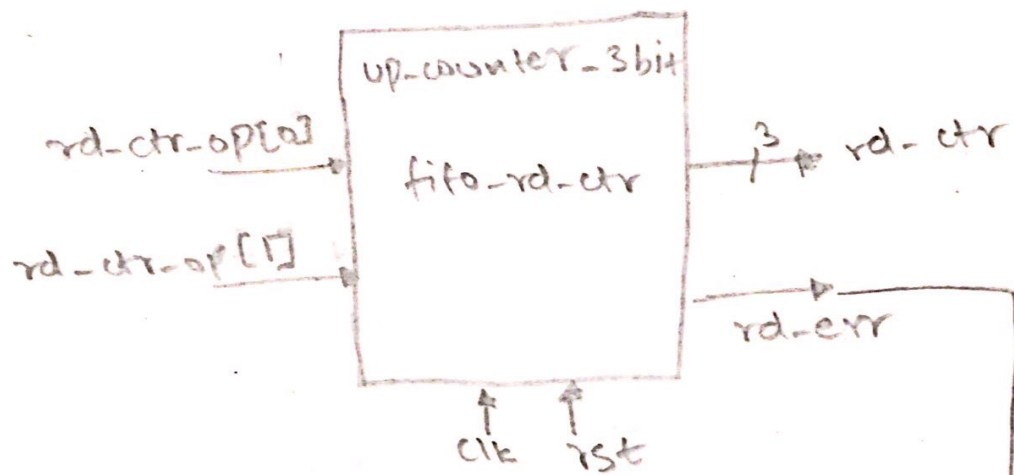
INPUTS

rd-ctr-op
1
2

wd-ctr-op
1
2

clk
→

rst
→



OUTPUTS

3
1
rd-ctr

3
1
wd-ctr

err