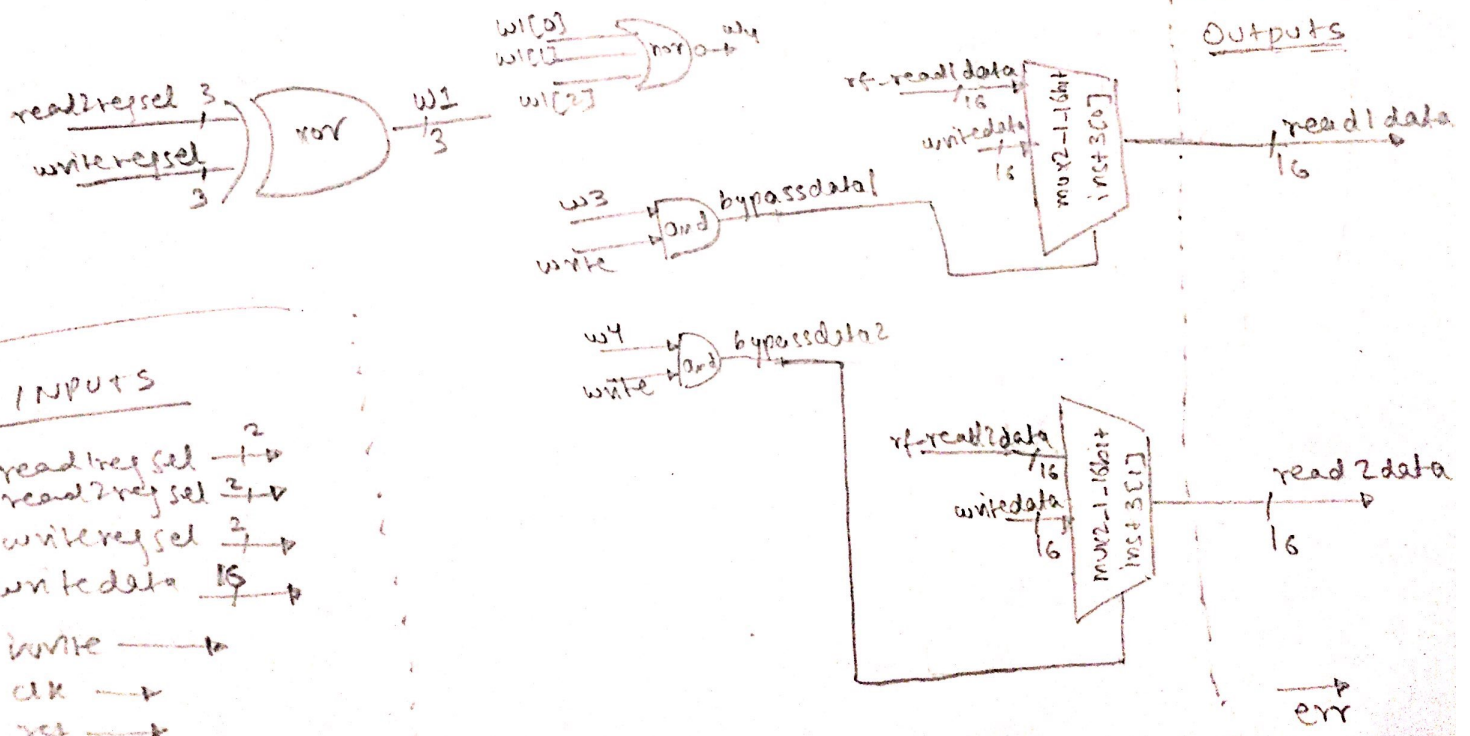
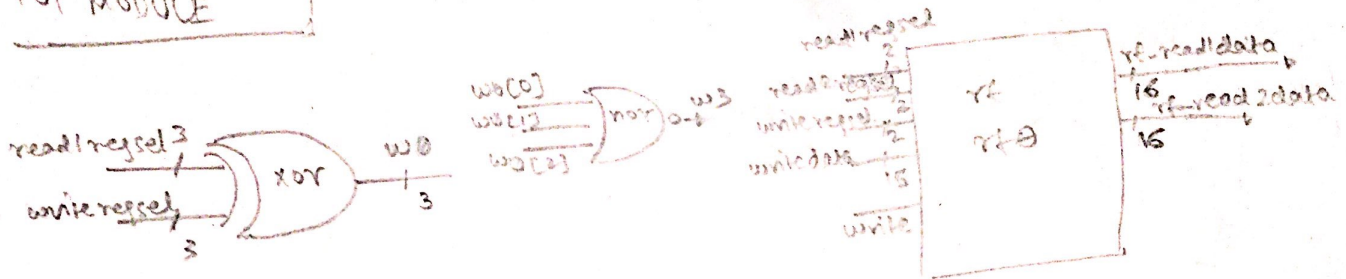


rf-by pass.v

TOP MODULE



INPUTS

read1regsel 2
 read2regsel 2
 write1regsel 2
 write2data 16
 write
 clk
 rst

Outputs

err