

TOP MODULE

fifo.v

INPUTS

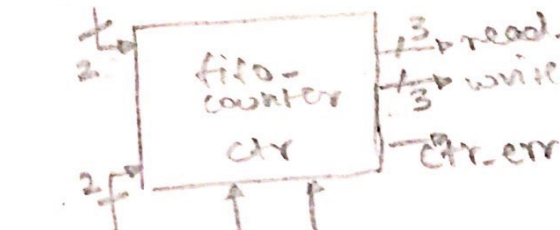
64 data-in
data-in-valid

pop-fifo

clk

rst

{16'b 0, inc-read-ctr}

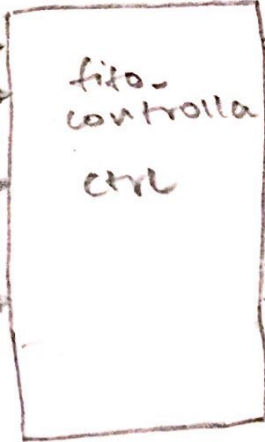


{16'b 0, inc-write-ctr}

3

data-in-valid

pop-fifo



inc-read-ctr

inc-write-ctr

read-ptr

write-ptr

writeEn

readEn

fifo-empty

fifo-full

OUTPUTS

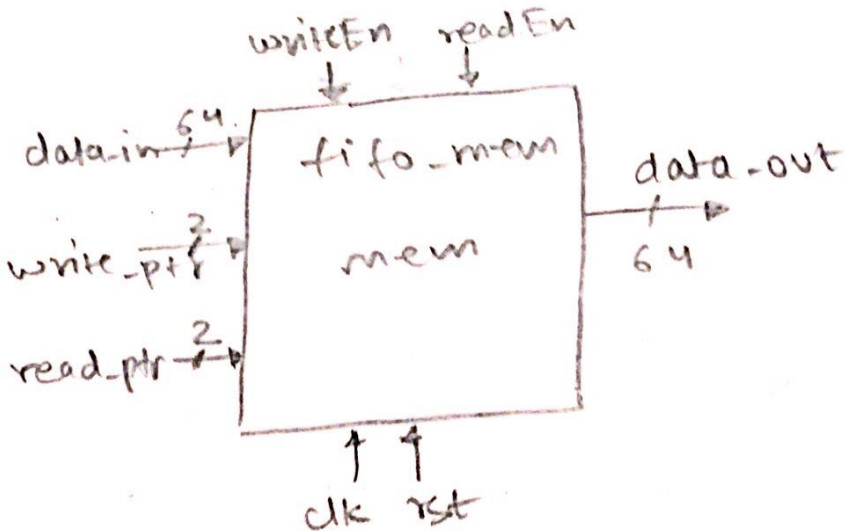
data-out
64

fifo-empty

fifo-full

err

err = ctr-err



data-in

write-ptr

read-ptr

clk rst

data-out

64