```
THEOREM, Spec, =>, DeadlockFree
<1>,DEFINE,TO,==,Trying(0)
UUUUUUUUUTT1U==UTrying(1)
LULULULULUSuccess_==UInCS(0)U\/UInCS(1)
<1>2. USUFFICES ASSUME [] LInv [] [Next] vars // Fairness
DeadlockFree Proventing the Proventi
\verb"uuuu" (* \verb"uBy" 1" and \verb"uthe" definition" of \verb"uSpec." uuuuuuuuuuuuuuuuuuuuuu*)
<1>3. SUFFICES ASSUME [] ~Success
LILILILILILILI PROVELLI (TOL\/LT1)L~>LFALSELLILILI LILILI PROVELLI (TOL\/LT1)L~
uu(*uThisuisuaustandardutemporaluproofubyucontradiction,usinceuuuuuuuuu)*)
uu(*uDeadlockFreeuequalsu(T0u\/uT1)u~>uSuccess.uuuuuuuuuuuuuuuuu+)
<1>4...T0..~>..FALSE
____(*_Assumption_[]LInv_implies_process_0_is_never_at_e3_or_e4.____*)
ULULI (*LTherefore, Lby Lthe Lcode Land Lassumption Fairness, Lwe Lsee Lthat Lif LTO LLUL*)
ulul (*LisutrueLanduprocess_0_never_reaches_cs_(which_is_implied_by_the_llul *)
UUUUU<3>1. USUFFICESUASSUMEU[](pc[0]U=U"e2")
____PROVE__TRUE_~>_ [] ~x [1] ______PROVE__TRUE_~>_ [] ~x [1] ______PROOF
____<3>2._TRUE_~>_([](pc[1]_-u"ncs")_\/_[]T1)____u____PROOF
ULULLILI (*LTheLcodeLandLassumptionLFairnessLimplyLthatLifLprocessL1LneverLLLL)*)
\square (*\squarereaches\squarecs\square(by\squarethe\squareassumption\square[] ~Success), \squarethen\squareeventually\squareit\square
unununu(*umustueitherureachuanduremainuforeveruatuncs,uoruT1umustubecomeuuuu)*)
____(*_true_and_remain_true_forever.______(********************)_OMITTED
PROOF عنان (pc[1] ان (pc[1] اi (pc[1] اi (pc[1] (pc
UUUUUU(*u[]LInvuimpliesux[1]uequalsuFALSEuwhenuprocessu1uisuatuncs.uuuuuuu*)
____<3>4.__[]T1_~>_[]~x[1]____PR00F
(*_{	t lequal}_{	t t} to_{	t lequal}_{	t lequal})
```

```
____<3>5._QED______PR00F
\verb| uuuuuu| (*_{\mathsf{U}} By_{\mathsf{U}} < 3 > 1 - (3 > 4_{\mathsf{U}} and_{\mathsf{U}} Leads - To_{\mathsf{U}} Induction. \verb| uuuuuuuuuuuuuuuuuuuu*)
______PR00F
____(*_process_0_reaching_cs,_contradicting_[]~Success._____*)
___<2>4._QED_____PROOF
(*_{\sqcup}BY_{\sqcup}<2>1_{\sqcup}-_{\sqcup}<2>3_{\sqcup}and_{\sqcup}Leads-To_{\sqcup}Induction.______uu_uu_uuuuuuuuuuuuuu*)
<1>5.,T1,,~>,,FALSE
PROOF را - حال T1 و - حال T1 و - 2>1 و - 2>1
\verb""" (*_{\tt U} \texttt{remains}_{\tt U} \texttt{forever}_{\tt U} \texttt{true}. \verb""" (*_{\tt U} \texttt{remains}_{\tt U} \texttt{forever}_{\tt U} \texttt{true}. \verb""")
\square < 2 > 2. \square [] T1_{\square \square}^{\sim} >_{\square \square} (T0_{\square} \setminus /_{\square} [] (T1_{\square} / \setminus_{\square}^{\sim} T0))
\verb| u_{\cup\cup\cup\cup}(*_{\cup}By_{\cup}the_{\cup}tautologies_{\cup}F_{\cup}^{~~}>_{\cup}(G_{\cup}\setminus\setminus_{\cup}(F_{\cup}/\setminus_{\cup}[]^{~~}G))_{\cup}and_{\cup\cup\cup\cup\cup\cup\cup\cup\cup\cup\cup\cup\cup\cup\cup\cup\cup\cup\cup\cup\cup}*)
(*_{\cup}[]F_{\cup}/_{\cup}[]G_{\cup}<=>_{\cup}[](F_{\cup}/_{\cup}G).
_____PROOF
LILILI (*LBYLTheLcodeLandLFairness, [] ~TOLimpliesLthatLeventuallyLprocessLOLILIL*)
\verb|uu|uu|(*uisualwaysuatuncs,uwhichuimpliesuthatux[0]ualwaysuequalsuFALSE.uuuuuu*)|
UUUU (*ureachesue2.uuAssumptionuFairnessuandu[]~x[0]uthenuimplyuthatuuuuuu*)
<1>6. QED_____PRO0F
___(*_By_<1>3_-_<1>5_and_Leads-To_Induction._____*)
```

CLOSE