

说明：

1、实线框表示已贴器件，虚线框表示未贴的可选器件；

2、实线"\_\_\_\_"表示PCB连线，虚线"-----"表示其它连线方式（如线缆，FPC等）。

3、主板与PHY板默认MAC接口为RMII，MII接口为预留。

A

B

C

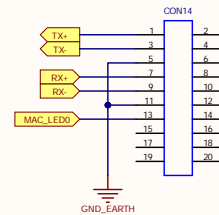
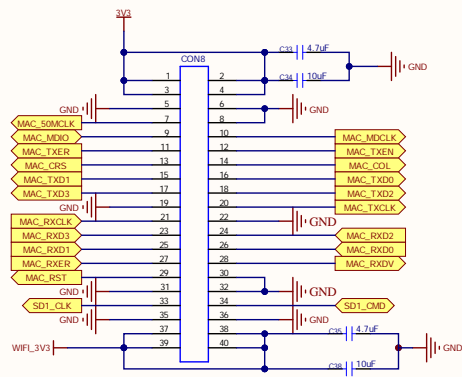
D

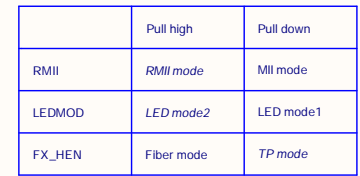
A

B

C

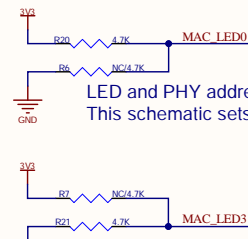
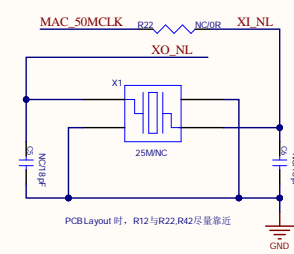
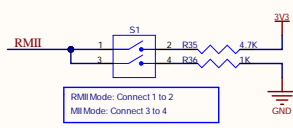
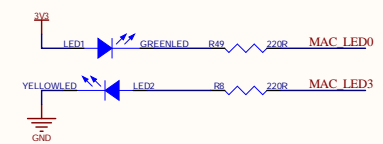
D





PS: *italic* is default

LEDMode	1	2
LED1	Link	Link /ACT
LED2	100M Link /ACT	100M Link



LED and PHY address Configuration  
This schematic sets PHY address to 00001b.

1. Do not let these PHY address pins floating for the latched-in settings after the power is ready.