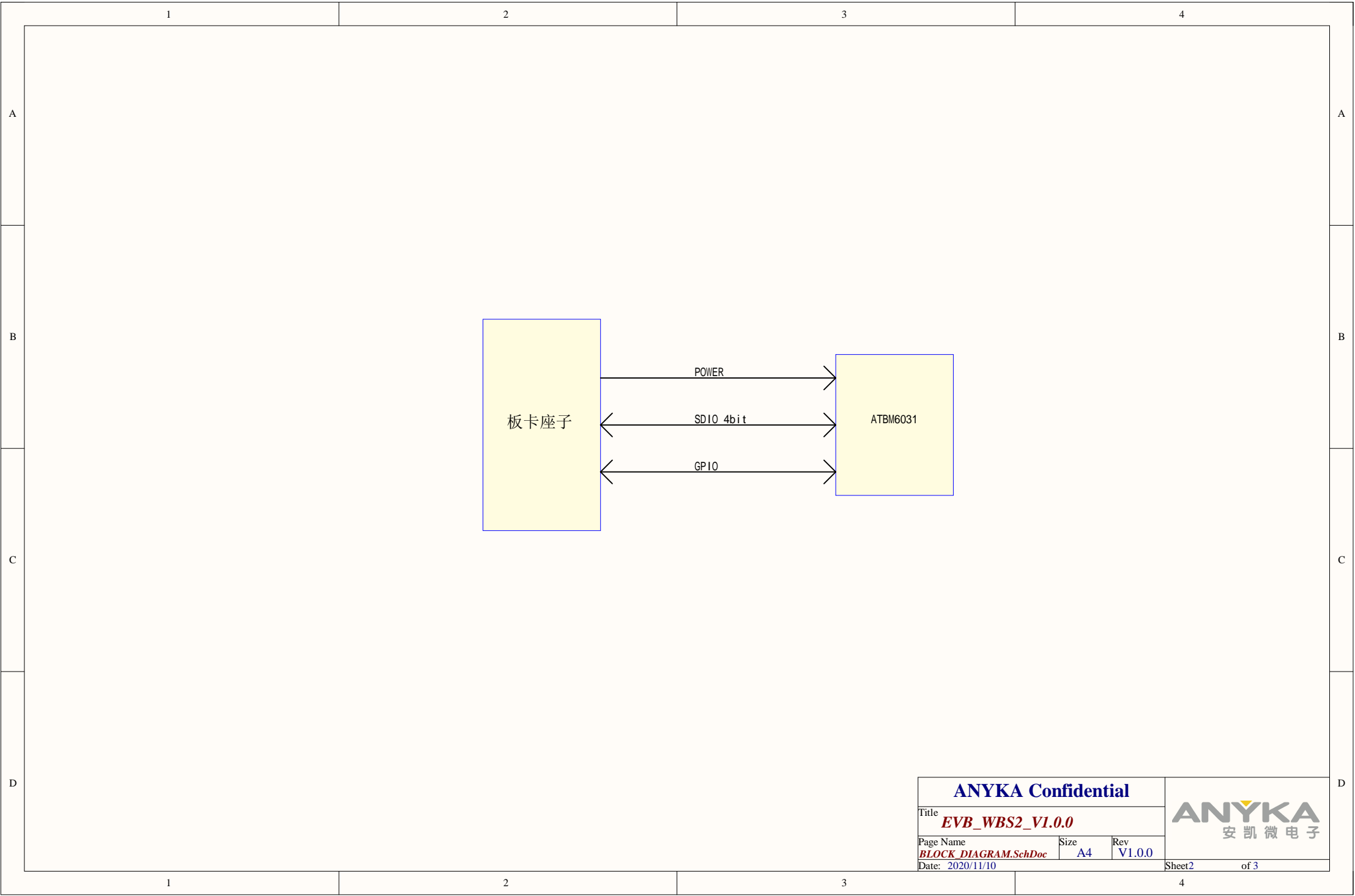
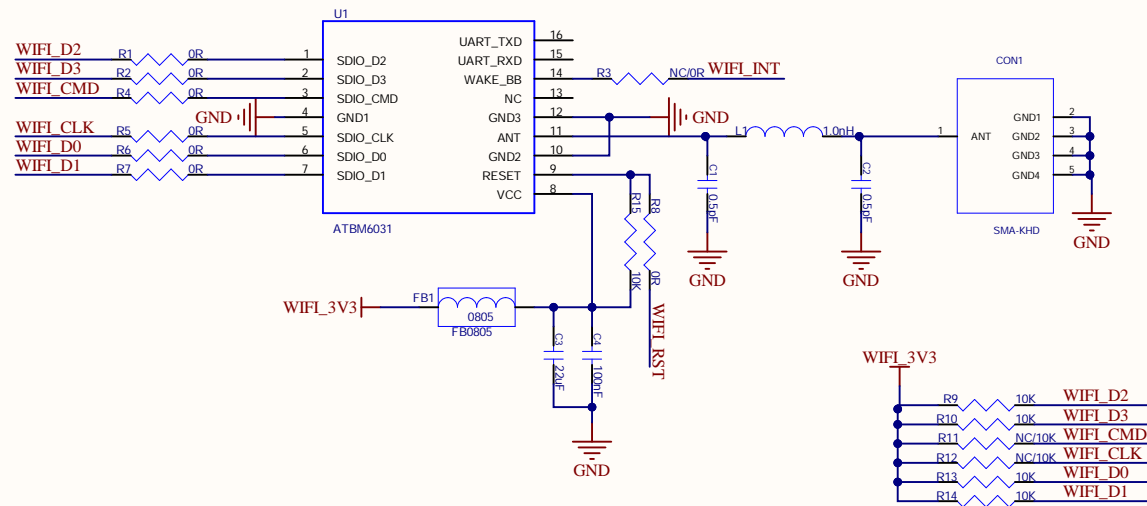


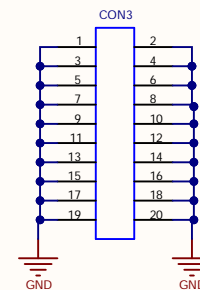
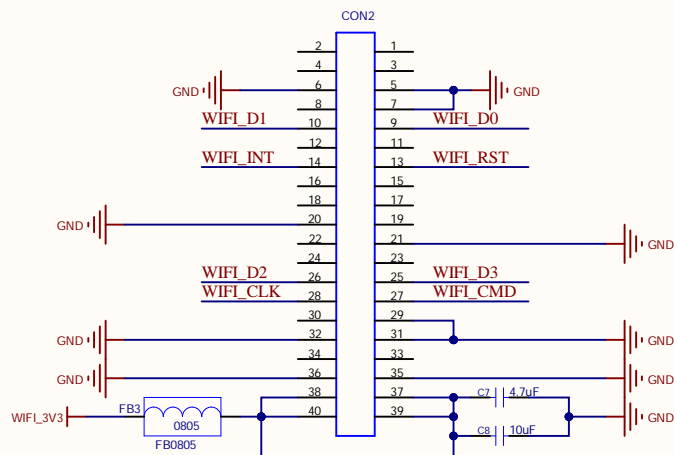
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A			A
B			B
C			C
D			D


NUM	DATE	DESIGNER	SCH REVISION	PCB REVISION	CAUSE	DESCRIPTION
01	2020-7-30	YangKangquan	V1.0.0	V1.0.0	初次设计	





SDIO WiFi小板接口



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Date: 2020/11/10		Sheet3 of 3	

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安凯微电子