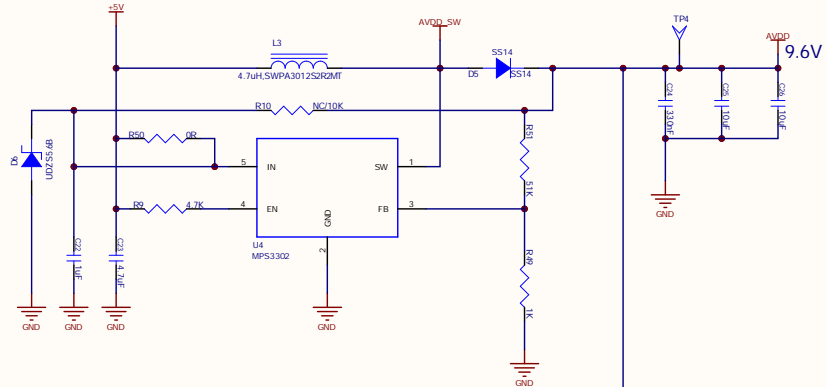
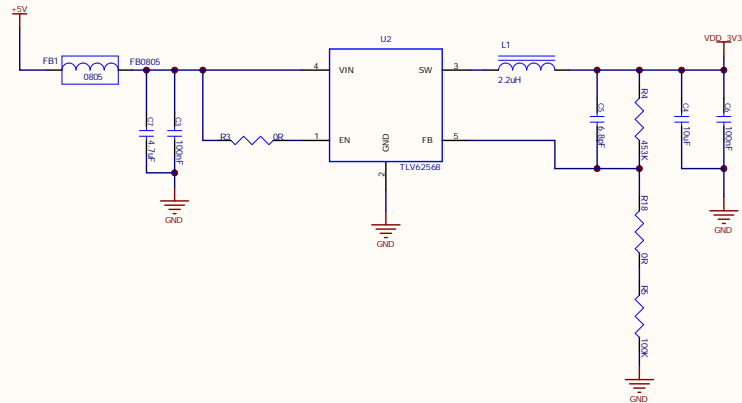
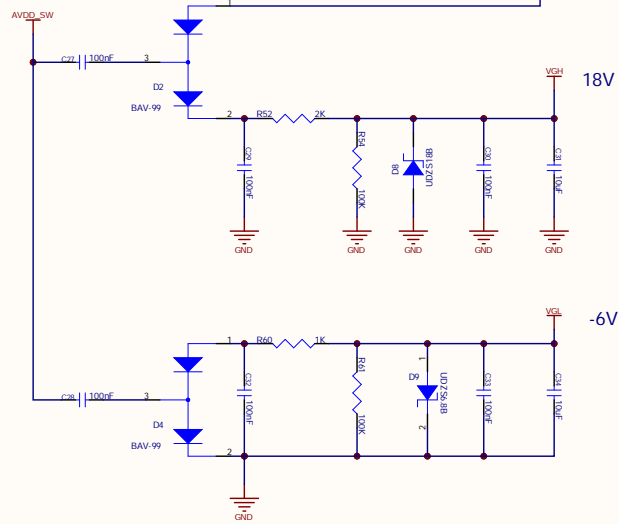
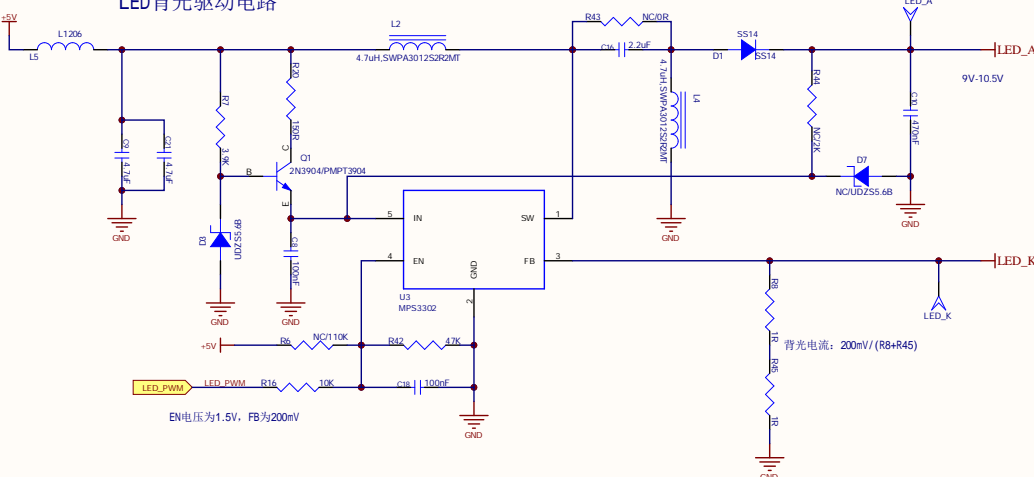


1	2	3	4	5	6	7	8
A							
B							
C							
D							

NUM	DATE	DESIGNER	SCH REVISION	PCB REVISION	DESCRIPTION
01	2019-3-21	YangKangquan	V1.0.0	V1.0.0	初次设计
02	2019-7-11	YangKangquan	V1.0.1	V1.0.1	修改背光电路
03	2019-4-13	YangKangquan	V1.0.2	V1.0.2	1、修改VCOM电压分压的采样点为AVDD 2、PCLK增加串连电阻



LED背光驱动电路



A

B

C

D

A

B

C

D

