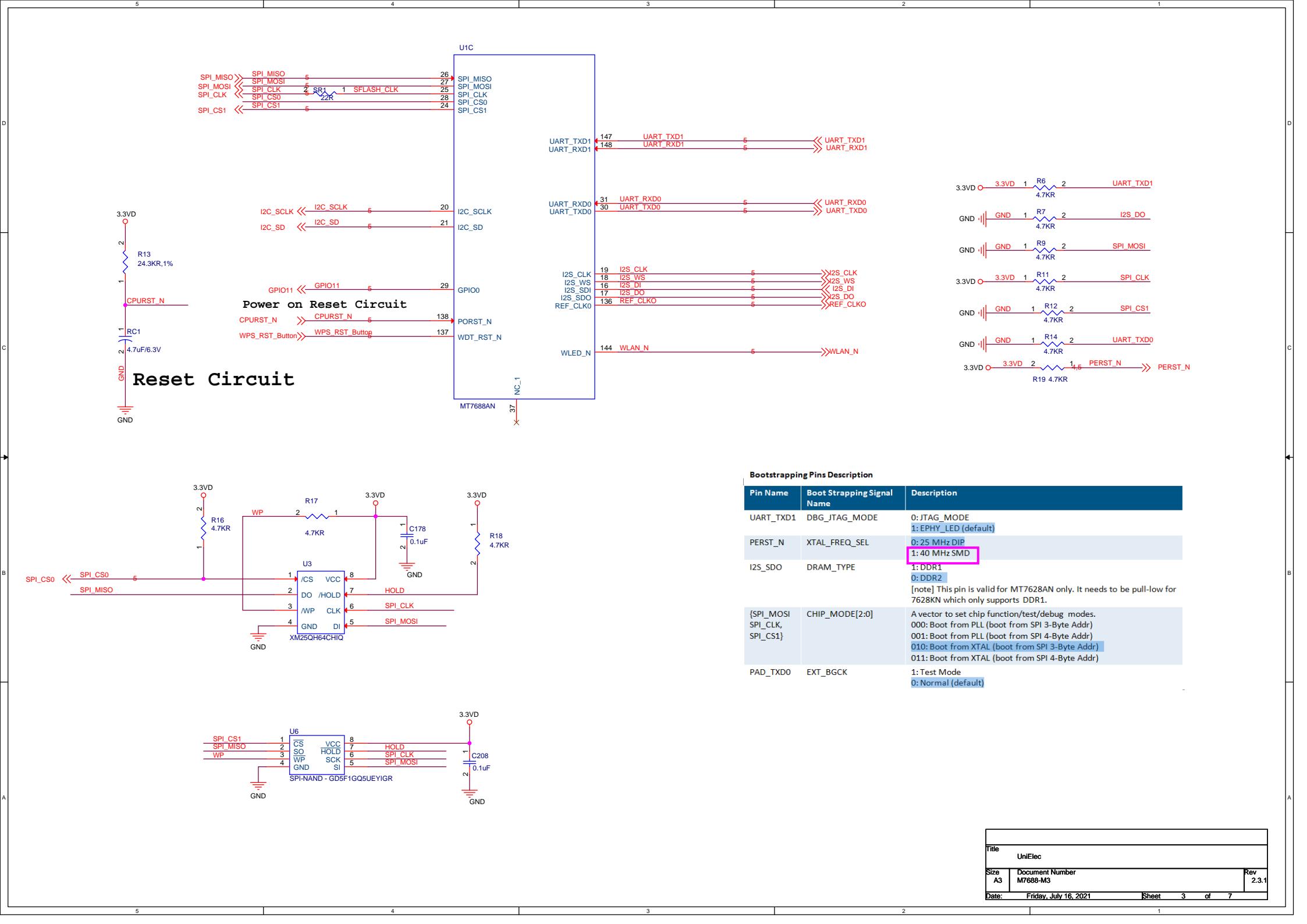
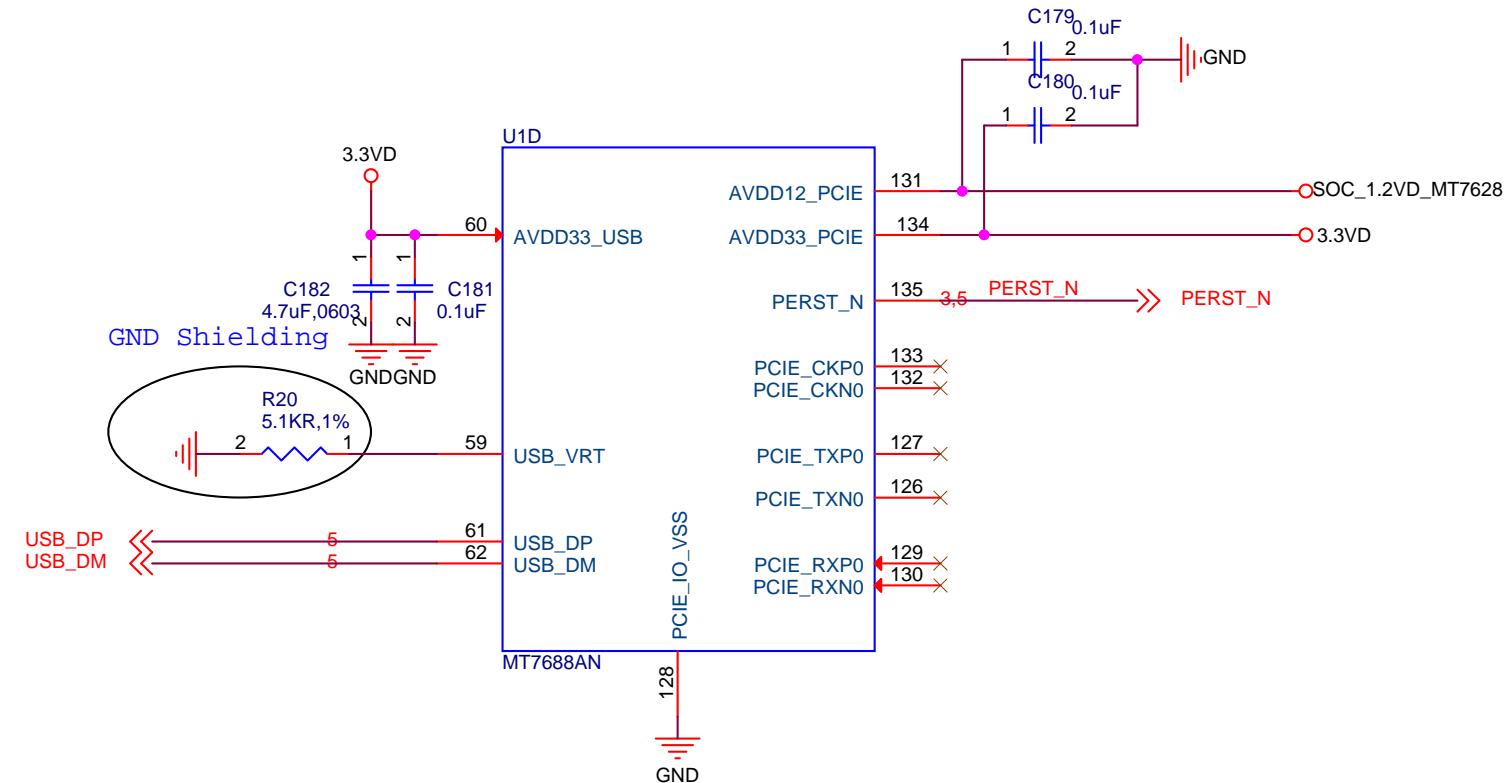
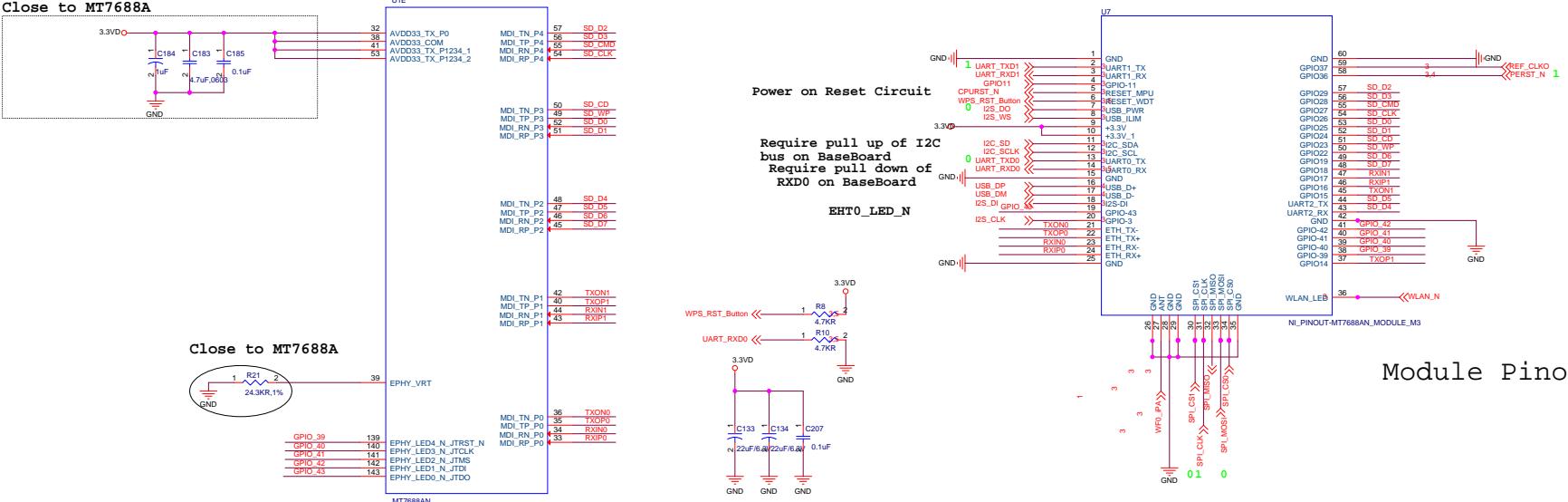


Title		
UniElec		
Size	Document Number	Rev
Custom	M7688-M3	2.3.1
Date:	Friday, July 16, 2021	Sheet 2 of 7



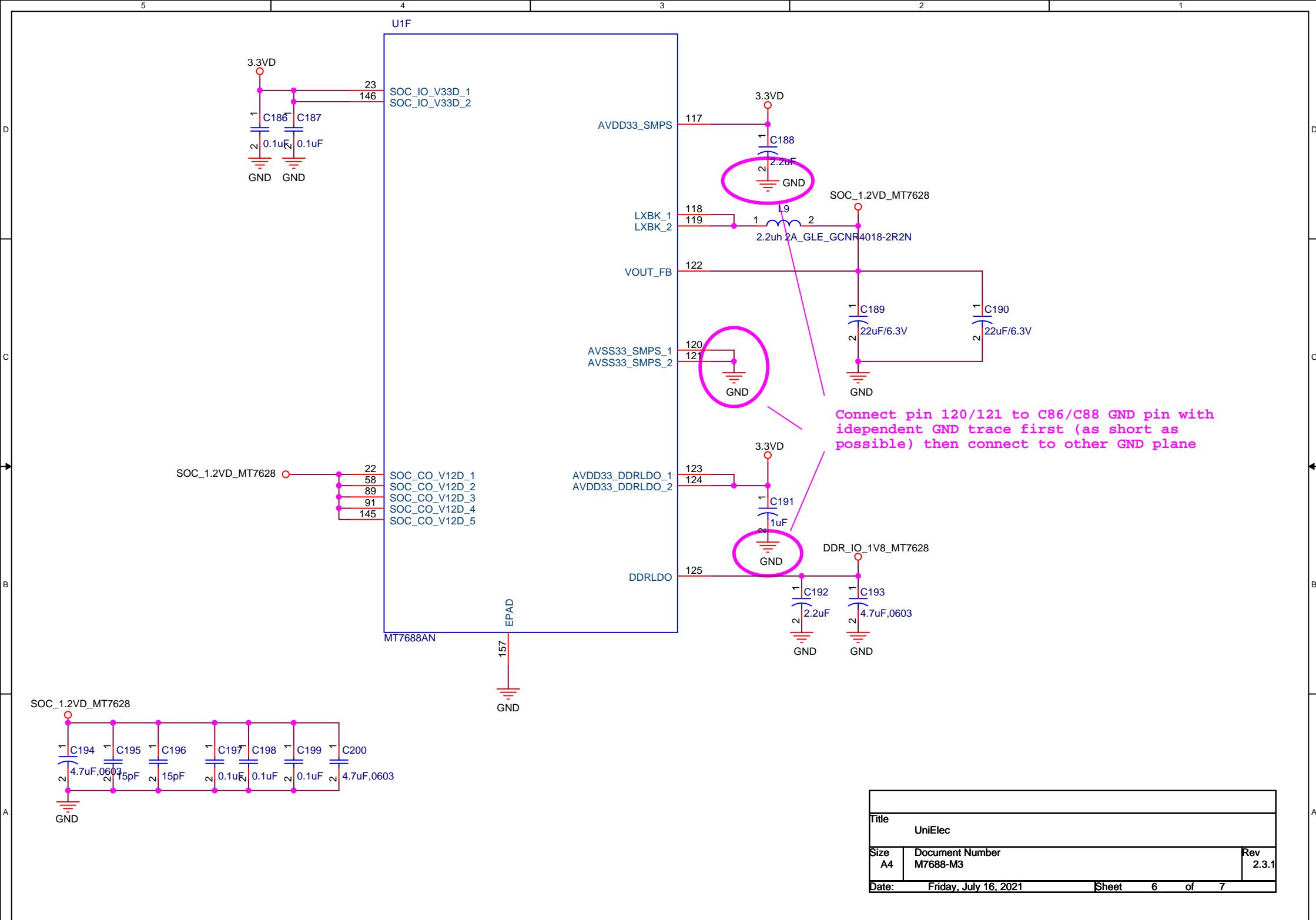


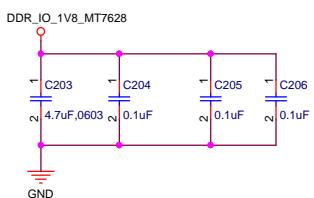
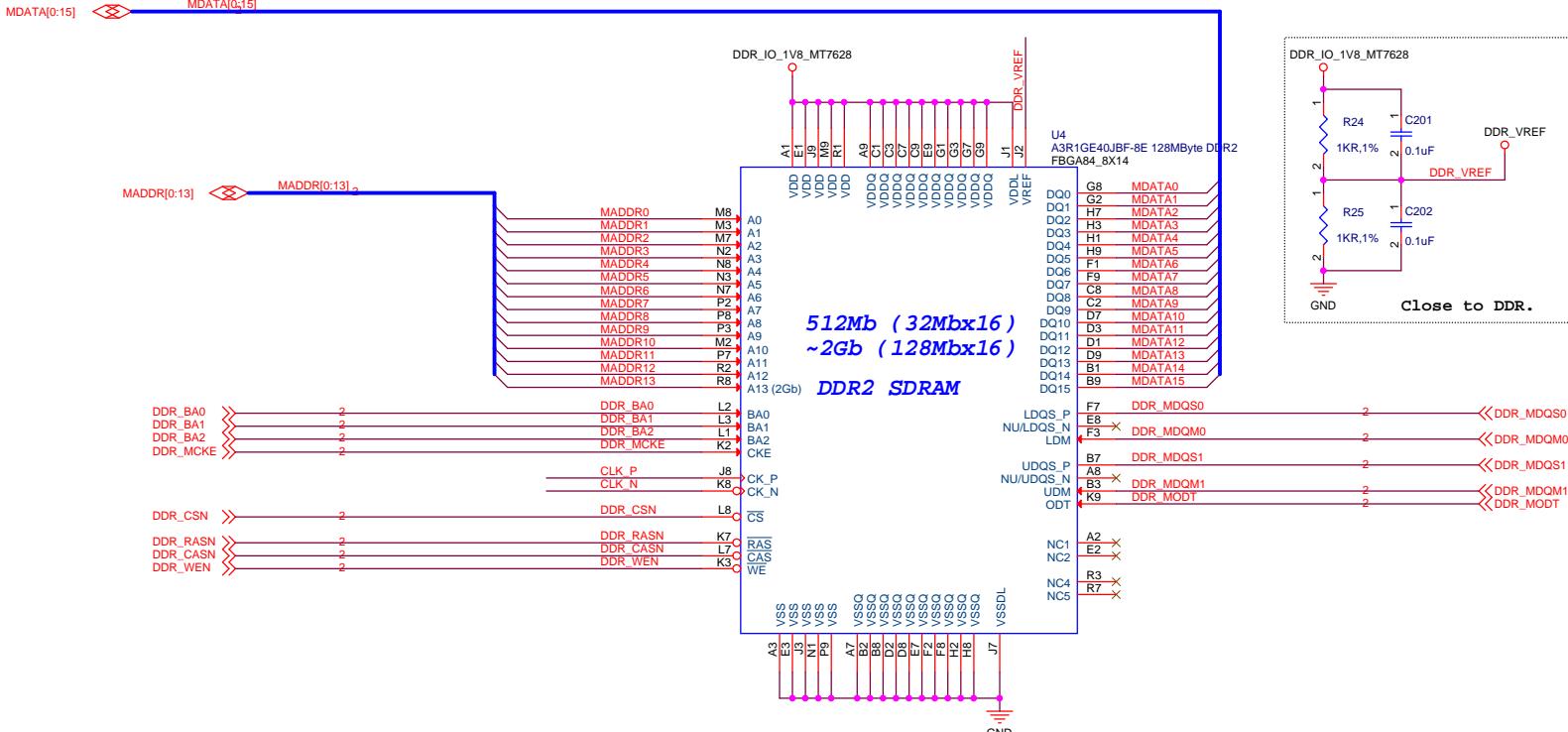
Title		UniElec
Size		Document Number
CustomM7688-M3		
Date:	Friday, July 16, 2021	Rev
		2.3.1
Sheet	4	of 7



## Module Pinout

Bootstrapping Pins Description			
Pin Name	Boot Strapping Signal Name	Description	
UART_TXD1	DBG_JTAG_MODE	0: JTAG_MODE 1: EPHY_LED (default)	
PERST_N	XTAL_FREQ_SEL	0: 25 MHz DIP 1: 40 MHz SMD	
I2S_SDO	DRAM_TYPE	1: DDR1 0: DDR2	[note] This pin is valid for MT7628AN only. It needs to be pull-low 7628NN which only supports DDR1.
[SPI_MOSI SPI_CLK, SPI_CS1]	CHIP_MODE[2:0]	A vector to set chip function/test/debug modes. 000: Boot from PLI (boot from SPI 3-byte Addr) 001: Boot from XTAL (boot from SPI 4-byte Addr) 010: Boot from XTAL (boot from SPI 3-byte Addr) 011: Boot from XTAL (boot from SPI 4-byte Addr)	
PAD_RXD0	EXT_BGCK	1: Test Mode 0: Normal Mode	





Title	UniElec	Rev		
Size	Document Number	2.3		
CustomM7688-M3				
Date:	Friday, July 16, 2021	Sheet		
		7	of	7