



E-EDID Errata

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VESA Enhanced Extended Display Identification Data Standard

E-EDID Standard Release A, Revision 2 Errata

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Purpose

This document provides updated information relevant to the VESA E-EDID standard. It contains the following sections:

- Clarification & known errors in the E-EDID standard Release A, Revision 2
- Other comments / guidance

Preface

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Support

Clarifications and application notes to support the E-EDID standard may be written. To obtain the latest standard and any support documentation, contact VESA.

All comments or reported errors should be submitted in writing to VESA using one of the following methods.

- Fax: 510 651 5127, *direct this note to Technical Support at VESA*
- e-mail support@vesa.org
- mail to Technical Support
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1 Clarification & Known Errors in the E-EDID standard

Note that section(s) in **bold print** are those which differ from the current E-EDID standard.

1.1 *MODE SUPPORT VALIDATION WITH MONITOR RANGE LIMIT*

In the E-EDID Standard (Release A, Revision 2), there is some discrepancy around how range limit information is to be used with respect to validation of video timing modes explicitly reported in EDID. Notes 2 and 7 (following Table 3.26) have caused some confusion concerning the relationship between video timing modes explicitly reported in EDID and the range limits. The following changes are recommended to be included in the next revision of the VESA E-EDID Standard.

Current text reads:

“2. Any timing outside these limits may cause the display to enter a self-protection mode (out of range error) or may cause damage to the display. The host shall always verify that an intended timing falls within these limits before the timing is applied --- assumes that the Display Range Limits are defined.”

Change text to read:

“2. Any timing outside these limits may cause the display to enter a self-protection mode (out of range error) or may cause damage to the display. The host shall always verify that an intended timing falls within these limits before the timing is applied --- assumes that the Display Range Limits are defined. **There are displays that support DMTs outside of the range limits. In this case, display range limits verification is not required.**”

And current text reads:

“7. All video timing modes (listed in BASE EDID or timing extensions) shall be supported by the display and the frequencies for the listed video timing modes shall fall within the minimum and maximum horizontal and vertical frequency range limits.”

Change text to read:

“7. All Video Timing Modes explicitly listed in BASE EDID or extension blocks must be supported by the display. For a continuous frequency display (for example, a display that supports the GTF or CVT standard), a connected video source must generate timings that fall within the minimum and maximum horizontal and vertical frequency range limits and must not exceed the maximum pixel clock. The exception is sources that generate explicitly listed Timing Modes that fall outside indicated range limits.”

2 Other Comments/guidance

No comments/guidance