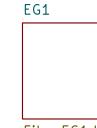


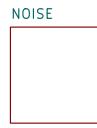
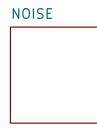
A

TimingGen
File: TimingGen.kicad_schLFO
File: LFO.kicad_schREG
File: REG.kicad_sch

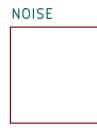
B

PG0
File: PG0.kicad_schPG1
File: PG1.kicad_schPG2
File: PG2.kicad_schPG3
File: PG3.kicad_sch

C

EG0
File: EG0.kicad_schEG1
File: EG1.kicad_schEG2
File: EG2.kicad_schOP0
File: OP0.kicad_schOP1
File: OP1.kicad_schOP2
File: OP2.kicad_sch

D

NOISE
File: NOISE.kicad_schACCO
File: ACC0.kicad_schACC1
File: ACC1.kicad_schTIMER
File: TIMER.kicad_sch

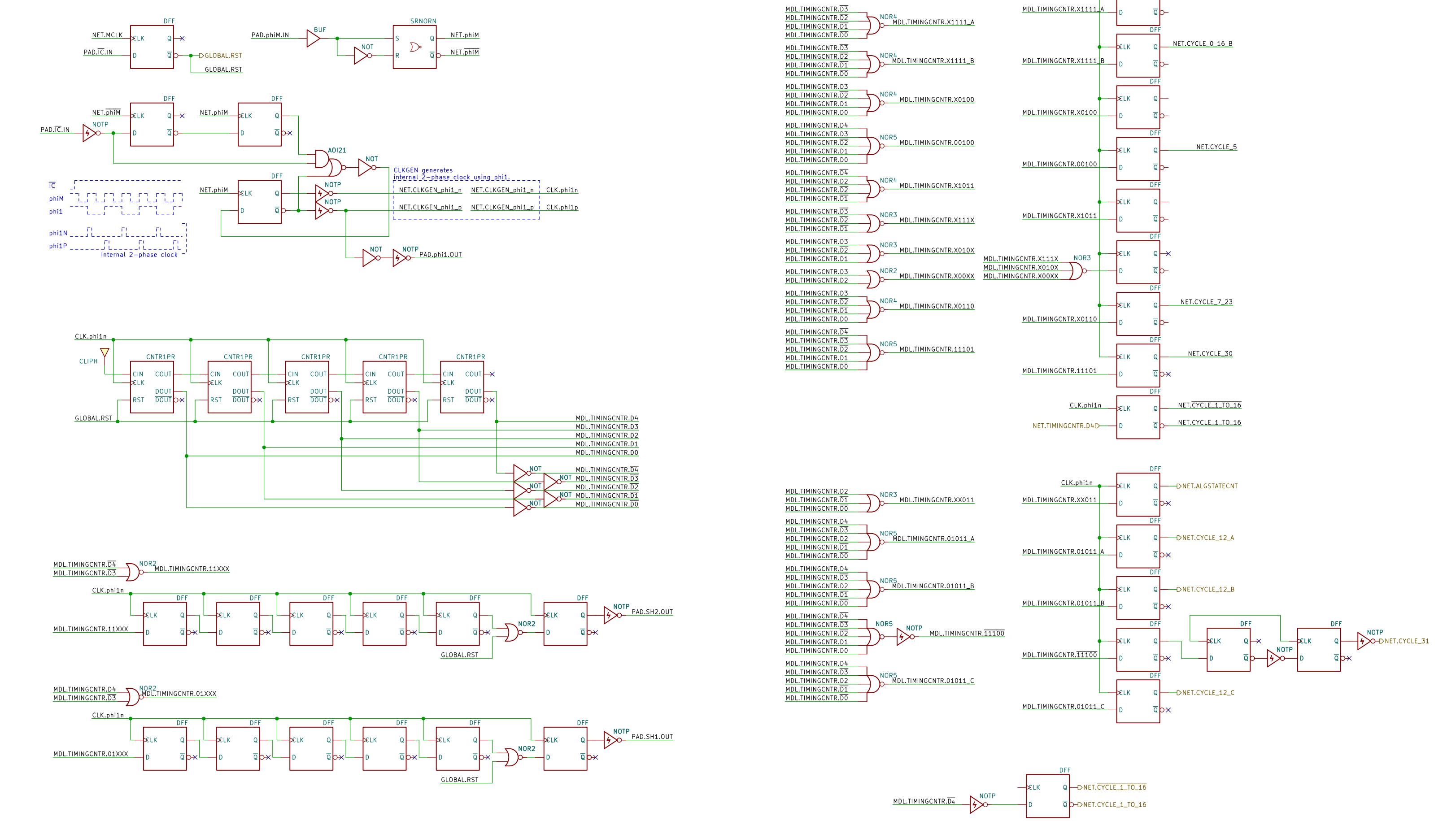
Do not repost unless otherwise approved by the author
YM2151 confidential preliminary schematics
IKA Victor Co., Inc.

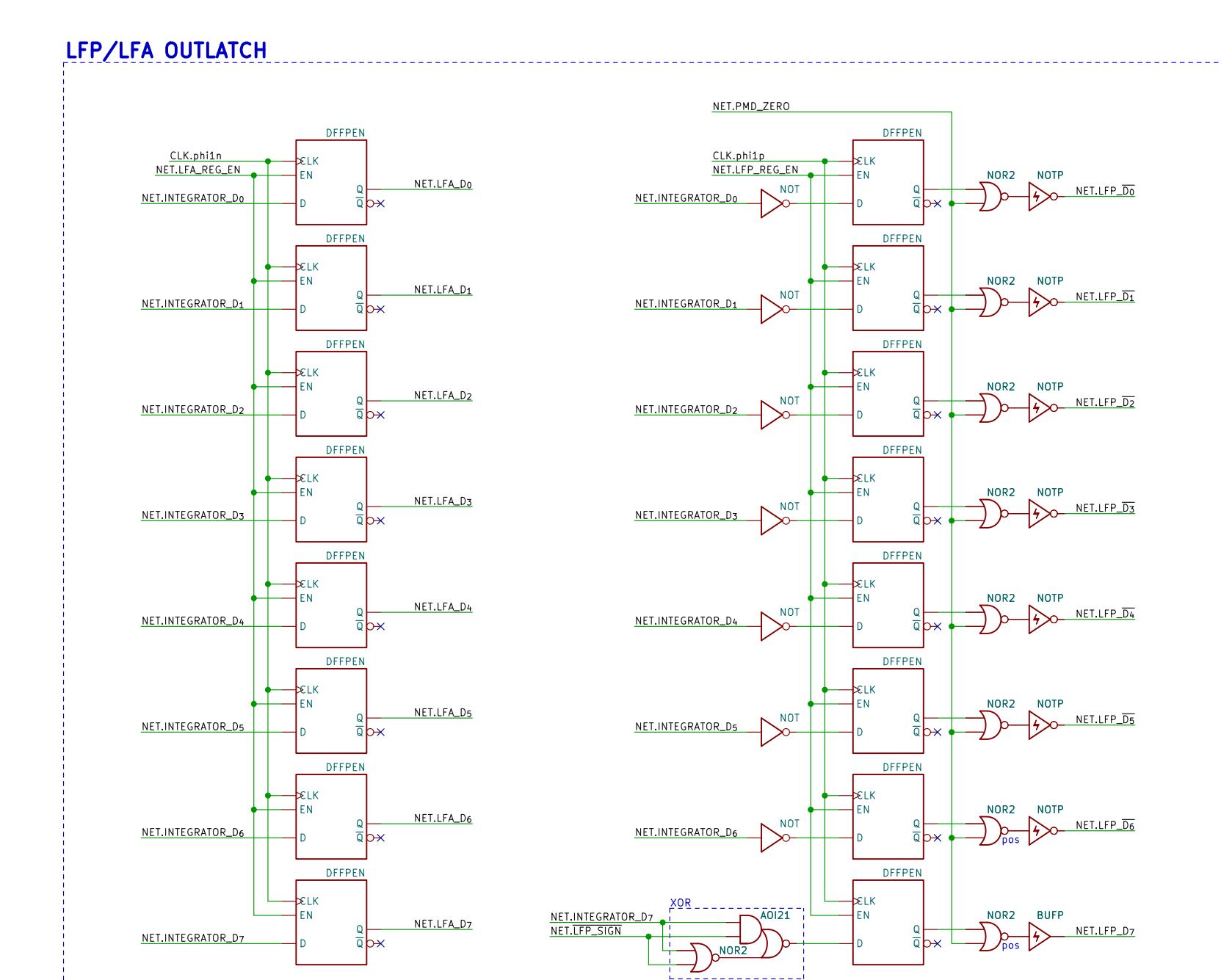
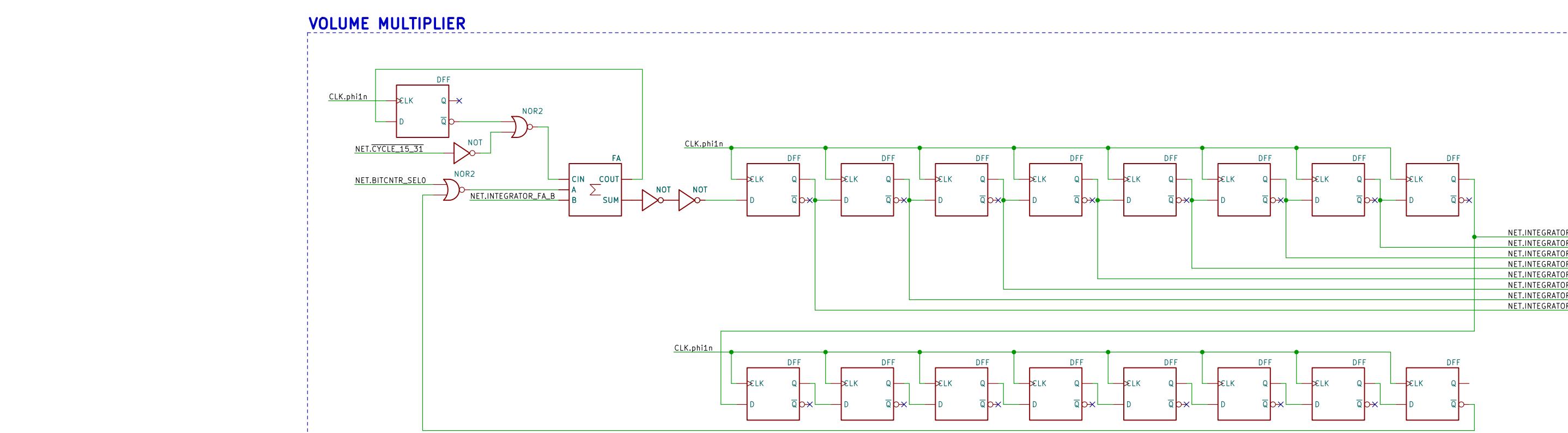
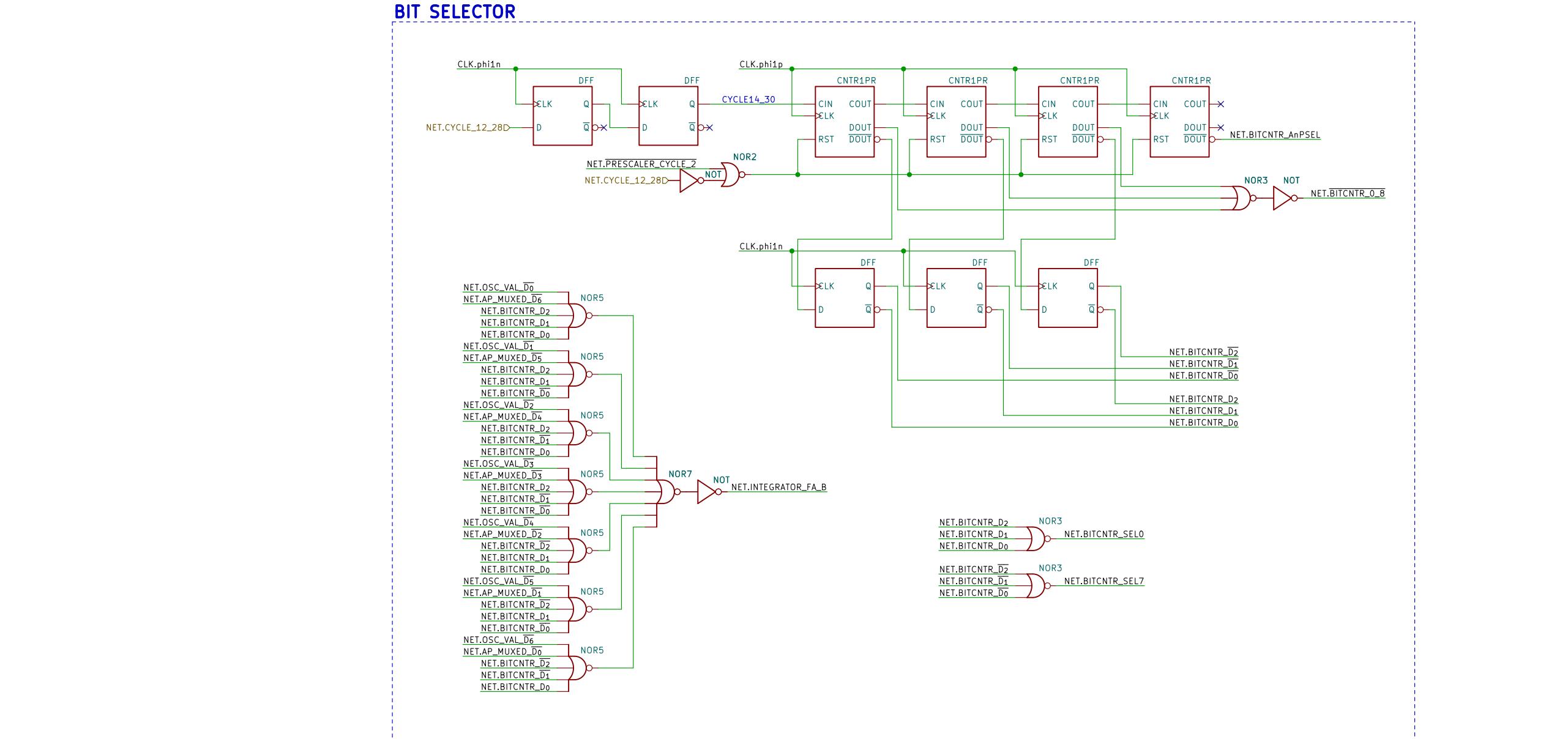
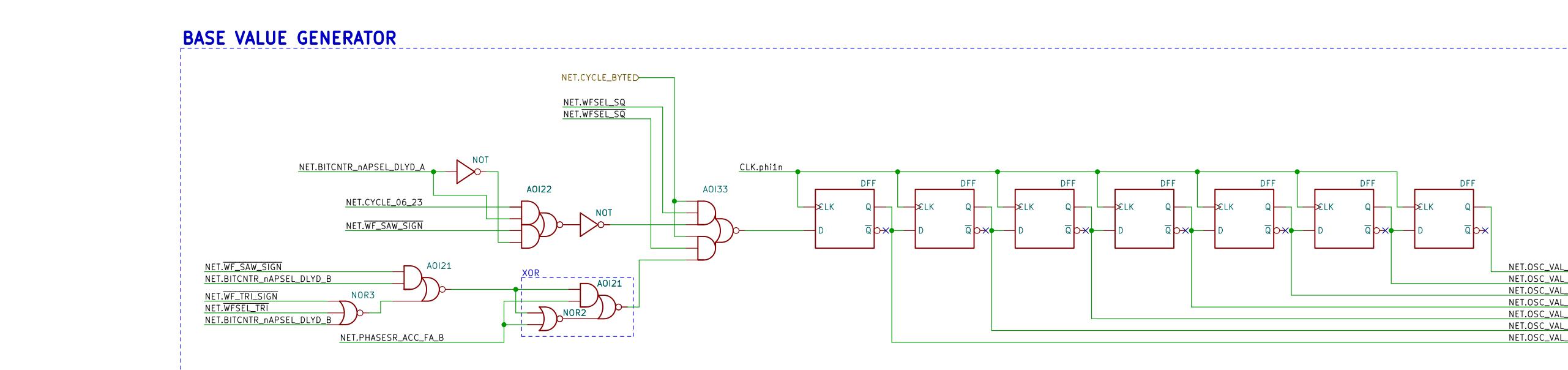
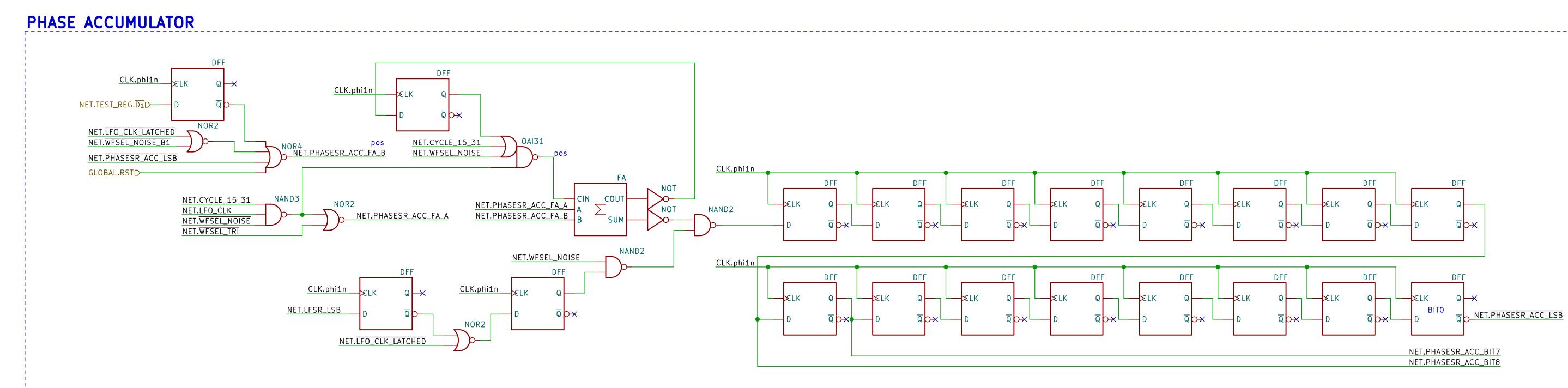
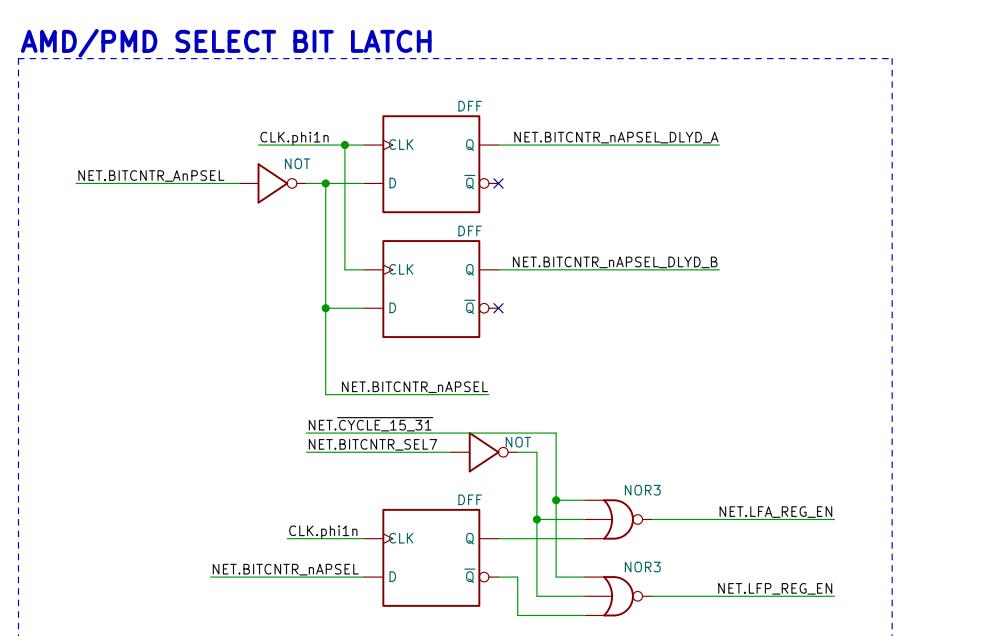
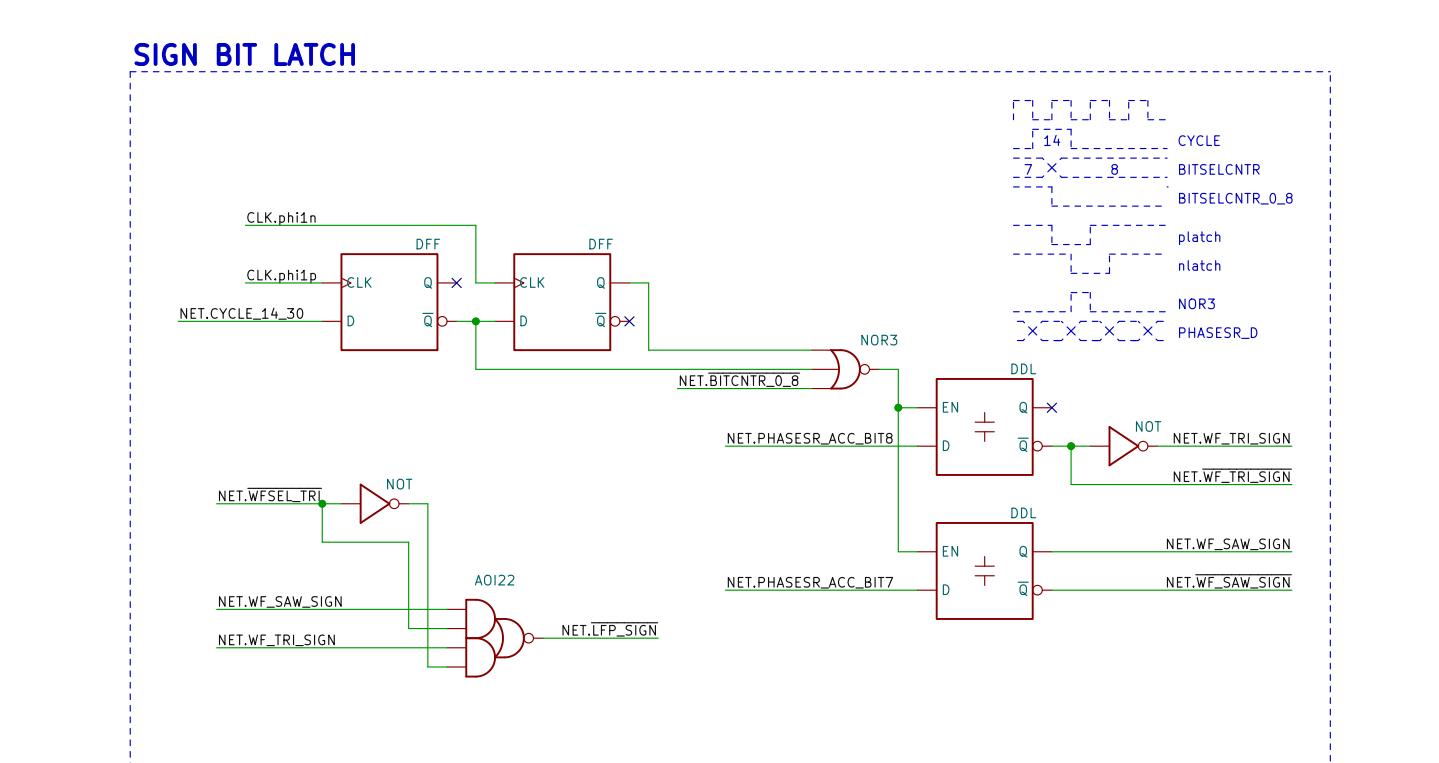
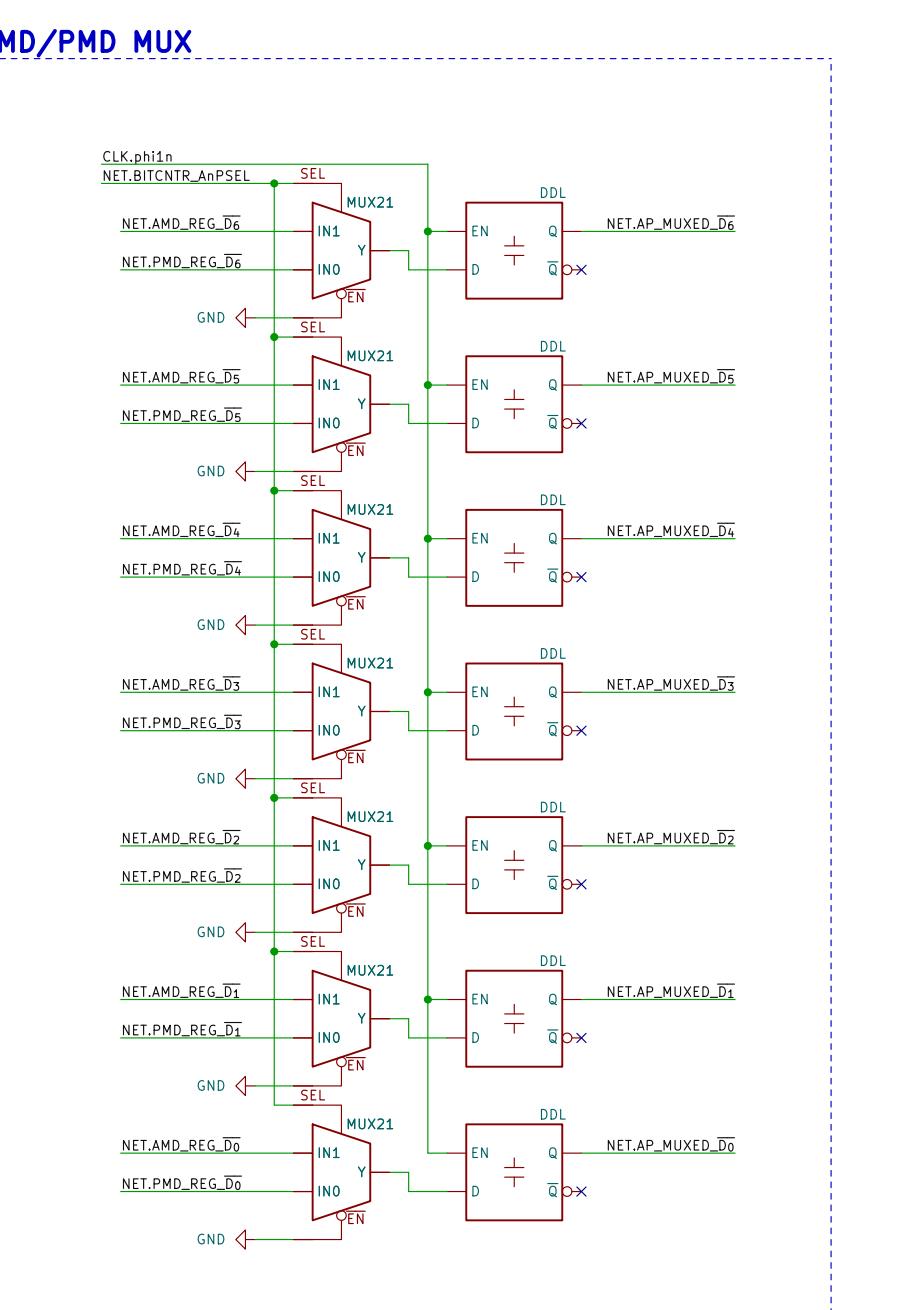
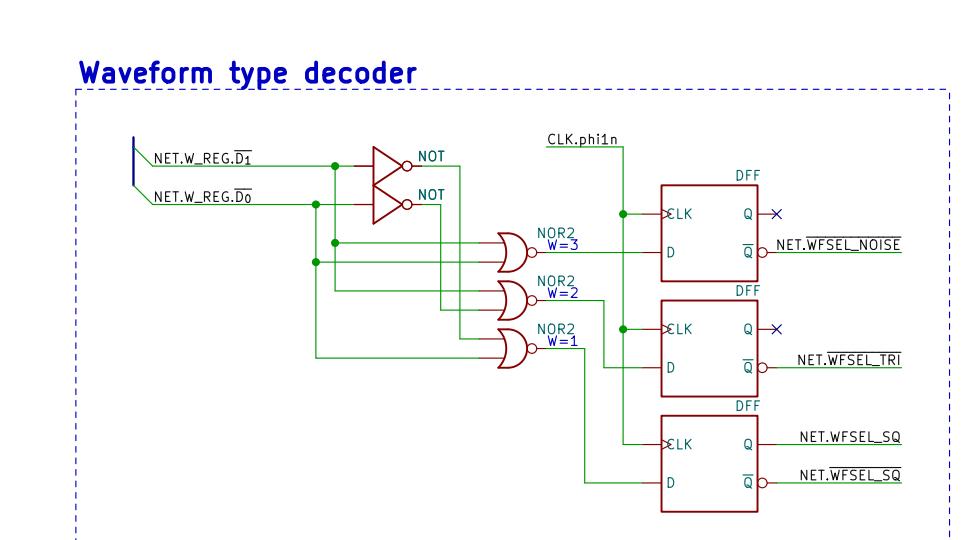
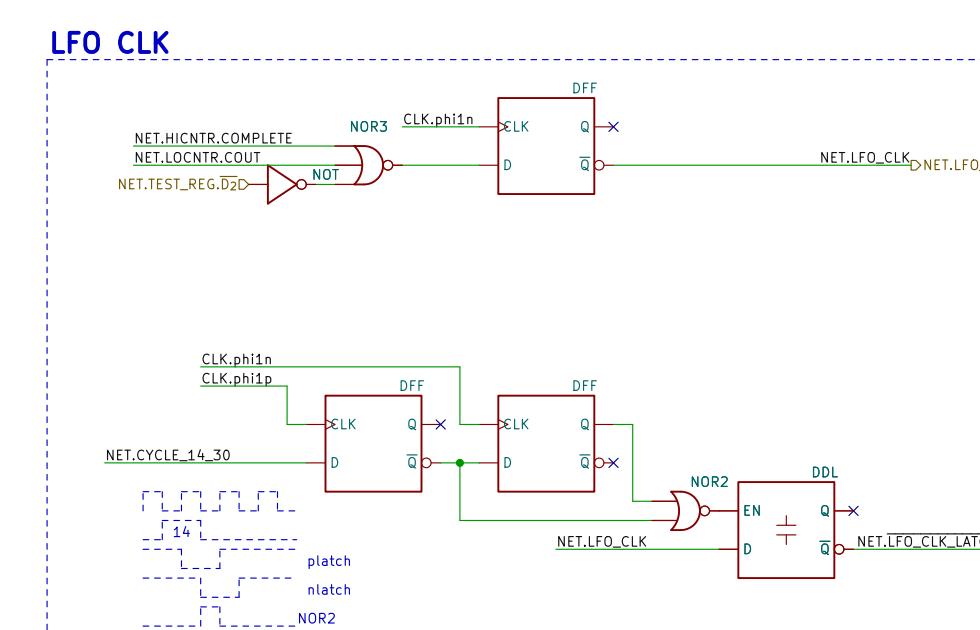
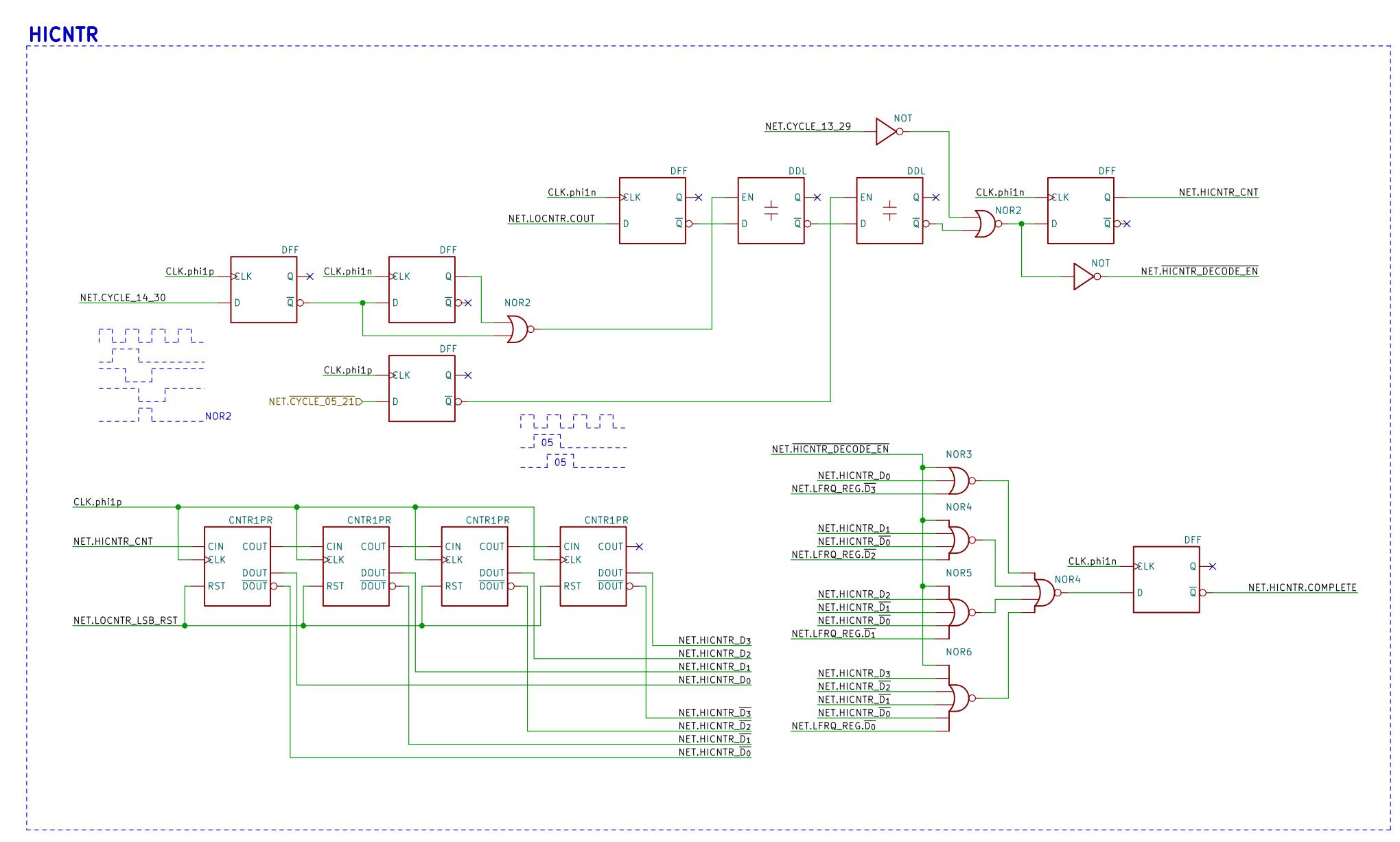
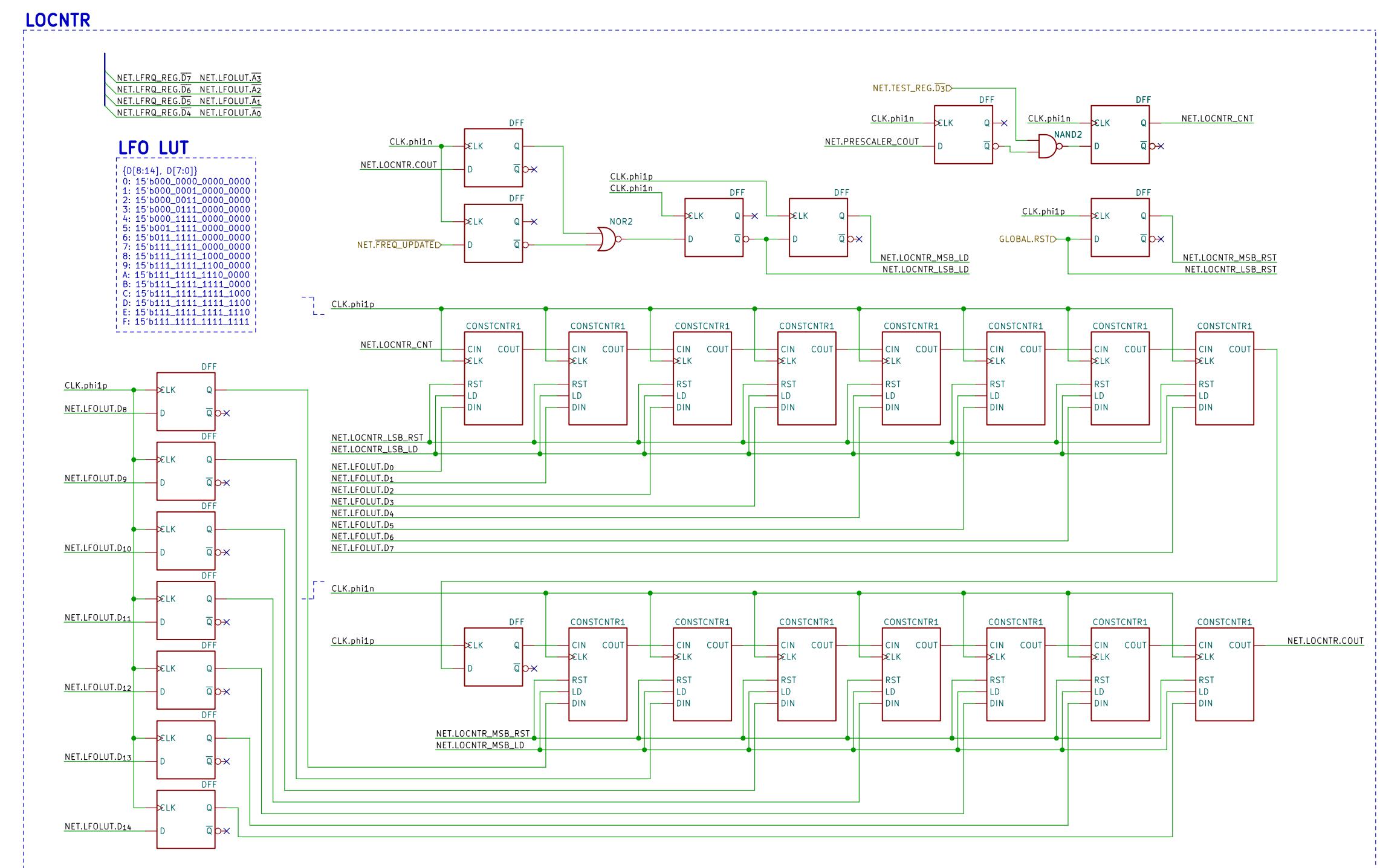
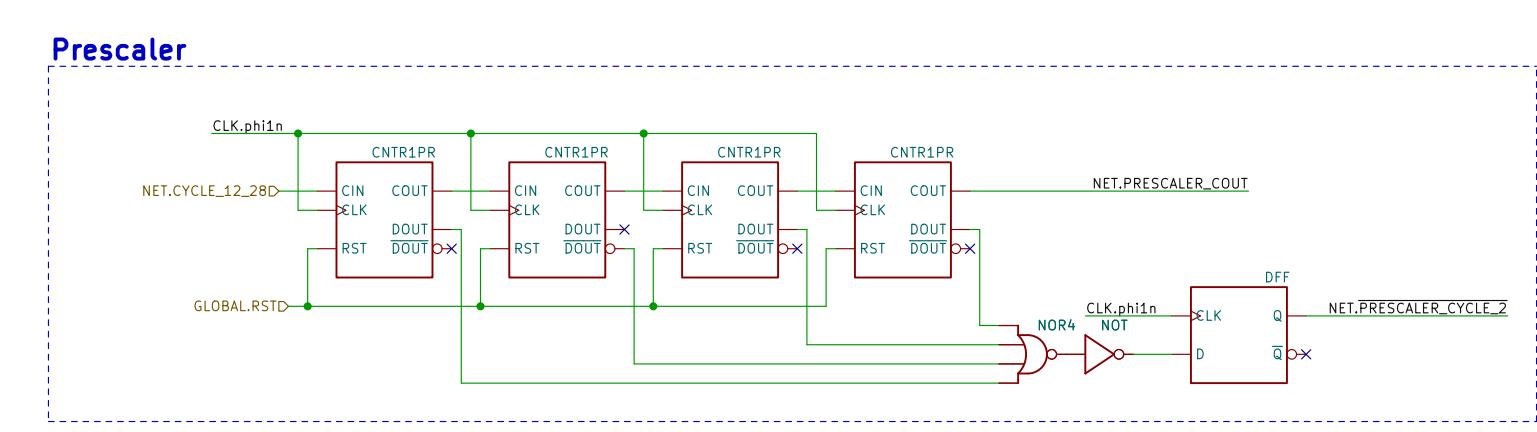
Sheet: /
File: YM2151.kicad_sch

Title: YM2151

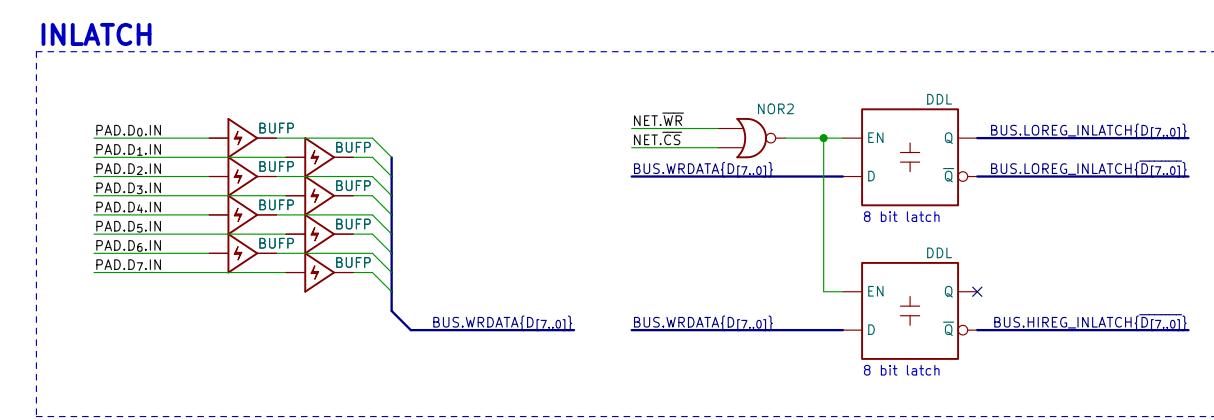
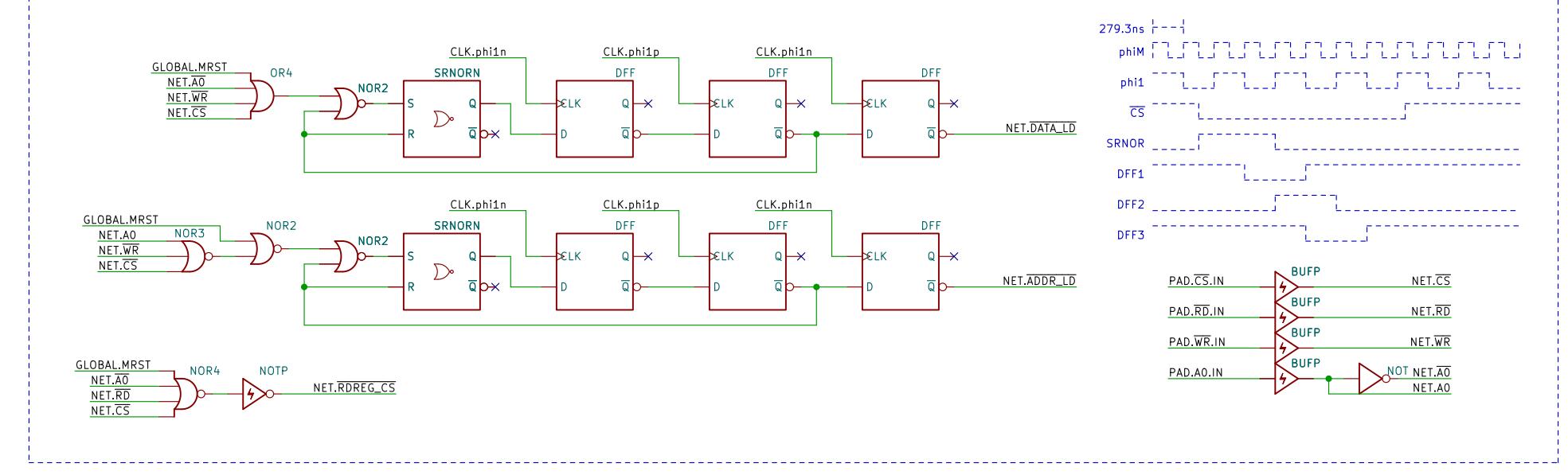
Size: A4 Date: 2023-02-05
KiCad E.D.A. eeschema (6.0.7)

Rev: v1.0
Id: 1/18

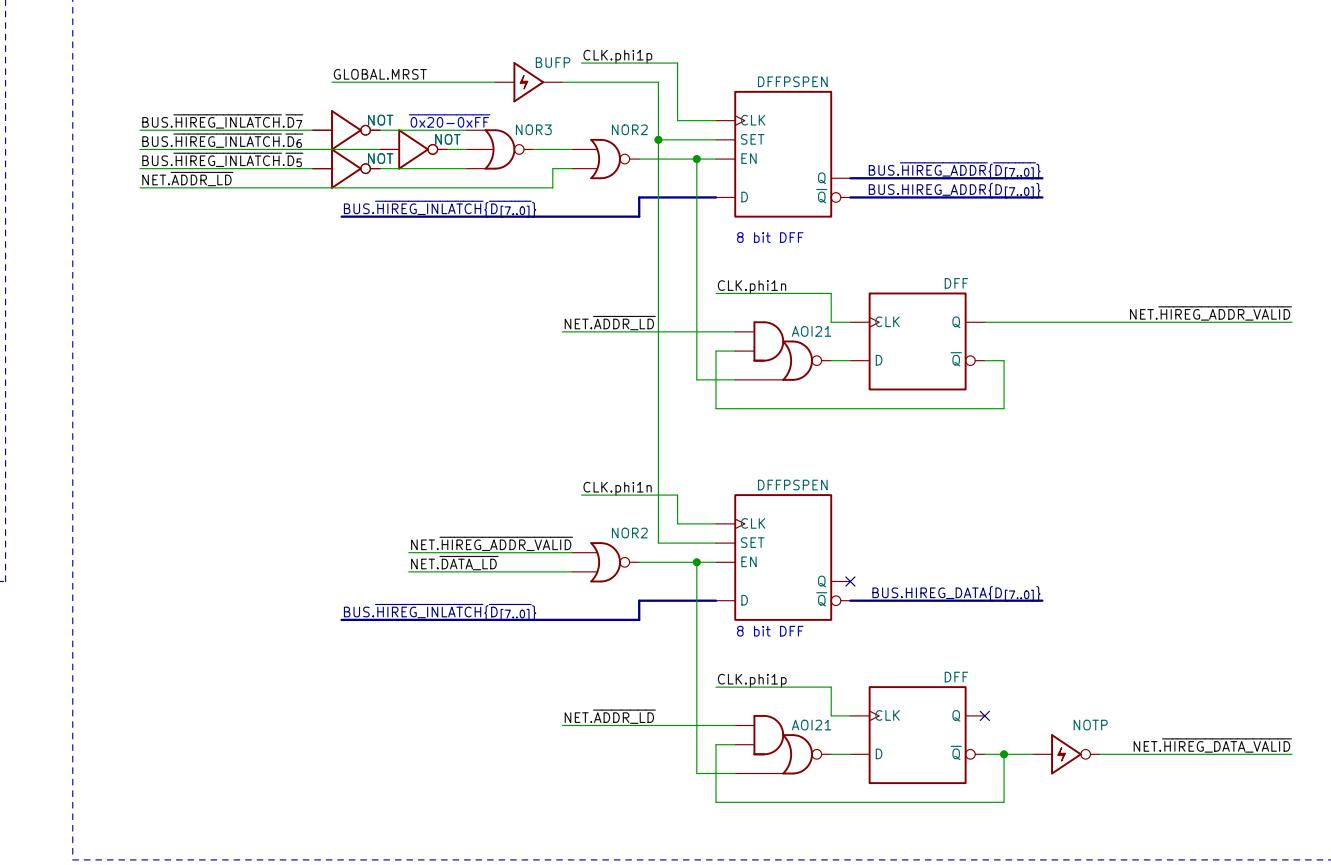




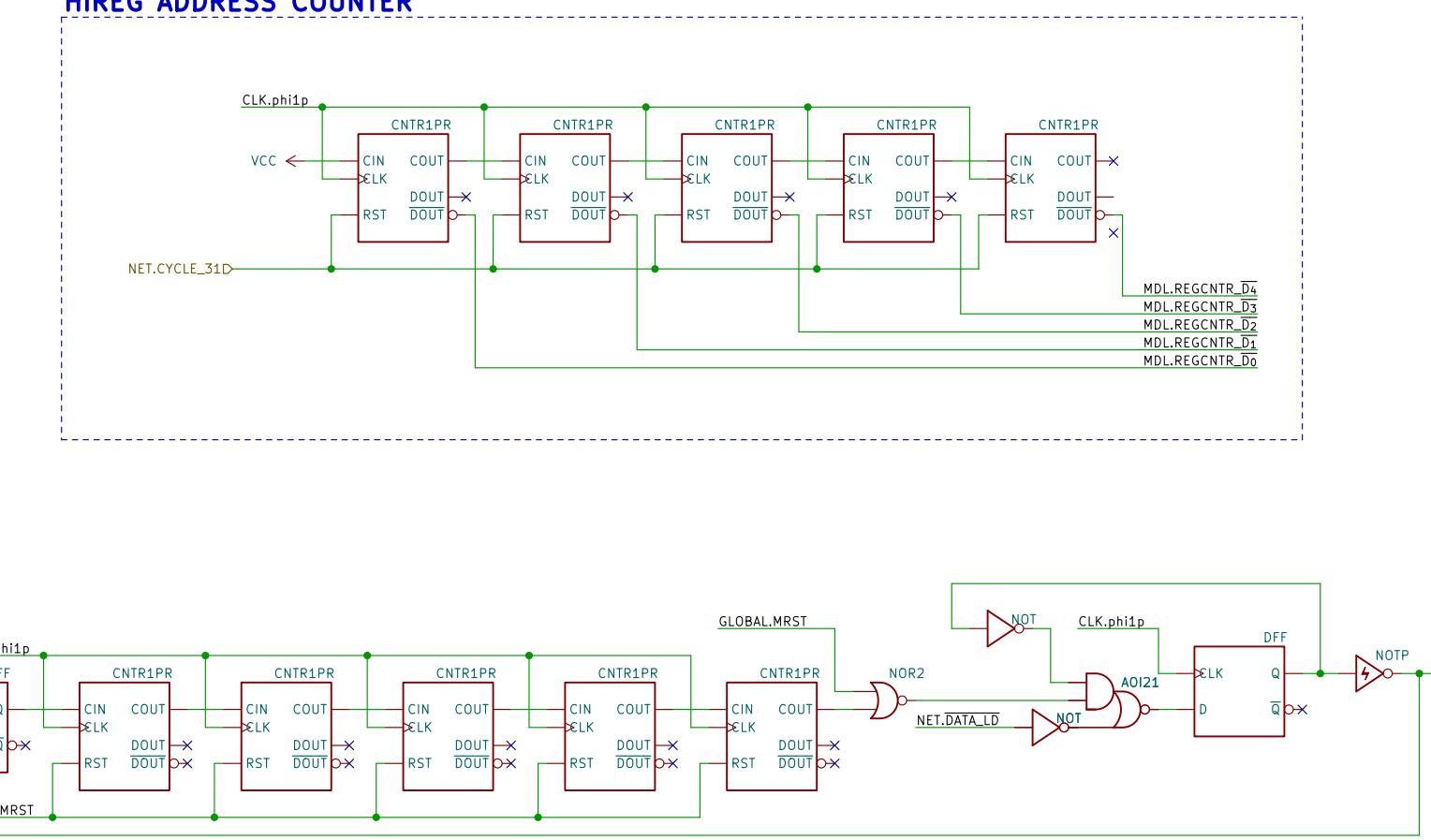
BUS CONTROL SIGNAL DECODER AND SYNCHRONIZER



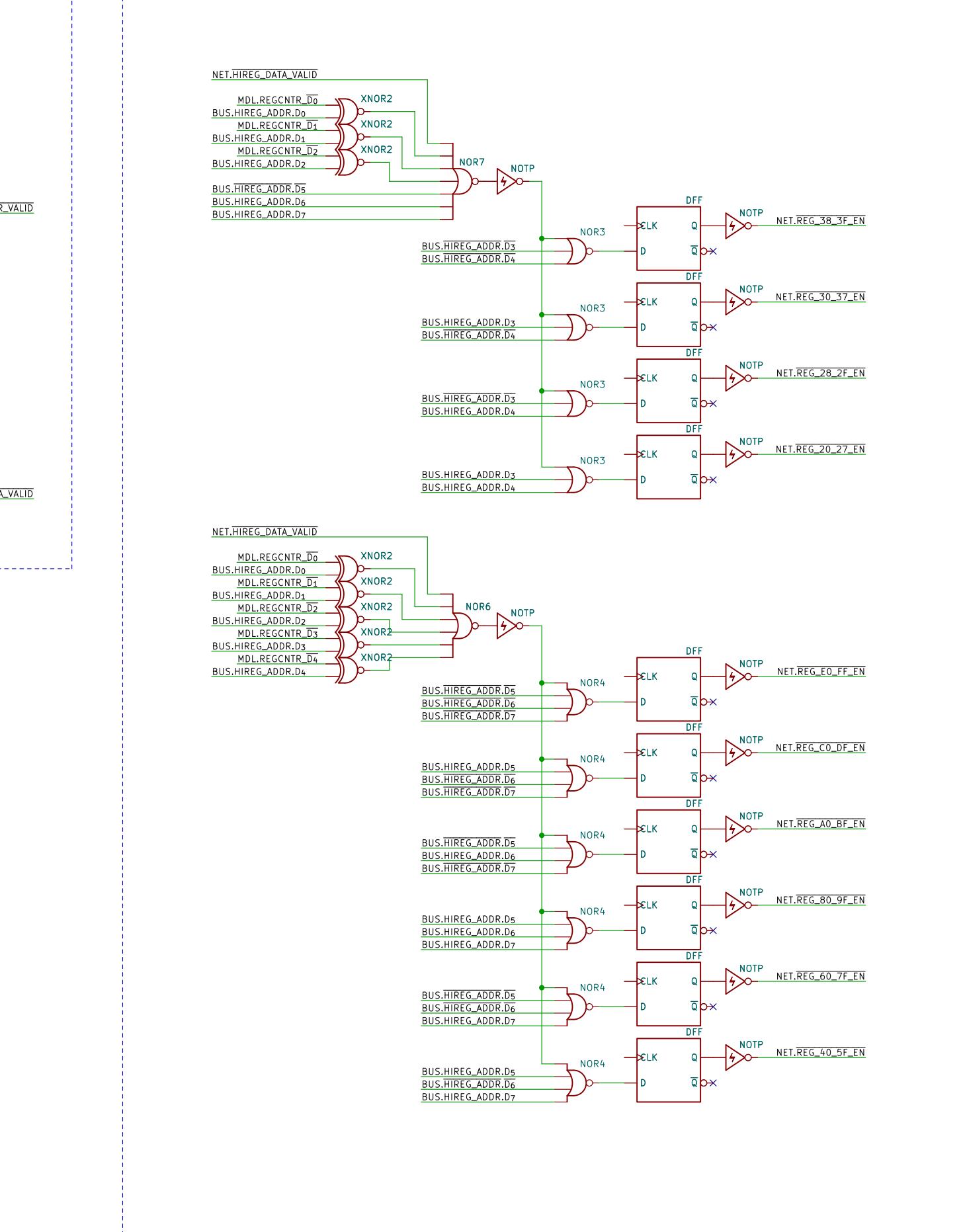
HIREG TEMPORARY REGISTER AND FLAGS



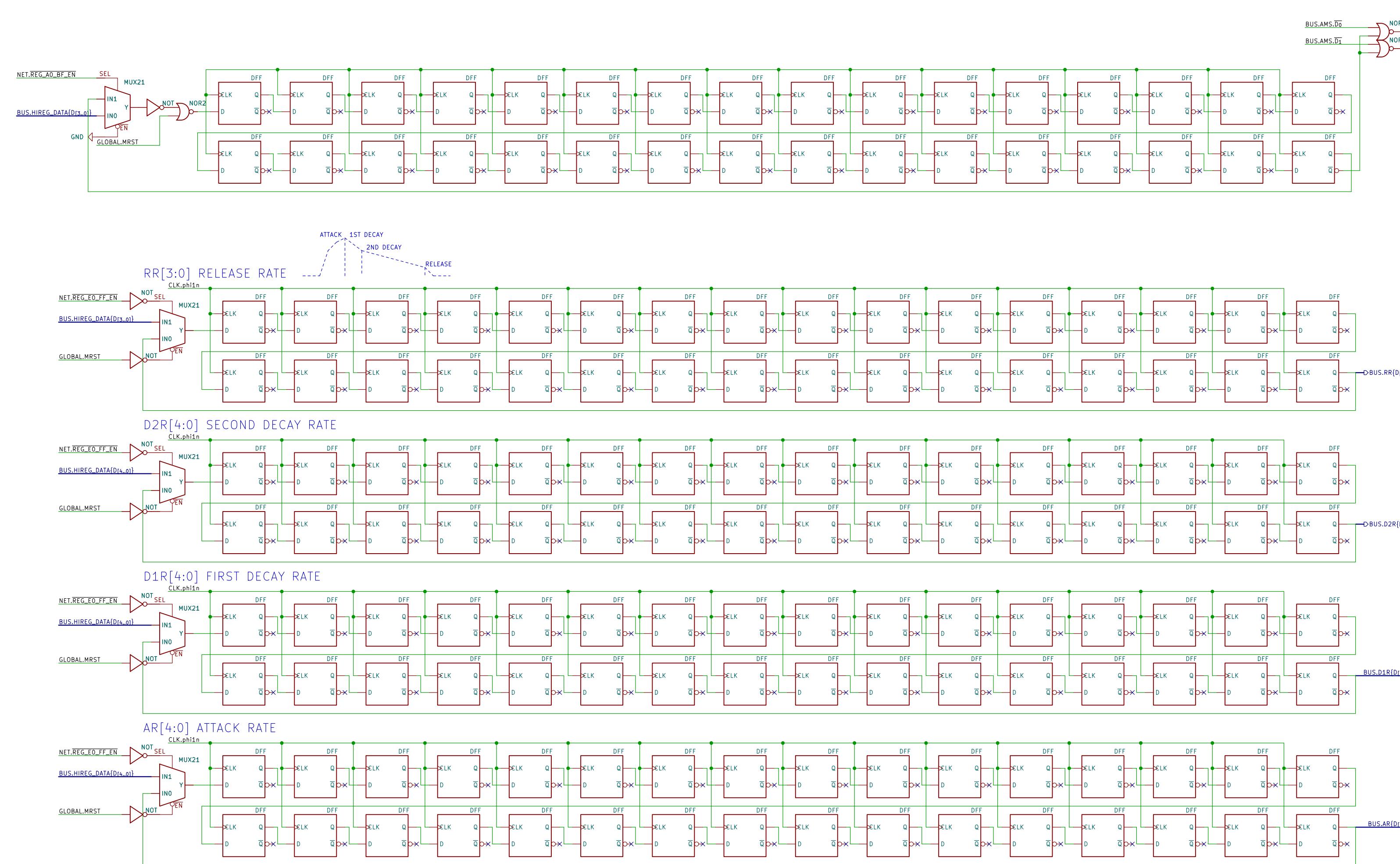
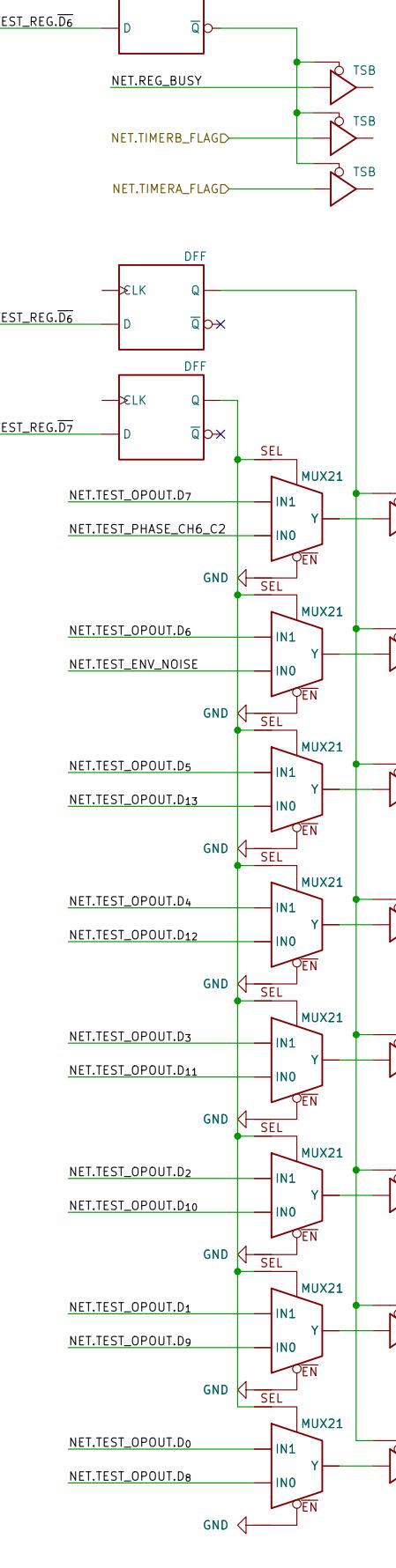
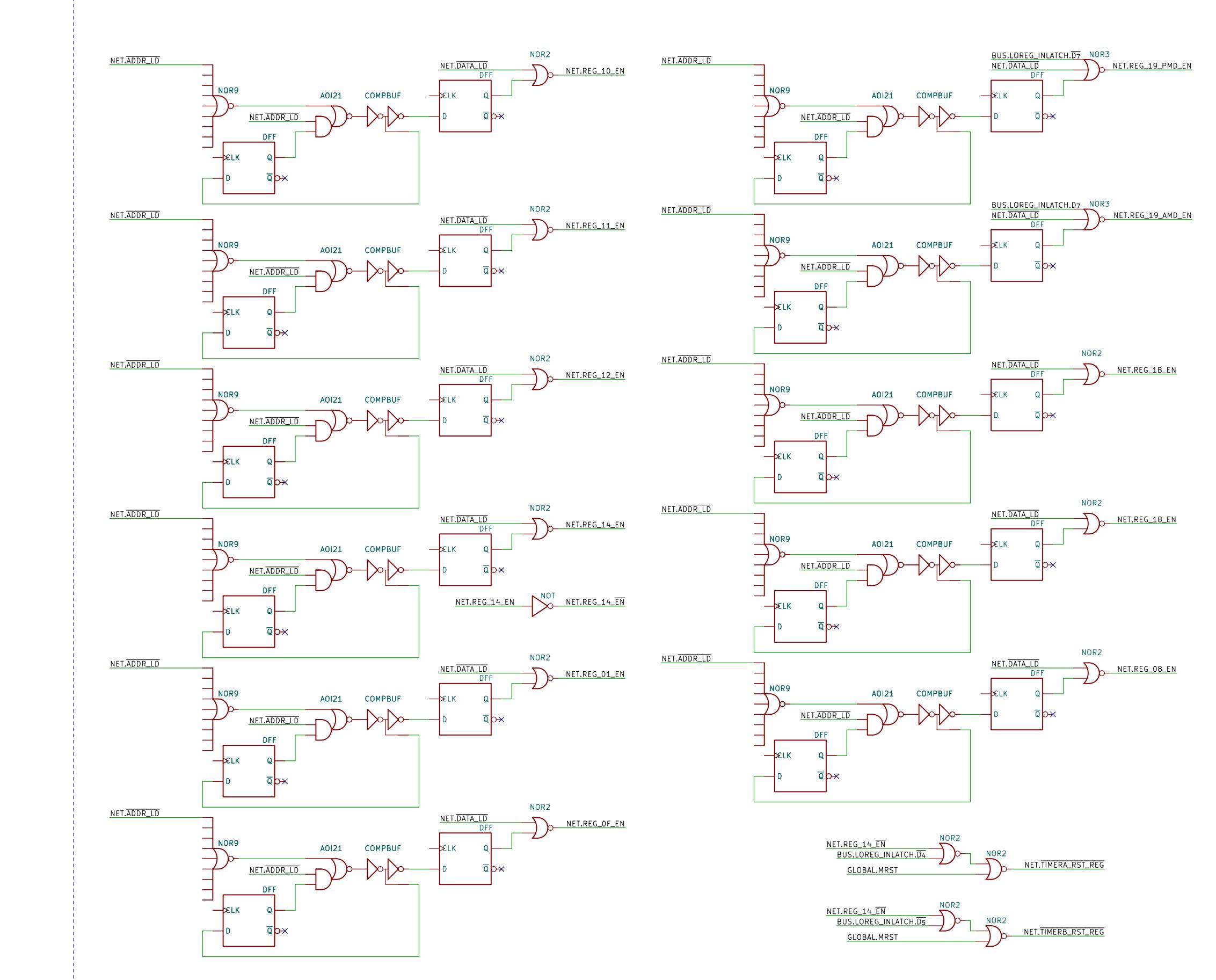
HIREG ADDRESS COUNTER



HIREG REGISTER ADDRESS DECODER



LOREG REGISTER ADDRESS DECODER



NET.CYC[10]

NET.CYC[9]

NET.CYC[8]

NET.CYC[7]

NET.CYC[6]

NET.CYC[5]

NET.CYC[4]

NET.CYC[3]

NET.CYC[2]

NET.CYC[1]

NET.CYC[0]

NET.CYC[15]

NET.CYC[14]

NET.CYC[13]

NET.CYC[12]

NET.CYC[11]

NET.CYC[10]

NET.CYC[9]

NET.CYC[8]

NET.CYC[7]

NET.CYC[6]

NET.CYC[5]

NET.CYC[4]

NET.CYC[3]

NET.CYC[2]

NET.CYC[1]

NET.CYC[0]

NET.CYC[15]

NET.CYC[14]

NET.CYC[13]

NET.CYC[12]

NET.CYC[11]

NET.CYC[10]

NET.CYC[9]

NET.CYC[8]

NET.CYC[7]

NET.CYC[6]

NET.CYC[5]

NET.CYC[4]

NET.CYC[3]

NET.CYC[2]

NET.CYC[1]

NET.CYC[0]

NET.CYC[15]

NET.CYC[14]

NET.CYC[13]

NET.CYC[12]

NET.CYC[11]

NET.CYC[10]

NET.CYC[9]

NET.CYC[8]

NET.CYC[7]

NET.CYC[6]

NET.CYC[5]

NET.CYC[4]

NET.CYC[3]

NET.CYC[2]

NET.CYC[1]

NET.CYC[0]

NET.CYC[15]

NET.CYC[14]

NET.CYC[13]

NET.CYC[12]

NET.CYC[11]

NET.CYC[10]

NET.CYC[9]

NET.CYC[8]

NET.CYC[7]

NET.CYC[6]

NET.CYC[5]

NET.CYC[4]

NET.CYC[3]

NET.CYC[2]

NET.CYC[1]

NET.CYC[0]

NET.CYC[15]

NET.CYC[14]

NET.CYC[13]

NET.CYC[12]

NET.CYC[11]

NET.CYC[10]

NET.CYC[9]

NET.CYC[8]

NET.CYC[7]

NET.CYC[6]

NET.CYC[5]

NET.CYC[4]

NET.CYC[3]

NET.CYC[2]

NET.CYC[1]

NET.CYC[0]

NET.CYC[15]

NET.CYC[14]

NET.CYC[13]

NET.CYC[12]

NET.CYC[11]

NET.CYC[10]

NET.CYC[9]

NET.CYC[8]

NET.CYC[7]

NET.CYC[6]

NET.CYC[5]

NET.CYC[4]

NET.CYC[3]

NET.CYC[2]

NET.CYC[1]

NET.CYC[0]

NET.CYC[15]

NET.CYC[14]

NET.CYC[13]

NET.CYC[12]

NET.CYC[11]

NET.CYC[10]

NET.CYC[9]

NET.CYC[8]

NET.CYC[7]

NET.CYC[6]

NET.CYC[5]

NET.CYC[4]

NET.CYC[3]

NET.CYC[2]

NET.CYC[1]

NET.CYC[0]

NET.CYC[15]

NET.CYC[14]

NET.CYC[13]

NET.CYC[12]

NET.CYC[11]

NET.CYC[10]

NET.CYC[9]

NET.CYC[8]

NET.CYC[7]

NET.CYC[6]

NET.CYC[5]

NET.CYC[4]

NET.CYC[3]

NET.CYC[2]

NET.CYC[1]

NET.CYC[0]

NET.CYC[15]

NET.CYC[14]

NET.CYC[13]

NET.CYC[12]

NET.CYC[11]

NET.CYC[10]

NET.CYC[9]

NET.CYC[8]

NET.CYC[7]

NET.CYC[6]

NET.CYC[5]

NET.CYC[4]

NET.CYC[3]

NET.CYC[2]

NET.CYC[1]

NET.CYC[0]

NET.CYC[15]

NET.CYC[14]

NET.CYC[13]

NET.CYC[12]

NET.CYC[11]

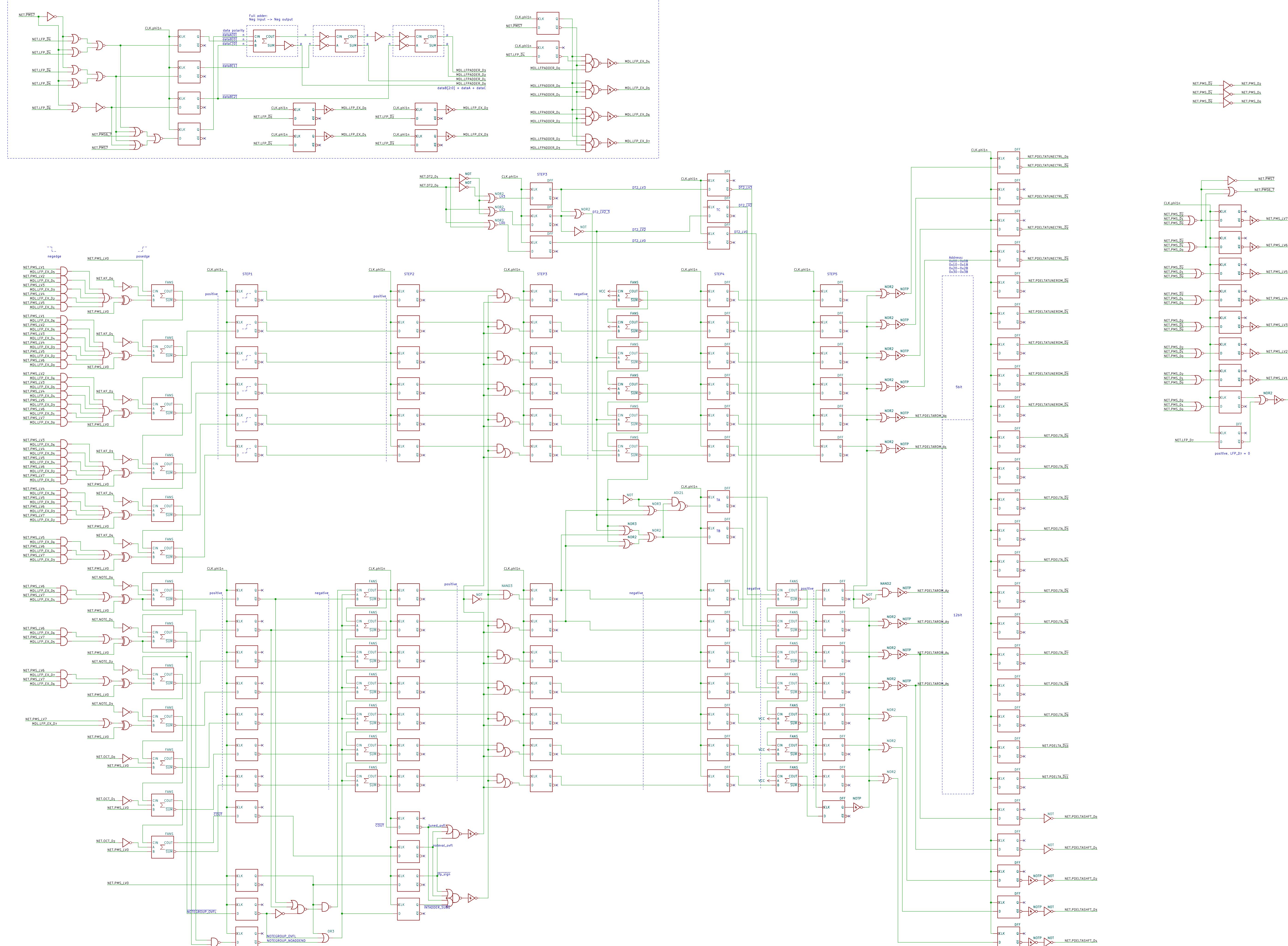
NET.CYC[10]

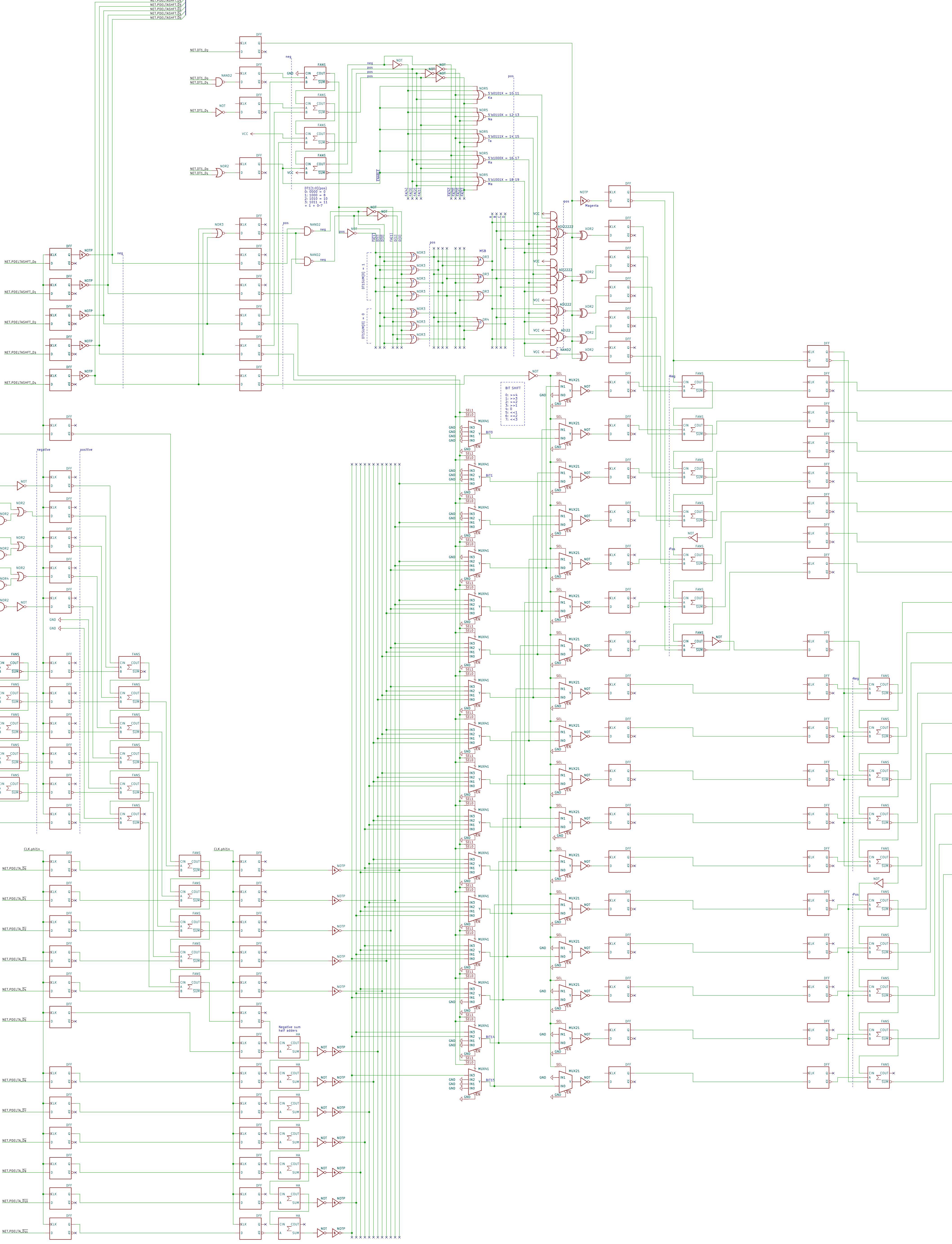
NET.CYC[9]

NET.CYC[8]

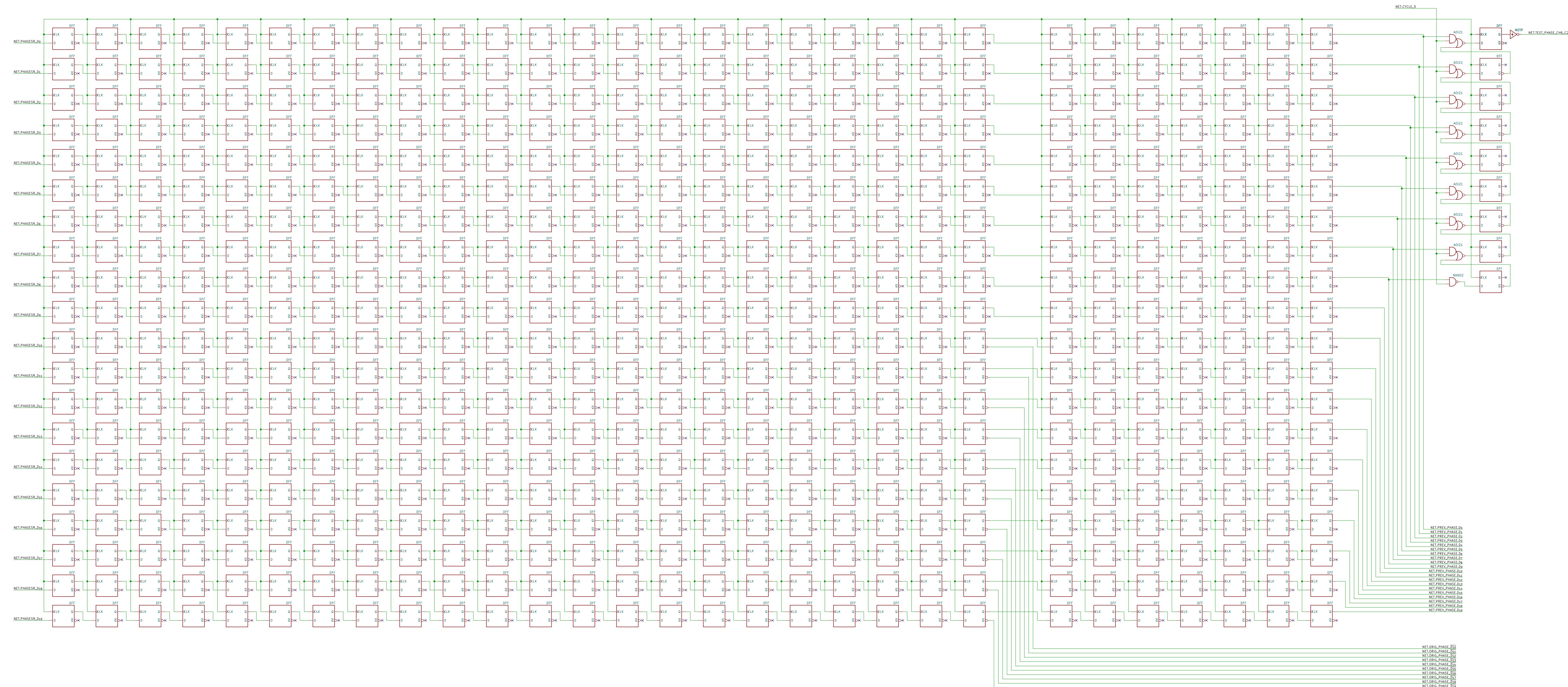
NET.CYC[7]

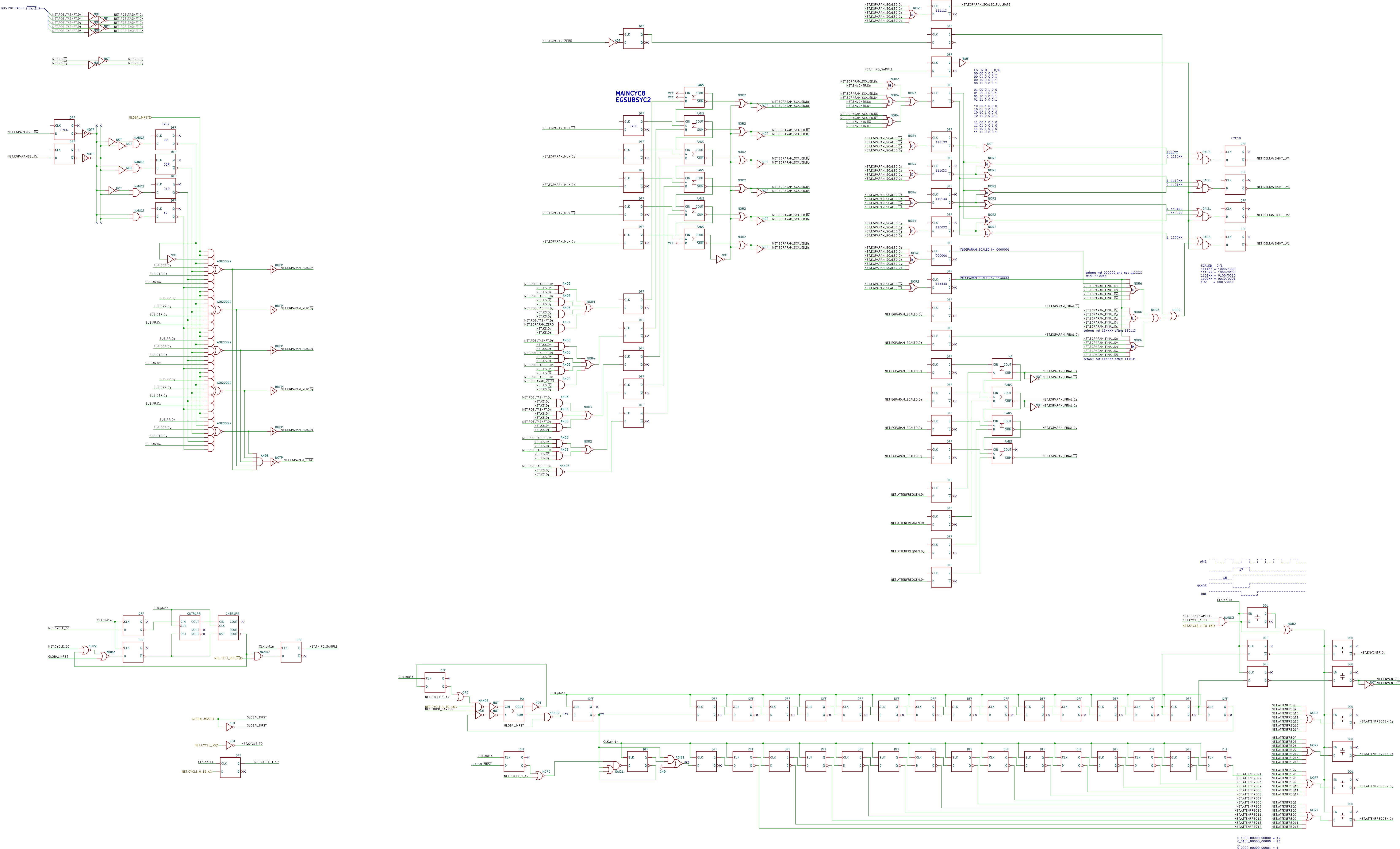
Extended LFP

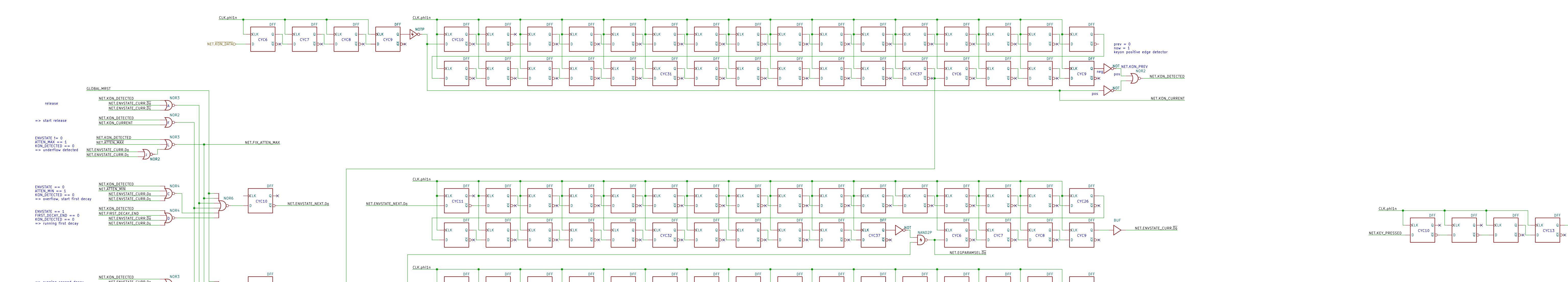
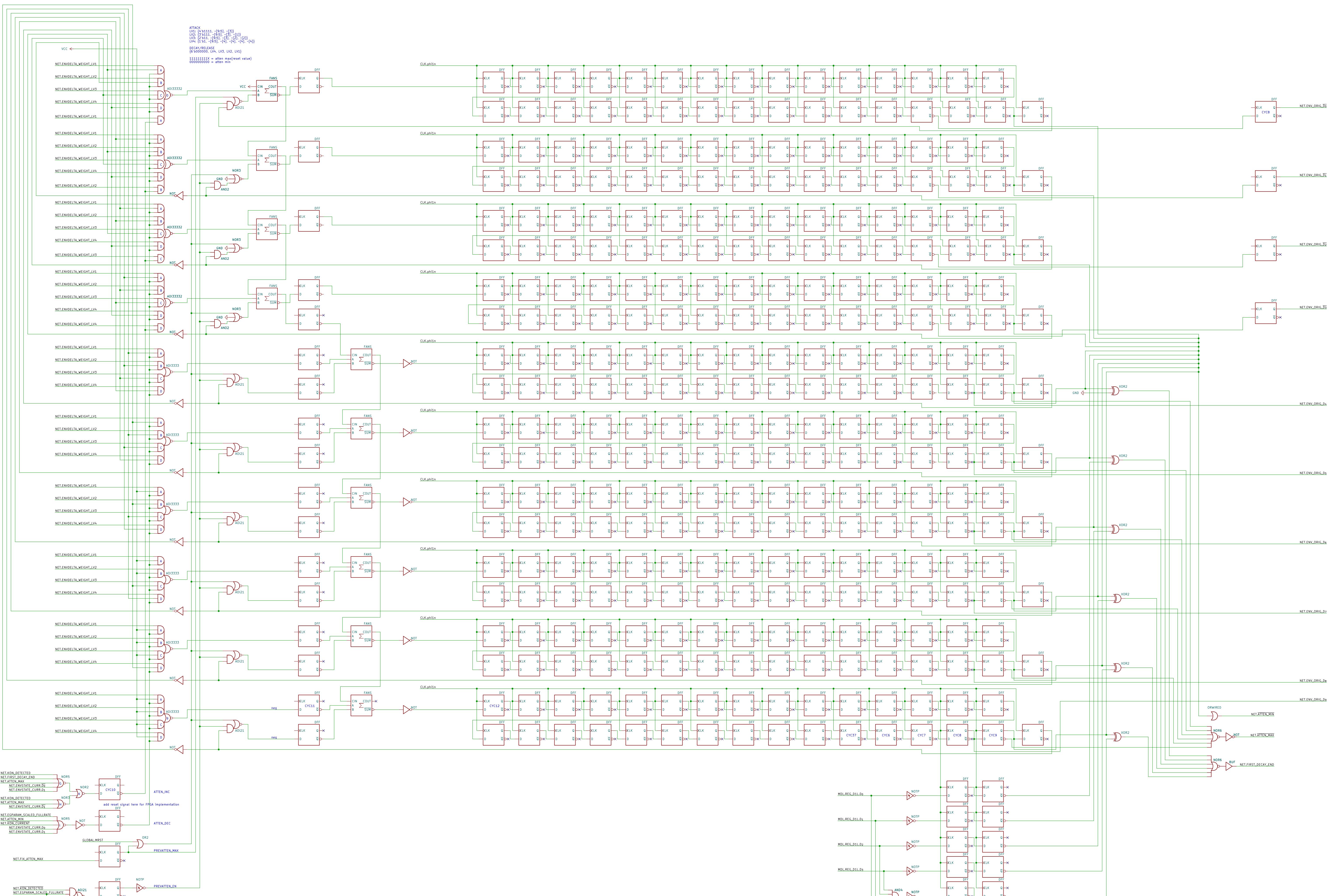












Legend for net names:

- `NETENDELTA_CURRENT`
- `NETENSTATE_CURR_D0`
- `NETENSTATE_CURR_D1`
- `NETFIRSTDECAYEND`
- `NETFIRSTDECAYEND`
- `NETENATTEN`
- `NETATTENMAX`
- `NETATTENMIN`

Global signals:

- `GLOBAL_MST`
- `GLOBAL_RST`

