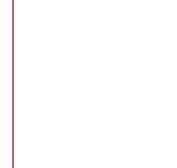


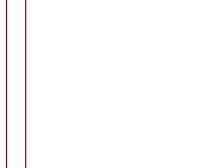
A

REG



File: REG.kicad_sch

LFO



File: LFO.kicad_sch

PG0



File: PG0.kicad_sch

PG1



File: PG1.kicad_sch

EG0



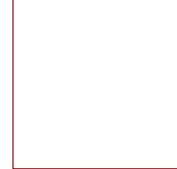
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EG1



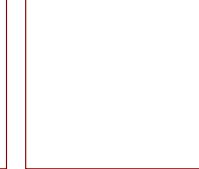
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OP0



File: OP0.kicad_sch

ACC



File: ACC.kicad_sch

TIMINGGEN



File: TIMINGGEN.kicad_sch

C

D

A

B

C

D

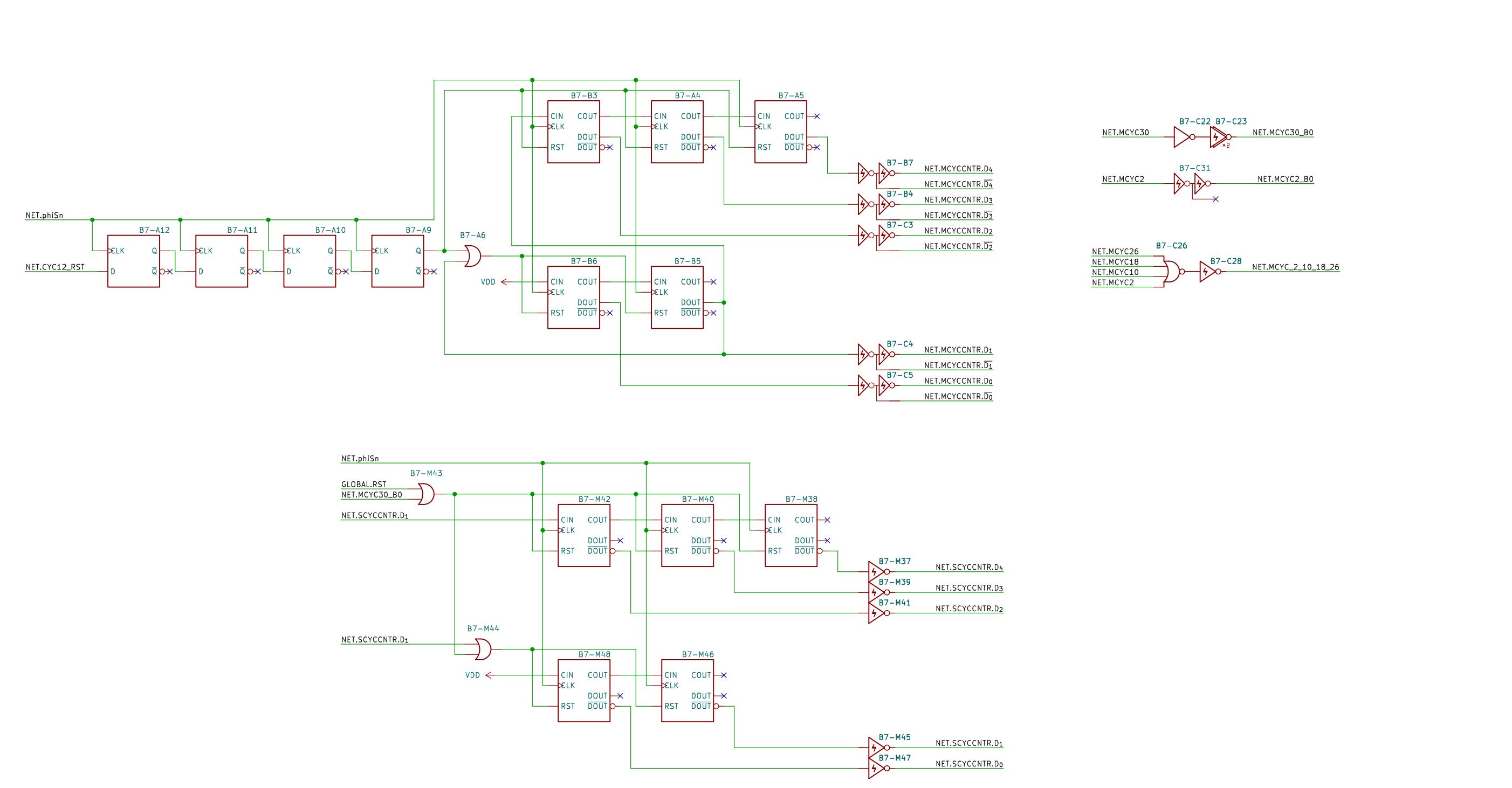
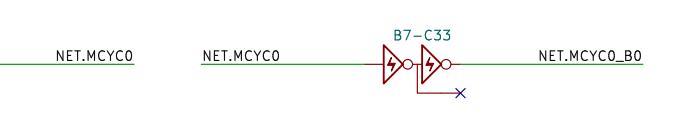
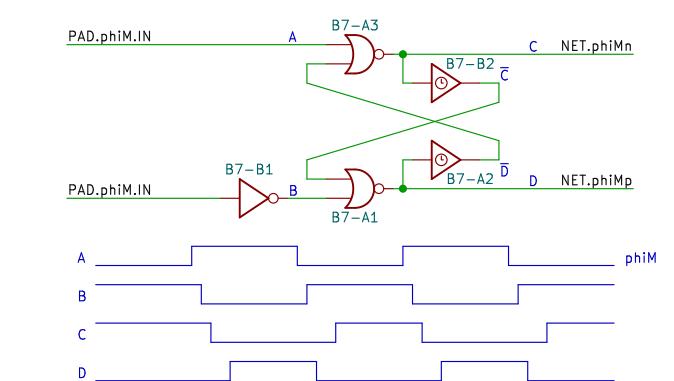
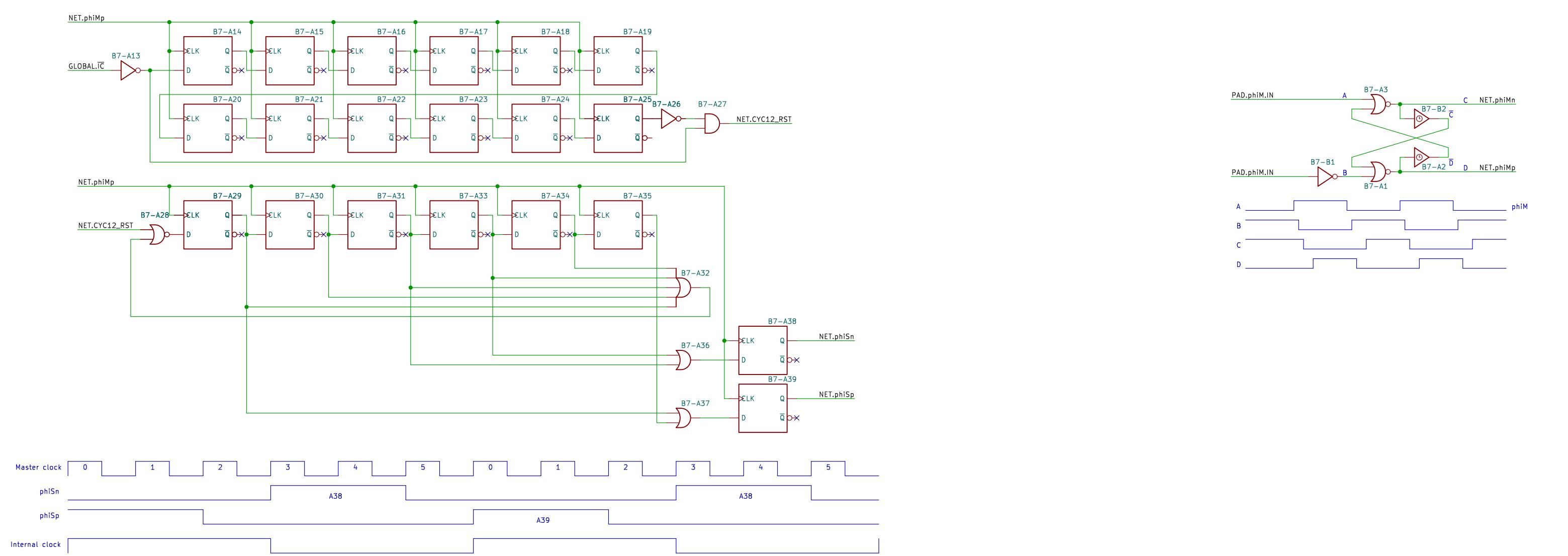
YM3438 preliminary schematics
IKA Victor Co., Inc.

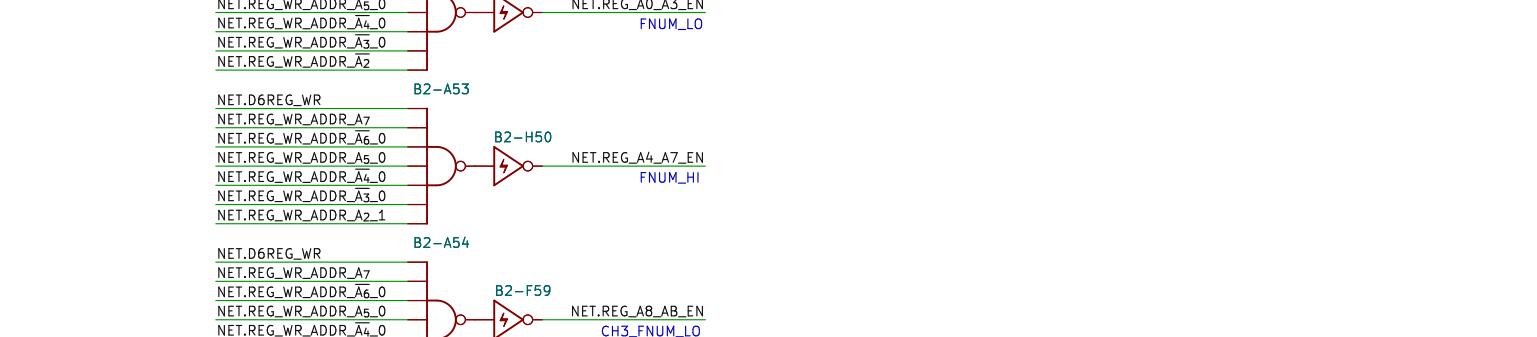
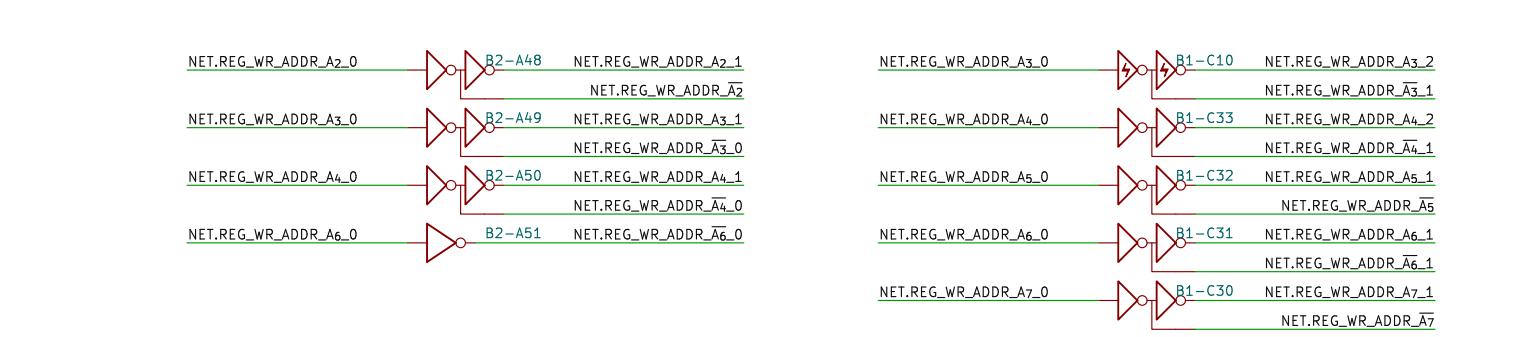
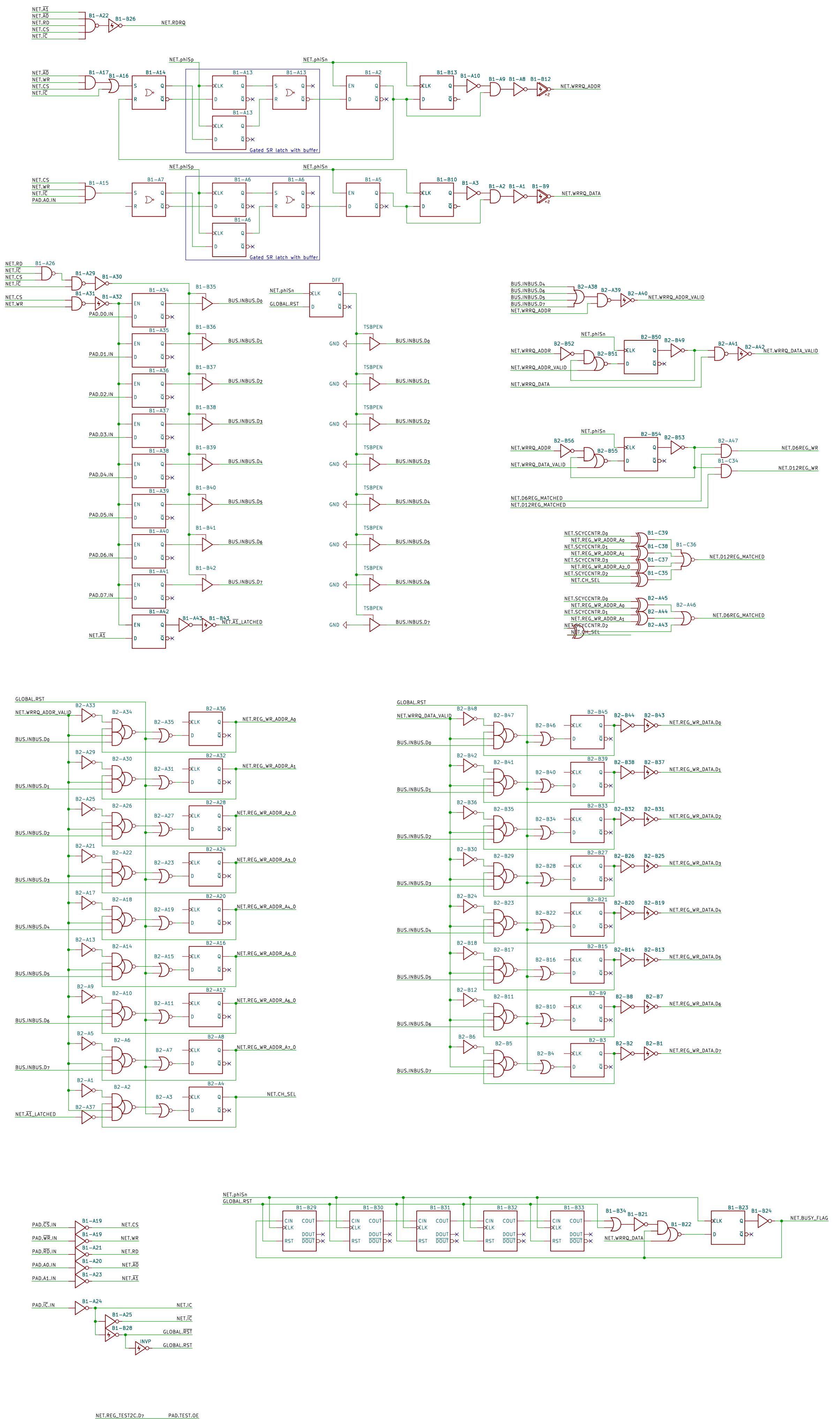
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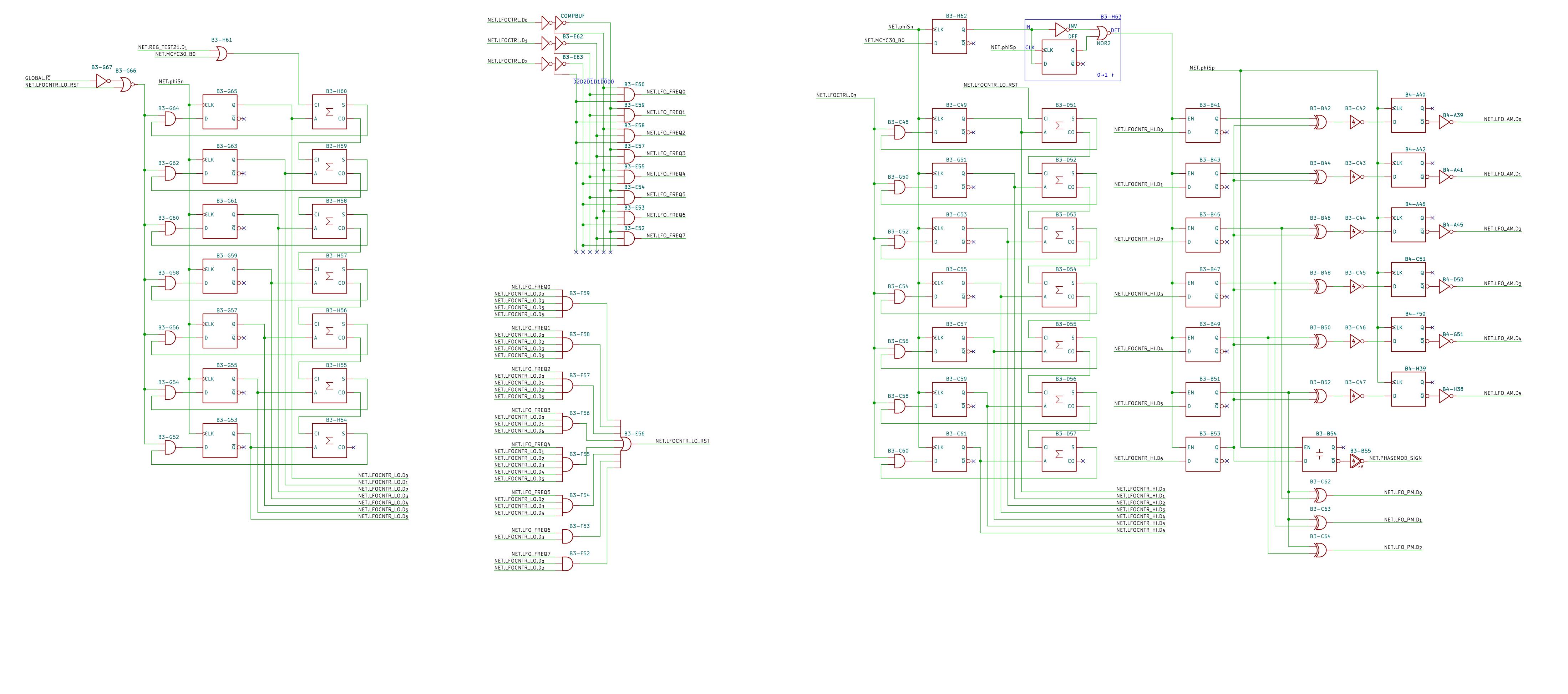
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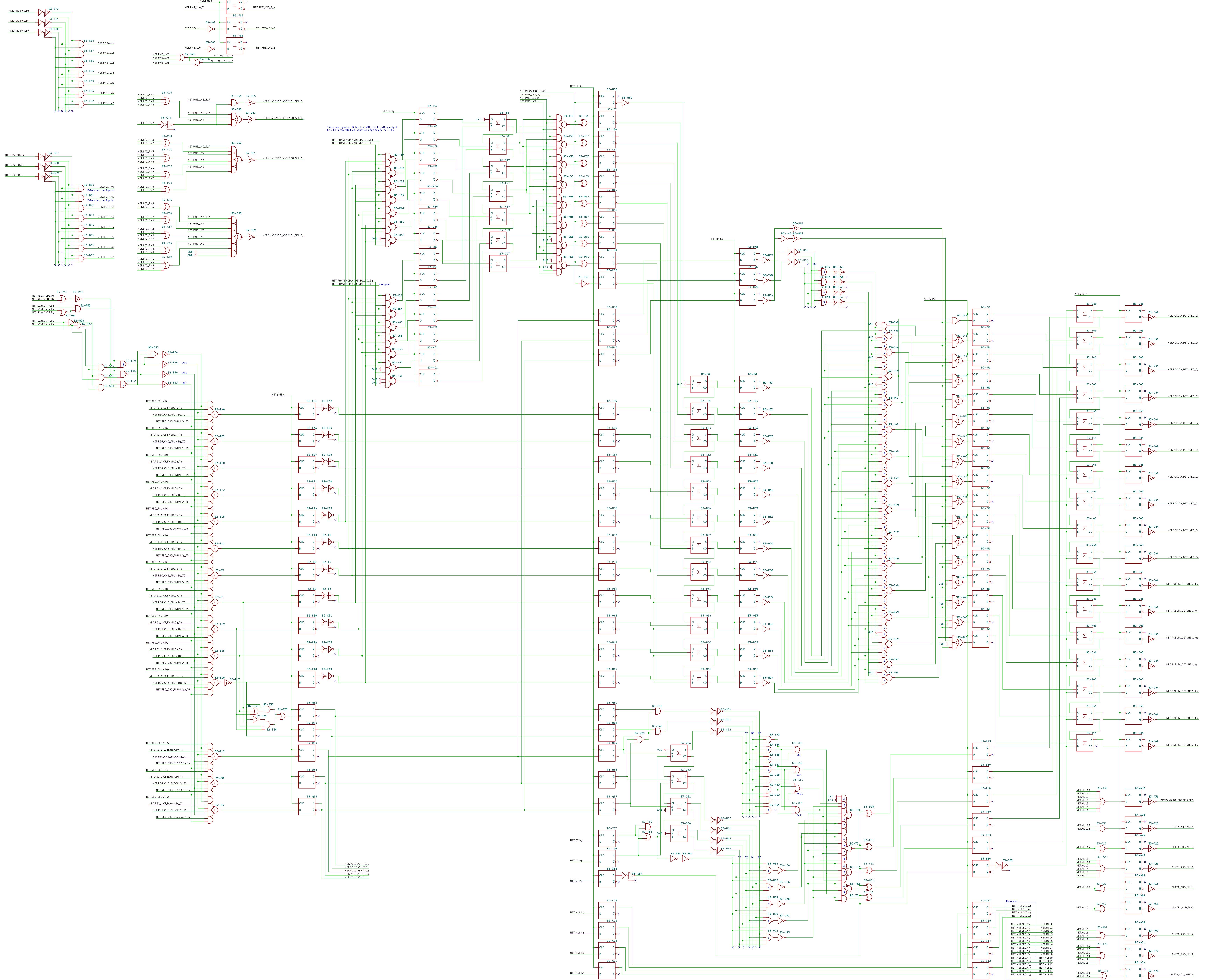
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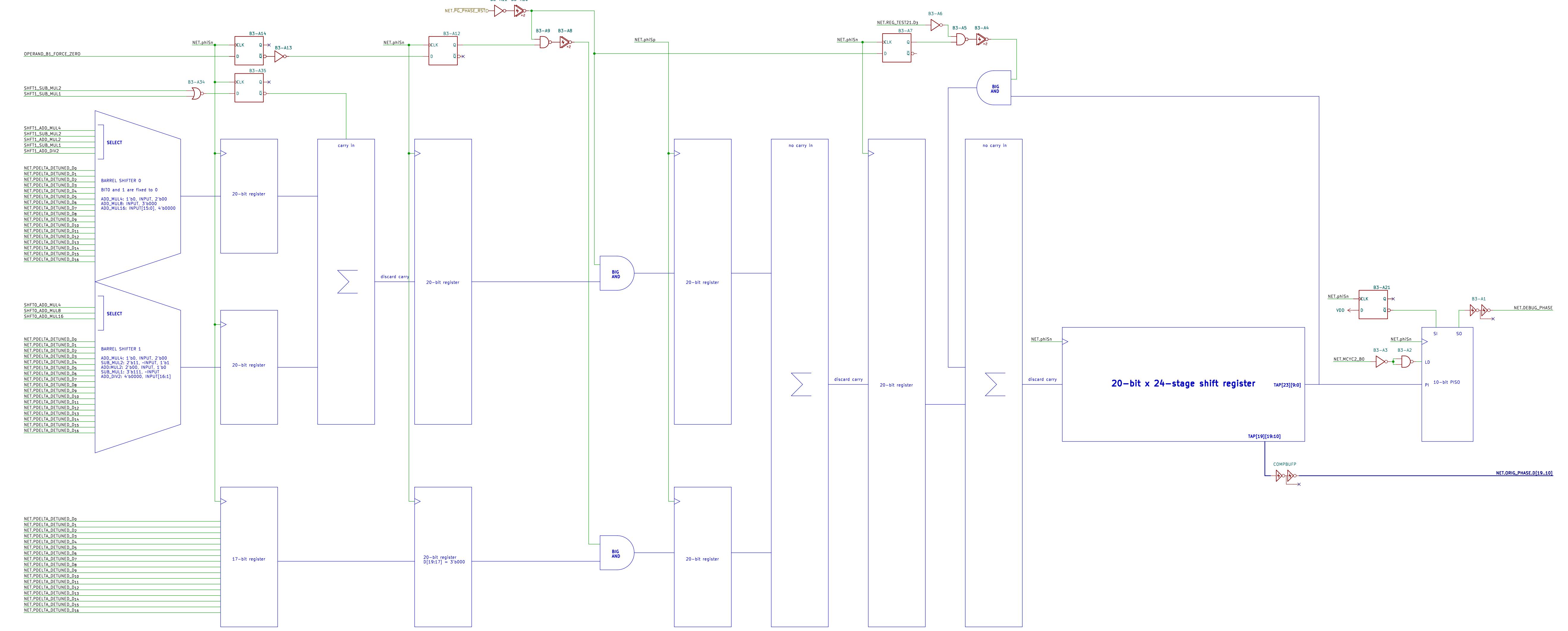
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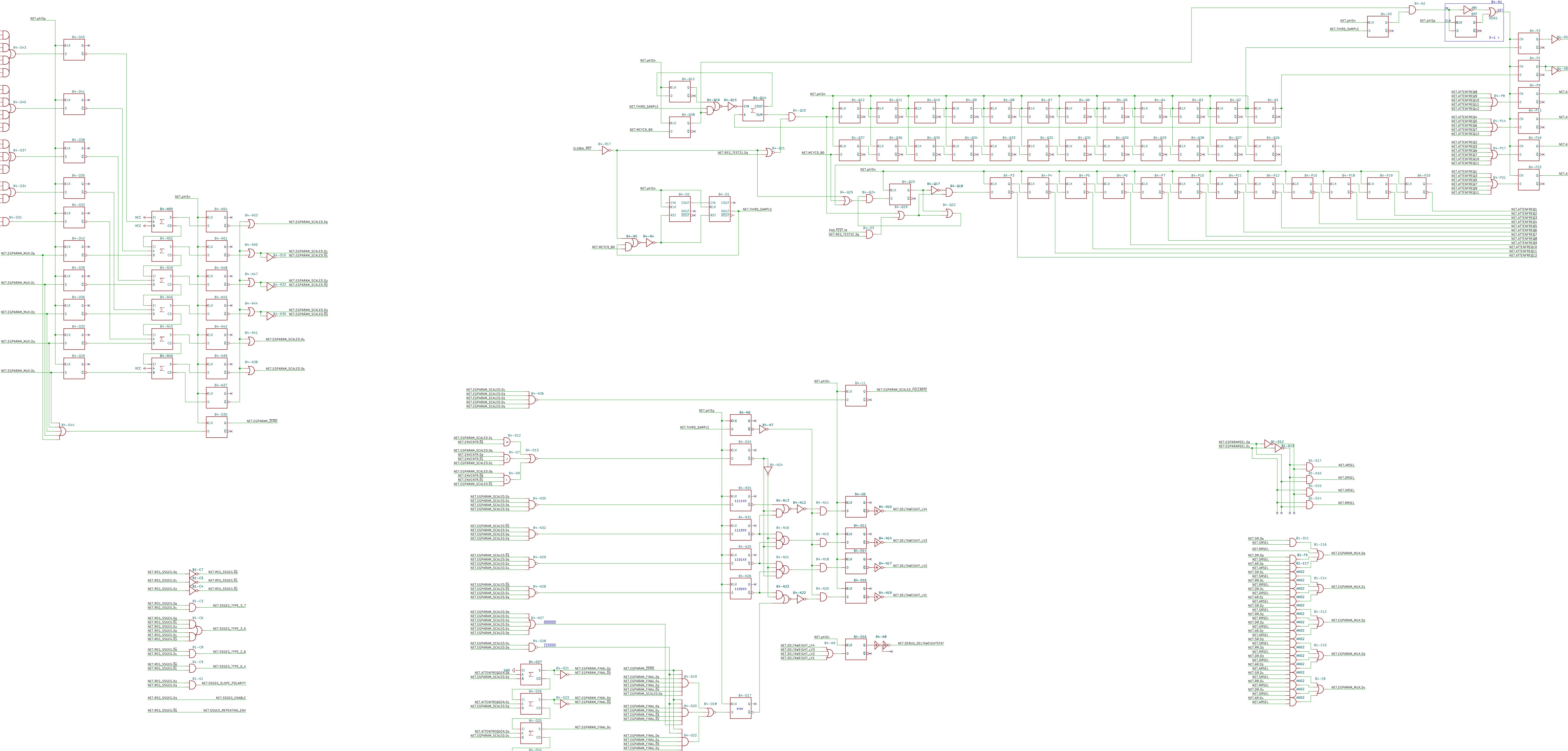





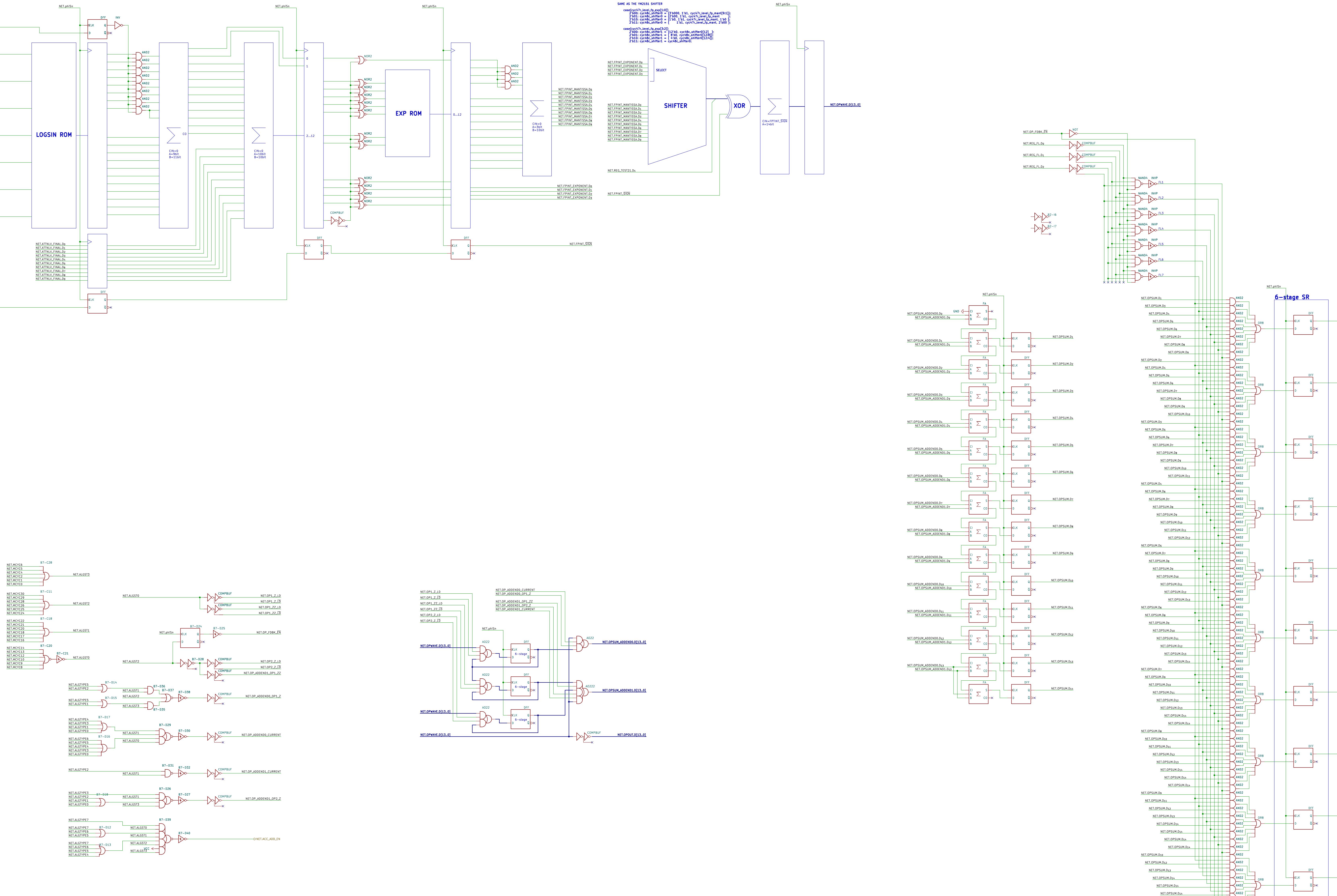


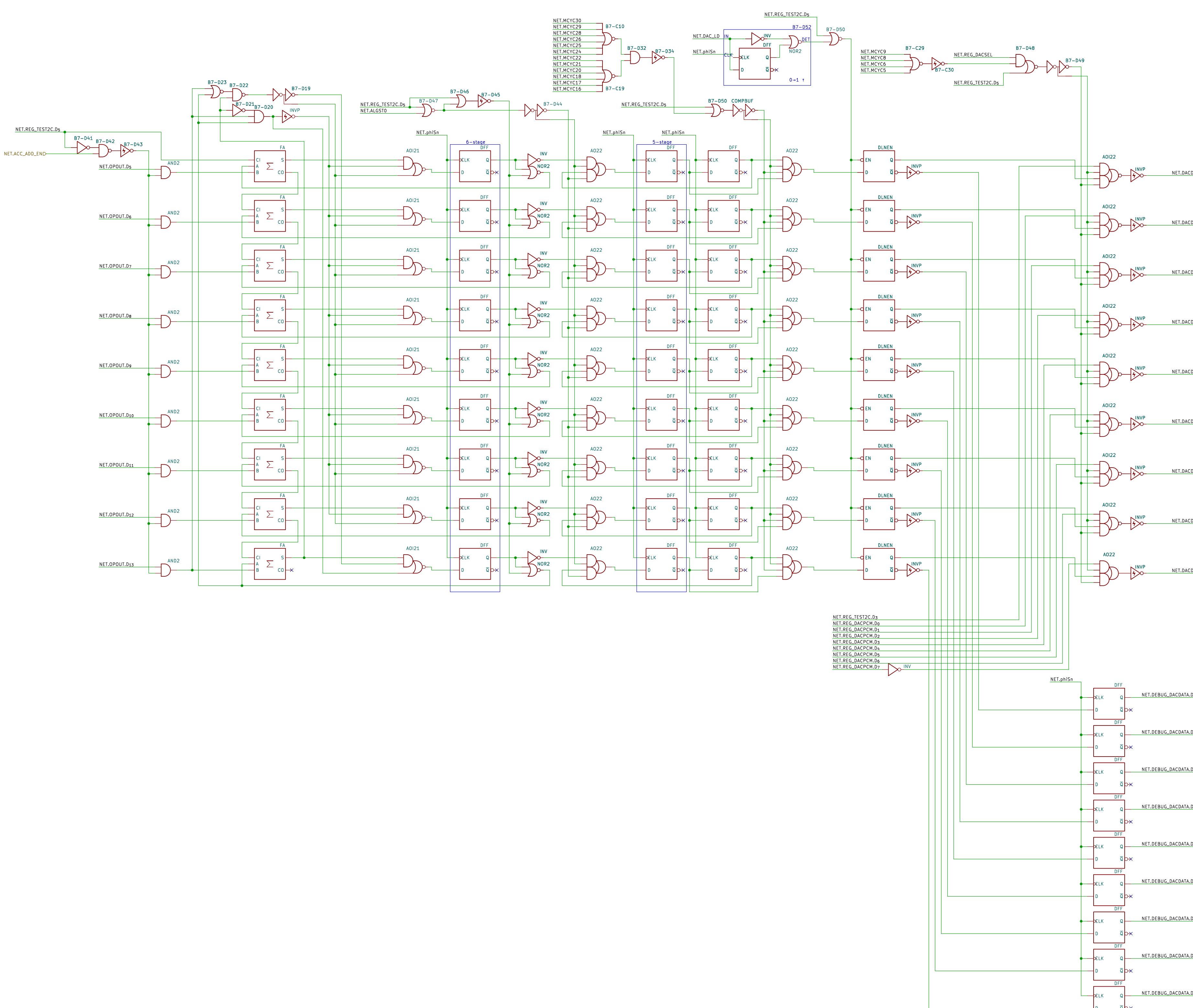




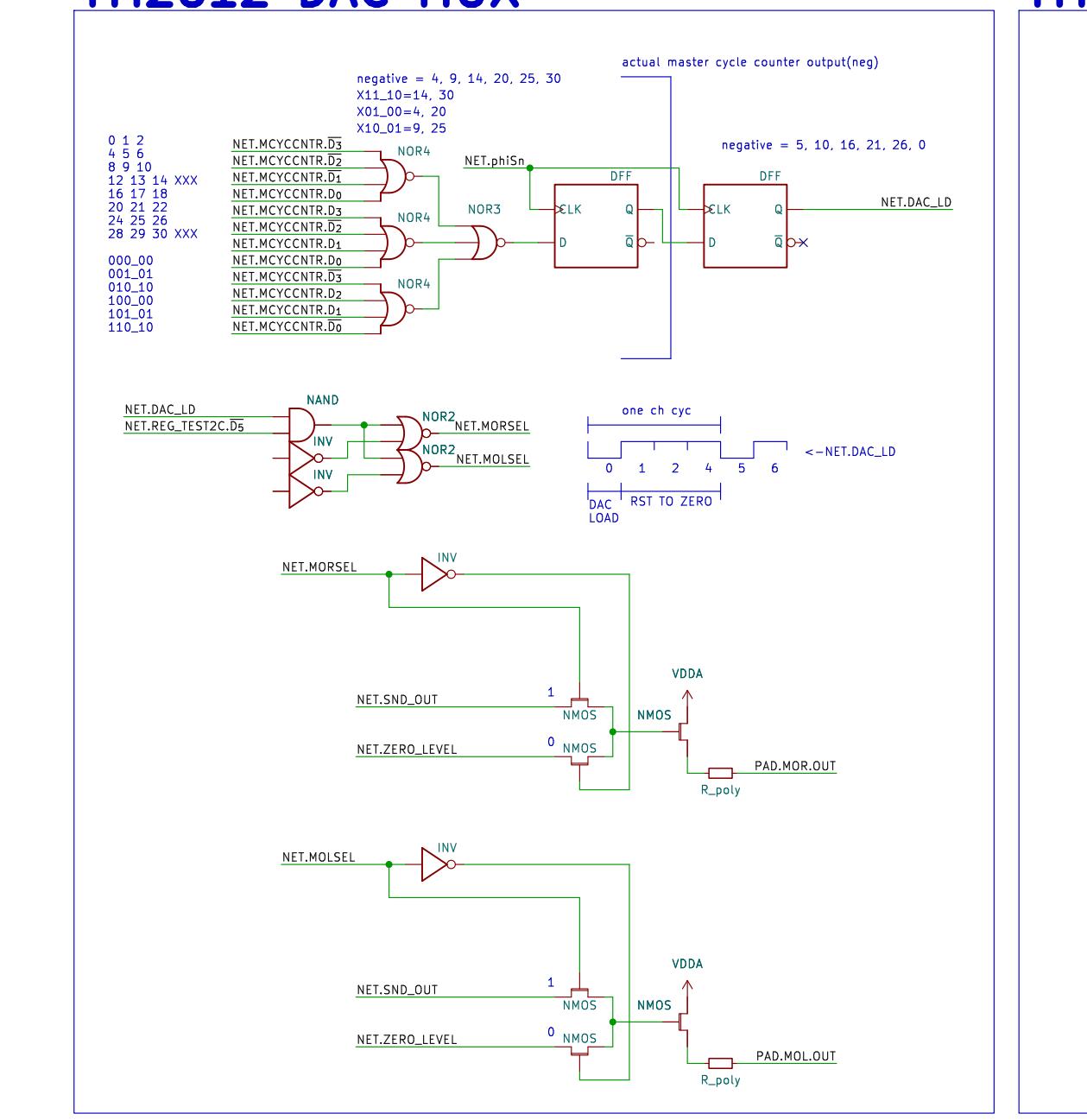




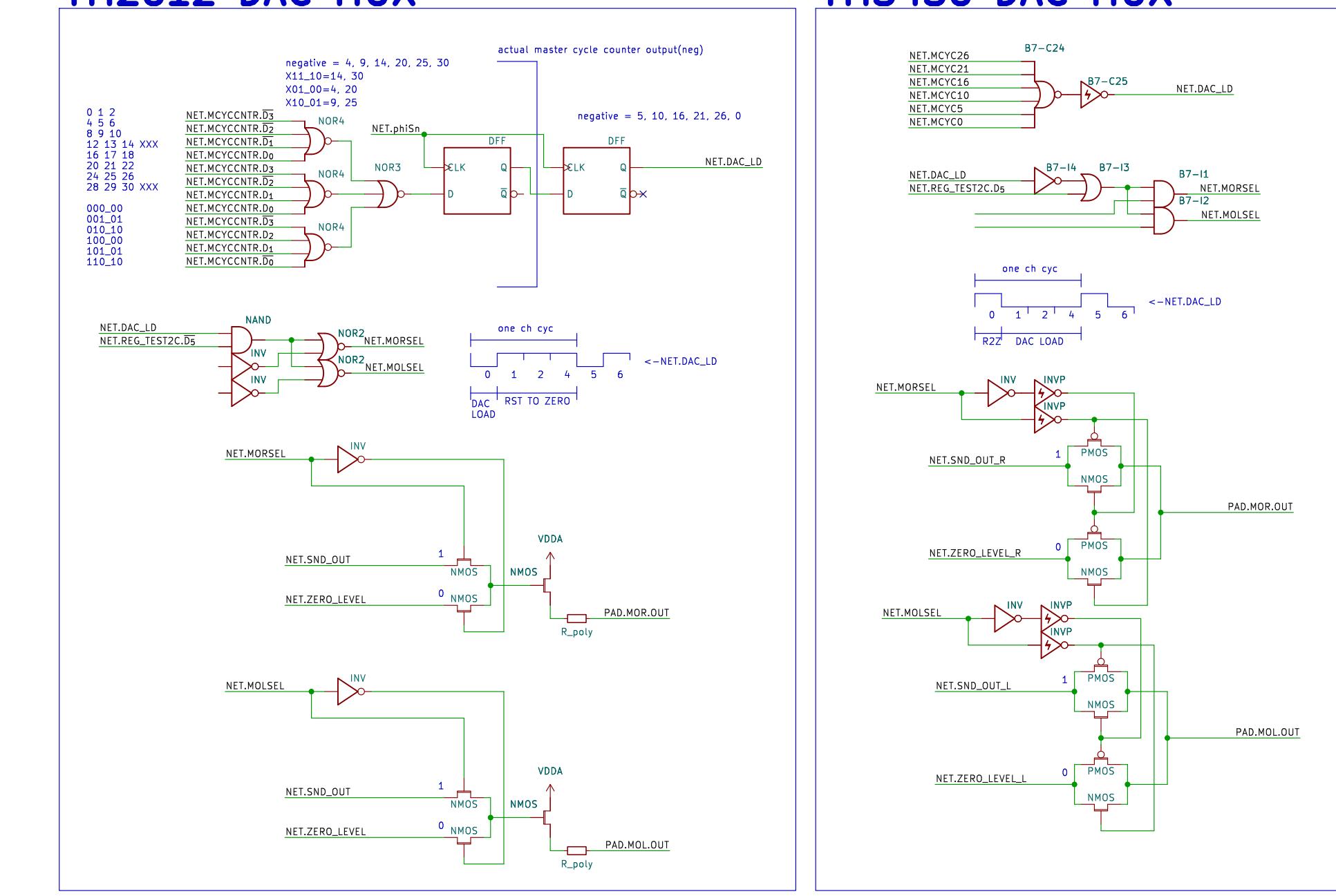




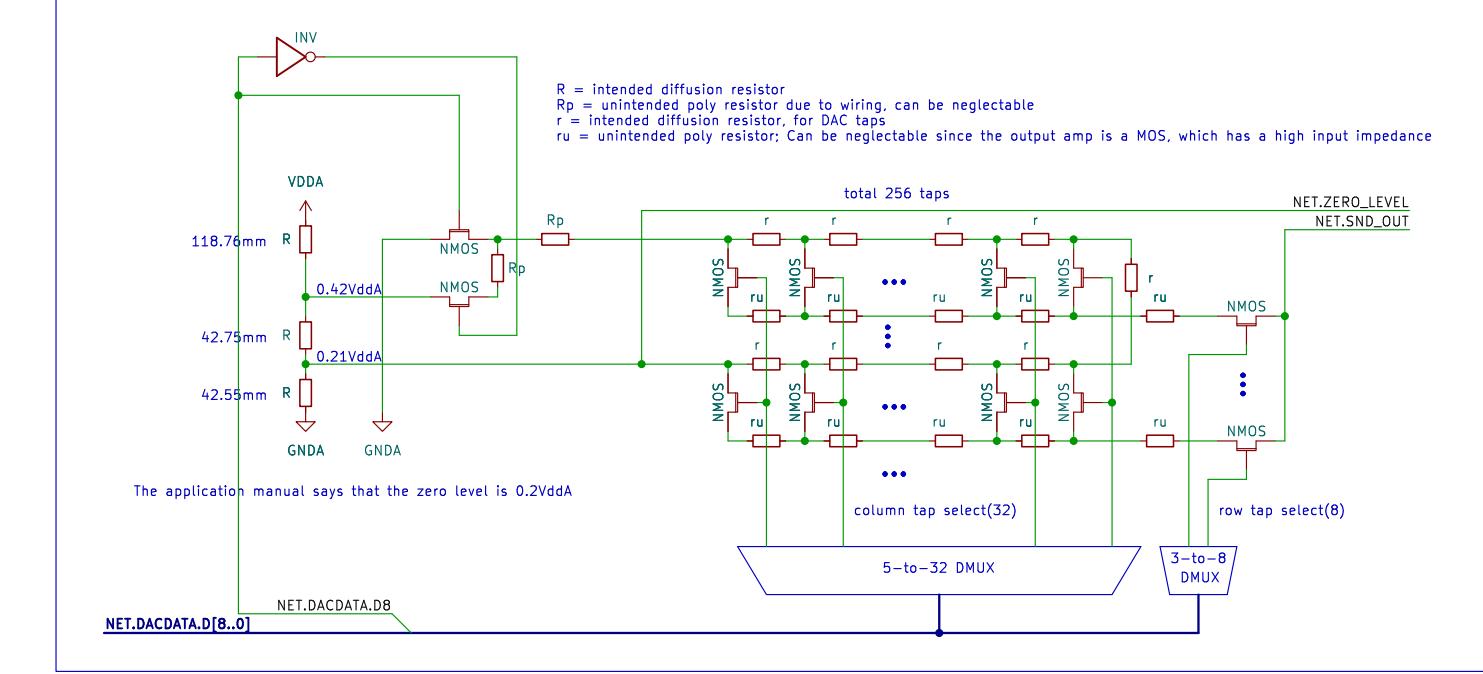
YM2612 DAC MUX



YM3438 DAC MUX



YM2612 DAC TOPOLOGY



YM3438 DAC TOPOLOGY

