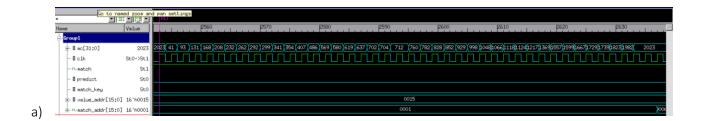
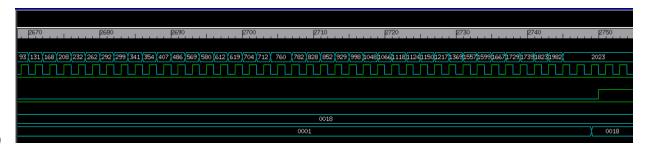
Content Addressable Reflex Memory Waveforms for "Role of a Sub-20nm CAM-based Reflex Memory in Augmenting the Hierarchical Temporal Memory"

1. This document shows the waveforms for Content-Addressable Reflex Memory (CARM) since the images are too large for the manuscript. Figure a) shows 41 clock cycles to acknowledge the 'key' through the minicolumns (MCs). Figure (c) shows 16 extra cycles before processing the 'value.' Image (b) and (c) show the 41 clock cycles for the MCs in the 'value.' There are three additional cycles to set the predicted signal, for a total of 101 clock cycles. These figures endorse the measurements on the "Latency Table," which further supports the table in the manuscript.







c)