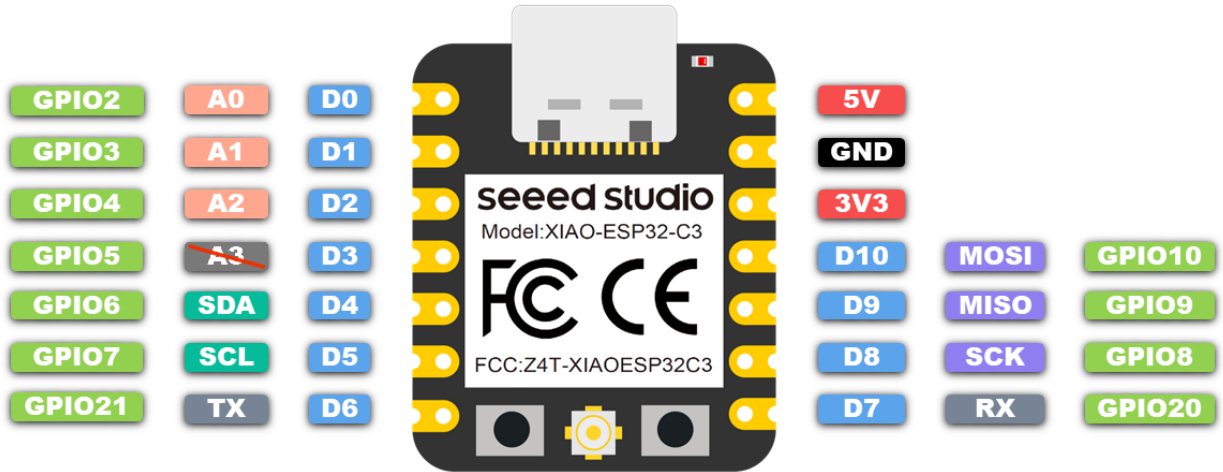
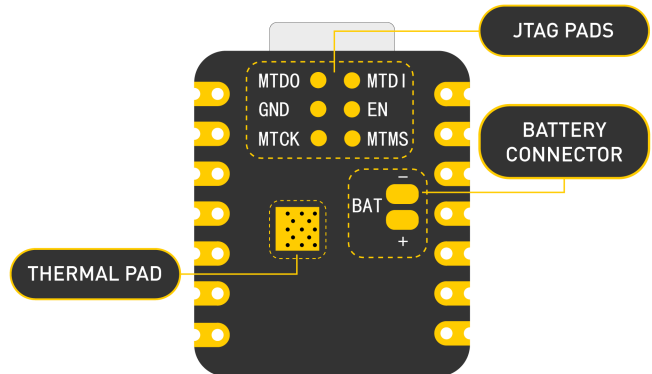
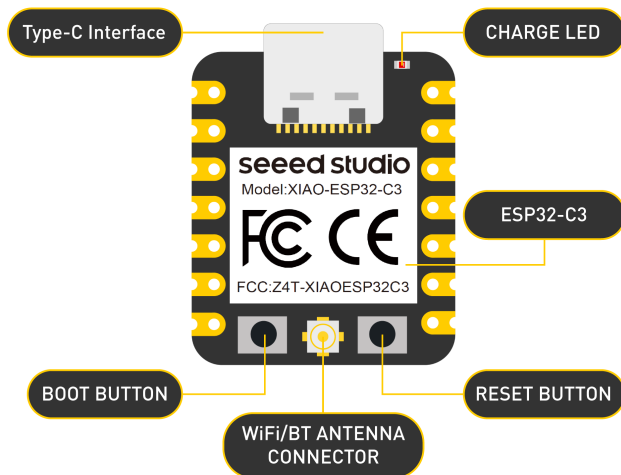


XIAO ESP32-C3

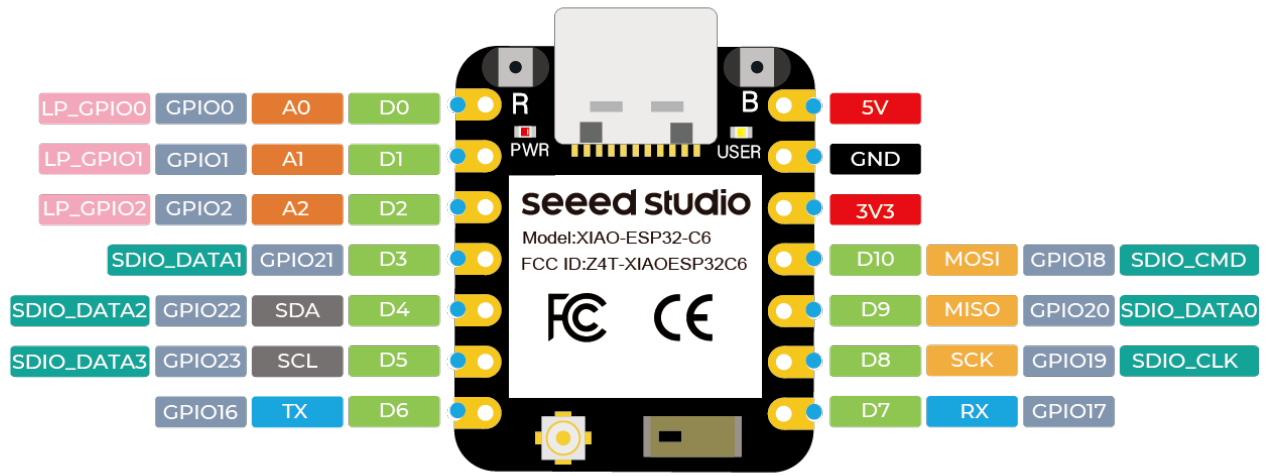


*A3(GPIO5) - Uses ADC2, which may become inoperative due to false sampling signals. For reliable analog reads, use ADC1 instead. Refer to the ESP32-C3 datasheet.

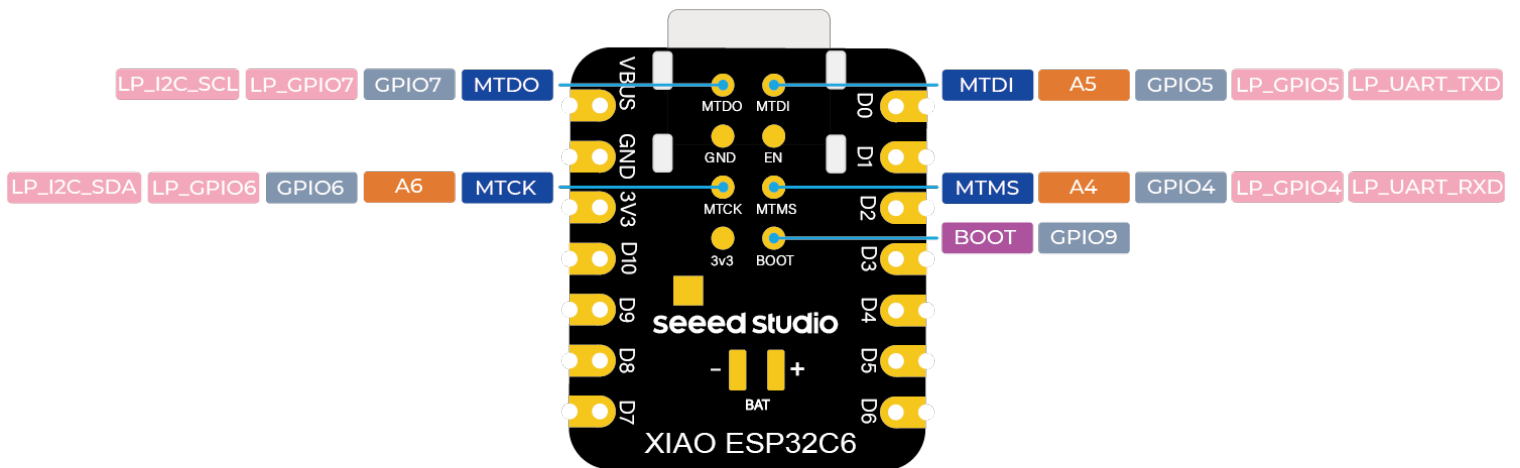


XIAO ESP32-C6

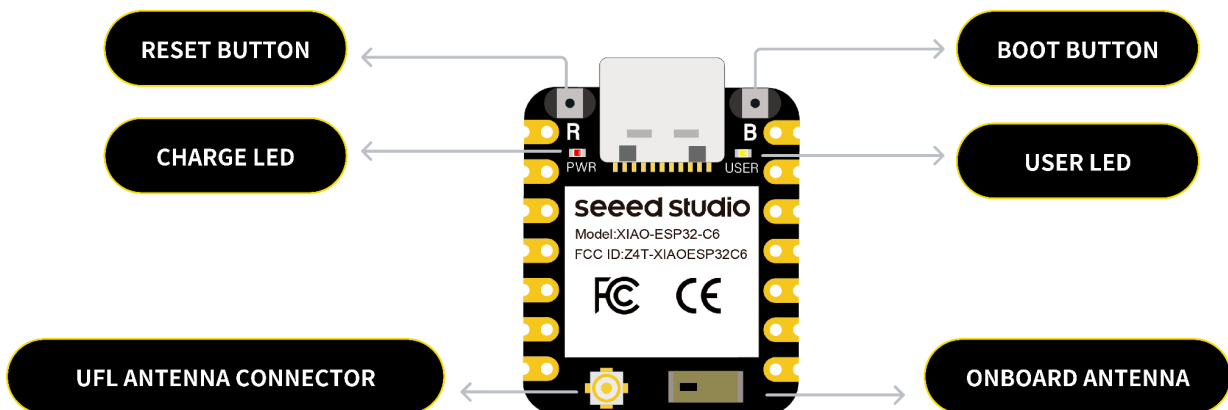
FRONT



BACK

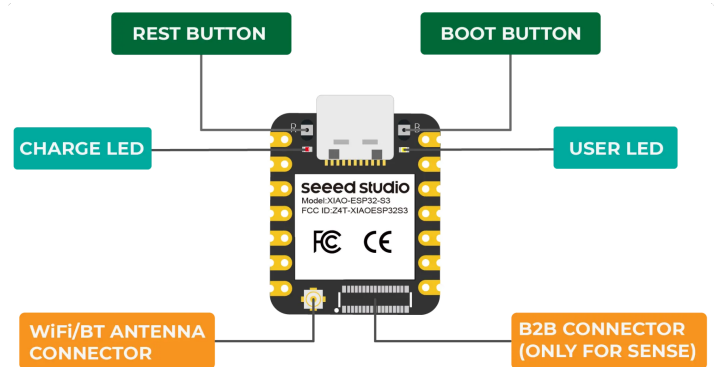
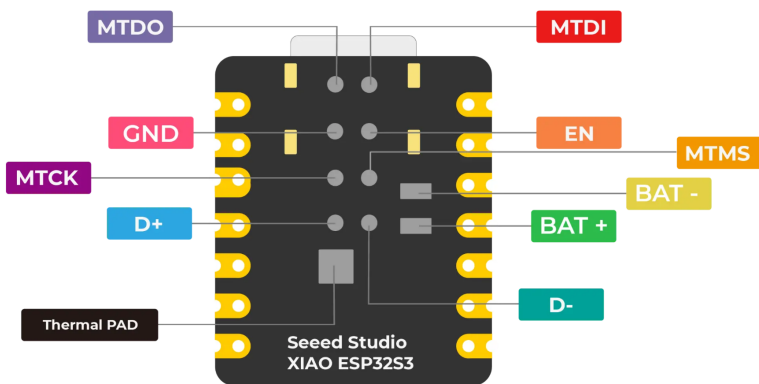
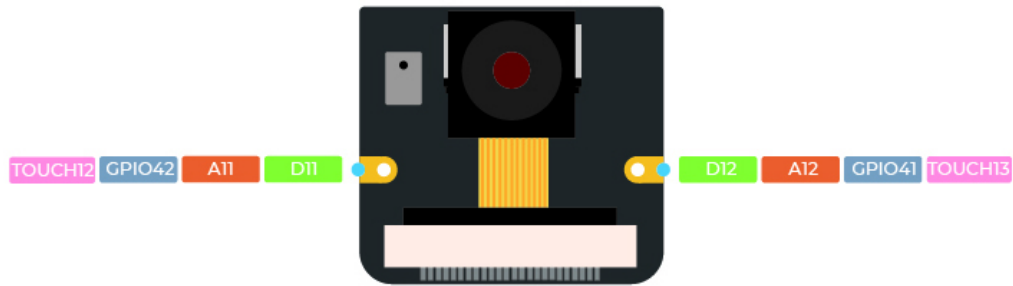
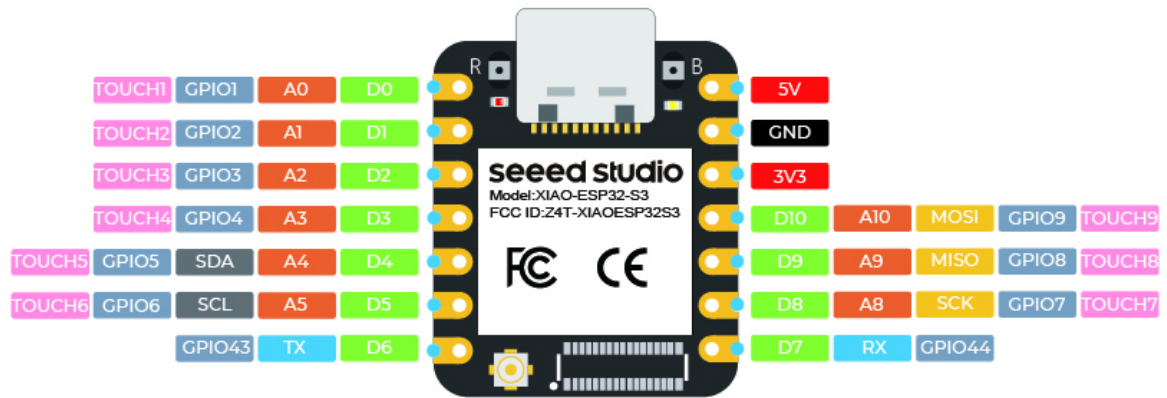


Digital Analog GPIO IIC UART SPI LP IO MUX SDIO JTAG BOOT Power GND ALL PWM ALL I2S



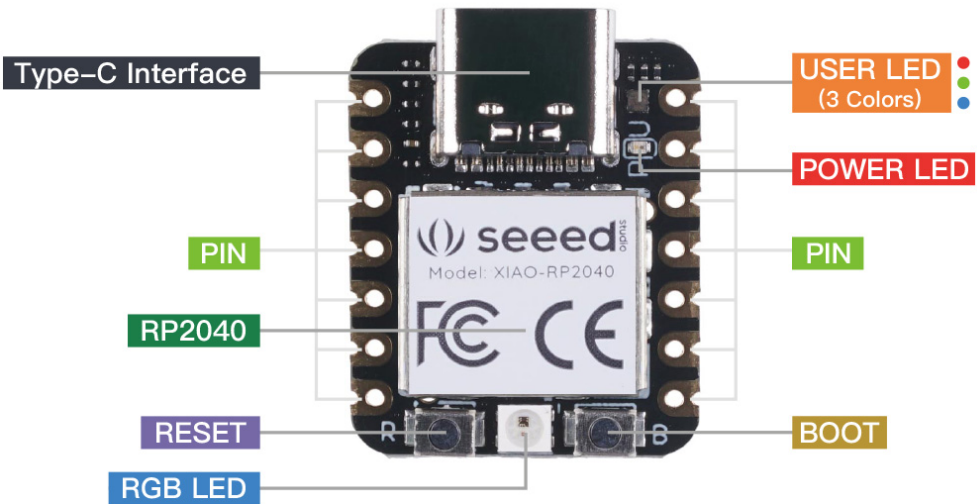
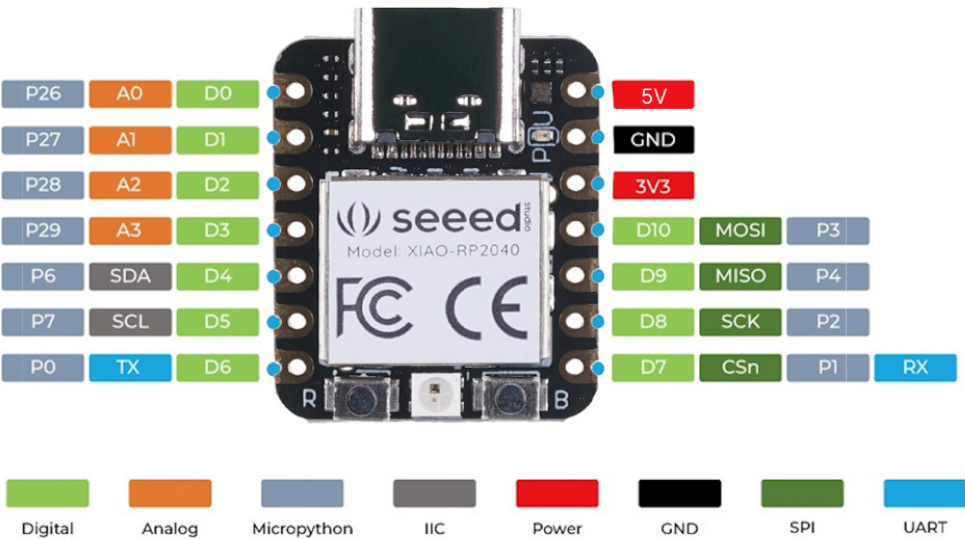
XIAO ESP32-S3

FRONT



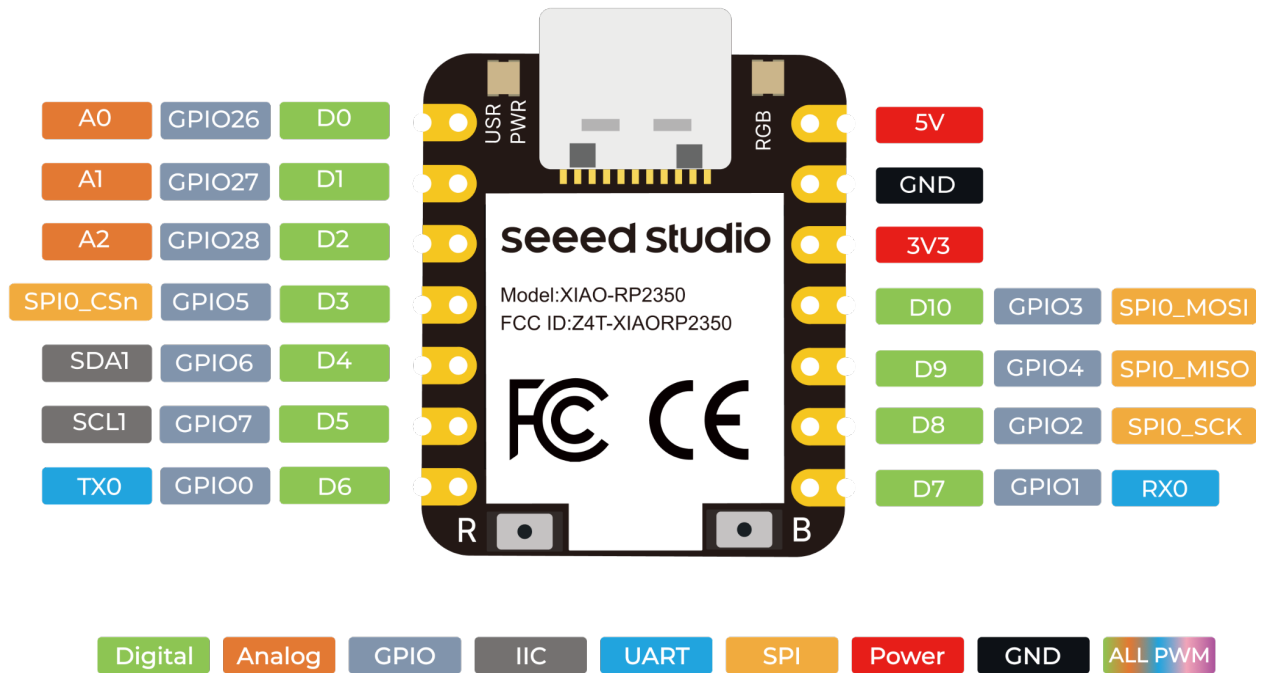
XIAO RP2040

FRONT



XIAO RP2350

Front



Back

