

Microcode

\PH* PH ^ currInt<<2
 \PL* PL ^ currInt<<2

test Din == 011xxxxx
 math Din != x11xxxxx
 jump Din == 111x11xx
 imm Din == 111xxxxx & !jump
 k Din == xxx1xxxx
 zs Din == xxxx11xx
 zd Din == xxxxxx11
 jop Din[4,1:0]
 aluOp Imm ? Din[4:2] : Din[7:5]
 cond Din[3:0]
 \fetch TZ?fetcha:timer

\decodeSkip \decode

test Math & zs & zf Math & !zs & zd Math & zs & !zd Math & !zs & !zd Imm & zd Imm & !zd Jump & jop == 0 Jump & jop == 1 Jump & jop == 2 Jump & jop == 3 Jump & jop == 4 Jump & jop == 5 Jump & jop == 6 Jump & jop == 7	fetch skip2 skip1 skip1 fetch skip2 skip1 skip2 skip2 fetch fetch skip1 skip1 fetch skip1	test useK?\$zpa,mathzzb:\$zpa,mathzz useK?\$zpa,kmathrz:\$zpa,mathrz \$zpa,mathzr mathrr mathiz mathir jump call fetch return \$zpa,jumpz callz fetch \$zpa,table	note:
---	---	---	-------

\memz svZInd?mem:(svZReg?(svZIO?(1,reg[0]):B):mem)
 \mA SvAluOp[2]?00:SvAluOp[1:0]
 \mL SvAluOp[2]?svAluOp[1:0]:11
 \mCin !svAluOp[2]&svAluOp[0]
 \S {00,svS}
 \D {00,svD}
 \kD prevK ? 0011 : {00,svD}
 \kD2 useK ? 0011 : {00,svD}

label	aAd	bAd	wAd	wReg	dIn	rMem	dOut	wMem	aSel	bSel	logSel	Cin
reset	x	x	PL		1 ?		0 ?		0	0 B	^	0
resetb	x	x	PH		1 ?		0 ?		0	0 B	^	0
resetc	x	x	TL		1 ?		0 ?		0	0 B	^	0
resetd	x	x	TH		1 ?		0 ?		0	0 B	^	0
timer	x	TL	TL		1 ?		0 ?		0	-2 B	B	1

Microcode

timerb	x	TH	TH	1 ?	0 ?	0	-2 B	B	1
timerc	x	TL	TL	1 ?	0 ?	0	-2 B	B	0
fetcha	PH*	PL*	PL*	1 ?	1 ?	0	0 B	B	1
fetchb	x	PH*	PH*	1 Inst	0 ?	0	0 B	B	1
fetchc	x	x	x	0 Inst	0 ?	0 x	x	x	x
skipf	PH*	PL*	PL*	1 ?	1 ?	0	0 B	B	1
skipfb	x	PH*	PH*	1 Inst	0 ?	0	0 B	B	1
skipfc	x	x	x	0 Inst	0 ?	0 x	x	x	x
skip2	x	PL*	PL*	1 ?	0 ?	0	0 B	B	1
skip2b	x	PH*	PH*	1 ?	0 ?	0	0 B	B	1
skip1	x	PL*	PL*	1 ?	0 ?	0	0 B	B	1
skip1b	x	PH*	PH*	1 ?	0 ?	0	0 B	B	1
test	x	x	K	1 ?	0 ?	0	0 B	^	tr
mathrr	S	kD	kD2	1 ?	0 ?	0 mA	B	mL	mCir
call	x	PL*	LL	1 ?	0 ?	0	0 B	B	0
callb	x	PH*	LH	1 ?	0 ?	0	0 B	B	0
jump	PH*	PL*	PL*	1 ?	1 ?	0	0 B	B	1
jumpb	x	x	ML	1 PCL	0 ?	0	0 mem	B	0
jumpc	x	x	ML	1 PCL	0 ?	0	0 mem	B	0
jumpd	x	PH*	PH*	1 ?	0 ?	0	0 B	B	1
jumpe	PH*	PL*	x	0 ?	1 ?	0 x	x	x	x
jumpf	x	x	PH*	1 PCH	0 ?	0	0 mem	B	0
jumpg	x	ML	PL*	1 ?	0 ?	0	0 B	B	0
return	x	LL	PL*	1 ?	0 ?	0	0 B	B	0
returnb	x	LH	PH*	1 ?	0 ?	0	0 B	B	0
mathir	PH*	PL*	PL*	1 ?	1 ?	0	0 B	B	1
mathirb	kD	x	D	1 #	0 ?	0 mA	mem	mL	mCir
mathirc	kD	x	D	1 #	0 ?	0 mA	mem	mL	mCir
mathird	x	PH*	PH*	1 ?	0 ?	0	0 B	B	1
zpa	PH*	PL*	PL*	1 ?	1 ?	0	0 B	B	1
zpab	x	x	ZL	1 zpa	0 ?	0	0 mem	B	0
zpac	x	x	ZL	1 zpa	0 ?	0	0 mem	B	0
zpad	x	PH*	PH*	1 ?	0 ?	0	0 B	B	1
zpaе	ZH	ZL	x	0 ?	1 ?	0 x	x	x	x
zpaф	ZH	ZL	x	0 ?	1 ?	0 x	x	x	x
zpag	x	x	ML	1 lowp	0 ?	0	0 mem	B	0
zpah	ZH	ZL	x	0 ?	1 ?	0 x	x	x	x
zpai	x	x	MH	1 highp	0 ?	0	0 mem	B	0
zpai	x	ML	x	0 ?	0 ?	0	0 B	B	1
zpak	ZH	ZL	x	0 ?	0 ML+1	1 x	x	x	x
zpal	ZH	ZL	x	0 ?	0 ML+1	1 x	x	x	x
zpaм	x	MH	x	0 ?	0 ?	0	0 B	B	1
zpan	ZH	ZL	x	0 ?	0 MH+1	1 x	x	x	x
zpaо	MH	ML	x	0 ?	1 ?	0 x	x	x	x

Microcode

mathiz	PH*	PL*	PL*	1 ?	1 ?	0	0 B	B	1
mathizb	x	reg	K	1 #	0 ?	0	0 memz	B	0
mathizc	x	reg	K	1 #	0 ?	0	0 memz	B	0
mathizd	K	reg	K	1 #	0 ?	0 mA	memz	mL	mCir
mathize	K	reg	K	1 #	0 ?	0 mA	memz	mL	mCir
mathizf	x	PH*	PH*	1 ?	0 ?	0	0 B	B	1
mathizg	x	PH*	PH*	1 ?	0 ?	0	0 B	B	1
mathizo	K	reg	K	1 value	0 ?	0 mA	memz	mL	mCir
mathizp	x	K	x	0 ?	0 ?	0	0 B	B	0
mathizq	MH	ML	x	0 ?	0 K	1 x	x	x	x
mathizr	x	K	reg	1 ?	0 K	0	0 B	B	0
mathizs	x	K	reg	0 ?	0 K	0	0 B	B	0
callz	x	PL*	LL	1 ?	0 ?	0	0 B	B	0
callzb	x	PH*	LH	1 ?	0 ?	0	0 B	B	0
jumpz	x	ML	PL*	1 ?	0 ?	0	0 B	B	0
jumpzb	x	MH	PH*	1 ?	0 ?	0	0 B	B	0
table	LL	reg	ML	1 ?	0 ?	0	0 memz	B	0
tableb	x	LH	MH	1 ?	0 ?	0	0 B	B	1
tablec	x	LH	MH	1 ?	0 ?	0	0 B	B	0
tabled	MH	ML	ML	1 ?	1 ?	0	0 B	B	1
tablee	x	x	PL	1 newPL	0 ?	0	0 mem	B	0
tablef	x	x	PL	1 newPL	0 ?	0	0 mem	B	0
tableg	MH	ML	x	0 ?	1 ?	0 x	x	x	x
tableh	x	MH	MH	1 ?	0 ?	0	0 B	B	1
tablei	x	x	PH	1 newPL	0 ?	0	0 mem	B	0
mathzr	kD	reg	kD2	1 value	0 ?	0 mA	memz	mL	mCir
mathrz	S	reg	K	1 value	0 ?	0 mA	memz	mL	mCir
mathrzb	MH	ML	x	0 ?	0 K	1 x	x	x	x
mathrzc	x	K	reg	1 ?	0 K	0	0 B	B	0
mathrzd	x	K	reg	0 ?	0 K	0	0 B	B	0
kmathrz	S	K	K	1 ?	0 ?	0 mA	B	mL	mCir
mathzz	x	reg	K	1 value	0 ?	0	0 memz	B	0
mathzzb	K	x	K	1 value	0 ?	0 mA	memz	mL	mCir
mathzzc	MH	ML	x	0 ?	0 K	1 x	x	x	x
mathzzd	x	K	reg	1 ?	0 K	0	0 B	B	0
mathzze	x	K	reg	0 ?	0 K	0	0 B	B	0

15: PH	PH*	PH*	PH*
14: PL	?	PL*	PL*
13: MH	MH	MH	MH
12: ML	?	ML	ML
11: iH	?	?	?
10: iL	?	?	?
9: LH	?	LH	LH

Microcode

8:	LL	LL	LL	LL
7:	ZH	ZH	?	?
6:	ZL	?	ZL	ZL
5:	TH	?	TH	TH
4:	TL	?	TL	TL
3:	K	K	K	K
2:	Y	?	?	kD2
1:	X	kD	kD	D
0:	W	S	reg	reg

aSel	bSel	logSel	Cin
x	x	x	tr
mA	memz	^	mCir
	-2 mem	mL	1
	0 B	B	0

Microcode

: useK will become prevK at the end of this clock cycle

sA7	sA0	A0	wIR	wFL	next
-----	-----	----	-----	-----	------

x	x	x	0	0 resetb	PH,PL = 0
x	x	x	0	0 resetc	
x	x	x	0	0 resetd	TH,TL = 0
x	x	x	0	0 fetch	

x	x	x	0	0 Z?(TZ?fetcha:timerb):timer	TL -= 1
---	---	---	---	------------------------------	---------

Microcode

x	x	x	0	0 timerc	TH -= 1
x	x	x	0	0 timer	TL -= 2
	0	0 x	0	0 C?fetchb:fetchc	
x	x	x	1	0 decode	
x	x	x	1	0 decode	
	0	0 x	0	0 C?skipfb:skipfc	
x	x	x	0	0 decodeSkip	
x	x	x	0	0 decodeSkip	
x	x	x	0	0 C?skip2b:skip1	
x	x	x	0	0 skip1	
x	x	x	0	0 C?skip1b:fetch	
x	x	x	0	0 fetch	
x	x	x	0	0 useK tr?fetch:skipf	
x	x	x	0	1 fetch	
x	x	x	0	0 callb	
x	x	x	0	0 jump	
	0	0 x	0	0 C?jumpb:jumpc	
x	x	x	0	0 jumpd	
x	x	x	0	0 jumpe	
x	x	x	0	0 jumpe	
	0	0 x	0	0 jumpf	
x	x	x	0	0 jumpg	
x	x	x	0	0 fetch	
x	x	x	0	0 returnb	
x	x	x	0	0 fetch	
	0	0 x	0	0 C?mathirb:mathirc	
x	x	x	0	1 mathird	
x	x	x	0	1 fetch	
x	x	x	0	0 fetch	
	0	0 x	0	0 C?zpab:zpac	
x	x	x	0	0 zpad	
x	x	x	0	0 svlnd?zpaf:zpae	
x	x	x	0	0 svlnd?zpaf:zpae	
	0	0 x	0	0 rtn	
	1	1 0	0	0 zpag	
x	x	x	0	0 zpah	
	1	1 1	0	0 zpai	
x	x	x	0	0 svlnc?zpaj:zpao	
x	x	x	0	0 C?zpak:zpal	
	1	1 0	0	0 zpam	
	1	1 0	0	0 zpao	
x	x	x	0	0 zpan	
	1	1 1	0	0 zpao	
x	x	x	0	0 rtn	

Microcode

0	0 x	0	0 prevK?(C?mathizd:mathize):(C?mathizb:mathizc)
0	0 x	0	0 \$zpa,mathizo
0	0 x	0	0 mathizf
x	x	x	0 1 \$zpa,mathizp
x	x	x	0 1 mathizg
x	x	x	0 0 \$zpa,mathizo
x	x	x	0 0 \$zpa,mathizp
x	x	x	0 1 mathizp
x	x	x	0 0 svZReg?(svZIO?mathizs:mathizr):mathizq
x	x	x	0 0 fetch
x	x	x	0 0 fetch
x	x	x	0 0 fetch
x	x	x	0 0 callzb
x	x	x	0 0 \$zpa,jumpz
x	x	x	0 0 jumpzb
x	x	x	0 0 fetch
x	x	x	0 0 C?tableb:tablec
x	x	x	0 0 tabled
x	x	x	0 0 tabled
x	x	x	0 0 C?tablee:tablef
x	x	x	0 0 tableg
x	x	x	0 0 tableh
x	x	x	0 0 tablei
x	x	x	0 0 tableg
x	x	x	0 0 fetch
x	x	x	0 1 fetch
x	x	x	0 1 svZReg?(svZIO?mathrzd:mathrzc):mathrzb
x	x	x	0 0 fetch
x	x	x	0 0 fetch
x	x	x	0 0 fetch
x	x	x	0 1 svZReg?(svZIO?mathrzd:mathrzc):mathrzb
x	x	x	0 0 \$zpa,mathzzb
x	x	x	0 1 svZReg?(svZIO?mathzze:mathzzd):mathzzc
x	x	x	0 0 fetch
x	x	x	0 0 fetch
x	x	x	0 0 fetch

1

ROM

0	0	E3308800	3811608576	reset	K	3 K	3 PL	14	1
1	1	F3308800	4080044032	resetb	K	3 K	3 PH	15	1
2	2	43308800	1127254016	resetc	K	3 K	3 TL	4	1
3	3	53308800	1395689472	resetd	K	3 K	3 TH	5	1
4	4	0	0						
5	5	44311800	1144068096	timer	K	3 TL	4 TL	4	1
6	6	55311800	1429280768	timerb	K	3 TH	5 TH	5	1
7	7	44310800	1144064000	timerc	K	3 TL	4 TL	4	1
8	8	0	0						
9	9	EEF01C00	4008713216	fetcha	PH*	15 PL*	14 PL*	14	1
10	A	FF301900	4281342208	fetchb	K	3 PH*	15 PH*	15	1
11	B	33300100	858784000	fetchc	K	3 K	3 K	3	0
12	C	0	0						
13	D	EEF01C00	4008713216	skipf	PH*	15 PL*	14 PL*	14	1
14	E	FF301800	4281341952	skipfb	K	3 PH*	15 PH*	15	1
15	F	33300000	858783744	skipfc	K	3 K	3 K	3	0
16	10	EE301800	3996129280	skip2	K	3 PL*	14 PL*	14	1
17	11	FF301800	4281341952	skip2b	K	3 PH*	15 PH*	15	1
18	12	EE301800	3996129280	skip1	K	3 PL*	14 PL*	14	1
19	13	FF301800	4281341952	skip1b	K	3 PH*	15 PH*	15	1
20	14	0	0						
21	15	3330B800	858830848	test	K	3 K	3 K	3	1
22	16	0	0						
23	17	21026810	553805840	mathrr	S	0 kD	1 kD2	2	1
24	18	0	0						
25	19	8E300800	2385512448	call	K	3 PL*	14 LL	8	1
26	1A	9F300800	2670725120	callb	K	3 PH*	15 LH	9	1
27	1B	EEF01C00	4008713216	jump	PH*	15 PL*	14 PL*	14	1
28	1C	C3340800	3274967040	jumpb	K	3 K	3 ML	12	1
29	1D	C3340800	3274967040	jumpc	K	3 K	3 ML	12	1
30	1E	FF301800	4281341952	jumpd	K	3 PH*	15 PH*	15	1
31	1F	3EF00400	1055917056	jumpe	PH*	15 PL*	14 K	3	0
32	20	F3340800	4080273408	jumpf	K	3 K	3 PH*	15	1
33	21	EC300800	3962570752	jumpg	K	3 ML	12 PL*	14	1
34	22	0	0						
35	23	E8300800	3895461888	return	K	3 LL	8 PL*	14	1
36	24	F9300800	4180674560	returnb	K	3 LH	9 PH*	15	1
37	25	0	0						
38	26	EEF01C00	4008713216	mathir	PH*	15 PL*	14 PL*	14	1
39	27	13166810	320235536	mathirb	kD	1 K	3 D	1	1
40	28	13166810	320235536	mathirc	kD	1 K	3 D	1	1
41	29	FF301800	4281341952	mathird	K	3 PH*	15 PH*	15	1
42	2A	0	0						
43	2B	EEF01C00	4008713216	zpa	PH*	15 PL*	14 PL*	14	1
44	2C	63340800	1664354304	zpab	K	3 K	3 ZL	6	1
45	2D	63340800	1664354304	zpac	K	3 K	3 ZL	6	1
46	2E	FF301800	4281341952	zpad	K	3 PH*	15 PH*	15	1
47	2F	36700400	913310720	zpa	ZH	7 ZL	6 K	3	0
48	30	367004C0	913310912	zpa	ZH	7 ZL	6 K	3	0
49	31	C3340800	3274967040	zpag	K	3 K	3 ML	12	1
50	32	367004E0	913310944	zpah	ZH	7 ZL	6 K	3	0
51	33	D3340800	3543402496	zpai	K	3 K	3 MH	13	1
52	34	3C301000	1009782784	zpaij	K	3 ML	12 K	3	0

ROM

53	35	367002C0	913310400	zpak	ZH	7 ZL	6 K	3	0
54	36	367002C0	913310400	zpal	ZH	7 ZL	6 K	3	0
55	37	3D301000	1026560000	zpan	K	3 MH	13 K	3	0
56	38	367002E0	913310432	zpan	ZH	7 ZL	6 K	3	0
57	39	3CD00400	1020265472	zpa0	MH	13 ML	12 K	3	0
58	3A	0	0						
59	3B	EEF01C00	4008713216	mathiz	PH*	15 PL*	14 PL*	14	1
60	3C	30380800	808978432	mathizb	K	3 reg	0 K	3	1
61	3D	30380800	808978432	mathizc	K	3 reg	0 K	3	1
62	3E	303A6810	809134096	mathizd	K	3 reg	0 K	3	1
63	3F	303A6810	809134096	mathize	K	3 reg	0 K	3	1
64	40	FF301800	4281341952	mathizf	K	3 PH*	15 PH*	15	1
65	41	FF301800	4281341952	mathizg	K	3 PH*	15 PH*	15	1
66	42	303A6810	809134096	mathizo	K	3 reg	0 K	3	1
67	43	33300000	858783744	mathizp	K	3 K	3 K	3	0
68	44	3CD00200	1020264960	mathizq	MH	13 ML	12 K	3	0
69	45	3300800	53479424	mathizr	K	3 K	3 reg	0	1
70	46	3300000	53477376	mathizs	K	3 K	3 reg	0	0
71	47	0	0						
72	48	8E300800	2385512448	callz	K	3 PL*	14 LL	8	1
73	49	9F300800	2670725120	callzb	K	3 PH*	15 LH	9	1
74	4A	EC300800	3962570752	jumpz	K	3 ML	12 PL*	14	1
75	4B	FD300800	4247783424	jumpzb	K	3 MH	13 PH*	15	1
76	4C	0	0						
77	4D	C0880800	3230140416	table	LL	8 reg	0 ML	12	1
78	4E	D9301800	3643807744	tableb	K	3 LH	9 MH	13	1
79	4F	D9300800	3643803648	tablec	K	3 LH	9 MH	13	1
80	50	CCD01C00	3436190720	tabled	MH	13 ML	12 ML	12	1
81	51	E3340800	3811837952	tablee	K	3 K	3 PL	14	1
82	52	E3340800	3811837952	tablef	K	3 K	3 PL	14	1
83	53	3CD00400	1020265472	tableg	MH	13 ML	12 K	3	0
84	54	DD301800	3710916608	tableh	K	3 MH	13 MH	13	1
85	55	F3340800	4080273408	tablei	K	3 K	3 PH	15	1
86	56	0	0						
87	57	201A6810	538601488	mathzr	kD	1 reg	0 kD2	2	1
88	58	0	0						
89	59	300A6810	805988368	mathrz	S	0 reg	0 K	3	1
90	5A	3CD00200	1020264960	mathrzb	MH	13 ML	12 K	3	0
91	5B	3300800	53479424	mathrzc	K	3 K	3 reg	0	1
92	5C	3300000	53477376	mathrzd	K	3 K	3 reg	0	0
93	5D	33026810	855795728	kmathrz	S	0 K	3 K	3	1
94	5E	0	0						
95	5F	30380800	808978432	mathzz	K	3 reg	0 K	3	1
96	60	333A6810	859465744	mathzzb	K	3 K	3 K	3	1
97	61	3CD00200	1020264960	mathzzc	MH	13 ML	12 K	3	0
98	62	3300800	53479424	mathzzd	K	3 K	3 reg	0	1
99	63	3300000	53477376	mathzze	K	3 K	3 reg	0	0
100	64	0	0						
101	65	0	0						
102	66	0	0						
103	67	0	0						
104	68	0	0						
105	69	0	0						

ROM

106	6A	0	0
107	6B	0	0
108	6C	0	0
109	6D	0	0
110	6E	0	0
111	6F	0	0
112	70	0	0
113	71	0	0
114	72	0	0
115	73	0	0
116	74	0	0
117	75	0	0
118	76	0	0
119	77	0	0
120	78	0	0
121	79	0	0
122	7A	0	0
123	7B	0	0
124	7C	0	0
125	7D	0	0
126	7E	0	0
127	7F	0	0

address	hex	data	label	aAd	bAd	wAd	wReg
				23:20	27:24	31:28	11

15: PH	PH*	PH*	PH*
14: PL	?	PL*	PL*
13: MH	MH	MH	MH
12: ML	?	ML	ML
11: iH	?	?	?
10: iL	?	?	?
9: LH	?	LH	LH

ROM

8:	LL	LL	LL	LL
7:	ZH	ZH	?	?
6:	ZL	?	ZL	ZL
5:	TH	?	TH	TH
4:	TL	?	TL	TL
3:	K	K	K	K
2:	Y	?	?	kD2
1:	X	kD	kD	D
0:	W	S	reg	reg

\PH* PH ^ currInt<<2
\PL* PL ^ currInt<<2

test Din == 011xxxxx
math Din != x11xxxxx
jump Din == 111x11xx
imm Din == 111xxxxx & !jump
k Din == xxx1xxxx
zs Din == xxxx11xx
zd Din == xxxxxx11
jop Din[4,1:0]
aluOp Imm ? Din[4:2] : Din[7:5]
cond Din[3:0]
\fetch TZ?fetcha:timer

\decode

test	fetch
Math & zs & zf	skip2
Math & !zs & zd	skip1
Math & zs & !zd	skip1
Math & !zs & !zd	fetch
Imm & zd	skip2
Imm & !zd	skip1
Jump & jop == 0	skip2
Jump & jop == 1	skip2
Jump & jop == 2	fetch
Jump & jop == 3	fetch
Jump & jop == 4	skip1
Jump & jop == 5	skip1
Jump & jop == 6	fetch
Jump & jop == 7	skip1

\memz svZInd?mem:(svZReg?(svZIO?[1,reg[0]]:B):men
\mA SvAluOp[2]?00:SvAluOp[1:0]

ROM

\mL	SvAluOp[2]?svAluOp[1:0]:11
\mCin	!svAluOp[2]&svAluOp[0]
\S	{00,svS}
\D	{00,svD}
\kD	prevK ? 0011 : {00,svD}
\kD2	useK ? 0011 : {00,svD}

ROM

0	0	0	0B	0^	2	0	0	0	0	0	0	0 resetb
0	0	0	0B	0^	2	0	0	0	0	0	0	0 resetc
0	0	0	0B	0^	2	0	0	0	0	0	0	0 resetd
0	0	0	0B	0^	2	0	0	0	0	0	0	0 fetch
0	0	-2	1B	0B	0	1	1	0	0	0	0	0 Z?(TZ?fetcha:
0	0	-2	1B	0B	0	1	1	0	0	0	0	0 timerc
0	0	-2	1B	0B	0	0	0	0	0	0	0	0 timer
1	0	0	0B	0B	0	1	1	0	0	0	0	0 C?fetchb:fetch
0	0	0	0B	0B	0	1	1	0	0	0	1	0 decode
0	0	0	0B	0B	0	0	0	0	0	0	1	0 decode
1	0	0	0B	0B	0	1	1	0	0	0	0	0 C?skipfb:skipf
0	0	0	0B	0B	0	1	1	0	0	0	0	0 decodeSkip
0	0	0	0B	0B	0	0	0	0	0	0	0	0 decodeSkip
0	0	0	0B	0B	0	1	1	0	0	0	0	0 C?skip2b:skip
0	0	0	0B	0B	0	1	1	0	0	0	0	0 skip1
0	0	0	0B	0B	0	1	1	0	0	0	0	0 C?skip1b:fetch
0	0	0	0B	0B	0	1	1	0	0	0	0	0 fetch
0	0	0	0B	0^	2tr	3	0	0	0	0	0	0 useK tr?fetch::
0	0 mA	2B	0mL	1 mCir	2	0	0	0	0	0	0	1 fetch
0	0	0	0B	0B	0	0	0	0	0	0	0	0 callb
0	0	0	0B	0B	0	0	0	0	0	0	0	0 jump
1	0	0	0B	0B	0	1	1	0	0	0	0	0 C?jumpb:jump
0	0	0	0 mem	1B	0	0	0	0	0	0	0	0 jumpd
0	0	0	0 mem	1B	0	0	0	0	0	0	0	0 jumpe
0	0	0	0B	0B	0	1	1	0	0	0	0	0 jumpe
1	0	0	0B	0B	0	0	0	0	0	0	0	0 jumpf
0	0	0	0 mem	1B	0	0	0	0	0	0	0	0 jumpg
0	0	0	0B	0B	0	0	0	0	0	0	0	0 fetch
0	0	0	0B	0B	0	0	0	0	0	0	0	0 returnb
0	0	0	0B	0B	0	0	0	0	0	0	0	0 fetch
1	0	0	0B	0B	0	1	1	0	0	0	0	0 C?mathirb:ma
0	0 mA	2 mem	1 mL	1 mCir	2	0	0	0	0	0	0	1 mathird
0	0 mA	2 mem	1 mL	1 mCir	2	0	0	0	0	0	0	1 fetch
0	0	0	0B	0B	0	1	1	0	0	0	0	0 fetch
1	0	0	0B	0B	0	1	1	0	0	0	0	0 C?zpab:zpac
0	0	0	0 mem	1B	0	0	0	0	0	0	0	0 zpad
0	0	0	0 mem	1B	0	0	0	0	0	0	0	0 svlnd?zpa:zpa
0	0	0	0B	0B	0	1	1	0	0	0	0	0 svlnd?zpa:zpa
1	0	0	0B	0B	0	0	0	0	0	0	0	0 rtn
1	0	0	0B	0B	0	0	0	1	1	0	0	0 zpag
0	0	0	0 mem	1B	0	0	0	0	0	0	0	0 zpah
1	0	0	0B	0B	0	0	0	1	1	1	0	0 zpai
0	0	0	0 mem	1B	0	0	0	0	0	0	0	0 svlnc?zpa:zpa
0	0	0	0B	0B	0	1	1	0	0	0	0	0 C?zpak:zpal

ROM

0	1	0	0B	0B	0	0	0	1	1	0	0	0 zpa
0	1	0	0B	0B	0	0	0	1	1	0	0	0 zpa
0	0	0	0B	0B	0	1	1	0	0	0	0	0 zpa
0	1	0	0B	0B	0	0	0	1	1	1	0	0 zpa
1	0	0	0B	0B	0	0	0	0	0	0	0	0 r
1	0	0	0B	0B	0	1	1	0	0	0	0	0 prevK?(C?ma
0	0	0	0 memz	2B	0	0	0	0	0	0	0	0 \$zpa,mathizo
0	0	0	0 memz	2B	0	0	0	0	0	0	0	0 mathizf
0	0 mA		2 memz	2 mL	1 mCir	2	0	0	0	0	0	1 \$zpa,mathizp
0	0 mA		2 memz	2 mL	1 mCir	2	0	0	0	0	0	1 mathizg
0	0	0	0B	0B	0	1	1	0	0	0	0	0 \$zpa,mathizo
0	0	0	0B	0B	0	1	1	0	0	0	0	0 \$zpa,mathizp
0	0 mA		2 memz	2 mL	1 mCir	2	0	0	0	0	0	1 mathizp
0	0	0	0B	0B	0	0	0	0	0	0	0	0 svZReg?(svZl
0	1	0	0B	0B	0	0	0	0	0	0	0	0 fetch
0	0	0	0B	0B	0	0	0	0	0	0	0	0 fetch
0	0	0	0B	0B	0	0	0	0	0	0	0	0 fetch
0	0	0	0B	0B	0	0	0	0	0	0	0	0 callzb
0	0	0	0B	0B	0	0	0	0	0	0	0	0 \$zpa,jumpz
0	0	0	0B	0B	0	0	0	0	0	0	0	0 jumpzb
0	0	0	0B	0B	0	0	0	0	0	0	0	0 fetch
0	0	0	0 memz	2B	0	0	0	0	0	0	0	0 C?tableb:table
0	0	0	0B	0B	0	1	1	0	0	0	0	0 tabled
0	0	0	0B	0B	0	0	0	0	0	0	0	0 tabled
1	0	0	0B	0B	0	1	1	0	0	0	0	0 C?tablee:table
0	0	0	0 mem	1B	0	0	0	0	0	0	0	0 tableg
0	0	0	0 mem	1B	0	0	0	0	0	0	0	0 tableh
1	0	0	0B	0B	0	0	0	0	0	0	0	0 tablei
0	0	0	0B	0B	0	1	1	0	0	0	0	0 tableg
0	0	0	0 mem	1B	0	0	0	0	0	0	0	0 fetch
0	0 mA		2 memz	2 mL	1 mCir	2	0	0	0	0	0	1 fetch
0	0 mA		2 memz	2 mL	1 mCir	2	0	0	0	0	0	1 svZReg?(svZl
0	1	0	0B	0B	0	0	0	0	0	0	0	0 fetch
0	0	0	0B	0B	0	0	0	0	0	0	0	0 fetch
0	0	0	0B	0B	0	0	0	0	0	0	0	0 fetch
0	0 mA		2B	0 mL	1 mCir	2	0	0	0	0	0	1 svZReg?(svZl
0	0	0	0 memz	2B	0	0	0	0	0	0	0	0 \$zpa,mathzzb
0	0 mA		2 memz	2 mL	1 mCir	2	0	0	0	0	0	1 svZReg?(svZl
0	1	0	0B	0B	0	0	0	0	0	0	0	0 fetch
0	0	0	0B	0B	0	0	0	0	0	0	0	0 fetch
0	0	0	0B	0B	0	0	0	0	0	0	0	0 fetch

ROM

rMem	wMem	aSel	bSel	logSel	Cin	sA7	sA0	A0	wIR	wFL	next
10	09	17:16	19:18	15:14	13:12	07	06	05	08	04	Z?(TZ?fetcha: C?b:c useK tr?fetch:: svInd?b:c prevK?(C?mai \$zpa,mathzzb rtn decode decodeSkip

ROM

aSel	bSel	logSel	Cin
x	x	x	tr
mA	memz	^	mCin
-2	mem	mL	1
0	B	B	0

\decode

test

useK?\$zpa,mathzzb:\$zpa,mathzz

useK?\$zpa,kmathrz:\$zpa,mathrz

\$zpa,mathzr

mathrr

mathiz

mathir

jump

call

fetch

return

\$zpa,jumpz

callz

fetch

\$zpa,table

note: useK will become prevK at the end of this cl

n)

ROM

timerb):timer

IC

C

1

1

skipf

IC

thirc

ae
ae

ao

$\text{thizd:mathize):(C?mathizb:mathizc)}$

$\text{O?mathizs:mathizr):mathizq}$

:C

:f

$\text{O?mathrzd:mathrzc):mathrzb}$

$\text{O?mathrzd:mathrzc):mathrzb}$

$\text{O?mathzze:mathzzd):mathzzc}$

ROM

timerb):timer

skipf

thizd:mathize):(C?mathizb:mathizc)

ROM

lock cycle