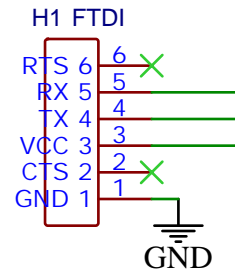
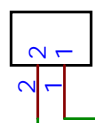


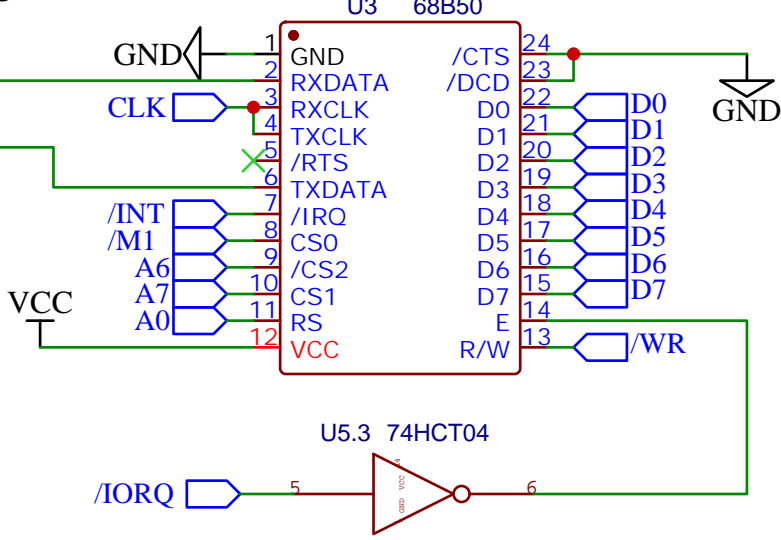
SERIAL CONSOLE
115200 BPS 8N1



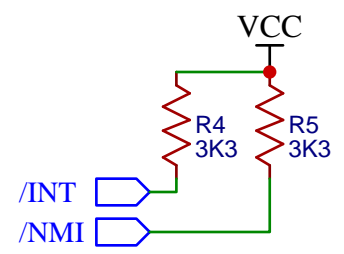
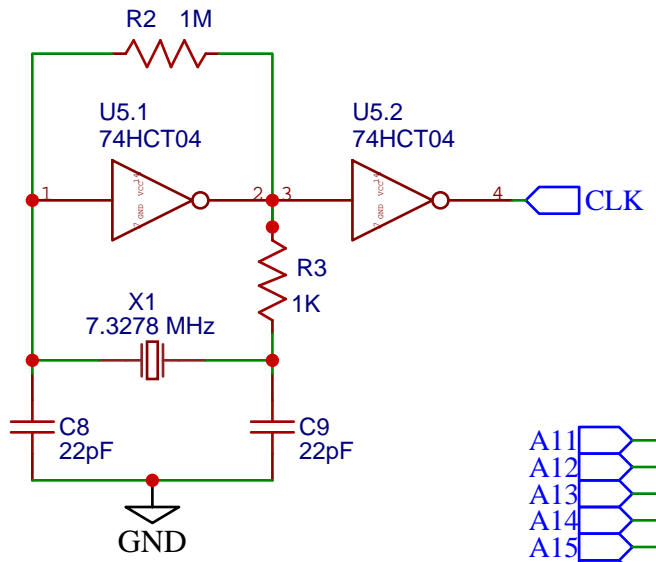
J1 USB POWER
VCC



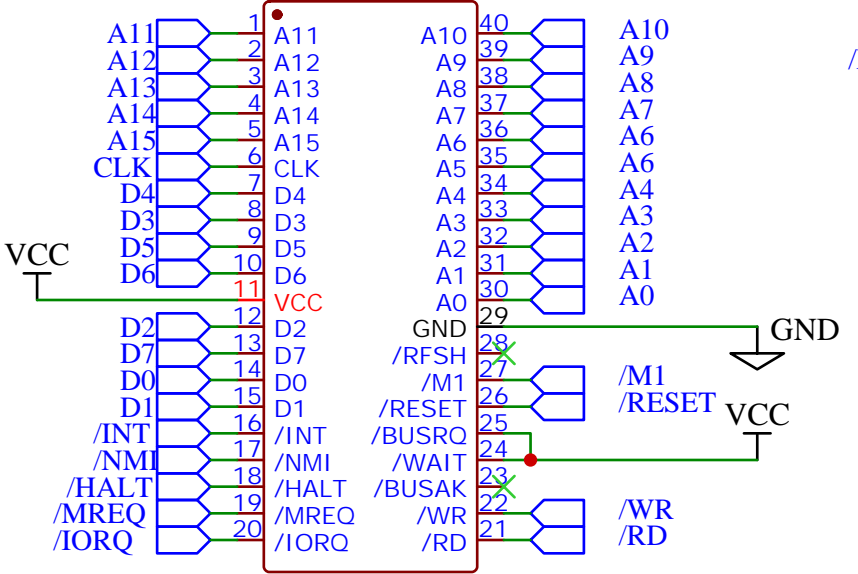
UART
U3 68B50



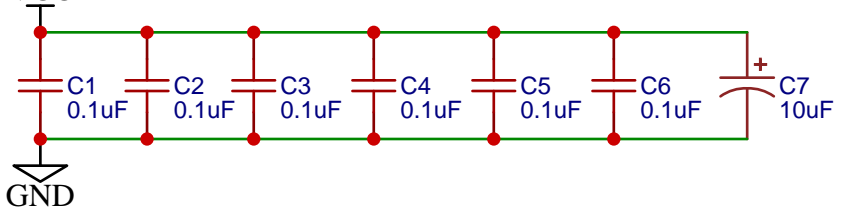
CLOCK



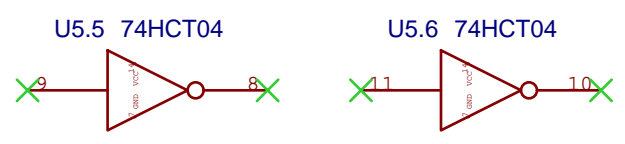
CPU
U2 Z80



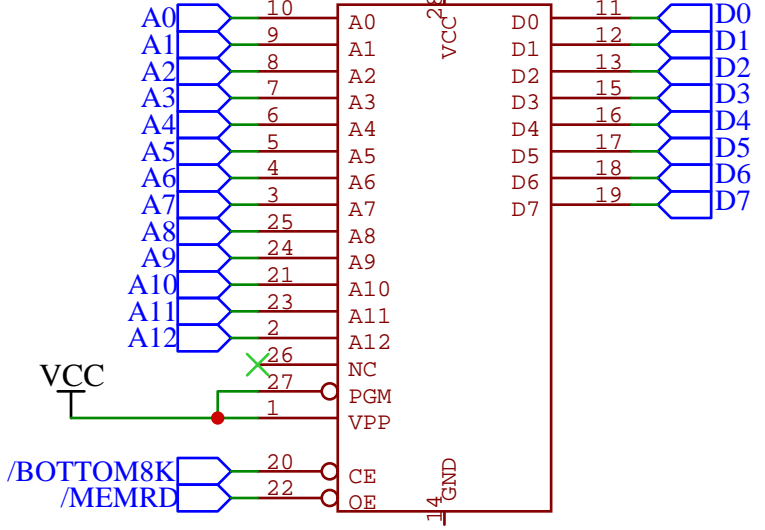
BYPASS CAPS (1 PER IC)



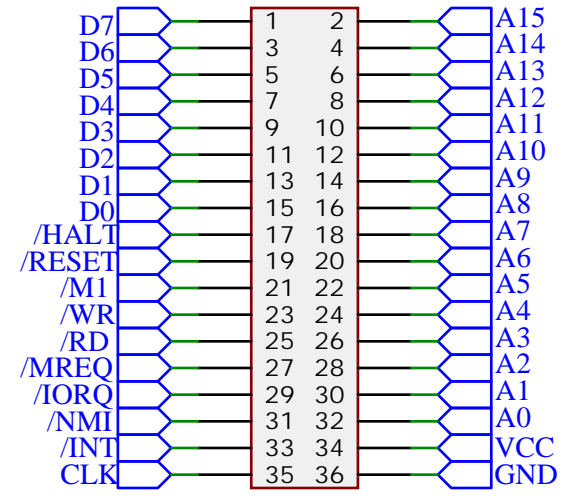
SPARE GATES



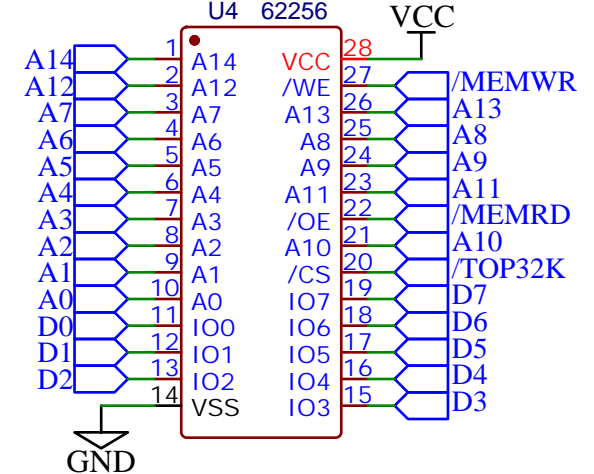
8K ROM
U6 27C64



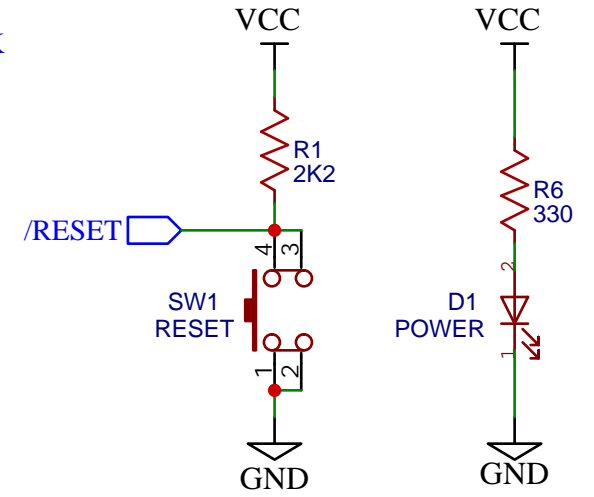
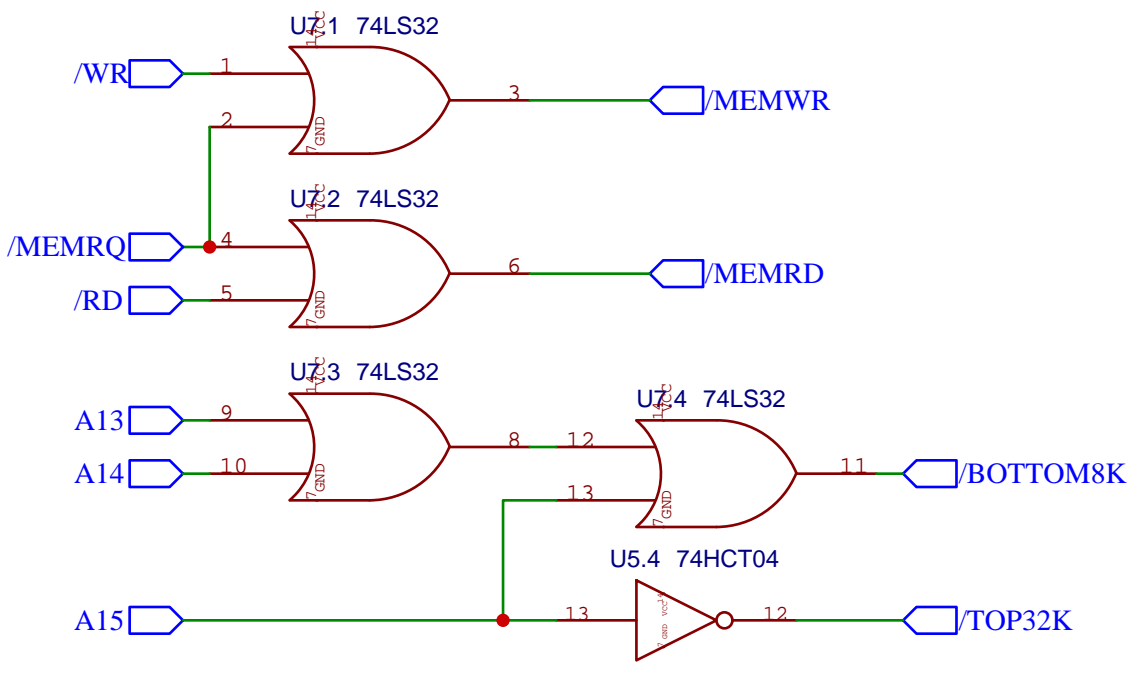
EXPANSION HEADER
P1



32K RAM
U4 62256



ADDRESS DECODING



TITLE: Z80 Single Board Computer		REV: 0.1
EasyEDA	Company: Jeff Tranter	Sheet: 1/1
	Date: 2021-06-13	Drawn By: JJT