	63	62	61	60	59	58	57	56	55	54	53	52	51	50	49	48	47–40	39–32	31–24	23–16	15–8	7–0
MUL	1	0	0	1	0	0	1	0	0	0	0	0	0	0	1	0	Destination Register #1	Source Register #1	Destination Flag Register		Destination Register #2	Source Register #2
MULL	1	0	0	1	0	0	1	0	0	0	0	0	0	0	0	0		Source Register #1	Destination Flag Register		Destination Register #2	Source Register #2
IMUL	1	0	0	1	0	0	1	0	0	0	0	0	0	0	1	1	Destination Register #1	Source Register #1	Destination Flag Register		Destination Register #2	Source Register #2
IMULL	1	0	0	1	0	0	1	0	0	0	0	0	0	0	0	1		Source Register #1	Destination Flag Register		Destination Register #2	Source Register #2

Bit 49: Output upper half
Bit 48: Signed multiply

Function code: 18