CC2420 Errata Note 002, rev. 2.0

March 18, 2005

For CC2420 devices marked with lot codes ranging from WA8402.00 to WB8341.00 correct operation of the SPI interface is not guaranteed at I/O-voltages below 2.1 V, unless the on-chip voltage regulator is used. This problem is now fixed and operation down to 1.6 V is ensured for devices with lot codes succeeding WB8341.00.

Description and reason for the problem

When using I/O-voltages below 2.1 V a limitation in the digital pads has been observed to cause reduced SPI speed and in some cases loss of functionality. This will only occur for I/O pins configured as inputs and at I/O-voltages below 2.1 V.

Suggested workaround

Using I/O-voltages at or above 2.1 V will ensure reliable operation and unaffected SPI speed. Designs already utilizing the CC2420 on-chip voltage regulator will comply with this requirement.

Fix

Using the CC2420 on-chip regulator or voltages above 2.1 V solves the problem for those devices affected.

Batches affected

This errata note applies to all CC2420 devices marked with lot codes ranging from WA8402.00 through WB8341.00. For devices with lot codes succeeding WB8341.00 this problem is resolved.

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Document History

Revision	Date	Description/Changes
2.0	2005-03-18	Problem fixed on lot codes higher than WB8341.00
1.0	2004-10-18	Initial release