PRU Pin Guide

The following table outlines the pin number for the commands such as digitalWrite and digitalRead. The second column shows which header that the particular pin applies to. The final two columns show whether that pin is an input (I) or output (O) for that particular PRU for the eGPIO feature of the PRU. Pins 0-27 are the eGPIO pins and can be set by applying the appropriate Device Tree Overlay (DTO) to pinmux the appropriate pin. Documentation on DTO's for the BeagleBone Black can be found at the following webpages.

https://learn.adafruit.com/introduction-to-the-beaglebone-black-device-tree/device-tree-overlays

http://hipstercircuits.com/beaglebone-black-gpio-mux-for-pru-with-device-tree-overlay/

The subsequent pins must use the regular GPIO feature and can be used as either inputs or outputs on either PRU.

For example, to turn high the pin assigned to the P8_11 header of the BeagleBone Black, you would use the command "digitalWrite(2, HIGH)".

Pin #	Header	PRU0	PRU1
0	P8_15	I	
1	P8_16	I	
2	P8_11	O	
3	P8_12	O	
4	P8_20		I/O
5	P8_21		I/O
6	P8_28		I/O
7	P8_29		I/O
8	P8_27		I/O
9	P8_40		I/O
10	P8_39		I/O
11	P8_42		I/O
12	P8_41		I/O
13	P8_44		I/O
14	P8_43		I/O
15	P8_46		I/O
16	P8_45		I/O
17	P9_26		I
18	P9_24	I	
19	P9_41b	I/O	
20	P9_25	I/O	
21	P9_41a	I	
22	P9_27	I/O	
23	P9_42	I/O	
24	P9_28	I/O	
25	P9_30	I/O	
26	P9_29	I/O	

27	P9_31	I/O	
28	P8_3		
29	P8_4		
30	P8_5		
31	P8_6		
32	P8_7		
33	P8_8		
34	P8_9		
35	P8_10		
36	P8_13		
37	P8_14		
38	P8_17		
39	P8_19		
40	P8_22		
41	P8_23		
42	P8_24		
43	P8_25		
44	P8_26		
45	P8_30		
46	P9_17		
47	P9_18		
48	P9_19		
49	P9_20		
50	P8_18		
51	P8_31		
52	P8_32		
53	P8_33		
54	P8_34		
55	P8_35		
56	P8_36		
57	P8_37		
58	P8_38		
59	P9_11		
60	P9_12		
61	P9_13		
62	P9_14		
63	P9_15		
64	P9_16		
65	P9_21		
66	P9_22		
67	P9_23		
68	P9_41a		
69	P9_42b		
70	P8_15		
71	P8_16		
72	P8_11		
73	P8_12		