POLITECNICO DI TORINO



Bachelor of Science in Mathematical Engineering
Bachelor degree thesis

Analysis of matrix vector product

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 $Ai\ miei\ genitori$

Abstract

We made a comparison between a naive implementation of the matrix vector product and the Eigen's library implementation. We found out that on equal hardware power our code runs <code>FASTEERR</code>

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Introduction

Let A be a $n \times n$ dense matrix and v be a vector of n entries. We define the kernel as the following operation:

$$result[i] = result[i] + A[i,j] * vector[i]$$

. We used LIKIWID to profile the code and gather insights as well as stream. We went through different implementation of our kernel to get to the theoretical limit of our implementation using one core.

- Naive implementation
- Using a temporary variable to speed up cache lookup
- Loop unrolling to exploit sum and multiplication pipelines

With the last implementation, we managed to get to the theoretical limit. After that, we addressed scalability. That is, we looked at how our kernel worked in parallel on the supermegacomputer of politenico (inserire specifiche)

Naive implementation

At first we started with the simplest thing one could think of:

```
for(int i = 0; i < n; i++){
  result[i] = 0;
  for(int j = 0; j < n; j++){
    result[i] += matrix[i][j]*vector[j];
  }
}</pre>
```

Using a temporary variable to speed up cache lookup

Loop unrolling to exploit sum and multiplication pipelines

Parallel computations

Bibliography

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