

Conn_01x06_Female

J3

1 SPL_CS

2 SPL_CLK

3 SPL_SI

4 SPL_SO

5 HOST_HRDY

6

GND

Conn_01x06_Female
J6

Pin	Signal
1	GND
2	SPLCS2
3	SPLCLK
4	SPLSI
5	SPLSO
6	HOST_HRDY

Diagram of J7 connector pinout:

- Pin 1: I2C_SCL
- Pin 2: I2C_SDA
- Pin 3: GND

Conn_01x04_Female

J5

1 I2C_SDA

2 I2C_SCL

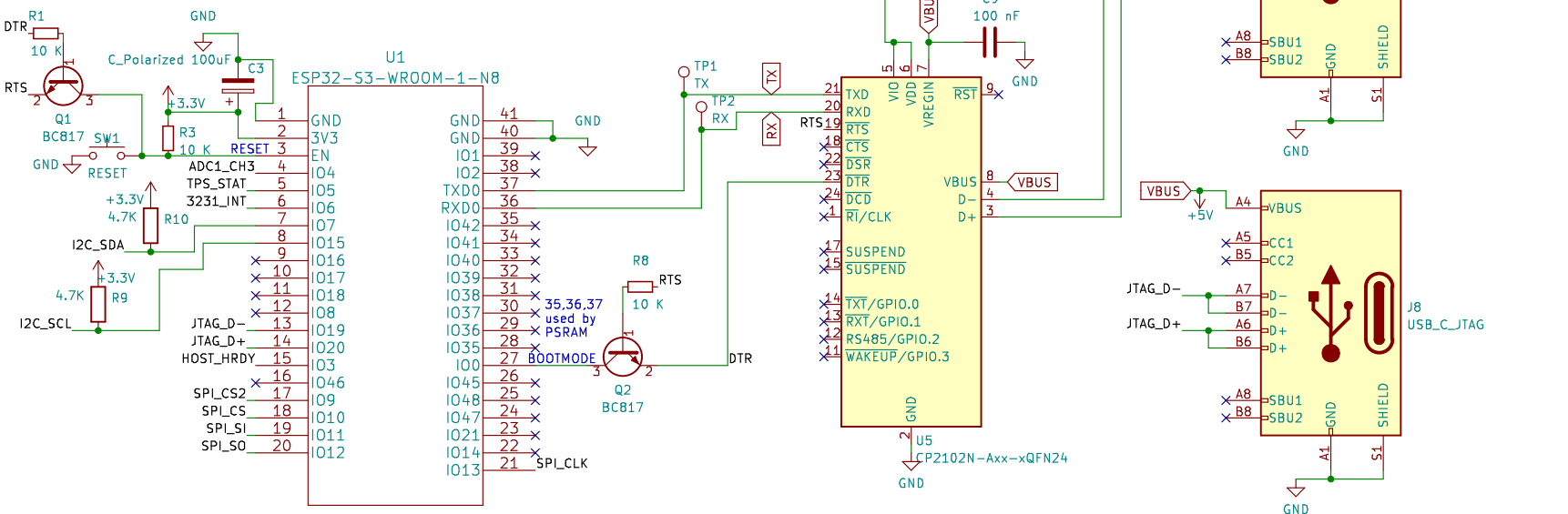
3

4

GND

3.3V

A diagram of a DC Jack connector labeled J1. The connector has two pins: Pin 1 is connected to GND (Ground) and Pin 2 is connected to BAT+ (Battery Positive).



3.3V for S3 MCU

STAT should be HIGH if USB is connected and LOW when is battery powered

STAT should be HIGH if VBUS is connected and let 5V flow from USB. Here it flows always ->

Referenced from:
<https://somanytech.com/3v-to-5v-boost-converter-circuit-diagram-dc-dc-buck-converter-schematics>