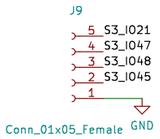
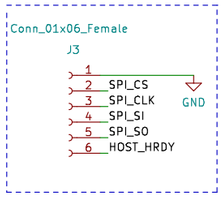
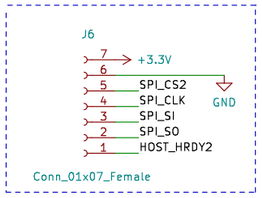
S3 available IOs



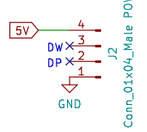
SPI Interface CINREAD



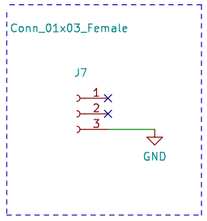
SPI Interface 2 (external)



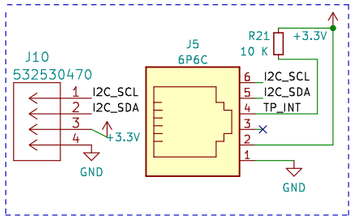
CINREAD IT8951 Power source



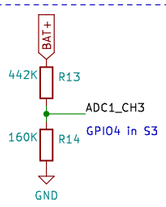
Support pins CINREAD



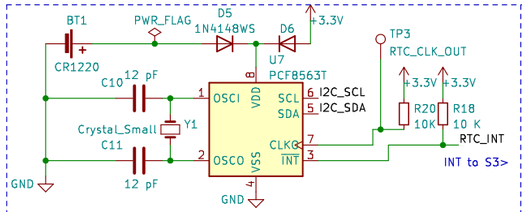
I2C interface 2 (Touch)



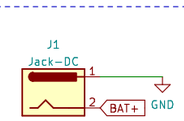
Voltage divider (via ADC1_CH3)



PCF8563T Real time clock via I2C

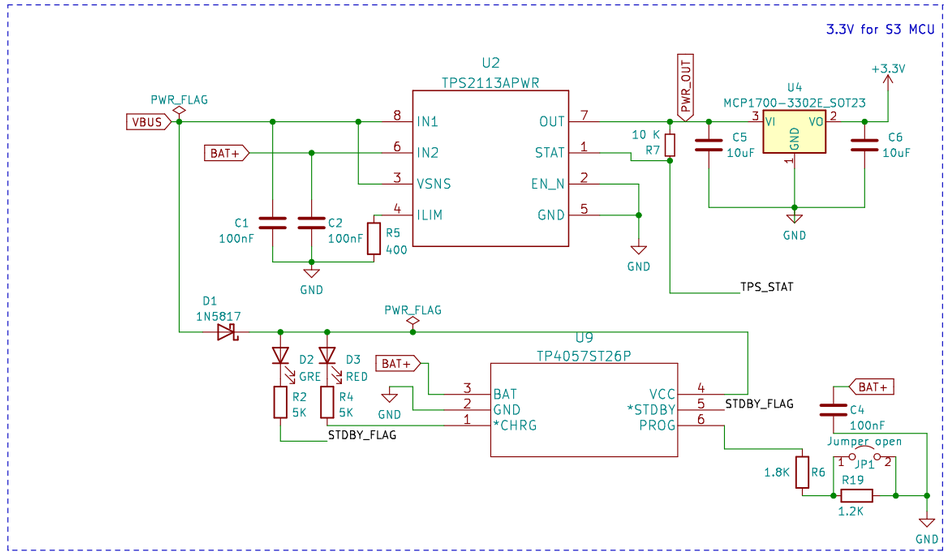


Battery jack



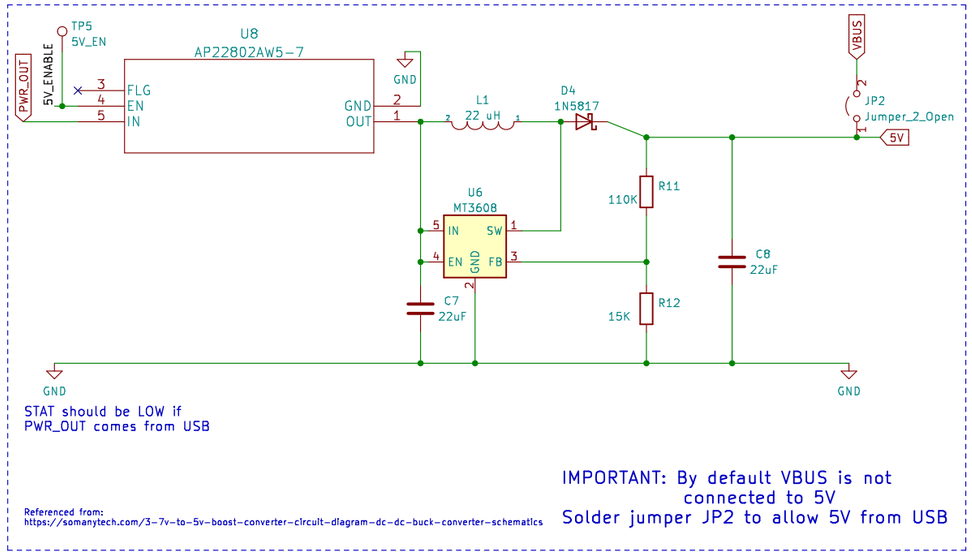
H1 MountingHole
H2 MountingHole
H3 MountingHole
H4 MountingHole

LiPo 3.7V charging circuit + 3.3 V step down

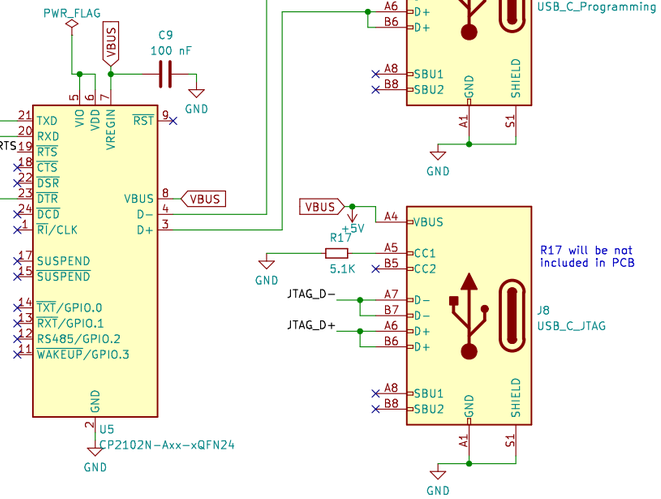


STAT is an open-drain output that is Hi if the IN2 switch is ON

3V to 5V BOOST converter MT3608. Enable with IO38 HIGH



FASANI CORP.



ATTENTION: There are 2 models
QFN20 and QFN24 → Is used in this design

GPIO3 Controls where JTAG signal goes
0: JTAG signal from on-chip JTAG pins
1: JTAG signal from USB Serial/JTAG controller