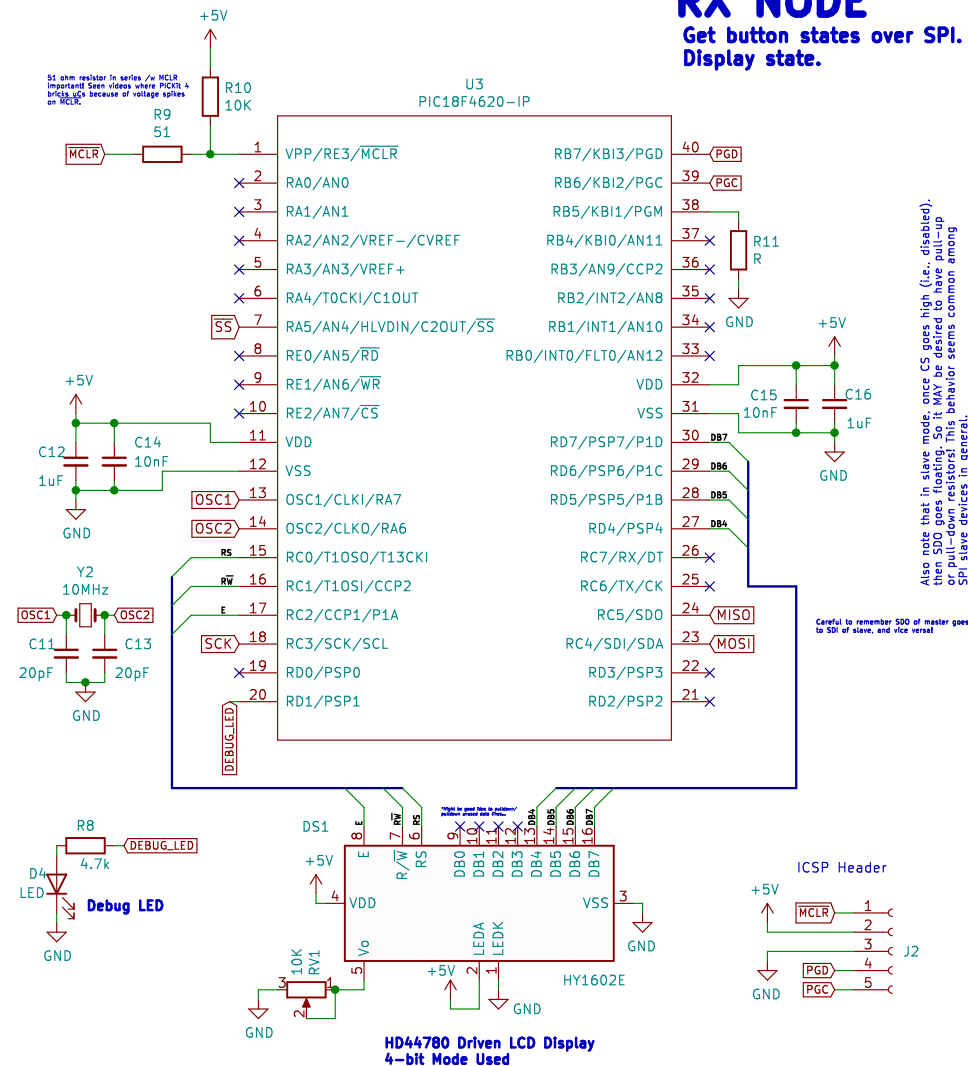


**Read button states.  
Send state over SPI.**

File: pic18f4620\_tx\_node.sch



Also note that in slave mode, once CS goes high (i.e., disabled), SDO goes floating. So it MAY be desired to have pull-up or pull-down resistors! This behavior seems common among SPI slave devices in general.

Careful to remember SDO of master goes to SDI of slave, and vice versa!

Author: Abdullah Almosalami

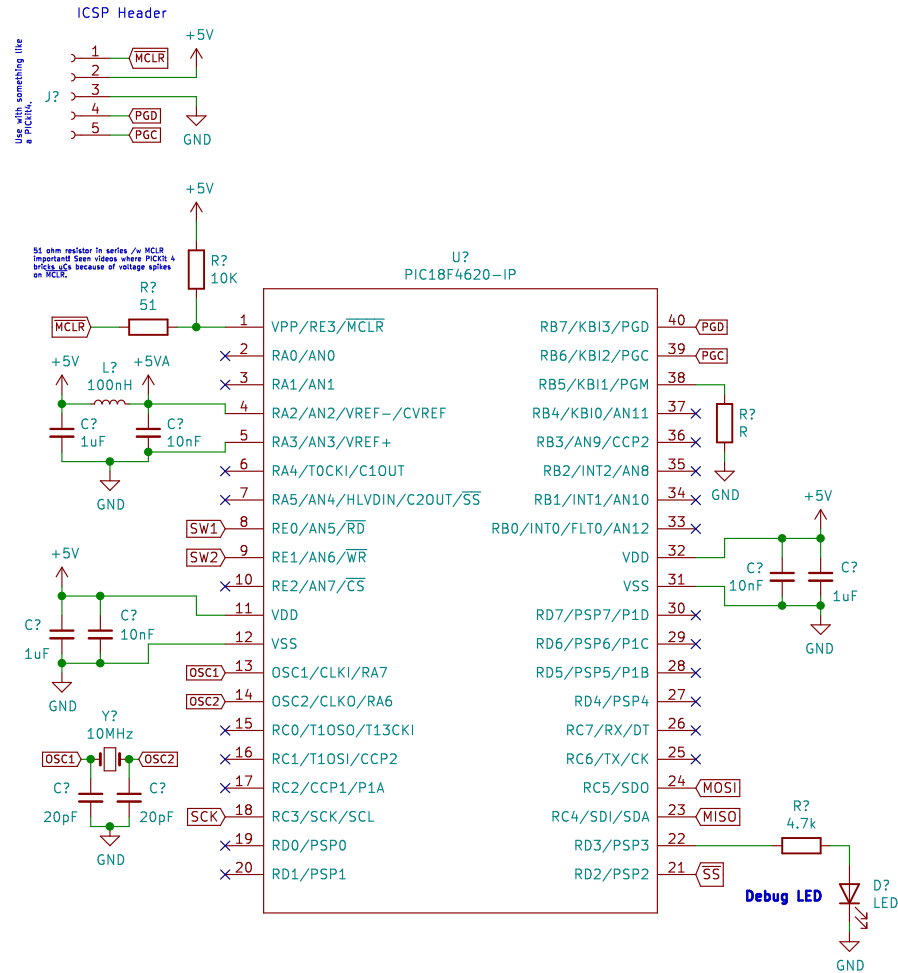
## Personal Project

File: pic18f4620\_spi\_test.sch

**Title: PIC18F4620 – MSSP SPI Test Circuit**

Size: A1	Date: 2021
KiCad E.D.A.	kicad (5.1.7)-1

Id: 1/4



## TX NODE

Read button states.  
Send state over SPI.

Author: Abdullah Almosalami

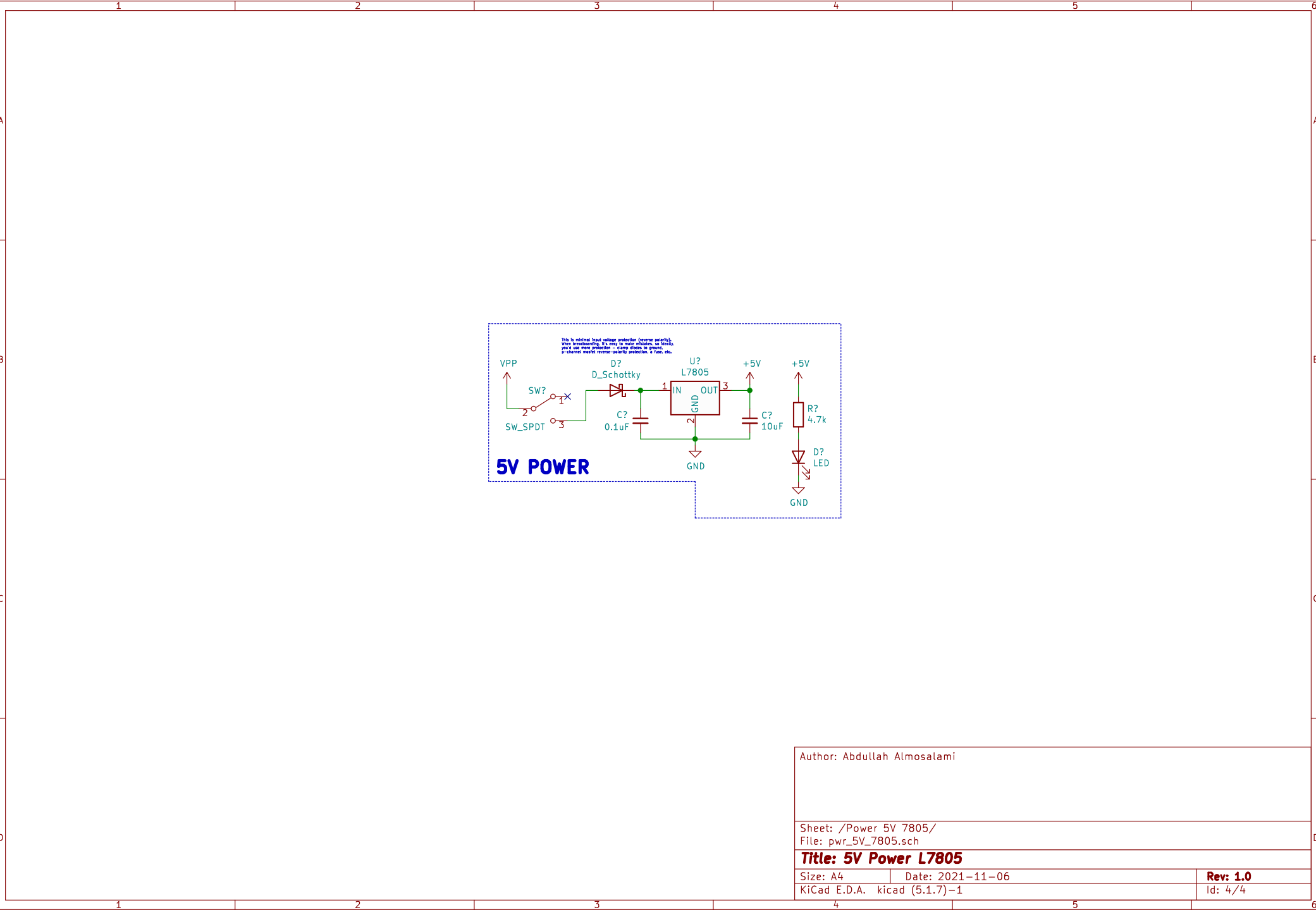
Sheet: /PIC18F4620 TX Node Setup/  
File: pic18f4620\_tx\_node.sch

**Title: PIC18F4620 Set Up as TX Node**

Size: A4 Date: 2021-11-06  
KiCad E.D.A. kicad (5.1.7)-1

Rev: 1.0  
Id: 2/4





Author: Abdullah Almosalami

Sheet: /Power 5V 7805/  
File: pwr\_5V\_7805.sch

**Title: 5V Power L7805**

Size: A4 Date: 2021-11-06

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**Rev: 1.0**

Id: 4/4