

# Instrumentation For The Anodization and Characterization of Titanium Electrodes for Electrolytic Capacitors

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**Abstract**—This paper presents a custom circuit for controlling the anodization of titanium capacitors and characterizing their performance. The circuitry provides a constant current source of 0-100mA up to a compliance voltage of 30V. The system can monitor and record leakage currents down to 10 nA over periods of up to 24 hours. Typical results obtained using sputtered titanium-zirconium capacitors are presented.

**Index Terms**—Capacitor, Switched Capacitor Circuits

## I. INTRODUCTION

**C**APACITORS with high energy and power are essential for efficient management of electrical power. Order of magnitude increases are desired for energy and power density. This may be possible with new self-repairing titanate dielectric capacitors using high surface area doped titanium and doped zirconium-titanium. Anodizing at DC voltages forms a uniform dielectric film of doped titanium (the energy density of this dielectric is estimated to be on the order of  $150\text{J}/\text{cm}^3$ ) [1]. This and the volume fraction of dielectric determines the energy density of the capacitor.

One of the major problems plaguing titanium electrolytic capacitors has been their high leakage currents [2]. In order to further research into titanium capacitors, custom anodization instrumentation has been developed to anodize and characterize a large number of candidate titanium capacitor materials. This instrumentation was necessary because conventional systems do not have the necessary dynamic range (1A-1nA measurement) or repeatability (need to get some kind of number) needed in this application.

### A. Anodization Process and Requirements

Anodization is the act of growing an oxide layer (dielectric) on top of a metal anode. Dielectrics are useful because they allow capacitors to store more energy for a given electric field [3]. The anodization process is preformed by connecting a voltage or current source to an anode and a cathode and immersing them in an electrolyte solution as shown in Fig. ??

Referring to Fig. 1, in the simplest case, the current transfer is an ionic transfer where the Ti anode reacts with  $\text{O}_2$  to create a  $\text{TiO}_2$  oxide layer. The reaction at the metal-oxide surface can be written as:

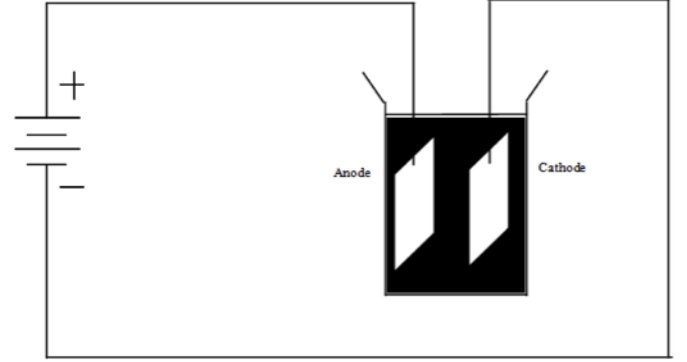
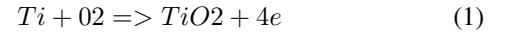
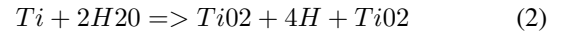


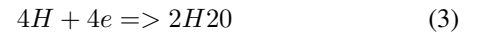
Fig. 1. Anodization Setup



The titanium also reacts with the electrolyte solution to give off hydrogen:



This hydrogen reacts with the electrons at the cathode to create hydrogen gas and complete the ionic circuit.



This process is very similar to anodizing aluminum. For an explanation of that process visit [4].

Since the rate of oxide formation is dependent on the charge transport into the anode during anodization [5], a current source was selected. A typical anodization process with a current source will see the current and voltage progress as in Fig. 4.

Our typical anodization tests work with a constant current of 20mA and a forming voltage of 30V. The anodization reaches the forming voltage in periods on the order of 30 seconds. The forming voltage then remains constant while the leakage current decreases over a much longer time period (on the order of 24 hours) to currents on the order of 1 microampere or less. The exact times and values are dependent upon the material and the size of the electrode.

If a constant current is introduced, the voltage will (ideally) rise linearly with time. This will happen until the DUT (Device Under Test) reaches the compliance voltage, at which point the current through the DUT will begin to drop off until it reaches the leakage current of the unpackaged capacitor.

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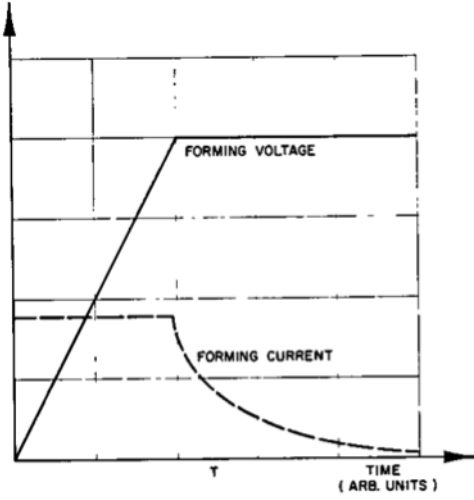


Fig. 2. Anodization Curve [5]

## II. DESIGN AND IMPLEMENTATION OF CUSTOM SYSTEM USED FOR ANODIZATION

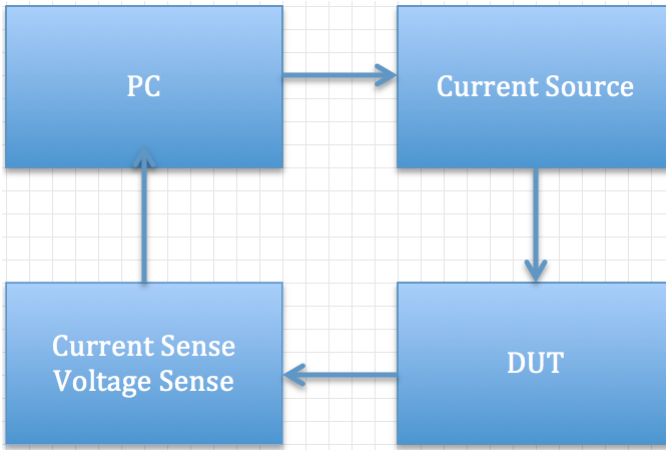


Fig. 3. Overall System Block Diagram

The overall system flow (Fig. 3), is as follows:

The user sets the test parameters of current, voltage compliance, and testing time. Then the computer sends the configuration settings to the hardware with a command to start the test. The current source then turns on and regulates to the set current until the test is finished. The system is designed to work with a passive DUT. During the duration of the test, the current and voltage senses characterize the DUT and send the data to a PC for post processing.

### A. Computer Control and Data Acquisition

The user sets the test parameters of current, voltage compliance, and testing time. The anodization is then controlled by a set of Python scripts, which appropriately configure the hardware and control the data logging. Voltage and current measurements are sampled at a constant, high rate and all data is recorded. However, once the electrode has reached the forming voltage and the (leakage) current is slowly decreasing

over time the data measurements are subsampled and only a fraction of the sampled data is preserved. (say more on this)

### B. Current Source

An ideal current source has the ability to output a constant, DC, current to any load with infinite voltage compliance. This ability makes a current source an attractive tool to use in anodization due to its ability to tightly control the rate of oxide growth on the anode.

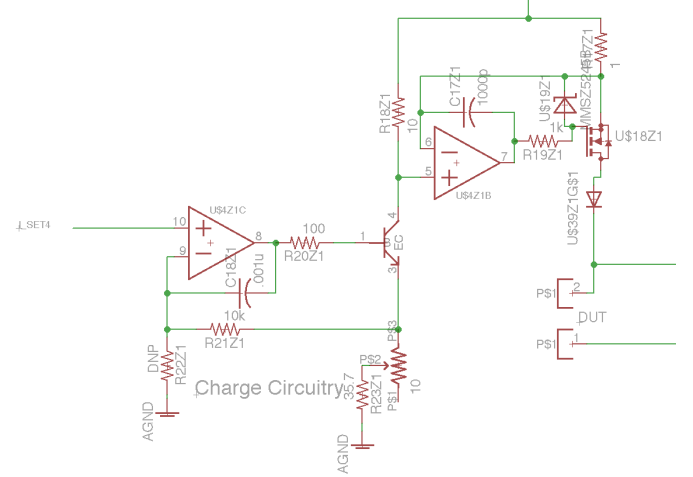


Fig. 4. Current Control Circuitry

The current source implementation (Fig. 4) was chosen around an op-amp based current mirror. The op-amp on the left, U4Z1C, is used to set and regulate the current through R23. This current functions as the reference current on the left leg of the current mirror. The op-amp on the right, U4Z1B, forces the voltage drop across R17 and R18 to be the same, hence causing the current in the right leg to go as:

$$I_2 = I_1 * R_{18}/R_{17} \quad (4)$$

With the values chosen in this design, this equates to a 10x current amplification from the reference to the current output. The adjustable supply voltage is applied to the node connecting resistors R17 and R18. The current source will be able supply a constant current up to an effective compliance voltage of the supply voltage minus the voltage drops of R17, the pass transistor, and the protection diode.

The real current source has several practical limitations that provide less than ideal performance.

- 1) Pass transistor power rating
- 2) Finite number of current settings
- 3) Slight nonlinearity due to the pass diode

The safe operating area (Fig. 5) is smaller than the desired operating output of 30V at 100mA. This difference comes from the limitations in the power dissipation of the pass transistor, U18Z1, in Fig. 4. Assuming the worse case scenario of a short on the output, the allowable output current for a given voltage compliance can be found as:

$$I_{out} = P_{rating}/V_{comp} \quad (5)$$

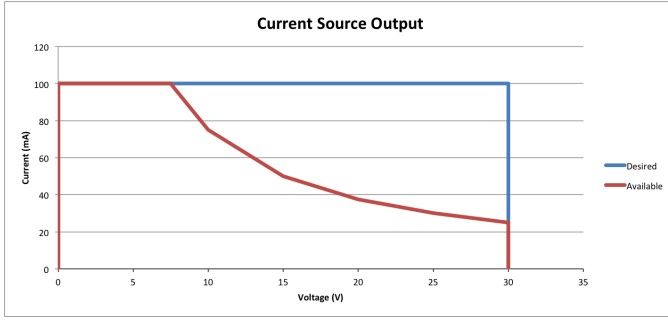


Fig. 5. Current Source: Safe Operating Area

The current source operates by controlling the gate voltage of U18Z1 in Fig. 4) ensure a constant current as the voltage on the DUT changes. Ideally, the source would be able to respond to a change in load impedance instantaneous to keep the current output constant. The real current source of (Fig. 4) is limited by the Gain Bandwidth Product of U4X1B (1.8Mhz reference datasheet). This limitation is of little concern, as the op-amp is still much faster then the fastest load change expected (See DUT section).

The ideal current source has the ability to output any desired current over its range with infinite precision. The current source in this design is limited in this regard by the discretization error of setting the current. Referring to (Fig. 4) the signal  $i_{set4}$  is used as a control signal to set the reference current in the device. This signal is controlled by a Microchip MCP4812 10-bit DAC. A first approximation of the discretization uncertainty in selecting DAC outputs can be found by:

$$U = V_{iset}/(2n - 1)/(R23 + R_{pot}) * (R18/R17) \quad (6)$$

Which yields an uncertainty in the current output of  $\pm 0.5\text{mA}$ . This can be calibrated away for a single current output, but all other outputs would be off by as much as the uncertainty.

Also, the protection diode, U39, in (Fig. 4) has the effect of increasing the voltage compliance as the current drops off after anodization. Looking at a standard diode curve, the voltage drop across the diode is roughly a constant 0.7V for high currents and exponentially diminishes towards zero as the current decreases. The tests are designed to not only anodize, but also continue to measure the long-term leakage current afterwards. This means that the current draw will decrease to the nA range, causing the effective voltage compliance to increase to about a diode drop above its anodization level. This affect will need to be considered during the analysis of the anodization data.

The circuitry is still being tested for emperical accuracy and repeatability, therefore that data is not yet available.

### C. DUT

The device under test was typically meant to be a titanium anode to be anodized or a titanium capacitor. The device is able to operate with resistive loads and any capacitive loads

(with capacitance large enough for the system to be able to respond).

### D. Current and Voltage Measurements

The second part of the instrumentation is the measurement circuitry. An ideal measurement system would have no error or time delay when measuring the signal. It would also be able to perfectly reconstruct the signal for post analysis.

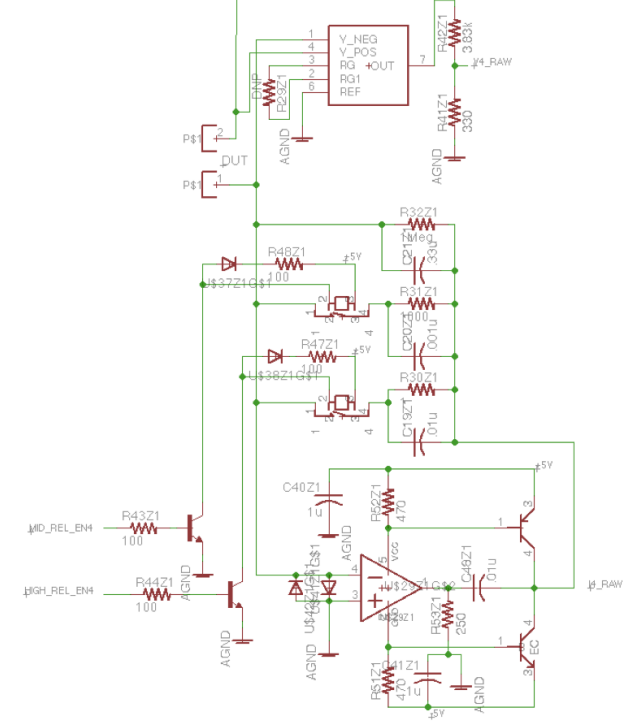


Fig. 6. Measurement Circuitry

The measurement circuitry (Fig. 6) implemented consists of two different parts, voltage and current measurements. The voltage of the DUT is measured differentially by Analog Devices AD8220 JFET instrumentation amplifier. This output is scaled and then sent to a 12-bit DAC. The current through the DUT is measure from a low side measurement topology know and a transimpedance amplifier. The amplifier creates a virtual ground at the negative terminal of the op-amp, U39, that allows for a current reading without subjecting the measurement circuitry to a high voltage and without floating the bottom of the DUT above ground.

Since it is desirable the measure both the anodization current and the leakage current afterwards, a basic transimpedance amplifier design was modified to include 3 switched feedback paths. This allows the current measurement to measure currents over 8 orders of magnitude. The circuitry can handle currents from 10nA to 100mA. The three current ranges are listed Table: I.

The real measurement circuitry has several practical limitations that limit it from performing ideally. Both the differential amplifier and the transimpedance amplifier send their signals to 6-pole Sallen-Key filters. These filters have a cutoff frequency of 10kHz(quote S. thesis), in order to filter out high

frequency harmonics and other undesirable noise. Any signals higher than this will be severely attenuated in the captured data.

The DACs inject digitization errors into the measurement signals. They are able to measure signals with resolutions shown in Table: I.

TABLE I  
DAC RESOLUTION

Resolution	Full Scale Measurement	Comment
7.32mV	30V	
.098mA	100mA	Hi Current (1A-1mA)
.98uA	1mA	Med Current (1mA-1uA)
.98nA	1uA	Lo Current (1uA-1nA)

Once the data is collected onto the microcontroller, it is sent to a PC via USB for further analysis. The data is sampled by the ADCs at a rate of baud and transferred to the PC at a rate of 2Mbaud. This allows for maximum flexibility on the PC side, where any data coming in at a rate greater than what is desired can simply be discarded.

### III. EXPERIMENTAL PROCEDURE

The experimental setup to anodize the anode of a titanium capacitor with the aforementioned circuitry is as follows. The anode sample is prepared by cleaning the surface oxide off with a chemical bath. It is then transported into a beaker of anodizing solution. The current source is connected to the DUT and acts as both a current source and data logger until the test is finished and the current through the DUT has dropped to the leakage current.

### IV. EXPERIMENTAL RESULTS

Typical results from early materials can be found it Fig 7. For additional data and complete testing curves, see Steves thesis and my thesis.

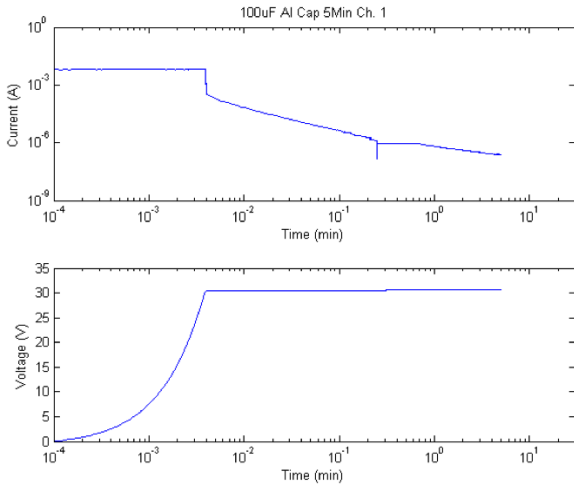


Fig. 7. Typical Results

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