heat2d Performance Report

20150330-191648

Abstract

This performance report is intended to support performance analysis and optimization activities. It includes details on program behavior, system configuration and capabilities, including support data from well-known performance analysis tools.

This report was generated using hotspot version 0.1. Homepage http://www.github.com/moreandres/hotspot. Full execution log can be found at ~/.hotspot/heat2d/20150330-191648/hotspot.log.

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1 Program

This section provides details about the program being analyzed.

- 1. Program: heat2d.
 - Program is the name of the program.
- $2. \ \, {\rm Timestamp:} \ \, 20150330\text{--}191648.$
 - Timestamp is a unique identifier used to store information on disk.
- 3. Parameters Range: [16384, 17408, 18432, 19456, 20480, 21504, 22528, 23552, 24576, 25600, 26624, 27648, 28672, 29696, 30720, 31744].

Parameters range is the problem size set used to scale the program.

2 System Capabilities

This section provides details about the system being used for the analysis.

2.1 System Configuration

This subsection provides details about the system configuration.

The hardware in the system is summarized using a hardware lister utility. It reports exact memory configuration, firmware version, mainboard configuration, CPU version and speed, cache configuration, bus speed and others.

The hardware configuration can be used to contrast the system capabilities to well-known benchmarks results on similar systems.

memory 3952MiB System memory Intel(R) Core(TM) i5-3320M CPU @ 2.60GHz processor 440FX - 82441FX PMC [Natoma] bridge 82371SB PIIX3 ISA [Natoma/Triton II] bridge 82371AB/EB/MB PIIX4 IDE storage network 82540EM Gigabit Ethernet Controller bridge 82371AB/EB/MB PIIX4 ACPI storage 82801HM/HEM (ICH8M/ICH8M-E) SATA Controller [AHCI mode]

The software in the system is summarized using the GNU/Linux platform string.

The software toolchain is built upon the following components.

Linux-3.16.0-30-generic-x86_64-with-Ubuntu-14.04-trusty

- 1. Host: ubuntu
- 2. Distribution: Ubuntu, 14.04, trusty.

This codename provides LSB (Linux Standard Base) and distribution-specific information.

- 3. Compiler: gcc (Ubuntu 4.8.2-19ubuntu1) 4.8.2.
 - Version number of the compiler program.
- 4. C Library: GNU C Library (Ubuntu EGLIBC 2.19-Oubuntu6.6) stable release version 2.19. Version number of the C library.

The software configuration can be used to contrast the system capabilities to well-known benchmark results on similar systems.

System Performance Baseline 2.2

This subsection provides details about the system capabilities.

A set of performance results is included as a reference to contrast systems and to verify hardware capabilities using well-known synthetic benchmarks.

The HPC Challenge benchmark [1] consists of different tests:

- 1. HPL: the Linpack TPP benchmark which measures the floating point rate of execution for solving a linear system of equations.
- 2. DGEMM: measures the floating point rate of execution of double precision real matrix-matrix multiplication.
- 3. PTRANS (parallel matrix transpose): exercises the communications where pairs of processors communicate with each other simultaneously.
- 4. RandomAccess: measures the rate of integer random updates of memory (GUPS).
- 5. STREAM: a simple synthetic benchmark program that measures sustainable memory bandwidth (in GB/s).
- 6. FFT: measures the floating point rate of execution of double precision complex one-dimensional Discrete Fourier Transform (DFT).

Table 1: Benchmarks				
Benchmark	Value	Unit		
hpl	0.00365811 TFlops	tflops		
dgemm	1.72977 GFlops	mflops		
ptrans	1.58619 GBs	MB/s		
random	0.0544077 GUPs	MB/s		
stream	7.00091 MBs	MB/s		
fft	2.32577 GFlops	MB/s		

Most programs will have a dominant compute kernel that can be approximated by the ones above, the results helps to understand the available capacity.

3 Workload

This section provides details about the workload behavior.

3.1 Workload Footprint

The workload footprint impacts on memory hierarchy usage.

heat2d: ELF 64-bit LSB executable, x86-64, version 1 (SYSV), dynamically linked (uses shared libs), for GNU/Linux

Binaries should be stripped to better fit inside cache.

```
struct _IO_FILE {
                                                            0
int
                             _flags;
/* XXX 4 bytes hole, try to pack */
char *
                             _IO_read_ptr;
                                                            8
                                                           16
char *
                             _IO_read_end;
char *
                             _IO_read_base;
                                                           24
                                                           32
char *
                             _IO_write_base;
                                                                   8 */
                                                           40
char *
                             _IO_write_ptr;
                                                                   8 */
char *
                             _IO_write_end;
                                                           48
                                                                   8 */
char *
                             _IO_buf_base;
                                                           56
/* --- cacheline 1 boundary (64 bytes) --- */
                             _IO_buf_end;
                                                           64
                                                                   8 */
char *
char *
                             _IO_save_base;
                                                           72
                                                                   8 */
```

```
80
                                                                8 */
char *
                            _IO_backup_base;
                                                         88
                                                                8 */
                            _IO_save_end;
char *
struct _IO_marker *
                                                         96
                                                                8 */
                            _markers;
struct _IO_FILE *
                            _chain;
                                                        104
                                                                8 */
                            _fileno;
int
                                                        112
                                                                4 */
int
                            _flags2;
                                                        116
                            _old_offset;
                                                        120
                                                                8 */
__off_t
/* --- cacheline 2 boundary (128 bytes) --- */
short unsigned int _cur_column;
                                                        128
                                                                2 */
                           _vtable_offset;
                                                        130
signed char
                                                   /*
                                                                1 */
                            _shortbuf[1];
                                                        131
char
                                                                1 */
/* XXX 4 bytes hole, try to pack */
_{\rm IO\_lock\_t} *
                            _lock;
                                                                8 */
                                                        136
                            _offset;
__off64_t
                                                        144
                                                                8 */
                            __pad1;
void *
                                                        152
                                                                8 */
                            __pad2;
void *
                                                        160
                                                                8 */
                            __pad3;
void *
                                                        168
                                                                8 */
                                                                8 */
                                                        176
void *
                            __pad4;
                                                        184
                                                                8 */
size_t
                            __pad5;
/* --- cacheline 3 boundary (192 bytes) --- */
                                                        192
                                                                4 */
int
                            _mode;
                            _unused2[20];
char
                                                        196
                                                               20 */
/* size: 216, cachelines: 4, members: 29 */
/* sum members: 208, holes: 2, sum holes: 8 */
/* last cacheline: 24 bytes */
};
struct _IO_marker {
struct _{IO\_marker} *
                                                                8 */
                                                          0
                            _next;
struct _IO_FILE *
                                                          8
                                                   /*
                                                                8 */
                            _sbuf;
                                                   /*
                                                         16
                                                                4 */
                            _pos;
/* size: 24, cachelines: 1, members: 3 */
/* padding: 4 */
/* last cacheline: 24 bytes */
};
```

The in-memory layout of data structures can be used to identify issues. Reorganizing data to remove alignment holes will improve CPU cache utilization.

More information https://www.kernel.org/doc/ols/2007/ols2007v2-pages-35-44.pdf

3.2 Workload Stability

(g) repetitions: 8 times

This subsection provides details about workload stability.

1. Execution time:

```
(a) problem size range: 16384 - 32768
(b) geomean: 16.37025 seconds
(c) average: 16.45469 seconds
(d) stddev: 1.66720
(e) min: 14.04511 seconds
(f) max: 19.33783 seconds
```

The histogram plots the elapsed times and shows how they fit in a normal distribution sample.

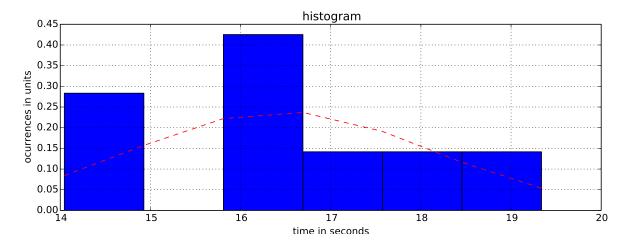


Figure 1: Results Distribution

The workload should run for at least one minute to fully utilize system resources. The execution time of the workload should be stable and the standard deviation less than 3 units.

3.3 Workload Optimization

This section shows how the program reacts to different optimization levels.

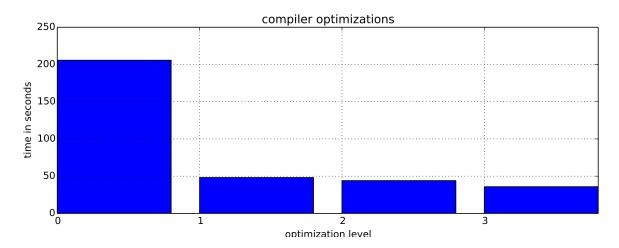


Figure 2: Optimization Levels

4 Scalability

This section provides details about the scaling behavior of the program.

4.1 Problem Size Scalability

A chart with the execution time when scaling the problem size.

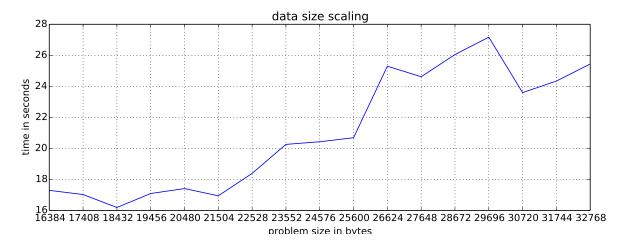


Figure 3: Problem size times

The chart will show how computing time increases when increasing problem size. There should be no valleys or bumps if processing properly balanced across computational units.

4.2 Computing Scalability

A chart with the execution time when scaling computation units.

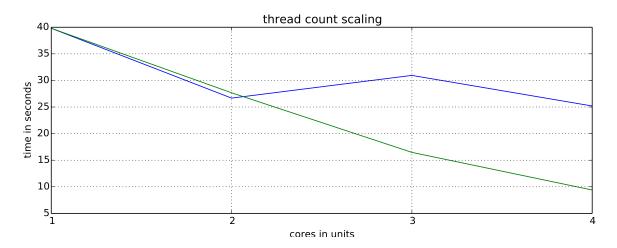


Figure 4: Thread count times

The chart will show how computing time decreases when increasing processing units. An ideal scaling line is provided for comparison.

The parallel and serial fractions of the program can be estimated using the information above.

- 1. Parallel Fraction: 0.65972.
 - Portion of the program doing parallel work.
- 2. Serial: 0.34028.
 - Portion of the program doing serial work.

Optimization limits can be estimated using scaling laws.

- 1. Amdalah Law for 1024 procs: 2.93872 times.
 - Optimizations are limited up to this point when scaling problem size. [2]
- 2. Gustafson Law for $1024~\mathrm{procs}$: 675.88983 times.
 - Optimizations are limited up to this point when not scaling problem size. [3]

5 Profile

This section provides details about the execution profile of the program and the system.

5.1 Program Profiling

This subsection provides details about the program execution profile.

5.1.1 Flat Profile

The flat profile shows how much time your program spent in each function, and how many times that function was called.

Flat profile:

Each sample counts as $0.01\ \text{seconds}.$

cu	ımulative	self		self	total			
time	seconds	seconds	calls	Ts/call	Ts/call	name		
29.94	22.15	22.15				<pre>compute_one_iterationomp_fn.1</pre>	(heat2d.c:78 @ 400d97)	
13.51	32.14	9.99				<pre>compute_one_iterationomp_fn.1</pre>	(heat2d.c:83 @ 400d93)	
9.84	39.42	7.28				<pre>compute_one_iterationomp_fn.1</pre>	(heat2d.c:83 @ 400d70)	
9.29	46.28	6.87				<pre>compute_one_iterationomp_fn.1</pre>	(heat2d.c:79 @ 400d50)	
8.99	52.93	6.65				<pre>compute_one_iterationomp_fn.1</pre>	(heat2d.c:82 @ 400d8f)	
6.96	58.08	5.15				<pre>compute_one_iterationomp_fn.1</pre>	(heat2d.c:81 @ 400d89)	
5.03	61.80	3.72				<pre>compute_one_iterationomp_fn.1</pre>	(heat2d.c:80 @ 400d69)	
4.36	65.03	3.23				<pre>compute_one_iterationomp_fn.1</pre>	(heat2d.c:83 @ 400d63)	
4.10	68.06	3.03				<pre>compute_one_iterationomp_fn.1</pre>	(heat2d.c:80 @ 400d83)	
3.65	70.77	2.70				<pre>compute_one_iterationomp_fn.1</pre>	(heat2d.c:75 @ 400bd0)	
2.10	72.32	1.56				<pre>compute_one_iterationomp_fn.1</pre>	(heat2d.c:83 @ 400d7b)	
1.72	73.60	1.28				<pre>compute_one_iterationomp_fn.1</pre>	(heat2d.c:79 @ 400d5c)	
~								

Call graph

granularity: each sample hit covers 2 byte(s) for 0.01 of 74.26 seconds index time self children called name

				<pre><spontaneous></spontaneous></pre>
[18]	0.0	0.02	0.00	<pre>compute_one_iteration (heat2d.c:54 @ 400ee0) [18]</pre>
[22]	0.0	0.00	0.00 247479	frame_dummy [22]

The table shows where to focus optimization efforts to maximize impact.

5.2 System Profiling

This subsection provide details about the system execution profile.

5.2.1 System Resources Usage

The following charts shows the state of system resources during the execution of the program.

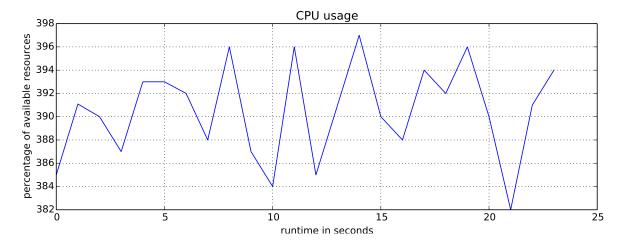


Figure 5: CPU Usage

Note that this chart is likely to show as upper limit a multiple of 100% in case a multicore system is being used.

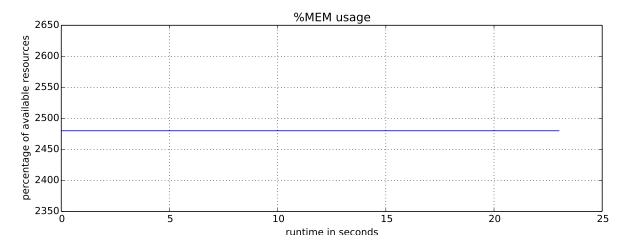


Figure 6: Memory Usage

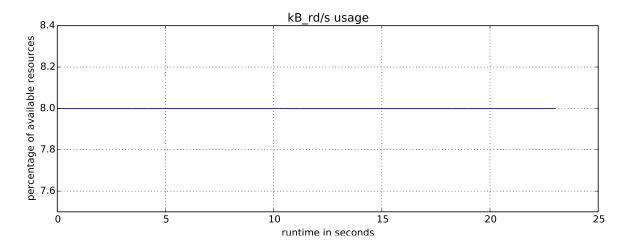


Figure 7: Reads from Disk

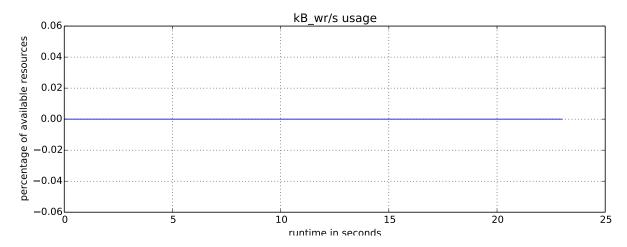


Figure 8: Writes to Disk

5.3 Hotspots

This subsection shows annotated code guiding the optimization efforts.

```
: solution[cur_gen][0][i] = solution[cur_gen][1][i];
: solution[cur_gen][CRESN - 1][i] = solution[cur_gen][CRESN - 2][i];
: }
: /* corners ? */
: #pragma omp parallel for shared(solution,cur_gen,next_gen,diff_constant) private(i,j)
2.66 : 400af3: movsd 0x365a15(rip),xmm5 # 766510 <diff_constant>
0.21 : 400b80: lea 0x10(rcx),r11
```

```
for (i = 1; i <= RESN; i++)
            for (j = 1; j <= RESN; j++)
              solution[next_gen][i][j] = solution[cur_gen][i][j] +
                (solution[cur_gen][i + 1][j] +
                 solution[cur_gen][i - 1][j] +
      : #pragma omp parallel for shared(solution,cur_gen,next_gen,diff_constant) private(i,j)
         for (i = 1; i <= RESN; i++)
            for (j = 1; j \le RESN; j++)
              solution[next_gen][i][j] = solution[cur_gen][i][j] +
                (solution[cur_gen][i + 1][j] +
0.13:
                        movhpd 0x8(r10,rax,1),xmm0
                 solution[cur_gen][i - 1][j] +
                 solution[cur_gen][i][j + 1] +
                 solution[cur_gen][i][j - 1] -
                 4.0 * solution[cur_gen][i][j]) * diff_constant;
1.87:
         400c1a:
                        movsd (rdx,rax,1),xmm1
      : #pragma omp parallel for shared(solution,cur_gen,next_gen,diff_constant) private(i,j)
         for (i = 1; i <= RESN; i++)
            for (j = 1; j \le RESN; j++)
              solution[next_gen][i][j] = solution[cur_gen][i][j] +
                (solution[cur_gen][i + 1][j] +
                 solution[cur_gen][i - 1][j] +
8.64:
          400c1f:
                        movhpd 0x8(rax,rsi,1),xmm2
                 solution[cur_gen][i][j + 1] +
                 solution[cur_gen][i][j - 1] -
                 4.0 * solution[cur_gen][i][j]) * diff_constant;
                        movhpd 0x8(rax,rdx,1),xmm1
      : #pragma omp parallel for shared(solution,cur_gen,next_gen,diff_constant) private(i,j)
         for (i = 1; i <= RESN; i++)
            for (j = 1; j \le RESN; j++)
              solution[next_gen][i][j] = solution[cur_gen][i][j] +
                (solution[cur_gen][i + 1][j] +
9.33 :
         400c2b:
                        \verb"addpd xmm2, xmm0"
                solution[cur_gen][i - 1][j] +
                 solution[cur_gen][i][j + 1] +
                 solution[cur_gen][i][j - 1] -
                 4.0 * solution[cur_gen][i][j]) * diff_constant;
0.25:
         400c2f:
                        movapd xmm1,xmm2
1.13:
         400c33:
                        mulpd xmm3,xmm2
      : #pragma omp parallel for shared(solution,cur_gen,next_gen,diff_constant) private(i,j)
         for (i = 1; i <= RESN; i++)
            for (j = 1; j \le RESN; j++)
              solution[next_gen][i][j] = solution[cur_gen][i][j] +
                (solution[cur_gen][i + 1][j] +
                 solution[cur_gen][i - 1][j] +
0.95:
         400c37:
                        addpd (rdi,rax,1),xmm0
                 solution[cur_gen][i][j + 1] +
8.68:
          400c3c:
                        addpd (r8,rax,1),xmm0
                 solution[cur_gen][i][j - 1] -
2.35:
          400c42:
                        subpd xmm2,xmm0
                 4.0 * solution[cur_gen][i][j]) * diff_constant;
9.75:
         400c46:
                        mulpd xmm4,xmm0
         /* corners ? */
      : #pragma omp parallel for shared(solution,cur_gen,next_gen,diff_constant) private(i,j)
         for (i = 1; i <= RESN; i++)
            for (j = 1; j \le RESN; j++)
              solution[next_gen][i][j] = solution[cur_gen][i][j] +
12.83:
         400c4a:
                        addpd xmm1,xmm0
16.47 :
         400c4e:
                        movlpd xmm0,(rcx,rax,1)
8.39 :
         400c59:
                        add
                               $0x10,rax
1.01:
         400c5d:
                        cmp
                               $0x960,rax
0.13:
         400c6c:
                        add
                               $0x970,r8
              solution[cur_gen][0][i] = solution[cur_gen][1][i];
              solution[cur_gen][CRESN - 1][i] = solution[cur_gen][CRESN - 2][i];
           }
         /* corners ? */
      : #pragma omp parallel for shared(solution,cur_gen,next_gen,diff_constant) private(i,j)
          int i, j;
          /* set boundary values */
```

```
for (i = 0; i < CRESN; i++)
       :
            {
              if (i < 256 || i > 768)
  7.76:
           400da0: cmp $0x200,eax
                solution[cur_gen][i][0] = solution[cur_gen][i][1];
              else
                solution[cur_gen][i][0] = MAX_HEAT;
   1.94: 400dab: movsd 0x135(rip),xmm1
                                                   # 400ee8 <_IO_stdin_used+0x18>
   0.22 : 400db3:
                      movsd xmm1,(rdx)
  9.70 : 400db7:
                      add $0x1,eax
   0.43 : 400dba:
                      add
                              $0x970,rdx
       : void compute_one_iteration()
       : {
          int i, j;
          /* set boundary values */
          for (i = 0; i < CRESN; i++)
   0.86:
          400dc1: cmp $0x2e,eax
   0.22:
           400dcd:
                       xor
                             eax,eax
              else
                solution[cur_gen][i][0] = MAX_HEAT;
          for (i = 0; i < CRESN; i++)
             solution[cur_gen][i][CRESN - 1] = solution[cur_gen][i][CRESN - 2];
   2.16:
           400dd0: movsd 0x602a20(rbx,rax,1),xmm0
           400de2:
                       add
                             $0x970,rax
              if (i < 256 || i > 768)
                solution[cur_gen][i][0] = solution[cur_gen][i][1];
                solution[cur_gen][i][0] = MAX_HEAT;
            }
          for (i = 0; i < CRESN; i++)
   1.72 :
                      400de8:
            {
              solution[cur_gen][i][CRESN - 1] = solution[cur_gen][i][CRESN - 2];
          for (i = 0; i < CRESN; i++)
            {
                 solution[cur_gen][i - 1][j] +
                 solution[cur_gen][i][j + 1] +
                 solution[cur_gen][i][j - 1] -
                 4.0 * solution[cur_gen][i][j]) * diff_constant;
   0.43 :
           400e35:
                       pop
                              rbx
              solution[cur_gen][0][i] = solution[cur_gen][1][i];
              solution[cur_gen][CRESN - 1][i] = solution[cur_gen][CRESN - 2][i];
          /* corners ? */
       : #pragma omp parallel for shared(solution,cur_gen,next_gen,diff_constant) private(i,j)
          /* set boundary values */
          for (i = 0; i < CRESN; i++)
       :
           -{
              if (i < 256 || i > 768)
               solution[cur_gen][i][0] = solution[cur_gen][i][1];
  7.54 :
          400e3b: movsd 0x8(rdx),xmm0
  37.72 :
          400e40:
                       movsd xmm0,(rdx)
  3.23 : 400e44:
                      jmpq 400db7 <compute_one_iteration+0x37>
      : Disassembly of section .text:
 10.26: 74d0: mov 0x206af1(rip),rax # 20dfc8 <omp_in_final_+0x203c18>
                     test rax,rax
  2.56 :
          74dc:
                  retq
 87.18 : 74e3:
Percent | Source code & Disassembly of libc-2.19.so for cpu-clock
      : Disassembly of section .text:
 18.18: 82df0: mov 0x33b0f1(rip),rax
                                                 # 3bdee8 <_IO_file_jumps+0x848>
                     mov (rax),rax
 18.18 : 82df7:
  45.45 : 82dfa: test rax,rax
```

```
lea
  13.64 : 82e10:
                               -0x10(rdi),rsi
                        je 82e2f <__libc_free+0x3f>
   4.55 : 82e21:
Percent | Source code & Disassembly of libc-2.19.so for cpu-clock
        : Disassembly of section .text:
  16.67: 82750: push rbp
  8.33 : 82773:
                       mov fs:(rax),rbx
  16.67: 8278e: je 8279c <__libc_malloc+0x4c>
16.67: 82794: jne 84dbb <malloc_info+0x4bb>
16.67: 827a8: mov rbx,rdi
  8.33 : 827bf: je 827cc <__libc_malloc+0x7c>
16.67 : 827c4: jne 84dd6 <malloc_info+0x4d6>
        : Disassembly of section .text:
        : Disassembly of section .text:
  66.67 : 7490: cmpq $0xfffffffffffffffffff,0x206cf8(rip)
33.33 : 7498: jne 749f <GOMP_parallel_end+0xf>
                                                                       Percent | Source code & Disassembly of libc-2.19.so for cpu-clock
        : Disassembly of section .text:
______
        : Disassembly of section .text:
```

6 Low Level

This section provide details about low level details such as vectorization and performance counters.

6.1 Vectorization Report

This subsection provide details about vectorization status of the program loops.

```
Analyzing loop at heat2d.c:46
heat2d.c:46: note: not vectorized: loop contains function calls or data references that cannot be analyzed
heat2d.c:46: note: bad data references.
heat2d.c:43: note: vectorized 0 loops in function.
heat2d.c:43: note: not vectorized: not enough data-refs in basic block.
heat2d.c:43: note: not vectorized: not enough data-refs in basic block.
heat2d.c:43: note: not vectorized: not enough data-refs in basic block.
heat2d.c:46: note: not vectorized: not enough data-refs in basic block.
heat2d.c:43: note: not vectorized: not enough data-refs in basic block.
heat2d.c:43: note: not vectorized: not enough data-refs in basic block.
heat2d.c:43: note: not vectorized: not enough data-refs in basic block.
heat2d.c:75: note: versioning for alias required: can't determine dependence between solution[pretmp_108][pretmp_:
heat2d.c:75: note: versioning not yet supported for outer-loops.
heat2d.c:75: note: bad data dependence.
Analyzing loop at heat2d.c:77
heat2d.c:77: note: versioning for alias required: can't determine dependence between solution[pretmp_108] [pretmp_
heat2d.c:77: note: versioning for alias required: can't determine dependence between solution[pretmp_108] [pretmp_
heat2d.c:77: note: versioning for alias required: can't determine dependence between solution[pretmp_108][i_3][j_2
heat2d.c:77: note: versioning for alias required: can't determine dependence between solution[pretmp_108][i_3][_29
heat2d.c:77: note: versioning for alias required: can't determine dependence between solution[pretmp_108][i_3][j_3
heat2d.c:77: note: misalign = 8 bytes of ref solution[pretmp_108][pretmp_112][j_39]
heat2d.c:77: note: misalign = 8 bytes of ref solution[pretmp_108][pretmp_113][j_39]
heat2d.c:77: note: misalign = 0 bytes of ref solution[pretmp_108][i_3][j_26]
heat2d.c:77: note: misalign = 0 bytes of ref solution[pretmp_108][i_3][_29]
heat2d.c:77: note: misalign = 8 bytes of ref solution[pretmp_108][i_3][j_39]
heat2d.c:77: note: misalign = 8 bytes of ref solution[pretmp_106][i_3][j_39]
heat2d.c:77: note: num. args = 4 (not unary/binary/ternary op).
heat2d.c:77: note: not ssa-name.
heat2d.c:77: note: use not simple.
heat2d.c:77: note: num. args = 4 (not unary/binary/ternary op).
heat2d.c:77: note: not ssa-name.
heat2d.c:77: note: use not simple.
heat2d.c:77: note: num. args = 4 (not unary/binary/ternary op).
heat2d.c:77: note: not ssa-name.
heat2d.c:77: note: use not simple.
heat2d.c:77: note: num. args = 4 (not unary/binary/ternary op).
heat2d.c:77: note: not ssa-name.
```

```
heat2d.c:77: note: use not simple.
heat2d.c:77: note: num. args = 4 (not unary/binary/ternary op).
heat2d.c:77: note: not ssa-name.
heat2d.c:77: note: use not simple.
Vectorizing loop at heat2d.c:77
heat2d.c:75: note: vectorized 1 loops in function.
heat2d.c:75: note: not vectorized: not enough data-refs in basic block.
heat2d.c:75: note: not vectorized: not enough data-refs in basic block.
heat2d.c:75: note: not consecutive access pretmp_106 = next_gen;
heat2d.c:75: note: not consecutive access pretmp_108 = cur_gen;
heat2d.c:75: note: not consecutive access pretmp_110 = diff_constant;
heat2d.c:75: note: Failed to SLP the basic block.
heat2d.c:75: note: not vectorized: failed to find SLP opportunities in basic block.
heat2d.c:75: note: not vectorized: not enough data-refs in basic block.
heat2d.c:75: note: not vectorized: not enough data-refs in basic block.
heat2d.c:75: note: not vectorized: not enough data-refs in basic block.
heat2d.c:75: note: not vectorized: not enough data-refs in basic block.
heat2d.c:75: note: not vectorized: not enough data-refs in basic block.
heat2d.c:79: note: can't determine dependence between solution[pretmp_108][pretmp_112][j_140] and solution[pretmp.
heat2d.c:79: note: can't determine dependence between solution[pretmp_108][pretmp_113][j_140] and solution[pretmp.
heat2d.c:79: note: can't determine dependence between solution[pretmp_108][i_3][j_146] and solution[pretmp_106][i.
heat2d.c:79: note: can't determine dependence between solution[pretmp_108][i_3][_149] and solution[pretmp_106][i_3]
heat2d.c:79: note: can't determine dependence between solution[pretmp_108][i_3][j_140] and solution[pretmp_106][i_3][i_40] and solution[pretmp_106][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_40][i_
heat2d.c:79: note: SLP: step doesn't divide the vector-size.
heat2d.c:79: note: Unknown alignment for access: solution
heat2d.c:79: note: SLP: step doesn't divide the vector-size.
heat2d.c:79: note: Unknown alignment for access: solution
heat2d.c:79: note: SLP: step doesn't divide the vector-size.
heat2d.c:79: note: Unknown alignment for access: solution
heat2d.c:79: note: SLP: step doesn't divide the vector-size.
heat2d.c:79: note: Unknown alignment for access: solution
heat2d.c:79: note: SLP: step doesn't divide the vector-size.
heat2d.c:79: note: Unknown alignment for access: solution
heat2d.c:79: note: SLP: step doesn't divide the vector-size.
heat2d.c:79: note: Unknown alignment for access: solution
heat2d.c:79: note: Failed to SLP the basic block.
heat2d.c:79: note: not vectorized: failed to find SLP opportunities in basic block.
heat2d.c:75: note: not vectorized: not enough data-refs in basic block.
heat2d.c:75: note: not vectorized: not enough data-refs in basic block.
heat2d.c:79: note: not vectorized: no vectype for stmt: vect_var_.72_169 = MEM[(double[2][302][302] *)vect_psolut:
 scalar_type: vector(2) double
heat2d.c:79: note: Failed to SLP the basic block.
heat2d.c:79: note: not vectorized: failed to find SLP opportunities in basic block.
{\tt heat2d.c:75:} note: not vectorized: not enough data-refs in basic block.
heat2d.c:75: note: not vectorized: not enough data-refs in basic block.
heat2d.c:43: note: not vectorized: not enough data-refs in basic block.
Analyzing loop at heat2d.c:64
heat2d.c:64: note: misalign = 0 bytes of ref solution[pretmp_34][i_41][300]
heat2d.c:64: note: misalign = 8 bytes of ref solution[pretmp_34][i_41][301]
heat2d.c:64: note: not consecutive access _19 = solution[pretmp_34][i_41][300];
heat2d.c:64: note: not vectorized: complicated access pattern.
heat2d.c:64: note: bad data access.
Analyzing loop at heat2d.c:57
heat2d.c:57: note: not vectorized: control flow in loop.
heat2d.c:57: note: bad loop form.
heat2d.c:53: note: vectorized 0 loops in function.
heat2d.c:53: note: not consecutive access pretmp_34 = cur_gen;
heat2d.c:53: note: Failed to SLP the basic block.
heat2d.c:53: note: not vectorized: failed to find SLP opportunities in basic block.
heat2d.c:59: note: not vectorized: not enough data-refs in basic block.
heat2d.c:60: note: misalign = 8 bytes of ref solution[pretmp_34][i_40][1]
heat2d.c:60: note: misalign = 0 bytes of ref solution[pretmp_34][i_40][0]
heat2d.c:60: note: not consecutive access _12 = solution[pretmp_34][i_40][1];
heat2d.c:60: note: not consecutive access solution[pretmp_34][i_40][0] = _12;
heat2d.c:60: note: Failed to SLP the basic block.
heat2d.c:60: note: not vectorized: failed to find SLP opportunities in basic block.
heat2d.c:62: note: misalign = 0 bytes of ref solution[pretmp_34][i_40][0]
heat2d.c:62: note: not consecutive access solution[pretmp_34][i_40][0] = 2.0e+1;
heat2d.c:62: note: Failed to SLP the basic block.
heat2d.c:62: note: not vectorized: failed to find SLP opportunities in basic block.
```

```
heat2d.c:57: note: not vectorized: not enough data-refs in basic block.
heat2d.c:53: note: not vectorized: not enough data-refs in basic block.
heat2d.c:53: note: not vectorized: not enough data-refs in basic block.
heat2d.c:66: note: misalign = 0 bytes of ref solution[pretmp_34][i_41][300]
heat2d.c:66: note: misalign = 8 bytes of ref solution[pretmp_34][i_41][301]
heat2d.c:66: note: not consecutive access _19 = solution[pretmp_34][i_41][300];
heat2d.c:66: note: not consecutive access solution[pretmp_34][i_41][301] = _19;
heat2d.c:66: note: Failed to SLP the basic block.
heat2d.c:66: note: not vectorized: failed to find SLP opportunities in basic block.
heat2d.c:53: note: not vectorized: not enough data-refs in basic block.
heat2d.c:70: note: not vectorized: not enough data-refs in basic block.
Analyzing loop at heat2d.c:28
heat2d.c:28: note: not vectorized: number of iterations cannot be computed.
heat2d.c:28: note: bad loop form.
heat2d.c:18: note: vectorized 0 loops in function.
heat2d.c:20: note: misalign = 0 bytes of ref final
heat2d.c:20: note: not consecutive access final = 1.024e+3;
heat2d.c:20: note: Failed to SLP the basic block.
heat2d.c:20: note: not vectorized: failed to find SLP opportunities in basic block.
heat2d.c:22: note: not vectorized: not enough data-refs in basic block.
heat2d.c:26: note: not vectorized: not enough data-refs in basic block.
heat2d.c:36: note: not vectorized: not enough data-refs in basic block.
heat2d.c:18: note: not vectorized: not enough data-refs in basic block.
heat2d.c:30: note: not vectorized: not enough data-refs in basic block.
heat2d.c:18: note: not vectorized: not enough data-refs in basic block.
```

The details above shows the list of loops in the program and if they are being vectorized or not. These reports can pinpoint areas where the compiler cannot apply vectorization and related optimizations. It may be possible to modify your code or communicate additional information to the compiler to guide the vectorization and/or optimizations.

6.2 Counters Report

This subsection provides details about software and hardware counters.

Performance counter stats for './heat2d' (3 runs):

```
97072.006990
                   task-clock (msec)
                                                  3.900 CPUs utilized
                                                                                (+-1.03)
                                                                                ( +- 11.25 )
         1,529
                    context-switches
                                             #
                                                  0.016 K/sec
            39
                   cpu-migrations
                                             #
                                                  0.000 K/sec
                                                                                (+-1.48)
                                                                                ( +- 0.31 )
           426
                   page-faults
                                             #
                                                 0.004 K/sec
<not supported>
                    cycles
<not supported>
                    stalled-cycles-frontend
<not supported>
                    stalled-cycles-backend
<not supported>
                    instructions
<not supported>
                    branches
<not supported>
                    branch-misses
  24.890967206 seconds time elapsed
                                                                           (+-1.01)
```

The details above shows counters that provide low-overhead access to detailed performance information using internal registers of the CPU.

References

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- [2] Amdahl, Gene M., Validity of the single processor approach to achieving large scale computing capabilities. Communications of the ACM, Proceedings of the April 18-20, 1967, spring joint computer conference Pages 483-485, 1967.
- [3] John L. Gustafson, Reevaluating Amdahl's Law. Communications of the ACM, Volume 31 Pages 532-533, 1988.
- [4] OpenMP Architecture Review Board, OpenMP Application Program Interface. http://www.openmp.org, 3.0, May 2008.